

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 8,891,347 B2
APPLICATION NO. : 13/522422
DATED : November 18, 2014
INVENTOR(S) : Yin

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In The Drawings

In Fig. 3, Sheet 2 of 4, delete “UP/UC/DSP” and insert -- $\mu P/\mu C/DSP$ --, therefor. (See Attached Sheet)

In The Specification

In Column 1, below Title, insert -- RELATED APPLICATION DATA

The present application is a U.S. national stage filing under 35 U.S.C. §371 of International Application No. PCT/CN2011/077718, filed on July 28, 2011, the entire contents of which are incorporated herein by reference. --.

In Column 6, Line 39, delete “and or” and insert -- and/or --, therefor.

In Column 8, Line 29, delete “Signal” and insert -- Signal --, therefor.

In Column 9, Line 34, delete “i-1,2” and insert -- i-1,2, --, therefor.

In Column 9, Line 35, delete “Where” and insert -- where --, therefor.

In Column 9, Line 41, delete “ $c_{TX}(\Omega_1)$ and c_{RX}^* ” and insert -- $c_{TX}^*(\Omega_1)$ and $c_{RX}^*(\Omega_1)$ --, therefor.

In The Claims

In Column 17, Line 27, in Claim 6, delete “comprisingequalizing” and insert -- comprising equalizing --, therefor.

Signed and Sealed this
Ninth Day of June, 2015



Michelle K. Lee
Director of the United States Patent and Trademark Office

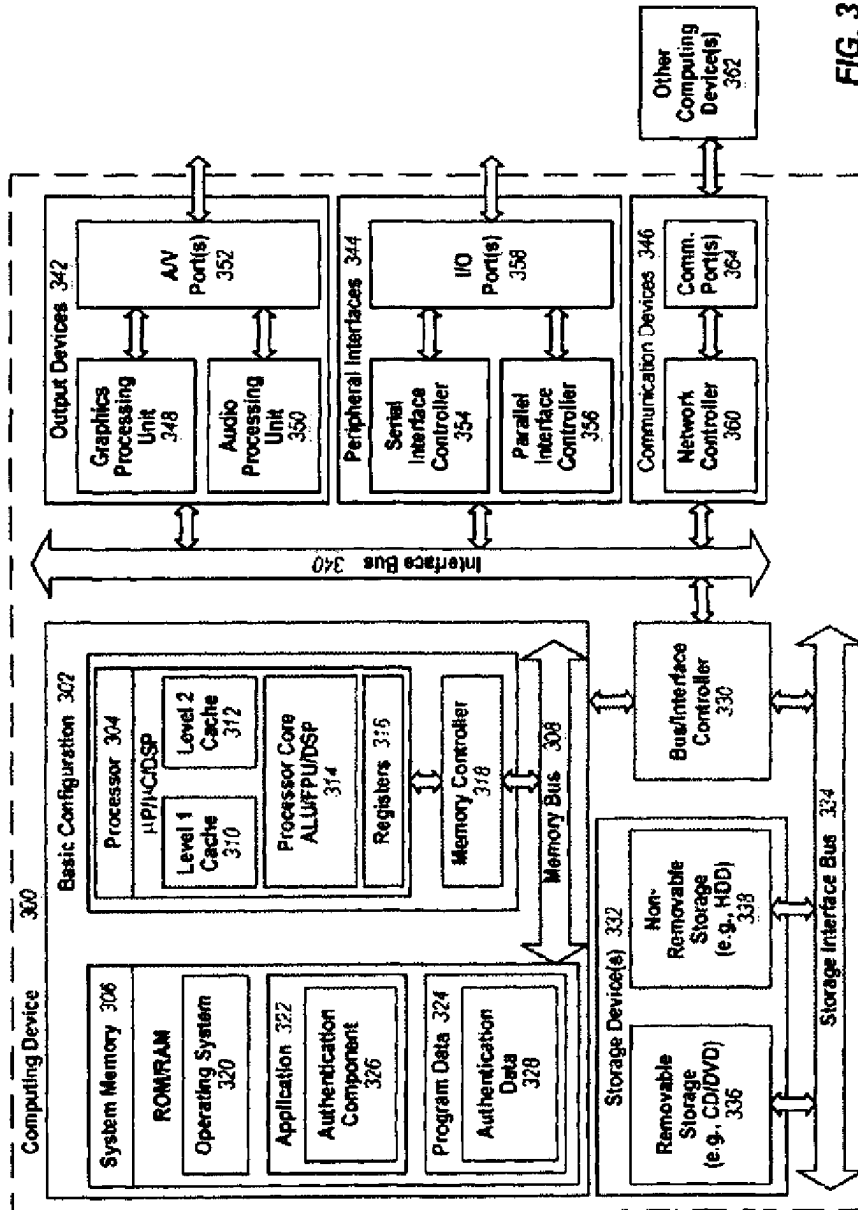


FIG. 3