

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 8,891,347 B2

APPLICATION NO. : 13/522,422

ISSUE DATE : November 18, 2014

INVENTOR(S) : Yin

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Fig. 3, Sheet 2 of 4, delete “UP/UC/DSP” and insert - - $\mu P/\mu C/DSP$ - -, therefor.

In Column 1, below Title, insert - - CROSS-REFERENCE TO RELATED APPLICATION
The present application is a U.S national stage filing under 35 U.S.C. §371 of International Application No. PCT/CN2011/077718, filed on July 28, 2011, the entire contents of which are incorporated herein by reference. - -

In Column 6, Line 39, delete “and or” and insert - - and/or - -, therefor.

In Column 8, Line 29, delete “Signal” and insert - - Signal - -, therefor.

In Column 9, Line 34, delete “i=1,2” and insert - - i=1,2, - -, therefor.

In Column 9, Line 35, delete “Where” and insert - - where - -, therefor.

In Column 9, Line 41, delete “ $c_{Tx}(\Omega_1)$ and c_{Rx}^* ” and insert - - $c_{Tx}^*(\Omega_1)$ and $c_{Rx}^*(\Omega_1)$ - -, therefor.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Maschoff Brennan
1389 Center Drive, Suite 300
Park City, Utah 84098, United States

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In Column 17, Line 27, in Claim 6, delete “comprisingequalizing” and insert - - comprising equalizing - -, therefor.

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