

(12) **INTER PARTES REVIEW CERTIFICATE** (349th)

**United States Patent
Hidaka**

(10) Number: **US 6,233,181 K1**
(45) Certificate Issued: **Feb. 6, 2018**

(54) **SEMICONDUCTOR MEMORY DEVICE
WITH IMPROVED FLEXIBLE
REDUNDANCY SCHEME**

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(73) Assignee: **LIMESTONE MEMORY SYSTEMS
LLC**

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The results of IPR2016-00096 are reflected in this inter partes review certificate under 35 U.S.C. 318(b).

INTER PARTES REVIEW CERTIFICATE
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Trial No. IPR2016-00096
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AS A RESULT OF THE INTER PARTES
REVIEW PROCEEDING, IT HAS BEEN
DETERMINED THAT:

Claims 1, 2, 4, 6 and 7 are cancelled.

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