

# EXHIBIT H



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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
14/172,392	02/04/2014	David F. Sorrells	19200.1.1.1.2.2.1.2	8448
22913	7590	08/27/2014	EXAMINER	
Workman Nydegger 60 East South Temple Suite 1000 Salt Lake City, UT 84111			BHATTACHARYA, SAM	
			ART UNIT	PAPER NUMBER
			2646	
			NOTIFICATION DATE	DELIVERY MODE
			08/27/2014	ELECTRONIC

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

Docketing@wnlaw.com



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1. The present application is being examined under the pre-AIA first to invent provisions.

### **DETAILED ACTION**

#### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of pre-AIA 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claim 1 is rejected under pre-AIA 35 U.S.C. 102(b) as being anticipated by Arpaia et al. (US 6,192,225).

Regarding claim 1, Arpaia discloses a system for frequency down-converting a modulated carrier signal (FIG. 5), comprising: a first switch, a first aperture signal, and a first energy storage element that down-converts said modulated carrier signal according to a first control signal and outputs a down-converted in-phase signal portion of said modulated carrier signal; a second switch, a second aperture signal, and a second energy storage element that down-converts said modulated carrier signal according to a second control signal and outputs a down-converted inverted in-phase signal portion of said modulated carrier signal (col. 4, lines 21-50); and a first differential amplifier circuit that combines said down-converted in-phase signal portion with said inverted in-phase signal portion and outputs a first channel down-converted differential in-phase signal (col. 5, lines 31-50).

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***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sam Bhattacharya whose telephone number is (571)272-7917. The examiner can normally be reached on Weekdays, 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lester Kincaid can be reached on (571) 272-7922. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

sb

/Sam Bhattacharya/  
Primary Examiner, Art Unit 2646