

# Exhibit K



App.	Title	Primary Author/ Publisher	Publication Date	Short Title
B-16	Faster Scan Conversion Using the TMS320C80	Akhan, TI	09/1996	Akhan
B-17	Neon: A (Big) (Fast) Single-Chip 3D Workstation Graphics Accelerator	McCormack	07/1999	McCormack
B-18	October '98 Video Accelerator Comparison	Shimpi	10/12/1998	Shimpi
	The Best Distribution for a Parallel OpenGL 3D Engine with Texture Caches	Vartanian	12/1999	
	High-Performance Polygon Rendering;	Akeley	08/1988	
	Compilation, Architectural Support, and Evaluation of SIMD Graphics Pipeline Programs on a General Purpose CPU	Breternitz, Jr.	01/2003	
	Graphics Rendering Architecture for a High Performance Desktop Workstation	Harrell	09/1993	
	Simulation and Expected Performance Analysis of Multiple Processor Z-Buffer Systems	Parke	07/1980	
	Pixel-Planes 5: A Heterogeneous Multiprocessor Graphics System Using Processor-Enhanced Memories	Fuchs	08/1989	
	WireGL: A Scalable Graphics System for Clusters	Humphreys	06/2001	
	Neon: A Single-Chip 3D Workstation Graphics Accelerator	McCormack	01/1998	
	Breaking The Frame-Buffer Bottleneck: The Case For Logic-Enhanced Memories	Poulton	12/1992	
	InfiniteReality: A Real-Time Graphics System	Montrym	1997	
	RealityEngine Graphics	Akeley	1993	
	Designing Graphics Architectures Around Scalability and Communication	Eldridge	6/2001	
	Pomegranate: A Fully Scalable Graphics Architecture	Eldridge	2000	
	A Sorting Classification of Parallel Rendering	Molnar	8/1994	
	TMS320C6000 CPU and Instruction Set Reference Guide	TI	10/2000	
	TMS320C6000 Imaging Developers Kit (IDK) Users Guide	TI	12/2000	

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	TMS320C6000 Technical Brief	TI	1999	Technical Brief
	TMS320C80 Multimedia Video Processor	TI	1994	
	Texas Instruments TMS320C6000 Press Release	EDN	03/30/2000	Press Release
	Video & Graphics Processors: 1997	Watlington	1997	
	Kyro – Integrated 2D/3D Graphics Accelerator – Product Overview	STMicro and Imagination	2000	Kyro Product Overview
	Kyro from STMicroelectronics/Imagination Technologies (available at <a href="http://ixbtlabs.com/articles/kyro/">http://ixbtlabs.com/articles/kyro/</a> )	Worobyew, IXBT Labs		Kyro Article
	Kyro – Tile Based Rendering – the Future of 3D Graphics	STMicro and Imagination	2000	Kyro Tile Based
	Imagination Technologies / STMicro PowerVR Series 3: KYRO	Andrawes, AnandTech	06/07/2000	Power VR Article
	STMicroelectronics/Imagination Technologies Kyro II (available at <a href="http://ixbtlabs.com/articles/kyro2/">http://ixbtlabs.com/articles/kyro2/</a> )	Woroblyw, IXBT Labs		Kyro II Article
	STMicroelectronics Kyro II	Witheiler, AnandTech	03/13/2001	
	Kyro – Internal True Color	STMicro and Imagination,	2000	
	A Survey of Media Processing Approaches	Panchanathan	08/2002	

### 3. Prior Art Devices

In addition to the printed publications identified above, Defendant discloses the following prior art devices. The descriptions of the systems and events relating to the devices are stated on information and belief, and are supported by documents that will be produced by Defendants and/or third parties. As discovery is ongoing, Defendants continue to investigate these devices.

Defendant reserves the right to rely upon any device, public knowledge or use embodying or otherwise incorporating any of the prior art disclosed below, alone or in combination.

October 11, 2022

/s/ Catherine Garza

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**CERTIFICATE OF SERVICE**

I hereby certify that on October 11, 2022 the foregoing document, as well as all associated Appendices, were served via e-mail upon all counsel of record in this case.

/s/ Catherine Garza