EXHIBIT 7

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George Jacobs · 3rd

Senior Engineer at Doble Engineering Company

- doble   Doble Engineering Company
- MAINE   University of Maine

Princeton, Massachusetts, United States · **Contact info**

284 connections

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About

Semiconductor design engineering professional with extensive experience in all phases of hardware design cycle. Effectively delivers and implements complex architectures in today's deep sub micron technology. Success record of many ASICs in existing systems delivered on time with a proven track record.

Activity

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Principal Engineer

Doble Engineering Company · Full-time

Jul 2020 - Present · 2 yrs 3 mos

Marlborough, Massachusetts, United States

Xilinx Artix, Zynq PL System Design. Design/Verification
DxDesigner PCB Design

Principal Engineer

Test Evolution Corp · Full-time

Jan 2020 - Jul 2020 · 7 mos

Hopkinton, Massachusetts, United States

Xilinx Zynq Ultrascale platform development.
MIPI, CSI, DSI system designs

Senior Member of Technical Staff

AMD

Oct 2013 - Jul 2020 · 6 yrs 10 mos

Boxboro, MA

Principal Engineer

MKS Instruments

Aug 2016 - Jan 2020 · 3 yrs 6 mos

Wilmington, Massachusetts

FPGA coding in VHDL and MatLab/Simulink HDL coder.
DSP coding in C++ using ADI tools.   ...see more

Senior Engineer

Silicon Micro Display, Inc

Apr 2015 - Jul 2016 · 1 yr 4 mos

Newton, MA

FPGA architecture and design using Kintex/Virtex/Spartan parts using ISE
and Vivado Xilinx tools. Simulation with Modelsim.   ...see more

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Education

University of Maine

Skills

ASIC

👥 13 endorsements

Hardware Architecture

👤 Endorsed by Greg Sadowski who is highly skilled at this

🟦 Endorsed by 2 colleagues at Silicon Micro Display, Inc

👥 10 endorsements

Static Timing Analysis

◼ Endorsed by 2 colleagues at AMD

👥 8 endorsements

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Recommendations

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Interests

Companies   Groups   Schools

🔵 doble **Doble Engineering Company**
32,997 followers

◼ **Test Evolution Corp**

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Software QA Automation Engineer

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