# **EXHIBIT 9**





Jobs

# About

35+ years of hardware design experience in the video, film, and graphics industries. Expertise in massively parallel multithreaded programmable hardware for graphics and compute. Early innovator in digital paint and 3D graphics. Over 40 patents. Recipient of Scientific and Technical Oscar in 1993.

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**Senior Fellow** 

AMD Aug 2018 - Present · 4 yrs 2 mos Santa Clara



### Independent Consultant

AMD Jul 2014 - Dec 2017 · 3 yrs 6 mos

Architect for future graphics system implementing a number of revolutionary concepts designed to maximize performance and r ...see more



### **Senior Fellow**

ATI/AMD 2000 - May 2012 · 12 yrs 5 mos

Hardware designer and architect for a number of high end graphics products and game consoles. Chief architect for the R300 chip that used an innovative partial tiling method to give a 2x to 4x pixel rate advantage over the competition. Many other chip families were derived from this design. One of the pioneers of the DX9 graphics API which created a paradigm shift in photorealism, and revolutionized the graphics industry.

Architect for the "shader next" design currently in use in AMD's latest generation graphics products. This design provided significant area and power reduction over the original design through simplification and streamlining of the underlying instruction set.



### Engineer

ArtX Inc 1997 - 2000 · 3 yrs

Designer and architect for an integrated graphics northbridge. Designer and architect for the "Flipper" graphics architecture used by the Nintendo



### Member of Technical Staff

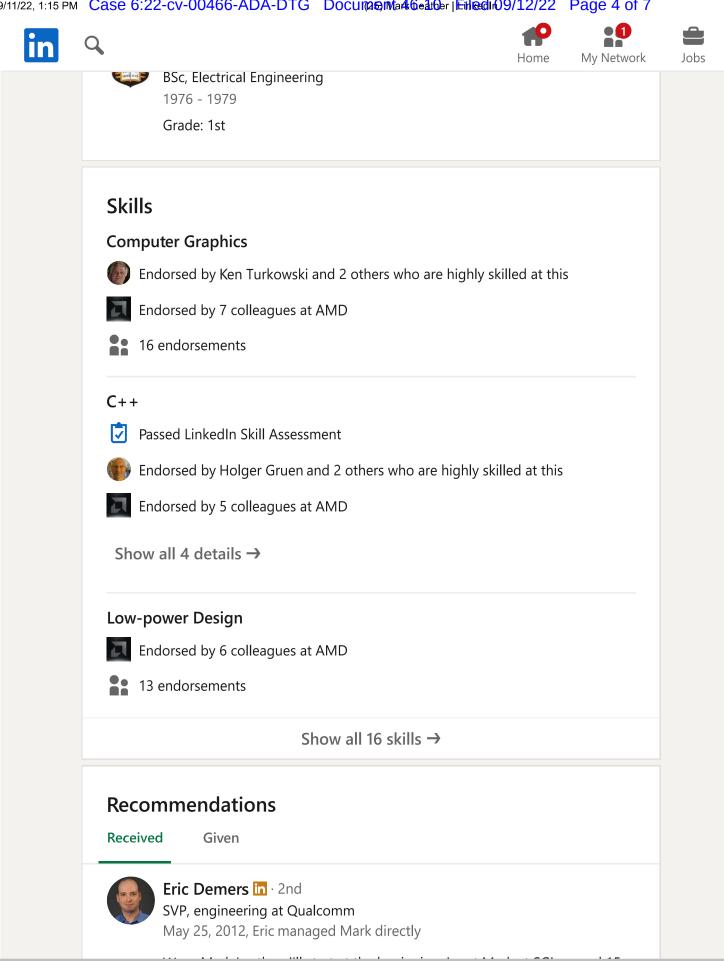
Silicon Graphics 1989 - 1997 · 8 yrs

Chip designer on the reality and infinite reality graphics systems. Coinventor of MSAA - a method for edge antialiasing now used almost

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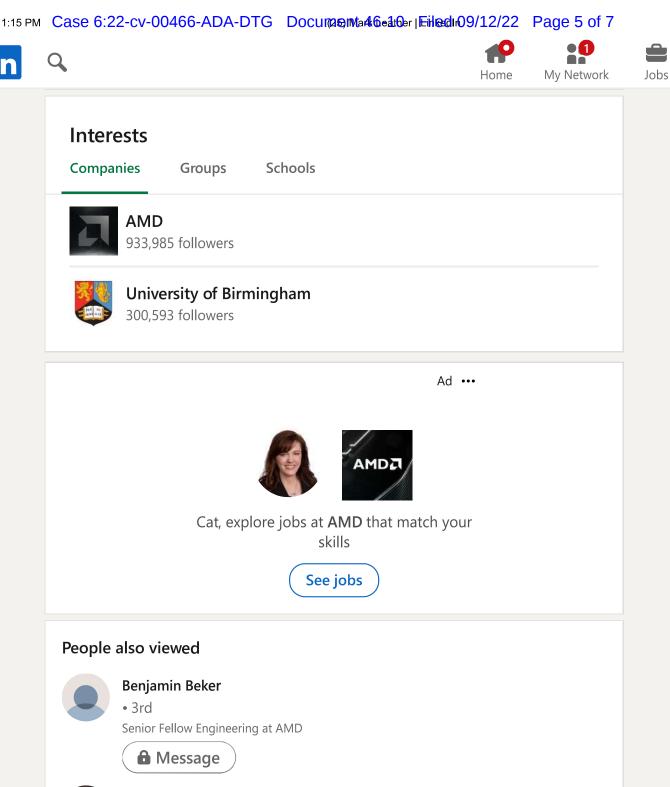
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# **Brian Emberling** • 3rd Senior Fellow, Graphics Hardware Architect at AMD

+ Connect



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**Vilas Sridharan** • 3rd AMD Senior Fellow

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