Exhibit 13

UNITED STATES DISTRICT COURT
FOR THE WESTERN DISTRICT OF TEXAS
WACO DIVISION

AIRE TECHNOLOGY LTD., Plaintiff, v. SAMSUNG ELECTRONICS CO., LTD. and SAMSUNG ELECTRONICS AMERICA, INC., Defendants.	Case No. 6:21-cv-955-ADA **JURY TRIAL DEMANDED**
AIRE TECHNOLOGY LTD., Plaintiff, v. APPLE INC. Defendant.	Case No. 6:21-cv-1101-ADA **JURY TRIAL DEMANDED**
AIRE TECHNOLOGY LTD., Plaintiff, v. GOOGLE LLC, Defendant.	Case No. 6:21-cv-1104-ADA **JURY TRIAL DEMANDED**

Expert Declaration of Michael C. Brogioli, Ph.D.
in Support of Aire's Claim Construction Brief

I. INTRODUCTION

1. I have been retained as an expert in the above captioned case by Plaintiff Aire Technology Ltd. ("Aire"). I understand that the parties dispute the meaning of certain claim terms. In particular, I have reviewed Defendants' Consolidated Opening Claim Construction Brief and provide this declaration in response. I have studied the intrinsic and relevant extrinsic evidence pertaining to those terms. In this declaration, I provide my opinions regarding how one of ordinary skill in the relevant art would understand two disputed terms for U.S. Patent No. 8,174,360 (the "'360 Patent").

II. QUALIFICATIONS

2. My qualifications for forming the opinions given in this declaration are summarized here and are addressed more fully in my curriculum vitae, which is attached as Exhibit 1. That exhibit also includes a list of my publications.

3. I am currently an Adjunct Professor of Electrical and Computer Engineering at Rice University in Houston, TX, teaching Ph.D. candidate level courses in wireless telecommunications, embedded computing software, embedded computing hardware, and software/hardware optimization in modern computing systems utilizing modern high-level programming languages, and I have held this position since July 2009. I am also currently the Managing Director of Polymathic Consulting in Austin, TX, a company that advises a variety of clients on engineering, research and development, intellectual property, and other technical leadership matters, and I have held this position since October 2011.

4. I am a named inventor on multiple U.S. Patents as well as various pending applications. The subject matter of these patents includes, but is not limited to, embedded processing, edge computing, heterogeneous wireless systems development and deployment, as

well as various aspects of distributed systems including computation, interconnectivity design and implementation.

5. From November 2009 to October 2011, I was Chief Architect, Senior Member Technical Staff at Freescale Semiconductor in Austin, TX (formerly Motorola), responsible for management of technology, engineering roadmaps, design lead on compiler infrastructure and optimizations for wireless and embedded computing solutions. From 2008 to 2009, I was Senior Engineer working with high performance compiler design and next generation wireless architectures at Freescale Semiconductor in Austin, Texas. Prior to this, I held roles at Texas Instruments in their Advanced Architecture and Chip Technologies group in the area of architectures for wireless communications, as well as roles at Intel's Microprocessor Research Labs.

6. From June 2006 to August 2007, I worked as the Technical Co-Founder of Method Seven LLC, in Boston, MA, working with high performance software and hardware systems architecture. I am currently the co-founder, co-inventor, and Chief Technology Officer of Network Native, an Internet of Things hardware and software company. I am also a co-founder, and co-inventor at AgCompute, an intelligent Agriculture Technology company developing solutions using wired/wireless networks and drone technologies.

7. While at Rice University, I developed various computer architecture designs relating to networking and wireless technologies. For example, from 2002 to 2004, I developed Spinach, a simulator design toolset for modeling programmable network interface architectures, which models system components common to all programmable computing environments as well as components specific to embedded. From 2004 to 2009, I developed Spinach DSP-FPGA, a modular and composable simulator design infrastructure for programmable and

reconfigurable embedded SOC architectures specifically targeting mobile and wireless computing devices. From 2005 to 2009, I developed and published a retargetable compiler infrastructure for software and systems related to mobile, wireless and embedded computing technologies. Many of these tools have been used at U.S. universities in the area of electrical and computer engineering research.

8. I am recognized as an expert in the field of computer hardware, computer software, and wireless technologies as they relate to the subject matter at hand. I am a member of the Institute of Electrical and Electronics Engineers (IEEE). I am formerly the Program Chair of Design Automation Conference in the areas of Embedded and Wireless Solutions, as well as a current technical committee member. I have previously been a Program Committee member for the IEEE and ACM Design Automation Conference from 2011 to the present, in addition to a number of other IEEE and ACM conferences related to wireless technologies and various aspects of computer hardware, software, and wireless technologies. I have published, co-published and contributed to numerous works on wireless and mobile computing, sensor technologies, software optimizations, signal processing systems, digital signal processing and related architectures, and low power computing, among other topics.

9. I have previously served as an engineering consultant and testifying witness on matters related to, and including, various types of wireless technologies including those described in the patents at issue. These include mobile devices, wireless cellular devices, and technologies such as near field communication, as well as hardware and software design for these types of systems. I have also worked on a number of technologies related to various types of wireless communications, including short range wireless in my 20+ years of experience.

10. My current curriculum vitae, provided in Exhibit 1, contains more information on

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