EXHIBIT 6-3

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EXHIBIT 2

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IN THE UNITED STATES DISTRICT COURT FOR THE WESTERN DISTRICT OF TEXAS WACO DIVISION

PARKERVISION, INC., Plaintiff

W-20-CV-00562-ADA

INTEL CORPORATION, Defendant

-V-

CLAIM CONSTRUCTION ORDER

The Court held a *Markman* hearing on June 24, 2021 and July 22, 2021. During those hearings, the Court provided its final constructions. The Court now enters those claim constructions.

SIGNED this 22nd day of July, 2021.

ALAN D ALBRIGHT UNITED STATES DISTRICT JUDGE

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| Term | Plaintiff's Proposed Construction | Defendants' Proposed Construction | Court's |
|---|---|---|---|
| "under-sample" / "undersamples" / "under-sampling" '706 patent, claims 1, 6, 7, 28, 34 | "sampling at an aliasing rate" or "sampling at less than or equal to twice the frequency of the input signal" | "sample[s/ing] at less than or equal to twice the frequency of the input signal using negligible apertures (i.e., pulse widths) that tend towards zero time in duration" | "sampling or "sampl equal to tw the input s |
| "storage module" '706 patent, claims 105, 114, 164, 175, 179, 186, 190 | "a module of an energy transfer system that stores non-negligible amounts of energy from an input electromagnetic signal for driving a low impedance load" | "a module that stores a non- negligible amount of energy from an input electromagnetic (EM) signal" | "a module transfer sy non-neglig energy fro electroma driving a 1 |
| "switch" '706 patent, claims 105, 164, 175, 186; '108 patent, claim 1 | "an electronic device for opening and closing a circuit as dictated by an independent control input" | "an electronic device for opening and closing a circuit" | Plain-and- wherein th meaning is device for a circuit as independe |

| Term | Plaintiff's Proposed | Defendants' Proposed | Court's |
|--|----------------------------|---|--------------------------|
| | Construction | Construction | |
| "a down-convert and delay module to under-sample an input signal to produce an input sample of a down- converted image of said input signal, and to delay said input sample" | Plain and ordinary meaning | Function: under-sample an input signal according to a control signal to produce an input sample of a down- converted image of said input signal, and to delay said input sample | Not subjec Plain-and- |
| '706 patent, claims 1, 7 | | Structure: the down convert and delay module 2624 in Fig. 26 and described at 26:1-27:21 and 28:2041, that includes the switches 2650 and 2654, and the capacitors 2652 and 2656; and equivalents thereof | |
| "a frequency translator to produce a sample of a down- converted image of an input signal, and to delay said sample" '706 patent, claim 34 | Plain and ordinary meaning | Function: produce a sample of a down-converted image of an input signal according to a control signal, and to delay said sample Structure: the down convert and delay module 2624 in Fig. 26 and described at 26:1-27:21 and 28:2041, that includes the switches 2650 and 2654, and the capacitors 2652 and 2656; and equivalents thereof. | Not subjec Plain-and- |

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