

Exhibit 4

U.S. Patent No. 6,622,108 (“’108 Patent”)

Accused Products

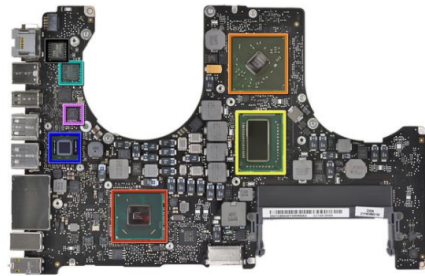
Apple Products including GDDR5 RAM, including without limitation the MacBook Pro (“MacBook Pro”) Claim 11 of the ’108 Patent.

Claim 11

Claim 11	MacBook Pro
11[pre]. A method of testing interconnects between a first electronic circuit and a second electronic circuit, the first electronic circuit comprising a main unit implementing a normal mode function of the first electronic circuit, and a test unit for testing the interconnects, the method comprising the steps of	To the extent the preamble is limiting, the MacBook Pro practices the method of testing interconnects between a first electronic circuit and a second electronic circuit, the first electronic circuit comprising a main unit implementing a normal mode function of the first electronic circuit, and a test unit for testing the interconnects. For example, the MacBook Pro tests interconnects between its CPU and GDDR5 SGRAM, as described in more detail below. Among other things, the MacBook Pro is designed to operate its GDDR5 memory at a data rate which requires use of the GDDR5 memory’s write training function. <i>See, e.g.:</i>

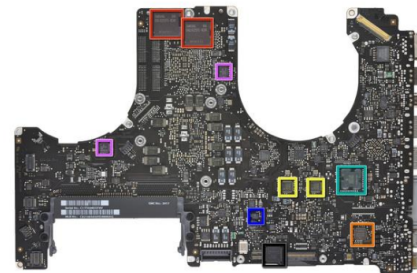
Claim 11

MacBook Pro



MacBook Pro

- AMD Radeon HD 6490
AMD 216-0809000)

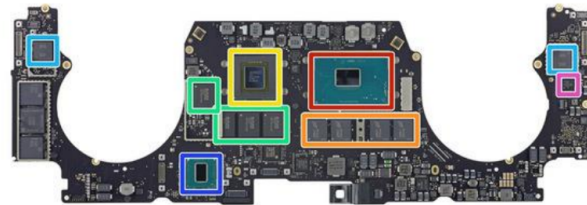


- Samsung K4G10325FE-
GDDR5 SGRAM graphic
2Gb (256 MB)

<https://www.ifixit.com/Teardown/MacBook+Pro+15-Inch+Unibody+Early+2011+Teardown/4990>

Claim 11

MacBook Pro



- AMD Radeon
- Elpida (Micron) MB GDDR5 R

<https://www.ifixit.com/Teardown/MacBook+Pro+15-Inch+Touch+Bar+Teardown/73395>

Claim 11

MacBook Pro

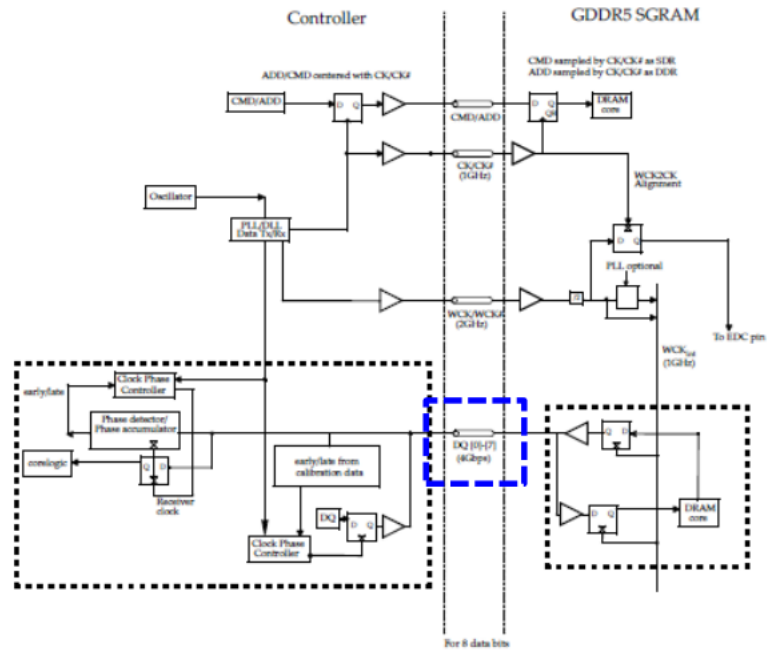


Figure 2 – Block Diagram of an example clock system

JEDEC Standard JESD212, Dec 2009

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