

Exhibit 12



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(54) **STRUCTURE IN A MICROELECTRONIC DEVICE INCLUDING A BI-LAYER FOR A DIFFUSION BARRIER AND AN ETCH-STOP LAYER**

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(52) **U.S. Cl.** **257/767**

(76) Inventors: **Grant Kloster**, Hillsboro, OR (US);
Jihperng Leu, Portland, OR (US);
Lawrence Wong, Beaverton, OR (US);
Andrew Ott, Hillsboro, OR (US);
Patrick Morrow, Portland, OR (US)

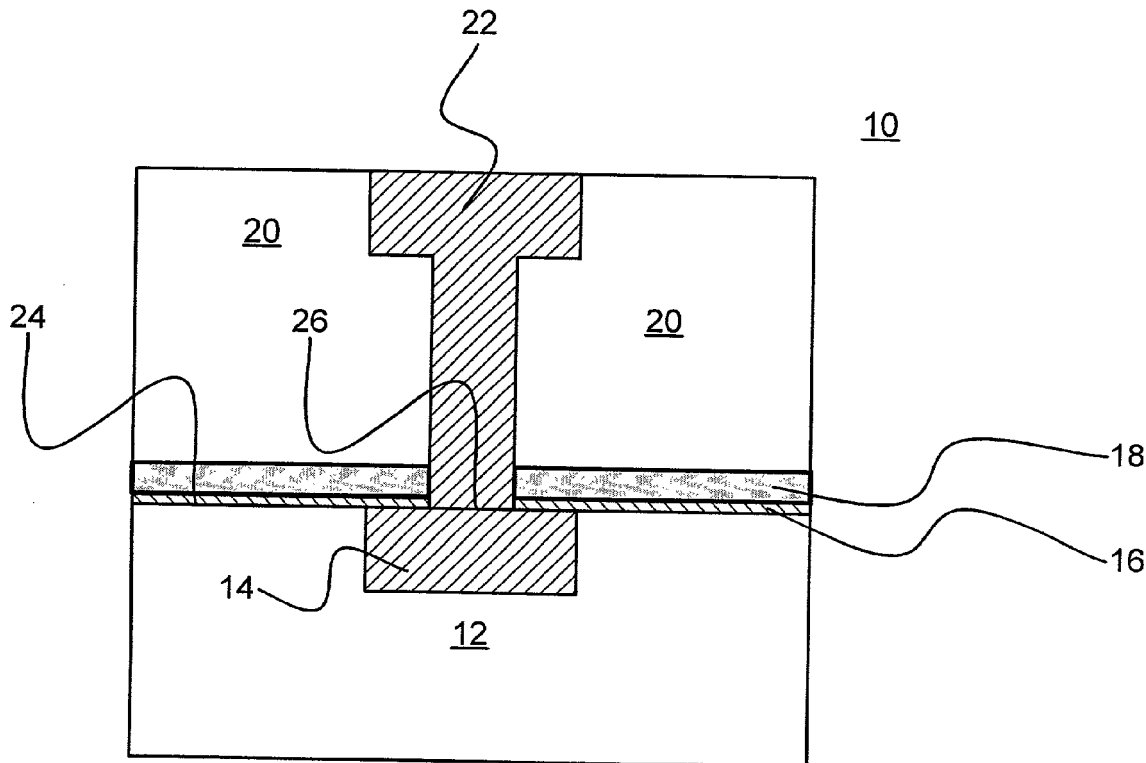
(57) **ABSTRACT**

Correspondence Address:

BLAKELY SOKOLOFF TAYLOR & ZAFMAN
12400 WILSHIRE BOULEVARD, SEVENTH
FLOOR
LOS ANGELES, CA 90025 (US)

The invention relates to a microelectronic device and a structure therein that includes a diffusion barrier layer having a first thickness and a first dielectric constant. An etch stop layer is disposed above and on the diffusion barrier layer. The etch stop layer has a second thickness and a second dielectric constant.

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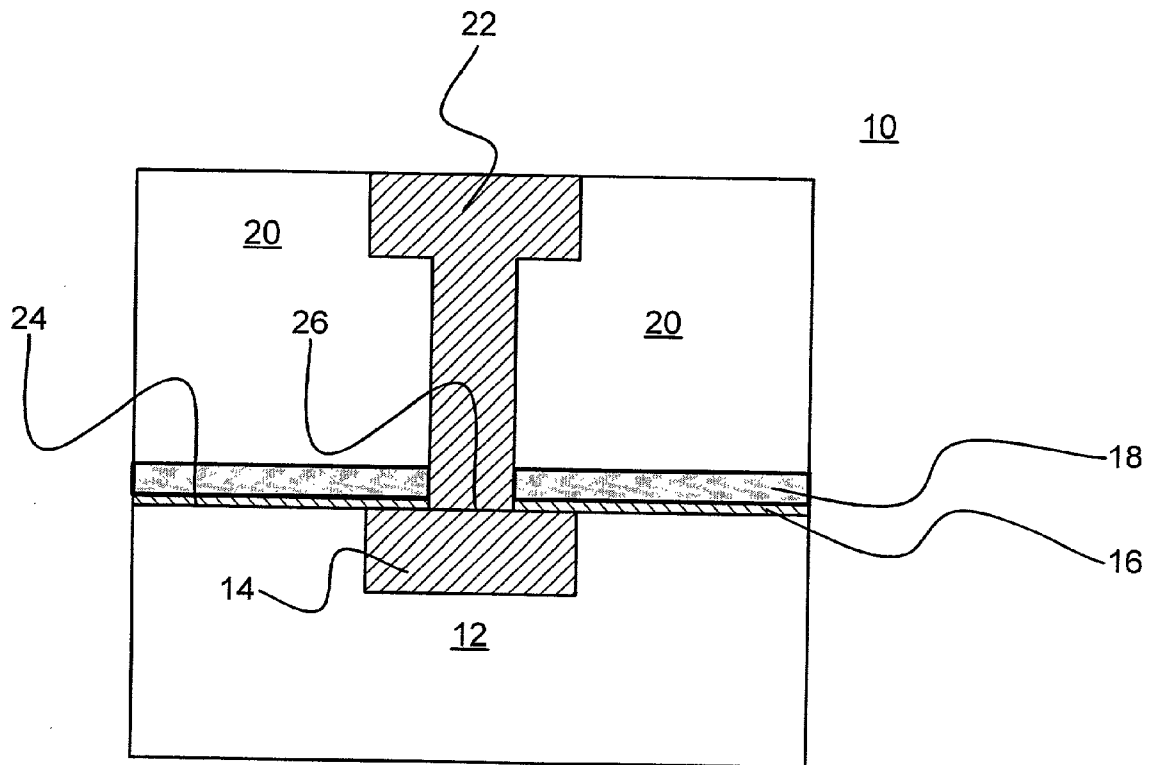


FIG. 1

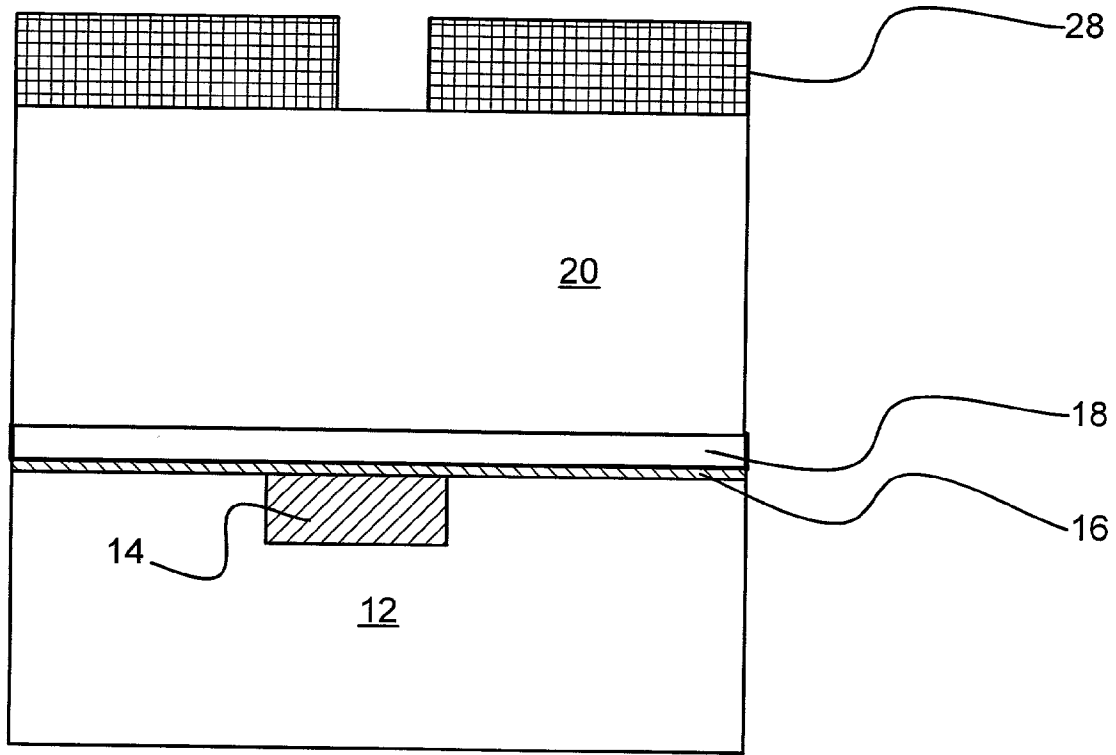


FIG. 2

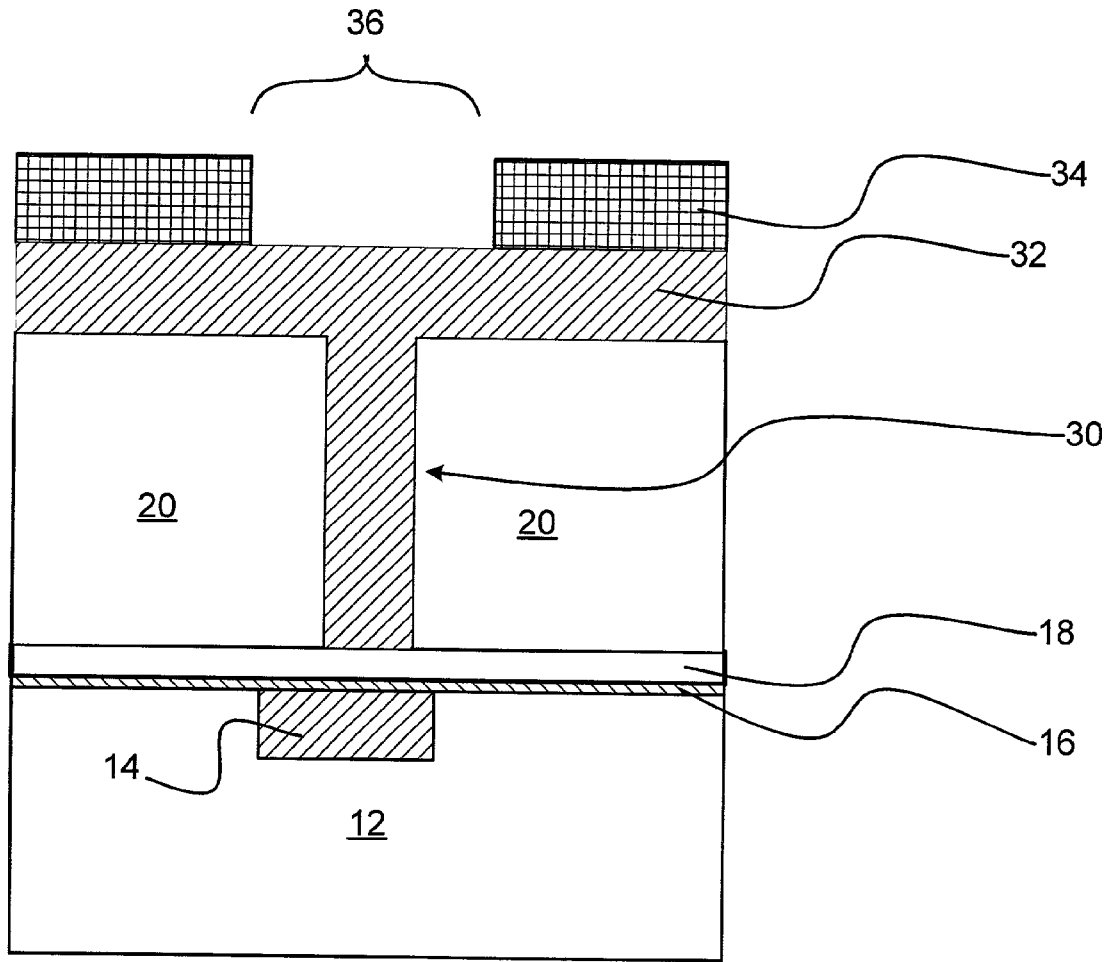


FIG. 3

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