EXHIBIT 8

IN THE UNITED STATES DISTRICT COURT
FOR THE EASTERN DISTRICT OF TEXAS
TEXARKANA DIVISION

MAXELL, LTD.,

Plaintiff,

v.

APPLE INC.,

Defendant.

Case No. 5:19-cv-00036-RWS

JURY TRIAL DEMANDED

DECLARATION OF VIJAY MADISETTI, PH.D. IN SUPPORT OF MAXELL'S CLAIM CONSTRUCTIONS

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I. INTRODUCTION

1. My name is Vijay Madisetti. I have been asked by the Plaintiff and patent holder in this case (Maxell, Ltd.) to provide my opinions as to the meaning of certain claim terms in U.S. Patent No. 8,339,493 (the "'493 Patent"). I have also been asked to provide my understanding of the level of ordinary skill in the art related to this patent, as well as to explain the relevant technical background related to this patent.

2. Although I am being compensated at my usual consulting rate of $500 per hour for my time reviewing materials and preparing this declaration, my opinions expressed here are my own. My compensation is in no way dependent on the outcome of this case or upon the Court accepting my opinions, and I have no other financial interest in this matter or the parties thereto.

3. Depending on new information learned during discovery, positions taken throughout the case by Defendant Apple Inc. or its experts, I may edit, add to, or otherwise refine the topics and expected testimony described here. I reserve the right to supplement my opinions based on new information.

4. If called upon to do so, I am prepared to testify before the Court regarding my opinions expressed here. In such a situation, I may rely on demonstratives, exhibits, or other visual aids to assist in presenting my testimony.

II. BACKGROUND AND QUALIFICATIONS

5. My qualifications to testify about the '493 Patent and the relevant technology are set forth in my curriculum vitae ("CV"), which I have attached hee as an Appendix. I will briefly summarize my qualifications in the following paragraphs.

6. I have thirty years of experience as an electrical and computer engineer in industry, education, and consulting. Currently, I am a Professor in Electrical and Computer

Engineering at Georgia Tech. I have worked extensively in the field of digital communications and have studied telecommunications and systems engineering since 1981.

7. I also have over 20 years of industry experience in computer engineering, signal processing, and telecommunications, including wireless communications and signal processing. Throughout this time, I have designed, implemented, and tested various products in the fields of electronics and communications.

8. In 1984, I received a Bachelor of Technology in Electronics and Electrical Communications Engineering from the Indian Institute of Technology (IIT). In 1989, I received my Ph.D. in Electrical Engineering and Computer Sciences (EECS) from the University of California, Berkeley. That year, I also received the Demetri Angelakos Outstanding Graduate Student Award from the University of California, Berkeley, and the IEEE/ACM Ira M. Kay Memorial Paper Prize.

9. In 1989, I joined the faculty of Georgia Tech. I began working at Georgia Tech as an assistant professor, became an associate professor in 1995, and have held my current position as Professor since 1998. As a member of the faculty at Georgia Tech, I have been active in, among other technologies, image and video processing, computer engineering, embedded systems, chip design, software systems, wireless networks, and cellular communications.

10. I have been involved in research and technology in the area of digital signal processing since the late 1980s, and I am the Editor-m-Chief the IEEE Press/CRC Press's three-volume Digital Signal Processing Handbook (Editions 1 & 2) (1998, 2010).

11. Over the past three decades, I studied, used, and designed image and video processing and wireless networking circuits for several applications, including digital and video cameras, mobile phones, and networking products for leading commercial firms.

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