

# EXHIBIT 8

**(12) United States Patent**  
**Oeda et al.**

**(10) Patent No.: US 6,329,794 B1**  
**(45) Date of Patent: Dec. 11, 2001**

(54) **INFORMATION PROCESSING DEVICE AND METHOD FOR CONTROLLING POWER CONSUMPTION THEREOF**

6,049,210 \* 4/2000 Hwang ..... 320/134  
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**OTHER PUBLICATIONS**

(75) Inventors: **Shigeto Oeda**, Fujisawa; **Naoki Mori**; **Hiroimichi Ito**, both of Yokohama, all of (JP)

Research File No. 5, ACPI, POS/V Magazine (1999), 7.15, p. 210.

(73) Assignee: **Hitachi, Ltd.**, Tokyo (JP)

\* cited by examiner

(\* ) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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**(57) ABSTRACT**

A power supply section includes a capacity detector capable of detecting the remaining capacity in a battery and a controller issuing power consumption reduction instructions to independently operable function devices based on usage priorities thereof. This allows power consumption operations to be performed such as stopping function devices with low priorities first based on the remaining battery capacity. As a result, power to function devices with higher priorities can be maintained. Based on the remaining battery capacity, the controller controls the operations of the function devices and uses a display to show information allowing the operator to identify operable and inoperable function devices as the battery capacity drops.

(21) Appl. No.: **09/657,151**

(22) Filed: **Sep. 7, 2000**

**(30) Foreign Application Priority Data**

May 22, 2000 (JP) ..... 12-154358

(51) **Int. Cl.**<sup>7</sup> ..... **H02J 7/00**

(52) **U.S. Cl.** ..... **320/132; 320/130**

(58) **Field of Search** ..... 320/132, 134, 320/139, 130; 324/426, 428

**(56) References Cited**

**U.S. PATENT DOCUMENTS**

5,969,529 \* 10/1999 Eiraku et al. .... 324/433

**14 Claims, 10 Drawing Sheets**

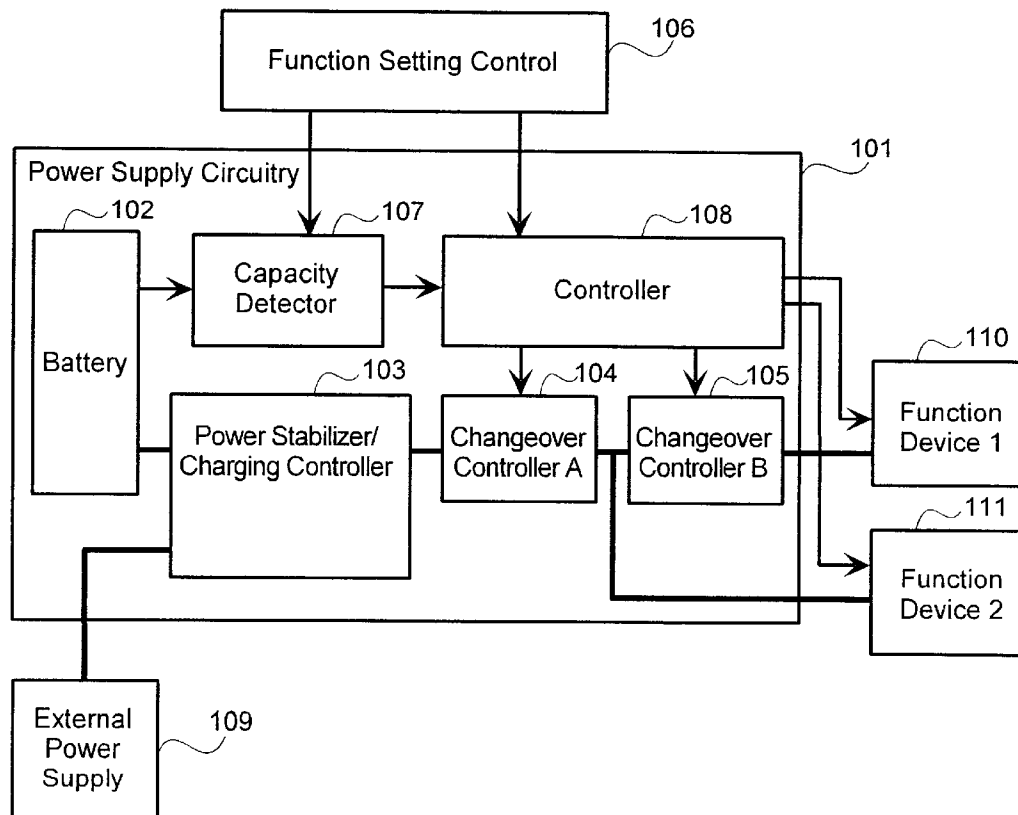


FIG. 1

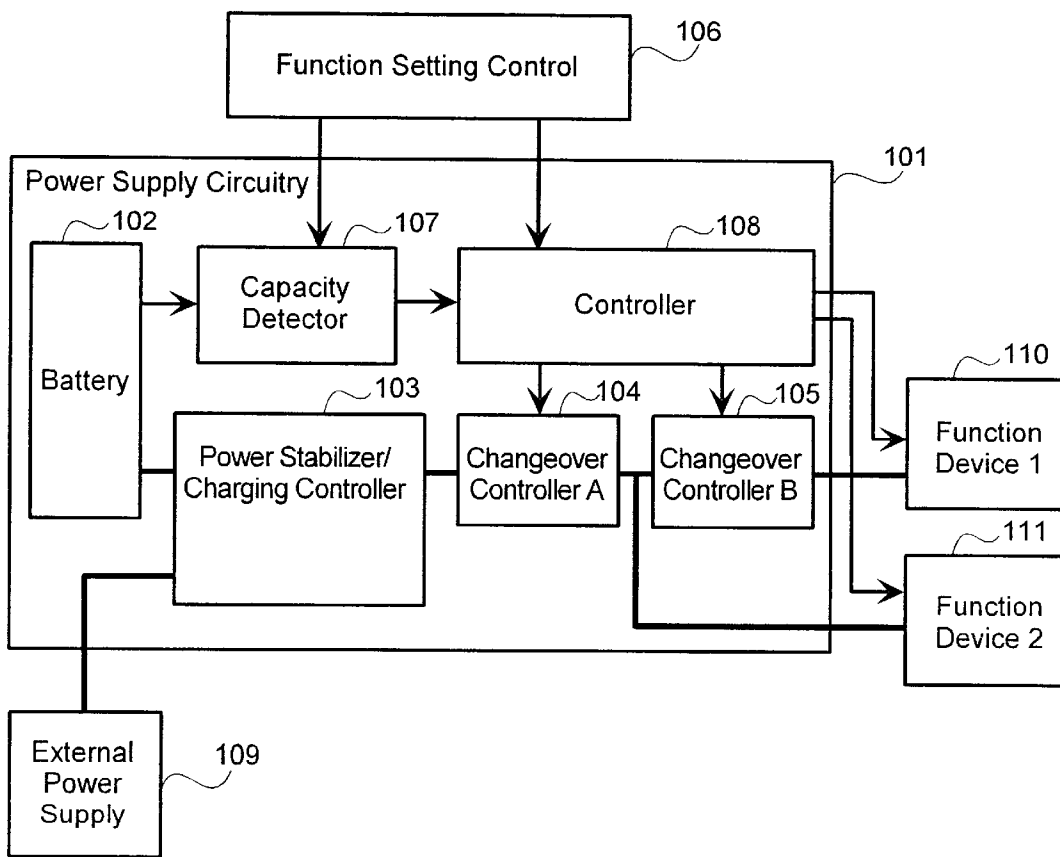


FIG.2

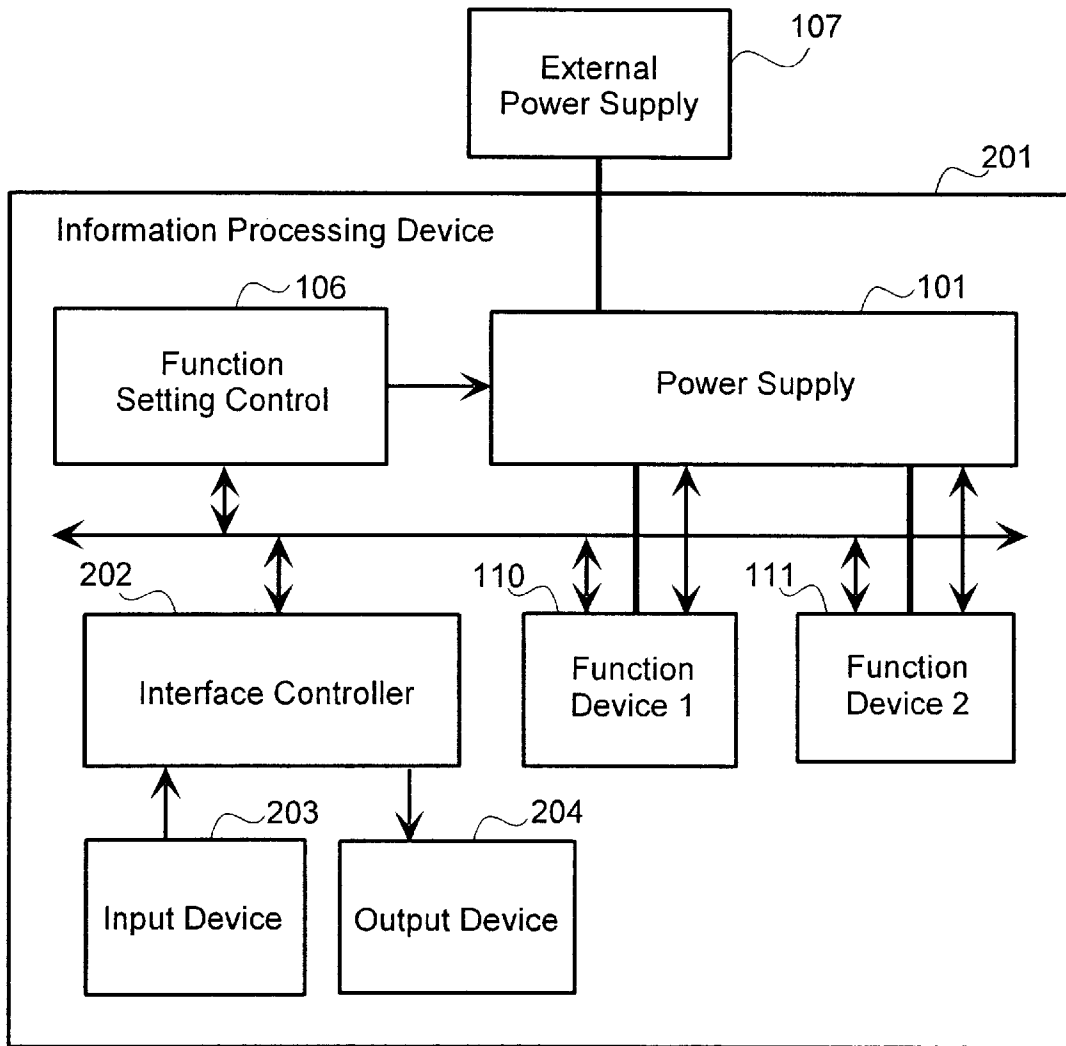
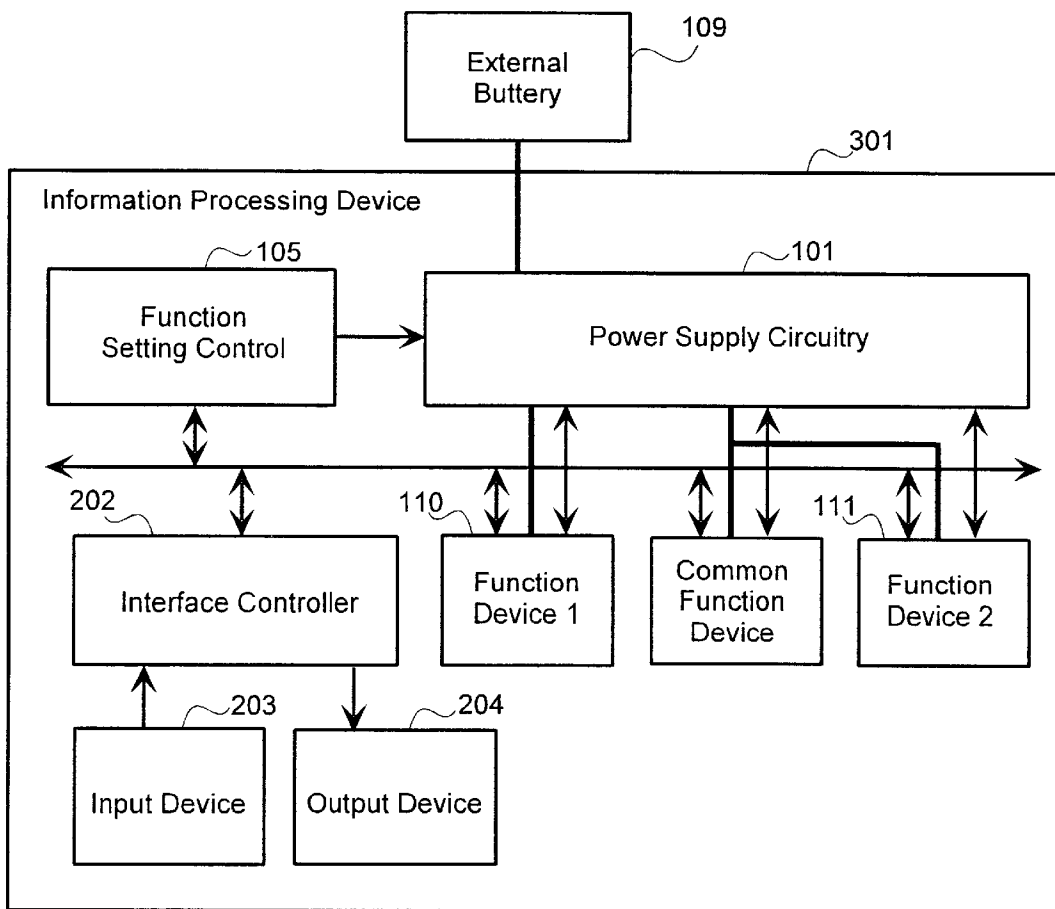


FIG.3



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