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EXHIBIT 8

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(12) United States Patent Oeda et al.

(54) INFORMATION PROCESSING DEVICE AND METHOD FOR CONTROLLING POWER CONSUMPTION THEREOF

- (75) Inventors: Shigeto Oeda, Fujisawa; Naoki Mori; Hiromichi Ito, both of Yokohama, all of (JP)
- (73) Assignee: Hitachi, Ltd., Tokyo (JP)
- (*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.
- (21) Appl. No.: 09/657,151
- (22) Filed: Sep. 7, 2000

(30) Foreign Application Priority Data

- May 22, 2000 (JP) 12-154358
- (51) Int. Cl.⁷ H02J 7/00
- - 320/139, 130; 324/426, 428

(56) **References Cited**

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Research File No. 5, ACPI, POS/V Magazine (1999), 7.15, p. 210.

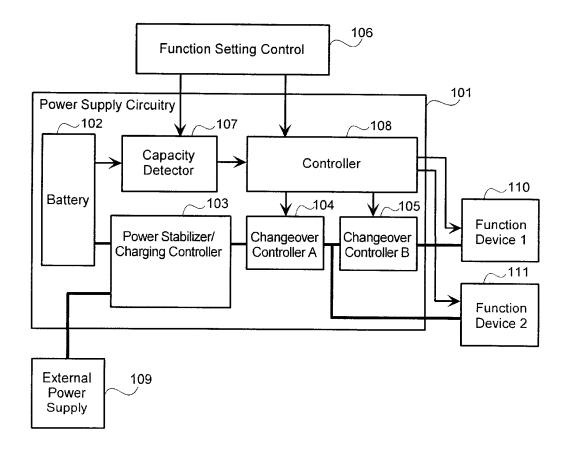
* cited by examiner

Primary Examiner—Peter S. Wong Assistant Examiner—Lawrence Luk (74) Attorney, Agent, or Firm—McDermott, Will & Emery

(57) ABSTRACT

A power supply section includes a capacity detector capable of detecting the remaining capacity in a battery and a controller issuing power consumption reduction instructions to independently operable function devices based on usage priorities thereof. This allows power consumption operations to be performed such as stopping function devices with low priorities first based on the remaining battery capacity. As a result, power to function devices with higher priorities can be maintained. Based on the remaining battery capacity, the controller controls the operations of the function devices and uses a display to show information allowing the operator to identify operable and inoperable function devices as the battery capacity drops.

14 Claims, 10 Drawing Sheets



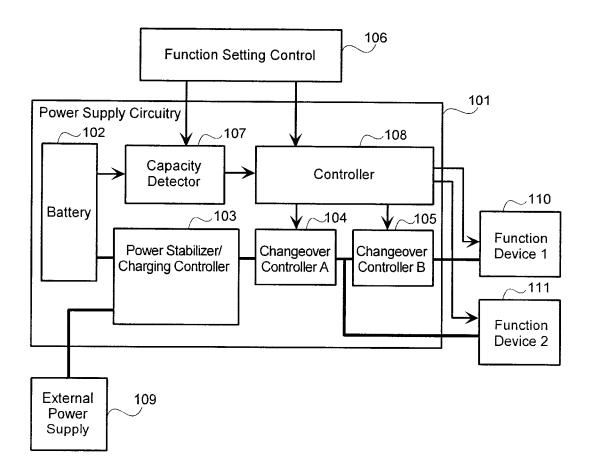
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(10) Patent No.: US 6,329,794 B1

(45) Date of Patent: Dec. 11, 2001

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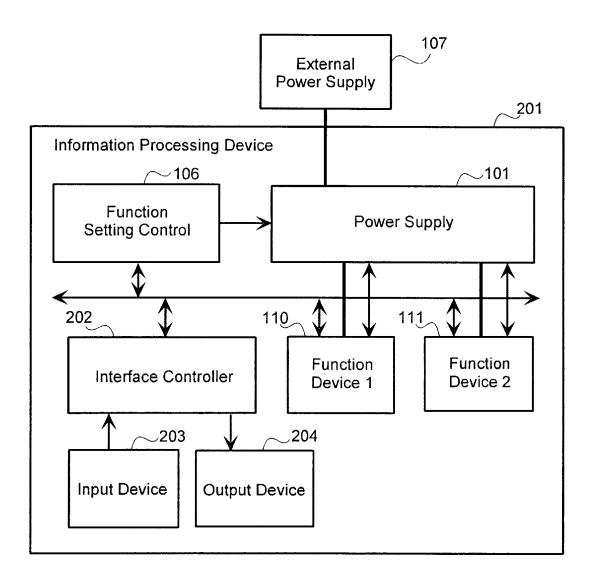
FIG.1



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FIG.2



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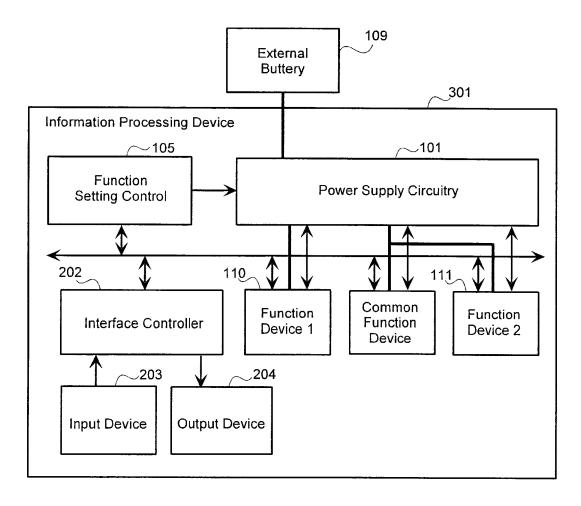


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FIG.3



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