

EXHIBIT E

| <i>Plaintiffs' #</i> | <i>Claim</i> | <i>Terms, Phrases and Clauses</i> | <i>Plaintiff's Proposal (Annotated)</i> | <i>Defendant's Proposal (Annotated)</i> |
|----------------------|----------------------|---|---|--|
| 59 | '578 patent claim 16 | “means for distributing an application launcher program associated with the application program to the client” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described in connection with block 52 of FIG. 2 and block 72 of FIG. 3 and at col. 3:61-67, 4:27-29, 5:4-7, 5:13-15, 5:27-29, 7:40-7:50, 7:66-8: 14, 8:21-47, 9:53-65, 10:50-53, 11:32-60, 12:22-36 ; in the '466 patent, col. 4:44-53, 8:12-16, 9:2-5, 9:42-57, 12:42-52, 14:9-13, 14:27-46, 15:4-9, 15:56-16:29 | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described in connection with block 52 of FIG. 2 and block 72 of FIG. 3 and at col. 3:61-67, 4:27-29, 5:4-7, 5:13-15, 5:27-29, 7:40-7:50, 7:66-8:14, 8:21-47, 9:53-65, 10:50-54, 11:32-60, 12:22-36 ; in the '466 patent, col. 4:44-53, 8:12-16, 9:2-5, 9:42-57, 12:42-52, 14:9-13, 14:27-46, 15:4-9, 15:56-16:29 |
| 92 | '578 patent claim 16 | “means for providing to the server a user set of the plurality of configurable preferences from one of the plurality of authorized users executing the application launcher program” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with blocks 98 and 100 of FIG. 4 and at col. 8:1-14, 8:55-60, 9:5-17, 10:12-15, 11:9-22, 11:32-54, 12:22-36; and '466 patent at col. 9:67-10:2 | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with blocks 98 and 100 of FIG. 4 and at col. 8:1-14, 8:55-60, 9:5-17, 10:12-15, 11:9-22, 11:32-54, 12:22-36; and '466 patent at col. 9:67-10:2 |

| <i>Plaintiffs' #</i> | <i>Claim</i> | <i>Terms, Phrases and Clauses</i> | <i>Plaintiff's Proposal (Annotated)</i> | <i>Defendant's Proposal (Annotated)</i> |
|----------------------|--------------------------|--|--|--|
| 94 | '578 patent claim 16 | “means for receiving an application launcher program associated with the application program from the server” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with block 90 of FIG. 4 and at col. 3:61-67, 4:27-29, 5:4-7, 5:13-15, 5:27-29, 7:66-8:14, 9:53-65, 10:50-54, 11:32-12:12, 12:22-36; in the '466 patent, col. 4:44-53, 8:12-16, 9:2-5, 9:42-57, 12:42-52, 14:9-13, 14:27-46, 15:4-9, 15:56-16:29 | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with block 90 of FIG. 4 and at col. 3:61-67, 4:27-29, 5:4-7, 5:13-15, 5:27-29, 7:66-8:14, 9:53-65, 10:50-54, 11:32-12:12, 12:22-36; in the '466 patent, col. 4:44-53, 8:12-16, 9:2-5, 9:42-57, 12:42-52, 14:9-13, 14:27-46, 15:4-9, 15:56-16:29 |
| 99 | '578 patent claim 16 | “means for requesting that the server initiate execution of the application program using the obtained user set and the administrator set of the plurality of configurable preferences responsive to a request from the one of the plurality of authorized users” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described at 8:1-20, 10:47-11:8, 11:32-54, 11:60-12: 36; and '466 patent at col. 9:30-57, 12:43-52, 15:56-16:12 | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described at 8:1-20, 10:47-11:8, 11:32-54, 11:60-12: 36; and '466 patent at col. 9:30-57, 12:43-52, 15:56-16:12 |
| 69 | '578 patent claim 16, 17 | “means for installing an application program having a plurality of configurable preferences and a plurality of authorized users on the server/a server coupled to the network” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described in connection with block 50 of FIG. 2 and blocks 70-72 of FIG. 3 and at col. 2:6-10; 3:50-55; 4:24-26; 5:11-13; 7:8-9, 7:50-65, 8:22-34, 9:57-63, | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described in connection with block 50 of FIG. 2 and blocks 70-72 of FIG. 3 and at col. 2:6-10; 3:50-55; 4:24-26; 5:11-13; 7:8-9, 7:50-65, 8:22-34, 9:57-63, |

| <i>Plaintiffs' #</i> | <i>Claim</i> | <i>Terms, Phrases and Clauses</i> | <i>Plaintiff's Proposal (Annotated)</i> | <i>Defendants'</i> |
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| | | | 12:13-21 ; in the '466 patent, 4:9-27, 5:29-44, 7:22-24, 9:42-43, 12:1-24, 14:51-53, 17:17-20:59 | 65, 9:57-6 '466 patent 5:40-44, 7 14:51-53, 18:1-14, 1 |
| 58 | '578 patent claim 17 | “means for distributing an application launcher program associated with the application program to a client coupled to the network” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithm described in connection with block 52 of FIG. 2 and blocks 72 & 74 of FIG. 3 and at col. 3:61-67, 4:27-29, 5:4-7, 5:13-15, 5:27-29, 7:40-7:50, 7:66-8: 14, 8:21-47, 9:53-65, 10:50-53, 11:32-60, 12:22-36, ; in the '466 patent, col. 4:44-53, 8:12-16, 9:2-5, 9:42-57, 12:42-52, 14:9-13, 14:27-46, 15:4-9, 15:56-16:29 | a processor program in described implement described connection FIG. 2 and FIG. 3 and 29, 5:4-7, 7:66-8:14, 10:50-54, in the '466 9:42-57, 1 15:56-16:2 |

| <i>Plaintiffs' #</i> | <i>Claim</i> | <i>Terms, Phrases and Clauses</i> | <i>Plaintiff's Proposal (Annotated)</i> | <i>Defendant's Proposal</i> |
|----------------------|----------------------|--|---|---|
| 79 | '578 patent claim 18 | “means for obtaining an administrator set of the plurality of configurable preferences from an administrator executing the configuration manager program” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with block 58 of FIG. 2 and block 76 of FIG. 3 and at col. 8:1-14, 8:61-9:25, 9:41-43, 9:66-10:1; '466 patent, col. 4:59-65, 8:51-53, 8:64-9:5, 9:10-29, 10:28-11:26, 16:38-39, and '826 patent, col. 7:54-64, 8:6-23, 8:48-63, and 15:37-21:63, | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with block 58 of FIG. 2 and block 76 of FIG. 3 and at col. 8:1-14, 8:61-9:25, 9:41-43, 9:66-10:1; '466 patent, col. 4:59-65, 8:51-53, 8:64-9:5, 9:10-29, 10:28-11:26, 16:38-39, and '826 patent, col. 7:54-64, 8:6-23, 8:48-63, and 15:37-21:63, |
| 82 | '578 patent claim 18 | “means for of distributing a configuration manager program associated with the application program to a second client coupled to the network” | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with block 58 of FIG. 2 and blocks 76 of FIG. 3 and at col. 7:66-8:11, 8:48-54, 9:11-9:25 | a processor executing computer program instructions, as described in col. 7:26-50, implementing the algorithms described in connection with block 58 of FIG. 2 and blocks 76 of FIG. 3 and at col. 7:66-8:11, 8:48-54, 9:11-9:25 |

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