APPENDIX B

Claim Construction Chart

No.	Term(s)	PUMA's Construction	ZTE's Construction	Court's Construction
1.	"bus" 789: claims 1, 13, 15, 28 459: claims 1-2, 7, 11, 13 194: claims 1-2, 9, 11, 16-18, 23 368: claims 1, 5, 7, 13, 19-20, 23 045: claims 1, 4-5, 12, 15 753: claims 1, 7 315: claims 1 164: claims 1, 6, 7	No construction necessary. Alternative construction: "a signal line or a set of associated signal lines to which a number of devices are coupled and over which information may be transferred between them"	"a signal line or set of associated signal lines to which a number of devices are connected and over which information may be transferred by only one device at a time"	
2.	"memory bus" 164: claims 1, 6, 7	No construction necessary. Alternative construction: "a signal line or a set of associated signal lines to which a number of devices, including a memory, are coupled and over which information may be transferred"	"a bus that connects directly with a memory"	
3.	"in real time" 789: claims 1, 13, 15, 28 315: claim 1 164: claims 1, 6	"fast enough to keep up with an input data stream"	*Indefinite.*	

No.	Term(s)	PUMA's Construction	ZTE's Construction	Court's Construction
4.	"fast bus"	"bus with a bandwidth equal to or greater than the required bandwidth to operate in real time"	"bus with a bandwidth greater than the bandwidth required for the decoder to operate in real time"	
5.	"arbiter" "arbitration circuit" "memory arbiter" "arbiter circuit" 789: claims 1, 19 459: claims 1-3, 7, 9, 11, 13 194: claims 1-3, 7, 9, 11, 16-18, 22-23 368: claims 1, 5, 7, 13, 17, 19-20, 23 045: claims 1, 4-5, 9, 12, 15 753: claims 1, 4, 7-10, 12 164: claims 1, 8, 12	"circuitry that uses a priority scheme to determine which requesting device will gain access"	"circuitry that uses a priority scheme to determine which requesting device will gain direct access"	
6.	"control circuit" 464: claims 1, 2, 7-13, 16-24, 32	No construction necessary.	"an electronic control device that is separate from the CPU or processor and that interacts with the operating system"	
7.	a. "directly supplied"	a. "supplied without being stored in main memory for	a. "supplied without intervening components"	

No.	Term(s)	PUMA's Construction	ZTE's Construction	Court's Construction
	194: claim 15 368: claim 3 b. "directly supplies" 194: claim 2 368: claims 2, 14, 21 045: claims 2, 6, 13 753: claim 3	purposes of decoding subsequent images" b. "supplies without being stored in main memory for purposes of decoding subsequent images"	b. "supplies without intervening components"	
8.	"monolithically integrated into" and "integrated into" 789: claims 6, 21, 23 194: claim 19 368: claims 17, 23 045: claims 9, 15 753: claim 12 164: claim 12 "monolithically integrated with" and "integrated with" 459: claims 2, 3 194: claims 2, 3 164: claim 8	"formed on a single semiconductor chip with"	"formed within"	

No.	Term(s)	PUMA's Construction	ZTE's Construction	Court's Construction
10.	a. "coupled" 789: claims 1, 5, 15 464: claims 1, 8, 10 , 12, 13, 17, 19, 20, 21, 23, 33, 34, 35 368: claims 1, 7, 13, 19, 20 045: claims 1, 4, 5, 12 753: claims 1, 7 315: claims 1, 14, 15 164: claims 1, 8-9, 11 b. "coupleable" 045: claims 1, 4, 12 753: claim 7 315: claim 1 164: claim 1 c. "coupling" 789: claim 1 194: claims 1, 16-17	a. "directly or indirectly connected" b. "directly or indirectly connectable" c. "directly or indirectly connecting"	a. "attached, resulting in an arrangement that includes no more than one bus" b. "attached, resulting in an arrangement that includes no more than one bus" c. "attached, resulting in an arrangement that includes no more than one bus" Alternatively: *indefinite*.	
10.	"simultaneously accesses the bus" 789: claim 1	[**AGREED**]	[**AGREED**]	"accesses the bus at the same time"
13.	"display device"	[**AGREED**]	[**AGREED**]	"screen and its circuitry"

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