

## Exhibit ZTE-CC-C

# Samsung Case Preliminary Constructions

U.S. Patent Nos. 5,812,789; 6,058,459; 6,427,19; 7,321,368; 7,542,045; 7,777,753; 8,054,315; 8,681,164 & 5,960,464

Parthenon Unified Memory Architecture, LLC v. Samsung Elecs. Co., Ltd. et al., Case No. 2:14-cv-902-JRG-RSP

disputed term	preliminary construction
“bus”	“a signal line or a set of associated signal lines to which a number of devices are coupled and over which information may be transferred between them”
“memory bus”	“bus [as construed] that is coupled with a memory”
“real time”	“fast enough to keep up with an input data stream”
“fast bus”	[agreed?]
“arbiter” / “arbitration circuit” / “memory arbiter” / “arbiter circuit”	“circuitry that uses a priority scheme to determine which requesting device will gain access”
“control circuit”	plain and ordinary meaning, no further construction necessary
“directly supplied” / “directly supplies”	“supplie[d, s] without being stored in [main, system] memory for purposes of decoding subsequent images”
“monolithically integrated into” / “integrated into”	“formed within”
“[first, second, third] onboard memory” / “[first, second, third] onboard memories”	“[first, second, third] memor[y, ies] within the decoder”

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disputed term	preliminary construction
“contiguous” / “noncontiguous”	plain and ordinary meaning
“coupled” / “coupleable” / “coupling”	“directly or indirectly connected” “directly or indirectly connectable” “directly or indirectly connecting”