



US 20120152604A1

(19) **United States**

(12) **Patent Application Publication**

Ahn et al.

(10) **Pub. No.: US 2012/0152604 A1**

(43) **Pub. Date: Jun. 21, 2012**

(54) **MOUNTING STRUCTURE OF CIRCUIT BOARD HAVING THEREON MULTI-LAYERED CERAMIC CAPACITOR, METHOD THEREOF, LAND PATTERN OF CIRCUIT BOARD FOR THE SAME, PACKING UNIT FOR MULTI-LAYERED CERAMIC CAPACITOR TAPED HORIZONTALLY AND ALIGNING METHOD THEREOF**

(30) **Foreign Application Priority Data**

Dec. 21, 2010 (KR) 10-2010-0131716

Publication Classification

(51) **Int. Cl.**
H05K 1/18 (2006.01)
H05K 3/00 (2006.01)
H01G 4/30 (2006.01)

(52) **U.S. Cl.** 174/260; 361/301.4; 29/829

(57) **ABSTRACT**

The present invention provides a method of mounting a circuit board having thereon a multi-layered ceramic capacitor and a land pattern of a circuit board for the same. The method of mounting a circuit board having thereon a multi-layered ceramic capacitor on which a plurality of dielectric sheet having internal electrodes formed thereon are stacked and the external terminal electrodes connected to the internal electrodes in parallel are formed on both ends thereof includes conductively connecting lands of a circuit board to the external terminal electrodes in such a way that internal electrode layers of the multi-layered ceramic capacitor and the circuit board are arranged in a horizontal direction, wherein a height T_s of conductive material to conductively connect the external terminal electrodes to the lands is less than $\frac{1}{3}$ of a thickness T_{MLCC} of the multi-layered ceramic capacitor.

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(21) Appl. No.: **13/331,619**

(22) Filed: **Dec. 20, 2011**

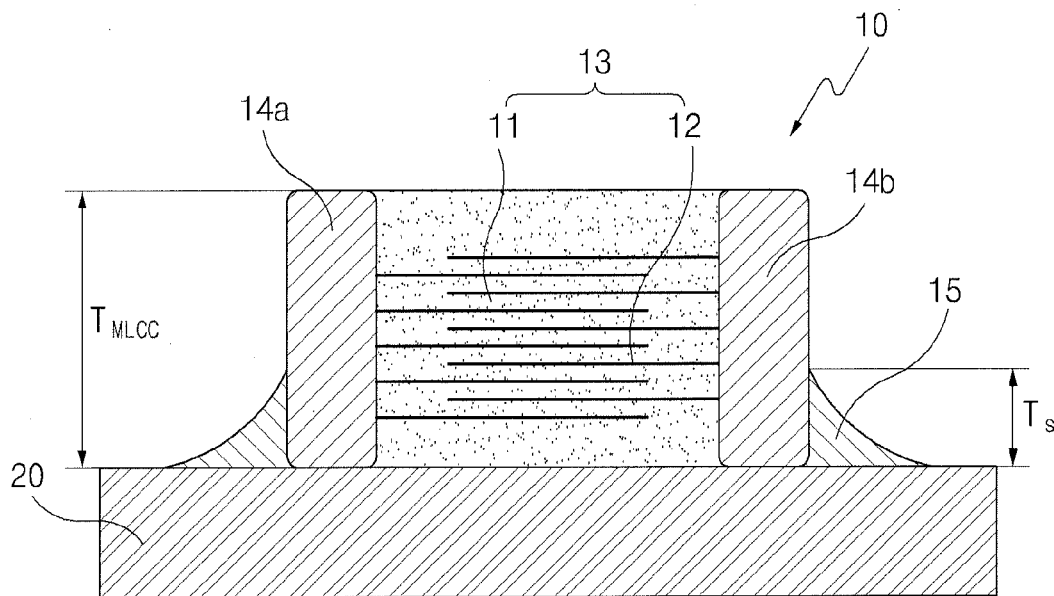
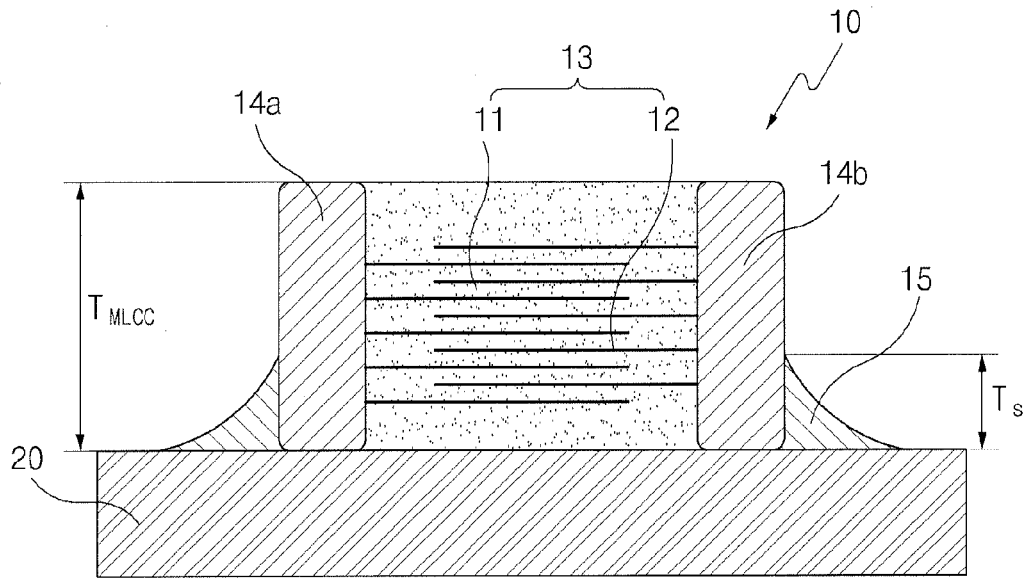
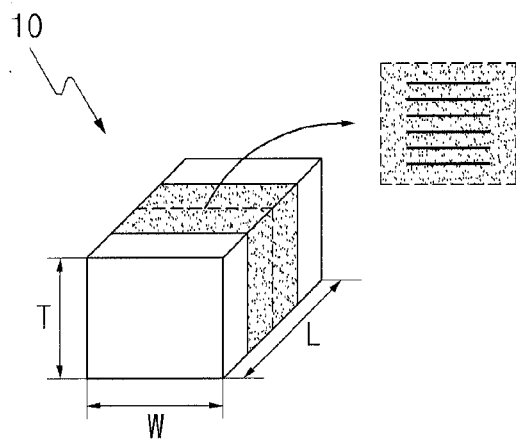


Exhibit 1007

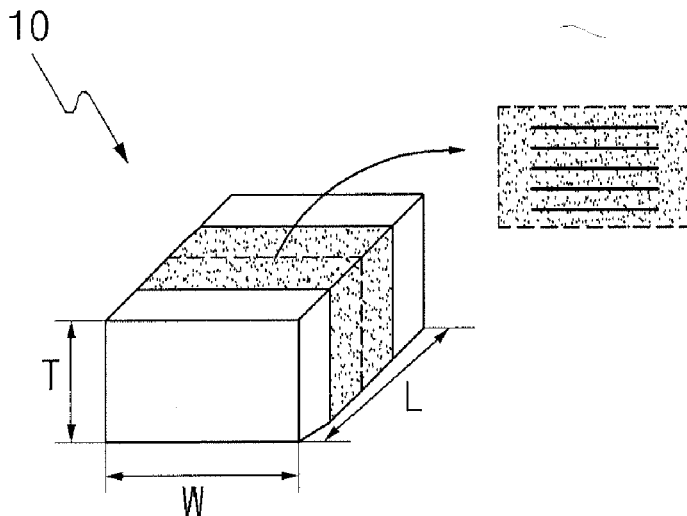
[FIG. 1]



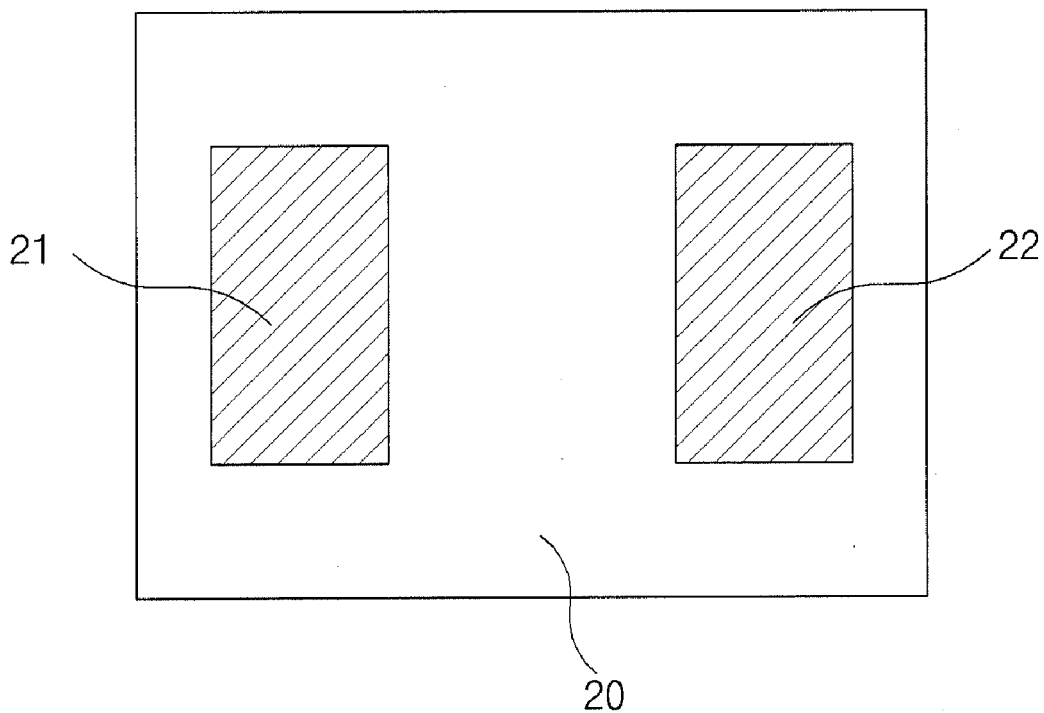
[FIG. 2A]



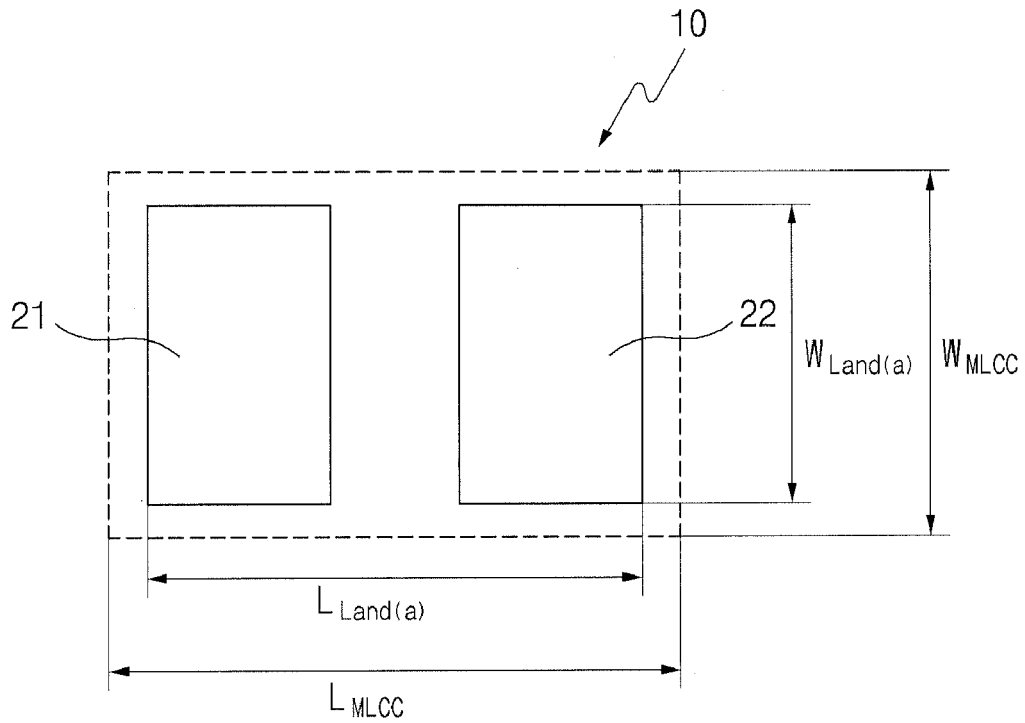
[FIG. 2B]



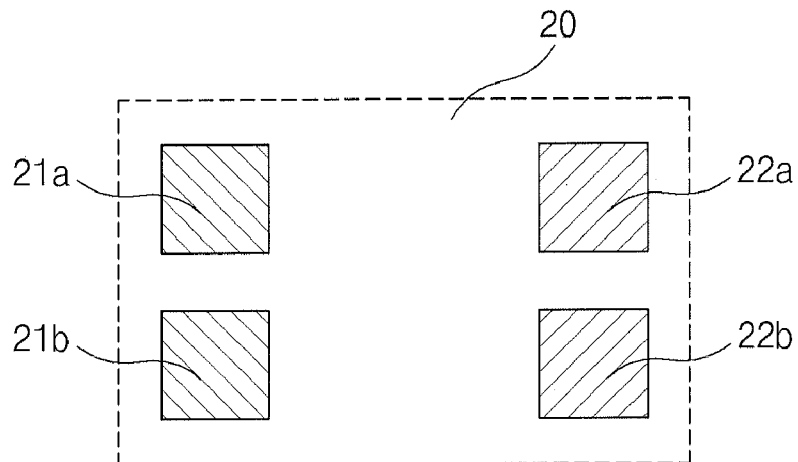
[FIG. 3]



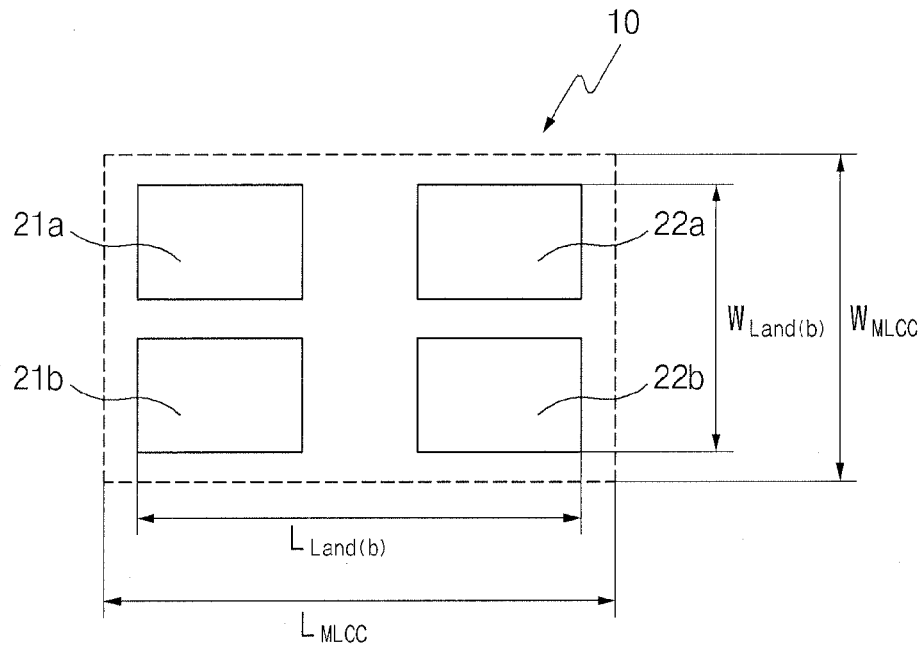
[FIG. 4]



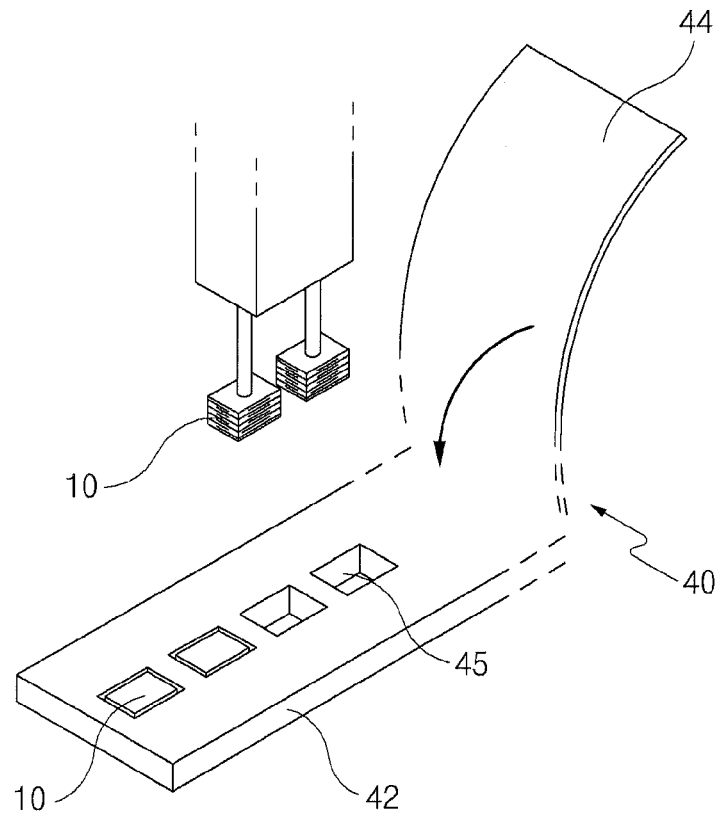
[FIG. 5]



[FIG. 6]



[FIG. 7]



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