

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

TCL INDUSTRIES HOLDINGS CO., LTD.,

Petitioners

v.

ATI TECHNOLOGIES ULC,

Patent Owner.

PTAB Case No. IPR2024-00366

Patent No. 8,760,454 B2

**PETITION FOR *INTER PARTES* REVIEW
OF U.S. PATENT NO. 8,760,454**

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PETITIONER'S EXHIBIT LIST

Exhibit #	Reference Name
1001	U.S. Patent 8,760,454 to Morein et al.
1002	Prosecution History of U.S. Patent 8,760,454
1003	Declaration of Hanspeter Pfister, Ph.D.
1004	Curriculum Vitae of Hanspeter Pfister, Ph.D.
1005	U.S. Patent 7,038,685 to Lindholm et al. (“Lindholm ’685”)
1006	U.S. Patent No. 7,015,913 to Lindholm et al. (“Lindholm ’913”)
1007	John Amanatides and Edward Szurkowski, <i>A Simple, Flexible, Parallel Graphics Architecture</i> , In Proceedings of Graphics Interface at 155-160 (Canadian Information Processing Society 1993) published in Proc. Graphics Interface ’93 in May 1993 (“Amanatides”)
1008	Les Kohn and Neal Margulis, <i>Introducing the Intel i860 64-bit Microprocessor</i> , IEEE, Volume 9, Issue 4, pages 15-30, August 1989 (“Kohn”)
1009	Harald Selzer, <i>Dynamic Load Balancing within a High Performance Graphics System</i> , In Proceedings of Rendering, Visualization and Rasterization Hardware (Eurographics' 91 Workshop) at 37-53 (Springer-Verlag 1993) published in 1993 (“Selzer”) [Library of Congress]
1010	Stuart Fiske and William J. Dally, <i>Thread prioritization: A Thread Scheduling Mechanism for Multiple-Context Parallel Processors</i> , In Proceedings of First Symposium on High-Performance Computer Architecture, 1995 at 210-221 (IEEE 1995) published in 1995 (“Fiske”)
1011	IEEE Xplore bibliography page for Fiske, <i>Thread prioritization: A Thread Scheduling Mechanism for Multiple-Context Parallel Processors</i> , visited on May 11, 2023
1012	Declaration of Gordon MacPherson re Authentication of Fiske (May 11, 2023)

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