(19) **United States**
(12) **Patent Application Publication** (10) Pub. No.: **US 2007/0083785 A1**
Sutardja (43) **Pub. Date:** **Apr. 12, 2007**

(54) **SYSTEM WITH HIGH POWER AND LOW POWER PROCESSORS AND THREAD TRANSFER**

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(21) Appl. No.: **11/523,996**

(22) Filed: **Sep. 20, 2006**

Related U.S. Application Data

(63) Continuation-in-part of application No. 11/503,016, filed on Aug. 11, 2006, and which is a continuation-in-part of application No. 10/865,368, filed on Jun. 10, 2004, and which is a continuation-in-part of application No. 11/322,447, filed on Dec. 29, 2005.

(60) Provisional application No. 60/825,368, filed on Sep. 12, 2006. Provisional application No. 60/823,453, filed on Aug. 24, 2006. Provisional application No.

60/822,015, filed on Aug. 10, 2006. Provisional application No. 60/820,867, filed on Jul. 31, 2006. Provisional application No. 60/799,151, filed on May 10, 2006. Provisional application No. 60/678,249, filed on May 5, 2005.

Publication Classification

(51) **Int. Cl.**
G06F 1/00 (2006.01)
(52) **U.S. Cl.** **713/323**

(57) **ABSTRACT**

A system on chip (SOC) includes first and second processors and a control module. The first processor implemented by the SOC has active and inactive states and processes first and second sets of threads during the active state. The second processor implemented by the SOC has active and inactive states, wherein the second processor consumes less power when operating in the active state than the first processor operating in the active state. The control module, implemented by the SOC communicates with the first and second processors, selectively transfers the second set of threads from the first processor to the second processor and selects the inactive state of the first processor. The second processor processes the second set of threads.
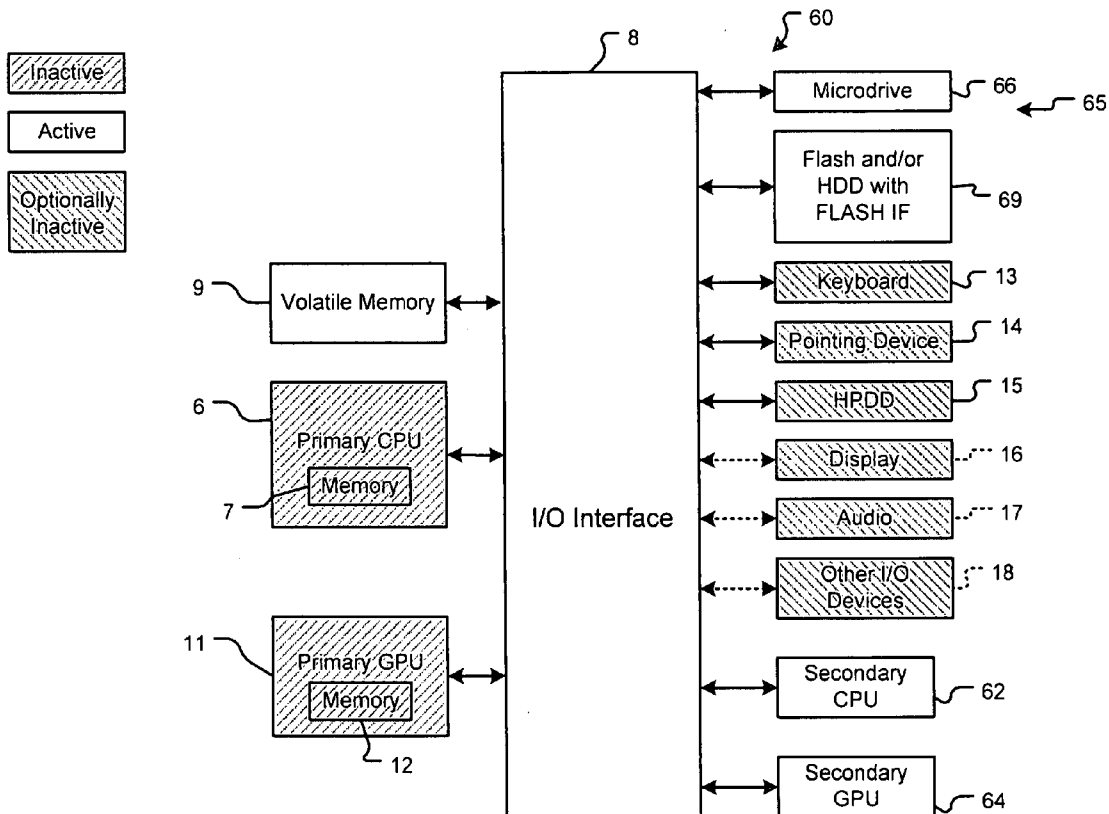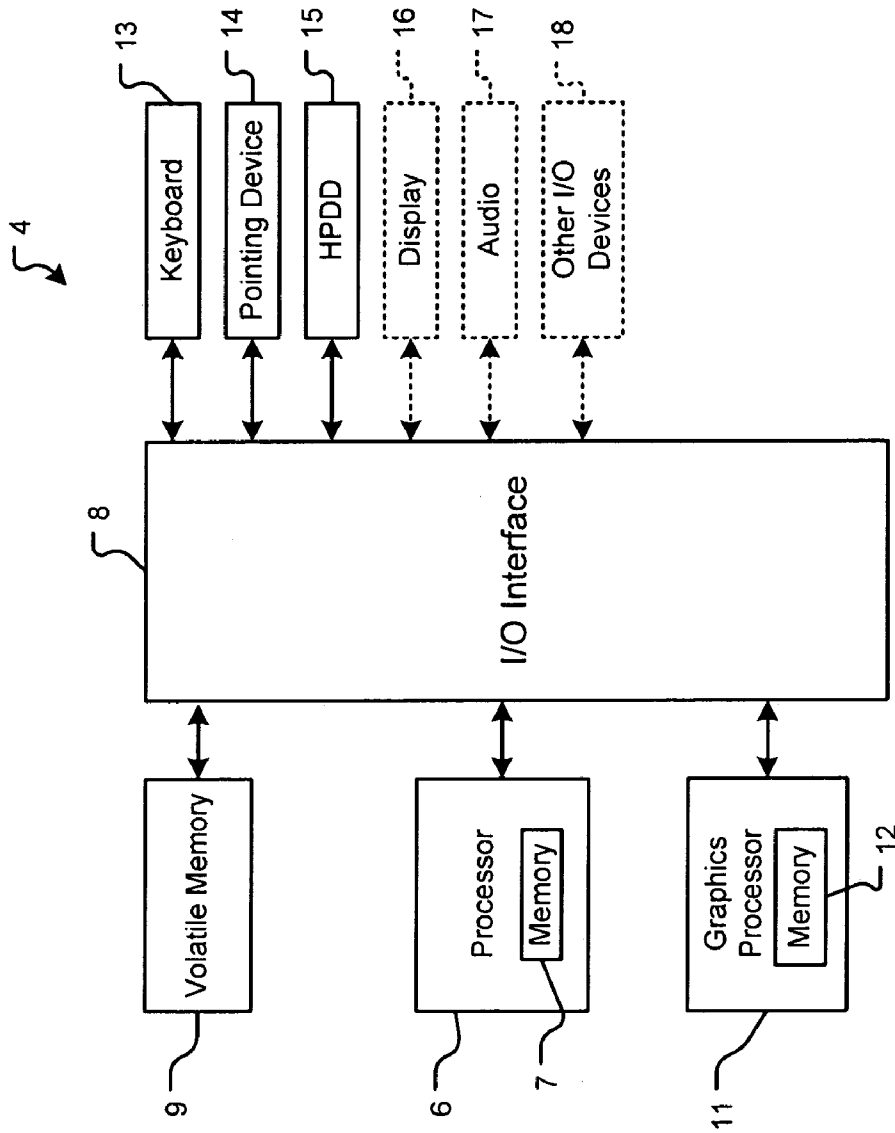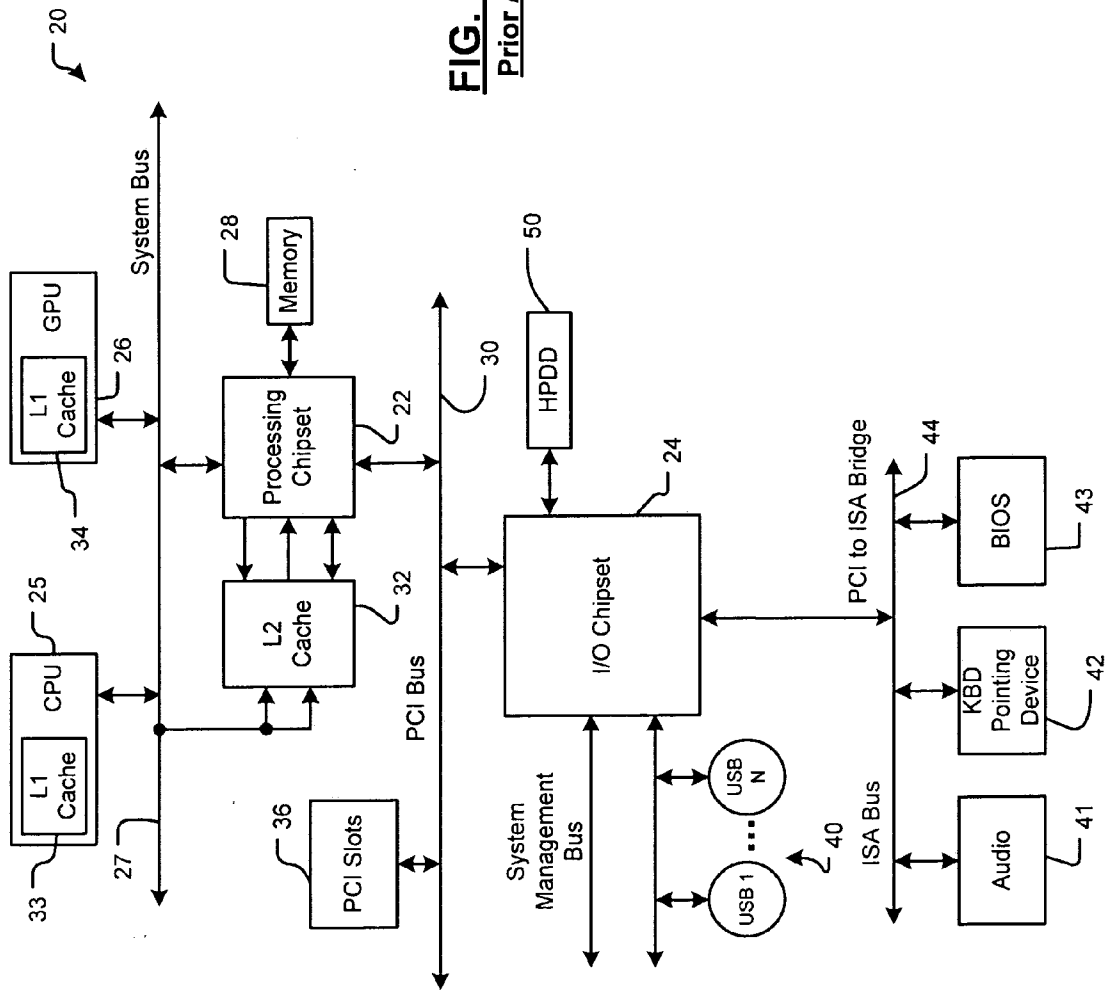
FIG. 1A
Prior Art

FIG. 1B
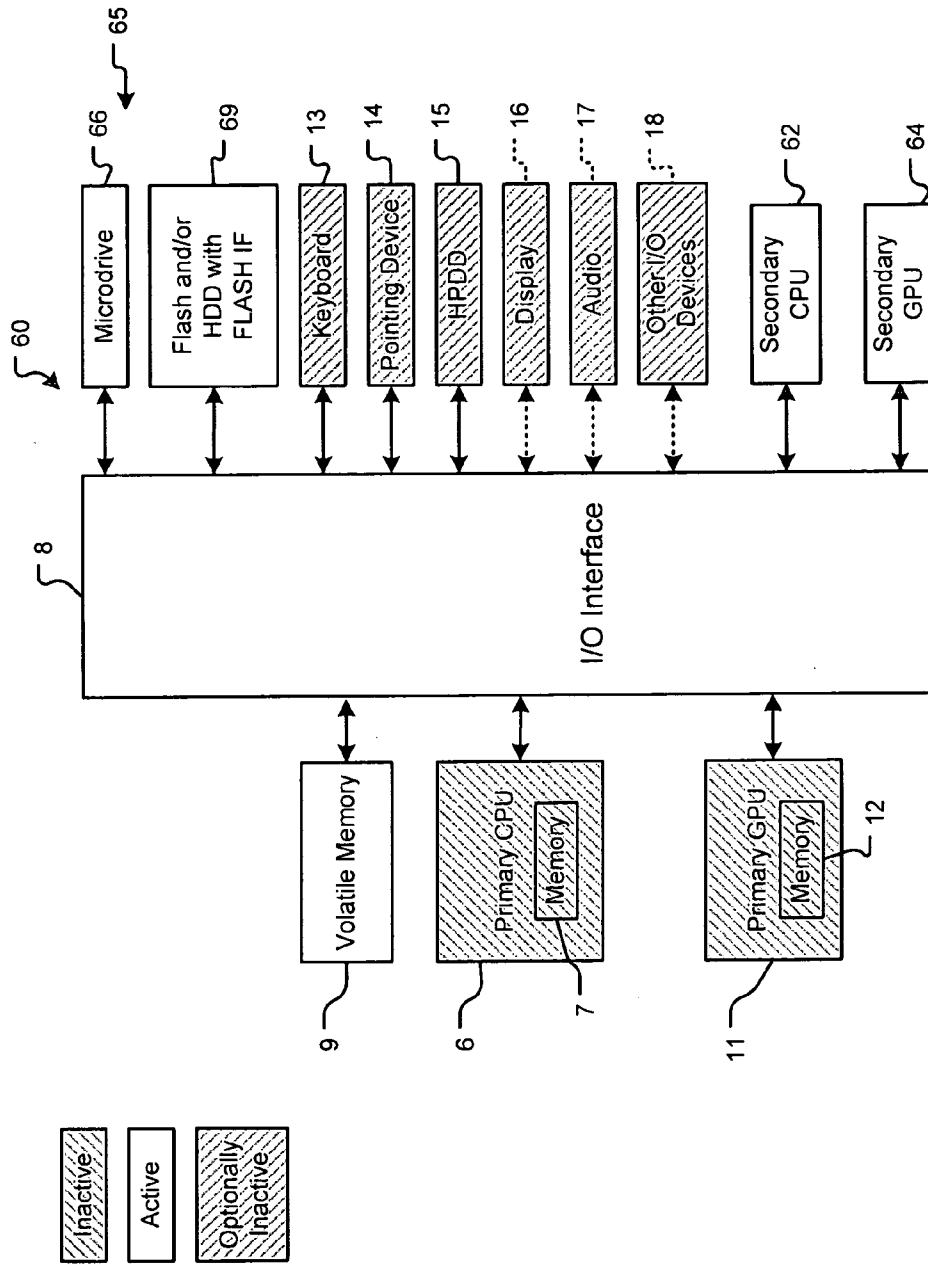_Prior Art_

FIG. 2A

FIG. 2B

DOCKET ALARM
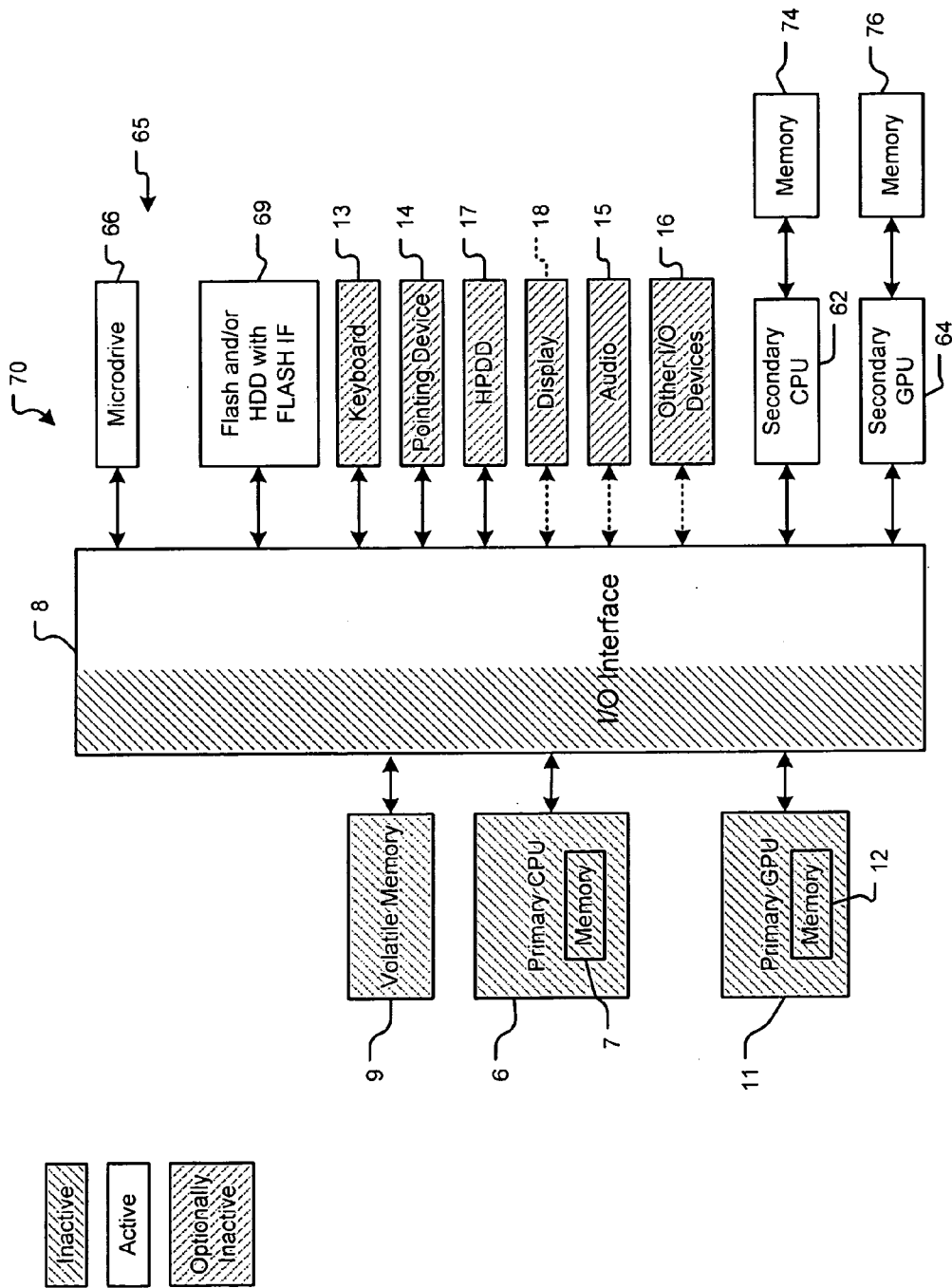
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