```
//depot/r400/devel/parts_lib/src/gfx/pa/pa.v
... #148 change 91060 edit on 2003/03/19 by dclifton@dclifton_r400 (ktext)
```

Reverted width of IO_PA_scan and PA_IO_scan back to 15 bits.

```
... #147 change 91033 edit on 2003/03/19 by dclifton@dclifton_r400 (ktext)
```

Changed number of IO_PA_scan and PA_IO_scan pins to 14 each.

```
... #146 change 90895 edit on 2003/03/19 by bhankins@fl_bhankins_r400_win (ktext)
```

add test signals

```
... #145 change 90862 edit on 2003/03/19 by bhankins@fl_bhankins_r400_win (ktext)
```

add common_dft test ports

```
... #144 change 85882 edit on 2003/02/21 by bhankins@fl_bhankins_r400_win (ktext)
```

add block_busy_extender module to pa.v

```
... #143 change 76202 edit on 2003/01/13 by scamlin@scamlin_crayola_win (ktext)
```

update memory order of patch chain

```
... #142 change 72728 edit on 2002/12/20 by bhankins@fl_bhankins_r400_win (ktext)
```

1. use sclk_ag to clock clipper registers that get reset when writes are done to
pa_cl_enhance register to ensure register is clocked.

2. add some test logic for soft reset (for test only; commented out).

3. removed reset from pa_cl_enhance register

```
... #141 change 72494 edit on 2002/12/19 by dclifton@dclifton_r400 (ktext)
```

Fixed makefile to compile cot gate netlist.
Added flush for writes to cl_enhance reg.
Added daisy chain reads.
Put state variable poiinter logic in pa_ag on hard reset.
Put perfmon on hard reset.

```
... #140 change 71769 edit on 2002/12/17 by bhankins@fl_bhankins_r400_win (ktext)
```

1. update some non state-based perf signals to more closely match csim.

2. remove some commented lines.

... #139 change 69259 edit on 2002/12/06 by dclifton@dclifton_r400 (ktext)

Eliminated ground nets in setup_debug buses.

... #138 change 69136 edit on 2002/12/06 by dclifton@dclifton_r400 (ktext)

attached registered reset to pa_perfmon

... #137 change 69079 edit on 2002/12/06 by bhankins@fl_bhankins_r400_win (ktext)

uncomment u0/1_pa_sx_wake_up_call signals

... #136 change 68717 edit on 2002/12/05 by bhankins@fl_bhankins_r400_win (ktext)

add logic for pa to sx wake up call.

commented out in pa.v

... #135 change 68177 edit on 2002/12/03 by scamlin@scamlin_crayola_unix_orl (ktext)

updated memory sizes

... #134 change 68127 edit on 2002/12/03 by bhankins@fl_bhankins_r400_win (ktext)

1. add vertex reordering bits from cliper to su

2. add clipped_prim_seq_stall logic and bit to clip enhance reg

3. add some non-state perf counter signals

... #133 change 67858 edit on 2002/12/02 by dclifton@dclifton_r400 (ktext)

added clip_su_reorder_vtx input and expanded fifo to hold it. Changed provoking vertex logic in pa_su_geom_prep block

... #132 change 67786 edit on 2002/12/02 by dclifton@dclifton_r400 (ktext)

Added temp fuse box for rams.

... #131 change 66979 edit on 2002/11/26 by viviana@viviana_crayola_unix_orl (ktext)

Added the delay block and IO connections.

... #130 change 66777 edit on 2002/11/25 by dclifton@dclifton_r400 (ktext)

Reducing clip_su_pt_size to 25 bits.

... #129 change 66692 edit on 2002/11/25 by bhankins@fl_bhankins_r400_win (ktext)

Reduced the width of the clipper's pointsize memory to 25 bits, as well as the
pointsize datapath within the clipper.

... #128 change 66598 edit on 2002/11/25 by bhankins@fl_bhankins_r400_win (ktext)

add a clipper debug bus

... #127 change 66573 edit on 2002/11/25 by bhankins@fl_bhankins_r400_win (ktext)

rearrange clipper debug bus

... #126 change 66269 edit on 2002/11/22 by dclifton@dclifton_r400 (ktext)

Connected up additional debug buses. Fixed debug_data mux select in pa_rbiu.

... #125 change 66199 edit on 2002/11/22 by bhankins@fl_bhankins_r400_win (ktext)

change state reg in pa_sxifccg so as not to support readback

... #124 change 65917 edit on 2002/11/21 by viviana@viviana_crayola_unix_orl (ktext)

Connected PA_rf_recen to STAR_testbus_rf[1].

... #123 change 65549 edit on 2002/11/20 by bhankins@fl_bhankins_r400_win (ktext)

added clipper-oriented performance monitoring signals

... #122 change 65541 edit on 2002/11/20 by viviana@viviana_crayola_unix_orl (ktext)

Changed the reset going to the memories to hard reset.

... #121 change 65488 edit on 2002/11/20 by bhankins@fl_bhankins_r400_win (ktext)

increase pa_clipper perf counter multicount values to 5 bits.

... #120 change 65272 edit on 2002/11/19 by bhankins@fl_bhankins_r400_win (ktext)

add signals to debug busses

... #119 change 65233 edit on 2002/11/19 by viviana@viviana_crayola_unix_orl (ktext)

Added a port connection to the pa_rf_stp.v that was missing inside the pa.v

... #118 change 65200 edit on 2002/11/19 by viviana@viviana_crayola_unix_orl (ktext)

Added the pa_rf_stp (Virage processor), the Test Controller and
Scan IO and new Virage memories.

... #117 change 64457 edit on 2002/11/15 by bhankins@fl_bhankins_r400_win (ktext)

add more and fix some existing non-state based performance monitoring signals to
pa_clipper.v

... #116 change 64397 edit on 2002/11/15 by bhankins@fl_bhankins_r400_win (ktext)

1. add non-state based performance monitoring signals to pa_cl_clipsm.

2. fix some leda warnings in pa_cl_vert_store1.v

... #115 change 64364 edit on 2002/11/15 by bhankins@fl_bhankins_r400_win (ktext)

add more to non-state based performance monitoring, including counter enable.

... #114 change 64055 edit on 2002/11/14 by bhankins@fl_bhankins_r400_win (ktext)

1. start to add clprim non-state based performance monitoring signals. not complete.

2. change assignment to non-blocking in register proc in pa_cl_vert_store3.v

... #113 change 64031 edit on 2002/11/14 by bhankins@fl_bhankins_r400_win (ktext)

add non-state based performance monitoring signals to pa_sxifccg

... #112 change 63921 edit on 2002/11/14 by bhankins@fl_bhankins_r400_win (ktext)

misc performance monitor bug fixes

... #111 change 63672 edit on 2002/11/13 by bhankins@fl_bhankins_r400_win (ktext)

add more performance monitoring

... #110 change 63528 edit on 2002/11/12 by dclifton@dclifton_r400 (ktext)

Added performance counters

... #109 change 63365 edit on 2002/11/12 by bhankins@fl_bhankins_r400_win (ktext)

add pa_clipper and pa_sxifsm performance counter signals, and bring up to pa.v level
for now.

... #108 change 63330 edit on 2002/11/12 by bhankins@fl_bhankins_r400_win (ktext)

add debug bus to pa_sxifccg (clipper_debug3)

... #107 change 62865 edit on 2002/11/08 by viviana@viviana_crayola_unix_orl (ktext)

Changed the STAR test buses for the pa_ag to get rid of the extra
instantiations for the memories that should have been bit write memories.

... #106 change 62724 edit on 2002/11/08 by bhankins@fl_bhankins_r400_win (ktext)

removed debug bus signal for synthesis (for now)

... #105 change 62708 edit on 2002/11/08 by bhankins@fl_bhankins_r400_win (ktext)

removed debug bus from sxifccg for now for a quick synthesis

... #104 change 62684 edit on 2002/11/08 by bhankins@fl_bhankins_r400_win (ktext)

register ROM_spn_disable_vtx inputs

... #103 change 62673 edit on 2002/11/08 by bhankins@fl_bhankins_r400_win (ktext)

add debug bus for pa_sxifccg block

... #102 change 62352 edit on 2002/11/07 by bhankins@fl_bhankins_r400_win (ktext)

replace pa_cl_clip_to_outsm_fifo.v with an rtl version without latency.

... #101 change 62087 edit on 2002/11/06 by dclifton@dclifton_r400 (ktext)

Connected up debug busses, added cl_enhance reg to su, removed reference to SU_ENHANCE,
fixed typo in counters.

... #100 change 61915 edit on 2002/11/06 by viviana@viviana_crayola_unix_orl (ktext)

Added another set of STAR_patch signals for the pntsz memory for the clipper.

... #99 change 61844 edit on 2002/11/05 by dclifton@dclifton_r400 (ktext)

Added performance counter outputs to pa_su

... #98 change 61217 edit on 2002/11/02 by viviana@viviana_crayola_unix_orl (ktext)

Changed the instance names for the ati master clock instances and the rbbm interface.

... #97 change 61081 edit on 2002/11/01 by viviana@viviana_crayola_unix_orl (ktext)

Added the STAR test bus signals and wired up the hierarchy.

... #96 change 61080 edit on 2002/11/01 by dclifton@dclifton_r400 (ktext)

Fixed problem with init_point_prim.

DOCKET ALARM

Explore Litigation Insights

Docket Alarm provides insights to develop a more informed litigation strategy and the peace of mind of knowing you're on top of things.

Real-Time Litigation Alerts

Keep your litigation team up-to-date with **real-time alerts** and advanced team management tools built for the enterprise, all while greatly reducing PACER spend.

Our comprehensive service means we can handle Federal, State, and Administrative courts across the country.

Advanced Docket Research

With over 230 million records, Docket Alarm's cloud-native docket research platform finds what other services can't. Coverage includes Federal, State, plus PTAB, TTAB, ITC and NLRB decisions, all in one place.

Identify arguments that have been successful in the past with full text, pinpoint searching. Link to case law cited within any court document via Fastcase.

Analytics At Your Fingertips

Learn what happened the last time a particular judge, opposing counsel or company faced cases similar to yours.

Advanced out-of-the-box PTAB and TTAB analytics are always at your fingertips.

API

Docket Alarm offers a powerful API (application programming interface) to developers that want to integrate case filings into their apps.

LAW FIRMS
Build custom dashboards for your attorneys and clients with live data direct from the court.

Automate many repetitive legal tasks like conflict checks, document management, and marketing.

FINANCIAL INSTITUTIONS
Litigation and bankruptcy checks for companies and debtors.

E-DISCOVERY AND LEGAL VENDORS
Sync your system to PACER to automate legal marketing.