

Change 96254 on 2003/04/17 by bhankins@bhankins_crayola_linux_orl

fix syntax error affecting synthesis

Change 96193 on 2003/04/17 by bhankins@bhankins_crayola_linux_orl

edits to support the replacement of sx_rb_detailed_quad_data_fifo

Change 96189 on 2003/04/17 by viviana@viviana_crayola_unix_orl

A new memory was added to the sx and all the simulation verilog models were checked into the test directory.

Change 96187 on 2003/04/17 by bhankins@bhankins_crayola_linux_orl

initial checkin to replace register based fifo with ram based; clone of ati_fifo_top.v

Change 96186 on 2003/04/17 by viviana@viviana_crayola_unix_orl

A new memory was added to the sx (128x104).

Change 95498 on 2003/04/14 by scamlin@scamlin_crayola_unix_orl

typo

Change 95432 on 2003/04/14 by mmantor@mmantor_crayola_linux_orl

<removed duplicate declaration>

Change 94920 on 2003/04/10 by ygiang@ygiang_r400_pv2_marlboro

changed: naming of sx perf counters

Change 94917 on 2003/04/10 by bhankins@bhankins_crayola_win_orl

move sample time negedge clk

Change 94901 on 2003/04/10 by bhankins@bhankins_crayola_win_orl

initial add

Change 94867 on 2003/04/10 by bhankins@bhankins_crayola_win_orl

update to enable injector to generate back to back exports for more robust testing. currently disabled.

Change 94830 on 2003/04/09 by mmantor@mmantor_crayola_linux_orl

AMD1044 0254148

<SQ/SX/SP out of order thread completion and remove redundant storage in sp for sq/sx communications some sq cfs bug fixed and texture kill mask generation and other misc things>

Change 94648 on 2003/04/09 by bhankins@bhankins_crayola_win_orl

remove unused tracker

Change 94480 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

update to current test hierarchy

Change 94467 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

reduce random wait time

Change 94436 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

parameterize random wait times

Change 94399 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

1. fix bug in tbmod_fake_rb to properly read request size value from dumps.2. fix test bench to properly detect a good end of test condition.

Change 94394 on 2003/04/08 by bhankins@bhankins_crayola_win_orl

remove unused signal

Change 94239 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

add some randomization to parameter cache deallocs

Change 94222 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

fix to allow the issue of parameter cache deallocates regardless of the number of lines available.

Change 94187 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

1. accomodate the pc_channel_mask column that was added to sp_sx.dmp2. remove references to sq_sx_pc.dmp (former home of pc_channel_mask)

Change 94178 on 2003/04/07 by bhankins@bhankins_crayola_win_orl

misc changes

Change 93951 on 2003/04/04 by bhankins@bhankins_crayola_win_orl

AMD1044 0254149

fix to properly get to end of file

Change 93335 on 2003/04/02 by bhankins@bhankins_crayola_win_orl

misc fixes

Change 92622 on 2003/03/28 by bhankins@bhankins_crayola_win_orl

misc fixes and cleanup

Change 92615 on 2003/03/28 by bhankins@bhankins_crayola_win_orl

initial add. very simple. probably will need work.

Change 92610 on 2003/03/28 by grayc@grayc_crayola_linux_orl

new tracker

Change 92554 on 2003/03/28 by bhankins@bhankins_crayola_win_orl

misc fixes and updates. passes mini-regress tests.

Change 92303 on 2003/03/27 by mmantor@mmantor_crayola_linux_orl

<export_id expanded from one to four bit, end_of_clause added to CFS-TIF interface and last_in_clause flag passed down with instruction to eventually trigger a free_done. thread_id outputs added to the SQ_SP and SQ_TP interfaces for testbench, and added sq_sx control signals for exp_table read >

Change 92192 on 2003/03/26 by bhankins@bhankins_crayola_win_orl

obsolete

Change 92191 on 2003/03/26 by bhankins@bhankins_crayola_win_orl

ongoing work. some perf and sp tests pass.paramater cache reads pass, but tbmod_fake_int.v may need some work.

Change 91453 on 2003/03/21 by bhankins@bhankins_crayola_win_orl

snoop into the sx's to get an rtr to throttle requests from the fake pa so as not to blow out the request fifo in the sx.

Change 91267 on 2003/03/20 by bhankins@bhankins_crayola_win_orl

get in sync with gui.

AMD1044 0254150

Change 91266 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

ok for now, but still needs work in properly detecting which pixels to track.

Change 91238 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

seems to be working.

Change 91234 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

seems to be working.

Change 91233 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

update. showing signs of life.

Change 91232 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

do paramater cache reads

Change 91230 on 2003/03/20 by bhankins@bhankins_crayola_linux_orl

starting to show signs of working

Change 91010 on 2003/03/19 by bhankins@fl_bhankins_r400_win

work in progress. dont use.

Change 90997 on 2003/03/19 by bhankins@bhankins_crayola_linux_orl

temp version; work in progress. do not use.

Change 90875 on 2003/03/19 by bhankins@fl_bhankins_r400_win

mods

Change 90865 on 2003/03/19 by bhankins@fl_bhankins_r400_win

updates

Change 90594 on 2003/03/17 by grayc@grayc_crayola_linux_orl

changed number of pipe stages for valid to 8

Change 90460 on 2003/03/17 by bhankins@fl_bhankins_r400_win

fixed to ignore don't care transfers

AMD1044 0254151

Change 90253 on 2003/03/14 by bhankins@fl_bhankins_r400_win

revert back to earlier version until new version is tested

Change 90250 on 2003/03/14 by bhankins@fl_bhankins_r400_win

obsolete

Change 90249 on 2003/03/14 by bhankins@fl_bhankins_r400_win

initial checkin

Change 90247 on 2003/03/14 by bhankins@fl_bhankins_r400_win

mods

Change 89936 on 2003/03/13 by bhankins@fl_bhankins_r400_win

add comment

Change 89935 on 2003/03/13 by bhankins@fl_bhankins_r400_win

simplify.

Change 89863 on 2003/03/12 by mmantor@mmantor_crayola_linux_orl

<Fix bug in the sx export buffers that prevents data corruption. Single color exports should now work also added a test to the sq_regress file to check export buffer back pressure>

Change 89448 on 2003/03/10 by mmantor@mmantor_crayola_linux_orl

<1. Added timestamp to dum_mem read and write from same location error message. 2. Moved flat/gouroud shading and provoking vertex to sq-pc from the sx and worked on ptr instead of data 3.Added control for the texture cylindrical wrapsubcycling. 4. Add rt parameter cache ptr selection in sq 5.Clamped and wrapped pc_ptrs in sq 6. Added support for points and lines in the parameter cache ptr determination. 7. prep seperate write address for export to memory 8. tmp fix for deallocation of export memory deallocation. 9. remove some old comment out code and redundant logic >

Change 89352 on 2003/03/10 by bhankins@fl_bhankins_r400_win

edits

Change 89344 on 2003/03/10 by bhankins@fl_bhankins_r400_win

changes

AMD1044 0254152

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