

Change 96318 on 2003/04/17 by rramsey@rramsey\_crayola\_linux\_orl

change Openning to Opening

Change 96278 on 2003/04/17 by scamlin@scamlin\_crayola\_unix\_orl

.

Change 96252 on 2003/04/17 by rramsey@rramsey\_crayola\_linux\_orl

Add new signal between sq\_vtx\_ctl and sq\_input\_arb to mark gpr\_ld\_busy which fixes a deadlock condition where gpr\_ld state machine is waiting on an ack from the arb, which is waiting on vsr\_ld machine not busy, which is waiting on the gpr\_ld machine to finish.

Add a reset for sp\_sel in sq\_vtx\_ctl when the vsr\_ld machine goes from ld to idle. This is needed if there is valid data sitting in the vgt fifo since a vsr load will happen on the following clock.

Connect pred\_kill\_valid bits in tb\_sqsp.

Fix typo in visr\_wr tracker.

Change 95859 on 2003/04/16 by rramsey@rramsey\_crayola\_linux\_orl

fix width

Change 95847 on 2003/04/16 by rramsey@rramsey\_crayola\_linux\_orl

script to build the verdi kdb for tb\_sqsp

Change 95839 on 2003/04/16 by rramsey@rramsey\_crayola\_linux\_orl

Multiple changes throughout the SQ to get tb\_sqsp working, and to fix bugs uncovered by the new testbench

sq\_gpr\_alloc - wrap was broken for vertex side if ptr wrapped exactly at max

sq\_rbbm\_interface - context/new\_ld sent to constant mems was being pulled from wrong side of skid buffer

sq\_pix\_thread\_buff - change to send context\_done event back to CP on the pixel side

buildtb, tb\_sqsp, tbrk\_\* - changes for new sp/spi configuration and new dump file fields

Change 95810 on 2003/04/16 by dougd@dougd\_r400\_linux\_marlboro

fixed bug in the pix\_cntx counter increment signal where both event and pix were trying to increment during the same cycle

Change 95623 on 2003/04/15 by mzini@mzini\_crayola\_linux\_orl

New SQ trackers

Change 95621 on 2003/04/15 by mzini@mzini\_crayola\_linux\_orl

Using an unregistered data-ready signal now to trigger compare

Change 95490 on 2003/04/14 by vromaker@vromaker\_r400\_linux\_marlboro

fix for CFSM end-of-clause detection

Change 95457 on 2003/04/14 by danh@danh\_crayola\_linux\_orl

Initial Release.

Change 95404 on 2003/04/14 by rramsey@rramsey\_crayola\_linux\_orl

Temporary fix to let flush events flow through the SC if they are not preceded by a draw\_init.

Note flushes are still broken if you want to use them for syncing state changes, for example changing bad\_pipe.

Change 95391 on 2003/04/14 by rramsey@rramsey\_crayola\_linux\_orl

Change SQ\_DEBUSSY option to control only SQ dumping

Add SP\_DEBUSSY option to control dumping of SPs

Add some internal trackers

Fix width on tp\_sqsp\_thread\_id

Change 95174 on 2003/04/11 by hartogs@fl\_hartogs

Added hooked up new "thread\_type" signal from SQ module. Enhanced timeout detection.

Change 94999 on 2003/04/10 by ygiang@ygiang\_r400\_pv2\_marlboro

fixed: sq perf counter "sq\_vertex\_vectors\_sub"

Change 94873 on 2003/04/10 by askende@askende\_r400\_linux\_marlboro

releasing the following changes:

1. creation of the new SPI block
2. top level changes to support 8 SP instances
3. tracker changes to support a few IO name changes

Change 94834 on 2003/04/09 by mmantor@mmantor\_crayola\_linux\_orl

<fixed a problem in ptr\_buff where he hangs on an event that is not a pixel event>

Change 94830 on 2003/04/09 by mmantor@mmantor\_crayola\_linux\_orl

<SQ/SX/SP out of order thread completion and remove redundant storage in sp for sq/sx

communications some sq cfs bug fixed and texture kill mask generation and other misc things>

Change 94724 on 2003/04/09 by mzini@mzini\_crayola\_linux\_orl

New SQ trackers

Change 94718 on 2003/04/09 by viviana@viviana\_crayola\_linux\_orl

SQ trackers for the sequencer control flow instructions.

Change 94518 on 2003/04/08 by vromaker@vromaker\_r400\_linux\_marlboro

fix for bug caused by resource change between EXEC control flow instructions

Change 94505 on 2003/04/08 by rramsey@rramsey\_crayola\_linux\_orl

don't inc vtx thread counter if thread is an event

Change 94211 on 2003/04/07 by hartogs@fl\_hartogs

Template Debussy waveform file.

Change 93959 on 2003/04/04 by vromaker@vromaker\_r400\_linux\_marlboro

temporarily disabled PVPS src select swizzle because it was causing SP tests from Andi's mini\_regress to fail

Change 93788 on 2003/04/03 by dougd@dougd\_r400\_linux\_marlboro

added event\_context register to correctly capture context when sending a vtx event to the thread buffer.

Change 93640 on 2003/04/03 by hartogs@fl\_hartogs

Added include file "vgt\_reg.v" to prevent compiler errors during Modelsim compile. Fixed wire definition to match output port definition.

Change 93585 on 2003/04/03 by dougd@dougd\_r400\_linux\_marlboro

move event filters to inputs of SQ in ptr\_buff and vtx\_ctl and remove from thread\_buff\_cntl, modify cntx0-17 busy counters in ptr\_buff to use new event logic, add RST\_VTX\_CNT functionality to sq\_vtx\_ctl, add SQ\_CP\_event and SQ\_RB event functionality to pix\_thread\_buff and vtx\_thread\_buf, remove obsolete SQ\_CP\_event functionality from thread\_buff\_cntl.

Change 93489 on 2003/04/02 by vromaker@vromaker\_r400\_linux\_marlboro

added end of clause detection for serialization and resource change to CFSM;  
fix for first\_in\_clause related to PVPS detection;  
added SQ\_SP\_thread\_type and SQ\_TP\_thread\_type outputs to sq.v;  
added predicate to Tex IQ - now predicate goes from TCFS, thru TIF, thru TIQ, to the  
TIS;  
removed ql pipeline stage for SP predicate data in AIS Output;

Change 93099 on 2003/04/01 by hartogs@fl\_hartogs

Fixed logic that holds off SC injector.

Fixed bug in threaded empty signal for tp\_sqsp\_dmp\_not\_empty.

Hacked logic that frees state contexts so that it sorta works.

Change 93054 on 2003/04/01 by rramsey@rramsey\_crayola\_linux\_orl

PV/PS determinations need to be made on post-swizzled component selects

Change 93053 on 2003/04/01 by dougd@dougd\_r400\_linux\_marlboro

added context\_id or state to event info stored in the status\_reg to correct a bug in  
the state logic supporting cntx0 and cntx17 busy

Change 93026 on 2003/03/31 by grayc@grayc\_crayola\_linux\_orl

minor changes for gc testbench

Change 92970 on 2003/03/31 by dougd@dougd\_r400\_linux\_marlboro

added pix event\_id to qualify events to increment the cntx17 busy counter. Also added  
`include "vgt\_reg.v" and removed hard coded parameters.

Change 92904 on 2003/03/31 by hartogs@fl\_hartogs

Added TP\_SP latency controls requested by Mantor.

Change 92884 on 2003/03/31 by hartogs@fl\_hartogs

Cleaned-up some "TODO" items.

Change 92679 on 2003/03/28 by hartogs@fl\_hartogs

Hooked-up new thread-id ports on SQ. Testbench should now handle most tests.

Change 92451 on 2003/03/27 by hartogs@fl\_hartogs

Added several missing input ports to sub-modules.

Change 92428 on 2003/03/27 by hartogs@fl\_hartogs

Deleted redundant wire declaration that was causing an error in modelsim.

Added explicit declarations for a bunch of implicitly used wires. The lack of the explicit declarations was causing warnings in Modelsim.

Change 92324 on 2003/03/27 by dougd@dougdr400\_linux\_marlboro

fixed bug with address wrapping in the gpr\_wr\_addr generation for pix and vtx

Change 92303 on 2003/03/27 by mmantor@mmantor\_crayola\_linux\_orl

<export\_id expanded from one to four bit, end\_of\_clause added to CFS-TIF interface and last\_in\_clause flag passed down with instruction to eventually trigger a free\_done. thread\_id outputs added to the SQ\_SP and SQ\_TP interfaces for testbench, and added sq\_sx control signals for exp\_table read >

Change 92262 on 2003/03/26 by hartogs@fl\_hartogs

Added untested code for random backpressure on the SQ\_TP interface.

Added untested code for random starve pressure on the TP\_SP interface.

Change 92142 on 2003/03/25 by hartogs@fl\_hartogs

This version passes milestone\_tri. Trackers and injectors are ready for multi-threading when those signals become available.

Change 92139 on 2003/03/25 by hartogs@fl\_hartogs

This version passes the "milestone\_tri" test completely. First working version for texture fetch.

Change 91978 on 2003/03/25 by hartogs@fl\_hartogs

Incremental check-in. This version will run "milestone\_tri" to completion (including injecting the texture fetch data); however the simulation mismatches on the SPSX tracker.

Change 91977 on 2003/03/25 by hartogs@fl\_hartogs

Changed "check flags" on one of the SQ memories to minimize garbage output during simulation.

Change 91976 on 2003/03/25 by hartogs@fl\_hartogs

Changed dump file name from tp\_sq.dmp to tp\_sqsp.dmp.

Change 91763 on 2003/03/24 by hartogs@fl\_hartogs

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