

Change 95093 on 2003/04/11 by rramsey@rramsey\_crayola\_linux\_orl

Regen with new genperfcode

Change 92454 on 2003/03/27 by rramsey@rramsey\_crayola\_linux\_orl

add reset for mp\_snd and mp\_loop regs

Change 92356 on 2003/03/27 by smoss@smoss\_crayola\_linux\_orl

removed space limiting on mismatch display

Change 91348 on 2003/03/20 by kmeekins@kmeekins\_crayola\_linux\_orl

Changed to use the default filenames created by build scripts.

Change 91177 on 2003/03/20 by donaldl@fl\_donaldl\_p4

Removed IO\_SC and IO\_SC\_B partition scan input/output signals.

Change 90931 on 2003/03/19 by kmeekins@kmeekins\_r400\_win

Changed the ROM\_BAD\_PIPE\_DISABLE\_REGISTER logic to only check those disable bit fields associated with pixel processing.

Change 90807 on 2003/03/18 by donaldl@fl\_donaldl\_p4

Added common test ports.

Change 90308 on 2003/03/14 by kmeekins@kmeekins\_r400\_win

Conditioned the use\_polymode\_grad mux select with the Real-Time stream enable such that the stored polymode gradient is never used during RTS. The dx and dy gradients are supplied during RTS so using the stored value is not necessary.

Change 90029 on 2003/03/13 by kmeekins@kmeekins\_r400\_win

Created script to convert Modelsim .do wave files to nWave .rc files.

Change 89510 on 2003/03/11 by kmeekins@kmeekins\_r400\_win

Changed compareScSq to not compare pixel mask on events or dealloc.

Change 88833 on 2003/03/06 by donaldl@fl\_donaldl\_p4

Wrapper around ati\_lrp\_state\_storage to register RBIU interface signals.

AMD1044 0254047

Change 88830 on 2003/03/06 by donaldl@fl\_donaldl\_p4

Delayed by 1 clk some of RBIU inputs/outputs to state storage modules (ie. w\_addr[2:0], we, sel, cp, w\_data[31:0], r\_addr[2:0], re, r\_data[31:0]). Done to reduce large fanout in post layout netlist.

Change 88560 on 2003/03/05 by rramsey@RRAMSEY\_P4\_r400\_win

only look at lower 5 bits of rbbm write data to get event id

Change 88556 on 2003/03/05 by kmeekins@kmeekins\_r400\_win

Modified quad select out and quad pair proc out trackers and dump routines to handle and additional vector generation for last quad pair of prim.

Change 88094 on 2003/03/03 by rramsey@RRAMSEY\_P4\_r400\_win

Fix a bug related that was allowing non-rts/rts quads to get packed with rts/non-rts quads when the switch happened with a quad in the overflow buf

Change 87945 on 2003/03/02 by donaldl@donaldl\_crayola\_unix\_orl

Fanout of state variable indices to help post layout timing.

Change 87944 on 2003/03/02 by donaldl@fl\_donaldl\_p4

Fanout of state variable indices to help post layout timing.

Change 87649 on 2003/02/28 by rramsey@RRAMSEY\_P4\_r400\_win

fix a problem with the sc\_sq rt tracker

Change 86893 on 2003/02/25 by donaldl@donaldl\_crayola\_unix\_orl

Reduce critical path of XY3 to meet post layout timing.

Change 86892 on 2003/02/25 by donaldl@fl\_donaldl\_p4

Reduce critical path of XY3 to meet post layout timing.

Change 86888 on 2003/02/25 by rramsey@RRAMSEY\_P4\_r400\_win

Make packer and sq rts trackers compare an entry for each of fpos, event, hit quads, and dealloc

Add monitors for sc\_sq and packer output

AMD1044 0254048

Clean up a couple of things in the vsim scripts

Change 85939 on 2003/02/21 by kmeekins@kmeekins\_r400\_win

Modified compareScSx task to handle differences between the dmp file write order and the RTL.

Change 85903 on 2003/02/21 by scamlin@scamlin\_crayola\_unix\_orl

changed width

Change 85595 on 2003/02/20 by scamlin@scamlin\_crayola\_unix\_orl

added testreg

Change 84525 on 2003/02/14 by danh@danh\_r400\_win

Added z\_interp\_jss\_enable.

Change 84488 on 2003/02/14 by rramsey@rramsey\_crayola\_linux\_orl

Add pixel-per-quad perfcounters to SC RTL

Change 84255 on 2003/02/13 by kmeekins@kmeekins\_r400\_win

Reset vq\_visible\_status registers to match emulator results.

Change 83897 on 2003/02/12 by rramsey@FL\_RAMSEY\_r400\_win

Don't compare tile x/y for events

update rand script to check for both done msgs (rt and non rt)

Change 83821 on 2003/02/12 by kmeekins@kmeekins\_r400\_win

Added control logic to handle tests where CONTEXT\_DONE event issued but never any state loaded for that context (2D prims).

Change 83575 on 2003/02/11 by kmeekins@kmeekins\_r400\_win

Modified the test for incomplete results.

Change 83337 on 2003/02/10 by kmeekins@kmeekins\_r400\_win

Added checks for incomplete files that look for multiple files compared for a single dump file. Real-Time stream trackers now generate two compare results per dump file.

Change 83210 on 2003/02/10 by smoss@smoss\_crayola\_linux\_orl

AMD1044 0254049

removed rom\_sc

Change 83079 on 2003/02/08 by smoss@smoss\_crayola\_linux\_orl\_regress

fixed starved condition where sc was waiting for change of context from suscan permanently

Change 82995 on 2003/02/07 by rramsey@rramsey\_crayola\_linux\_orl

stipple fixes related to realtime stream interruptions

Change 82991 on 2003/02/07 by rramsey@RRAMSEY\_P4\_r400\_win

Don't compare stipple values for realtime prims since there is no way to know when the rt prim will break in, and the rt tiles will inherit the stipple values that happen to be sitting in cur\_ptr/cur\_cnt at that time

Change 82943 on 2003/02/07 by kmeekins@kmeekins\_r400\_win

Added condition to permit certain events to pass PA\_SC even if their context has yet to be loaded. Allows flush event to pass to SQ so RBBM flush can terminate to permit context loading.

Change 82942 on 2003/02/07 by kmeekins@kmeekins\_r400\_win

Added file not empty condition to loop to prevent accidental compare of last vector in file.

Change 82565 on 2003/02/06 by kmeekins@kmeekins\_r400\_win

Cleaned up flush operations.  
Fixed rbbm read operations.  
Added debug counter to rbbm writes.

Change 81919 on 2003/02/04 by kmeekins@kmeekins\_r400\_win

Added synchronizing logic to prevent the SC from flushing when it sees a PA\_SC flush event prior to reading the same flush event on the RBBM bus.

Change 81873 on 2003/02/04 by kmeekins@kmeekins\_r400\_win

Rewrote testbench to recognize, implement and control the pipe flush commands and data flow control in both the RBBM and PA\_SC interfaces.

AMD1044 0254050

Change 81036 on 2003/01/31 by viviana@viviana\_crayola\_linux\_orl

Incorporated the SC\_SP\_PATH define inside of a specific `define for GCTEST, CHIP TEST and Block level tests.

Change 80866 on 2003/01/30 by viviana@viviana\_crayola\_linux\_orl

Took out the `define sc\_block\_sim and added it to the command line in buildtb and Makefile.

Change 80540 on 2003/01/29 by hartogs@fl\_hartogs

Added SC tracker (tbrk\_sc) to the tb\_sqsp testbench.

Change 80304 on 2003/01/29 by smoss@smoss\_crayola\_linux\_orl\_regress

<Orlando Hardware Regression Results >

Change 80177 on 2003/01/28 by mmantor@FL\_mmantorLT\_r400\_win

got basic sc stimulus to work for sq/sp test bench and reset multipass counter during reset

Change 80101 on 2003/01/28 by kmeekins@kmeekins\_r400\_win

Changed the bad pipe logic

- Removed use of rom\_sc.dmp
- Pipe disable controlled by RBBM data bus
- Removed rom\_sc\_file\_not\_empty from "simulation complete" testing

Added pipe flush logic

- Snoop the RBBM bus for flush commands
- Freeze RBBM bus
- Stop PA\_SC data xfer

Reformatted signal definitions and added comments

Optimized tbmod\_sc\_rbbm task

Change 79904 on 2003/01/28 by donaldl@fl\_donaldl\_p4

Checked in wrong bvrl file for previous change.

Change 79837 on 2003/01/27 by mmantor@FL\_mmantorLT\_r400\_win

improve timing path for iter\_cmd\_wdata path in the iterator

Change 79536 on 2003/01/26 by smoss@smoss\_crayola\_linux\_orl\_regress

added rts dumps now generated by emulator

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