

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Oct4
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Oct4
PASS			
3	cp_e2_one_blit		2002Oct4
PASS			
4	cp_e2_one_line		2002Oct4
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Oct4
PASS			
6	cp_e2paint_565		2002Oct4
PASS			
7	cp_e2paint_multi		2002Oct4
PASS			
8	cp_e2polyscanlines_brush		2002Oct4
PASS			
9	cp_e2polyscanlines_brush_mt		2002Oct4
PASS			
10	cp_e2smalltext_jcl		2002Oct4
PASS			
11	cp_simple_triangle		2002Oct4
PASS			
12	milestone_event_flush_tri1		2002Oct3
PASS			
13	milestone_event_flush_tri2		2002Oct3
PASS			
14	milestone_event_flush_tri3		2002Oct3
PASS			
15	milestone_event_flush_tri4		2002Oct3
PASS			
16	milestone_event_flush_tri5		2002Oct3
PASS			
17	milestone_event_flush_tri6		2002Oct3
PASS			
18	milestone_event_flush_tri7		2002Oct3
PASS			
19	milestone_event_flush_tri8		2002Oct3
PASS			
20	milestone_event_flush_tri9		2002Oct3

PASS		
21	milestone_event_randomized	2002Oct3
PASS		
22	milestone_event_standard	2002Oct3
PASS		
23	milestone_event_switch_flush_tri1	2002Oct3
PASS		
24	milestone_event_switch_flush_tri2	2002Oct3
PASS		
25	milestone_event_switch_flush_tri3	2002Oct3
PASS		
26	milestone_event_switch_flush_tri4	2002Oct3
PASS		
27	milestone_event_switch_flush_tri5	2002Oct3
PASS		
28	milestone_event_switch_flush_tri6	2002Oct3
PASS		
29	milestone_event_switch_flush_tri7	2002Oct3
PASS		
30	milestone_event_switch_flush_tri8	2002Oct3
PASS		
31	milestone_event_switch_flush_tri9	2002Oct3
PASS		
32	milestone_event_switch_tri2	2002Oct3
PASS		
33	milestone_event_switch_tri3	2002Oct3
PASS		
34	milestone_event_switch_tri4	2002Oct3
PASS		
35	milestone_event_switch_tri5	2002Oct3
PASS		
36	milestone_event_switch_tri6	2002Oct3
PASS		
37	milestone_event_switch_tri7	2002Oct3
PASS		
38	milestone_event_switch_tri8	2002Oct3
PASS		
39	milestone_event_switch_tri9	2002Oct3
PASS		
40	milestone_event_tri2	2002Oct3
PASS		
41	milestone_event_tri3	2002Oct3
PASS		
42	milestone_event_tri4	2002Oct3
PASS		
43	milestone_event_tri5	2002Oct3
PASS		
44	milestone_event_tri6	2002Oct3

PASS		
45	milestone_event_tri7	2002Oct3
PASS		
46	milestone_event_tri8	2002Oct3
PASS		
47	milestone_event_tri9	2002Oct3
PASS		
48	milestone_tri_random_rect	2002Oct3
PASS		
49	milestone_tri_random_tri	2002Oct3
PASS		
50	milestone_tri_randomized	2002Oct3
PASS		
51	milestone_tri_standard	2002Oct3
PASS		
52	milestone_tri_tril28_pix4	2002Oct3
PASS		
53	milestone_tri_tril_pix1	2002Oct3
PASS		
54	milestone_tri_tril_pix2	2002Oct3
PASS		
55	milestone_tri_tril_pix3	2002Oct3
PASS		
56	milestone_tri_tril_pix4	2002Oct3
PASS		
57	milestone_tri_tri24x24	2002Oct3
PASS		
58	milestone_tri_tri28x28	2002Oct3
PASS		
59	milestone_tri_tri2_pix1	2002Oct3
PASS		
60	milestone_tri_tri2_pix16	2002Oct3
PASS		
61	milestone_tri_tri2_pix4	2002Oct3
PASS		
62	milestone_tri_tri32_pix1	2002Oct3
PASS		
63	milestone_tri_tri32_pix4	2002Oct3
PASS		
64	milestone_tri_tri32x32	2002Oct3
PASS		
65	milestone_tri_tri36x36	2002Oct3
PASS		
66	milestone_tri_tri40x40	2002Oct3
PASS		
67	milestone_tri_tri44x44	2002Oct3
PASS		
68	milestone_tri_tri48x48	2002Oct3

PASS		
69	milestone_tri_tri52x52	2002Oct3
PASS		
70	milestone_tri_tri56x56	2002Oct3
PASS		
71	milestone_tri_tri60x60	2002Oct3
PASS		
72	milestone_tri_tri64x64	2002Oct3
PASS		
73	milestone_tri_tri8_pix4	2002Oct3
PASS		
74	primlib_1st_tri_june15	2002Oct4
PASS		
75	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct4
PASS		
76	primlib_gouraud_triangles_2_draw_passes	2002Oct4
PASS		
77	primlib_parameterized_simple_triangle	2002Oct4
PASS		
78	primlib_template_simple_triangle	2002Oct4
PASS		
79	primlib_tex_tri	2002Oct4
PASS		
80	r400cl_bary_texture_08_bug	2002Sep21
PASS		
81	r400cl_clip_10_verts_01	2002Oct4
PASS		
82	r400cl_clip_disable_01	2002Oct4
PASS		
83	r400cl_clip_space_dx_ogl_01	2002Oct4
PASS		
84	r400cl_clip_ucp_6bits_01	2002Oct4
PASS		
85	r400cl_cull_only_ena_01	2002Oct4
PASS		
86	r400cl_edgeflags_01	2002Oct4
PASS		
87	r400cl_edgeflags_01_bug	2002Oct4
PASS		
88	r400cl_edgeflags_frustum_bottom_01	2002Oct4
PASS		
89	r400cl_edgeflags_frustum_far_01	2002Oct4
PASS		
90	r400cl_edgeflags_frustum_left_01	2002Oct4
PASS		
91	r400cl_edgeflags_frustum_near_01	2002Oct4
PASS		
92	r400cl_edgeflags_frustum_right_01	2002Oct4

PASS		
93	r400cl_edgeflags_frustum_top_01	2002Oct4
PASS		
94	r400cl_edgeflags_gband_01	2002Oct4
PASS		
95	r400cl_edgeflags_gband_bottom_01	2002Oct4
PASS		
96	r400cl_edgeflags_gband_left_01	2002Oct4
PASS		
97	r400cl_edgeflags_gband_right_01	2002Oct4
PASS		
98	r400cl_edgeflags_gband_top_01	2002Oct4
PASS		
99	r400cl_frustum_01	2002Oct4
PASS		
100	r400cl_frustum_02	2002Oct4
PASS		
101	r400cl_frustum_03	2002Oct4
PASS		
102	r400cl_frustum_04	2002Oct4
PASS		
103	r400cl_frustum_05	2002Oct4
PASS		
104	r400cl_frustum_06	2002Oct4
PASS		
105	r400cl_frustum_07	2002Oct4
PASS		
106	r400cl_frustum_08	2002Oct4
PASS		
107	r400cl_frustum_09	2002Oct4
PASS		
108	r400cl_frustum_10	2002Oct4
PASS		
109	r400cl_frustum_11	2002Oct4
PASS		
110	r400cl_frustum_12	2002Oct4
PASS		
111	r400cl_frustum_13	2002Oct4
PASS		
112	r400cl_frustum_14	2002Oct4
PASS		
113	r400cl_frustum_15	2002Oct4
PASS		
114	r400cl_frustum_16	2002Oct4
PASS		
115	r400cl_frustum_17	2002Oct4
PASS		
116	r400cl_frustum_18	2002Oct4

PASS		
117	r400cl_frustum_19	2002Oct4
PASS		
118	r400cl_frustum_20	2002Oct4
PASS		
119	r400cl_frustum_21	2002Oct4
PASS		
120	r400cl_frustum_22	2002Oct4
PASS		
121	r400cl_frustum_23	2002Oct4
PASS		
122	r400cl_frustum_24	2002Oct4
PASS		
123	r400cl_frustum_25	2002Oct4
PASS		
124	r400cl_frustum_26	2002Oct4
PASS		
125	r400cl_frustum_27	2002Oct4
PASS		
126	r400cl_frustum_28	2002Oct4
PASS		
127	r400cl_frustum_all_vols_lines	2002Oct4
PASS		
128	r400cl_frustum_all_vols_tris	2002Oct4
PASS		
129	r400cl_frustum_lines_01	2002Oct4
PASS		
130	r400cl_frustum_lines_02	2002Oct4
PASS		
131	r400cl_frustum_lines_03	2002Oct4
PASS		
132	r400cl_frustum_lines_04	2002Oct4
PASS		
133	r400cl_frustum_lines_05	2002Oct4
PASS		
134	r400cl_frustum_lines_06	2002Oct4
PASS		
135	r400cl_frustum_lines_07	2002Oct4
PASS		
136	r400cl_frustum_lines_08	2002Oct4
PASS		
137	r400cl_frustum_lines_09	2002Oct4
PASS		
138	r400cl_frustum_lines_10	2002Oct4
PASS		
139	r400cl_frustum_lines_11	2002Oct4
PASS		
140	r400cl_frustum_lines_12	2002Oct4

PASS		
141	r400cl_frustum_lines_13	2002Oct4
PASS		
142	r400cl_frustum_lines_14	2002Oct4
PASS		
143	r400cl_frustum_lines_15	2002Oct4
PASS		
144	r400cl_frustum_lines_16	2002Oct4
PASS		
145	r400cl_frustum_lines_17	2002Oct4
PASS		
146	r400cl_frustum_lines_18	2002Oct4
PASS		
147	r400cl_frustum_lines_19	2002Oct4
PASS		
148	r400cl_frustum_lines_20	2002Oct4
PASS		
149	r400cl_frustum_lines_21	2002Oct4
PASS		
150	r400cl_frustum_lines_22	2002Oct4
PASS		
151	r400cl_frustum_lines_23	2002Oct4
PASS		
152	r400cl_frustum_lines_24	2002Oct4
PASS		
153	r400cl_gband_04	2002Oct4
PASS		
154	r400cl_gband_tcl_01	2002Oct4
PASS		
155	r400cl_nan_kill_01_bug	2002Oct4
PASS		
156	r400cl_pointlist_vertex_state_ucp_01	2002Oct4
PASS		
157	r400cl_polymode_line_fill_01_bug	2002Oct4
PASS		
158	r400cl_simple_triangle_01	2002Oct4
PASS		
159	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct4
PASS		
160	r400cl_ucp_combos_01	2002Oct4
PASS		
161	r400cl_ucp_combos_02	2002Oct4
PASS		
162	r400cl_ucp_combos_03	2002Oct4
PASS		
163	r400cl_ucp_combos_04	2002Oct4
PASS		
164	r400cl_ucp_combos_05	2002Oct4

PASS		
165	r400cl_ucp_combos_06	2002Oct4
PASS		
166	r400cl_ucp_combos_07	2002Oct4
PASS		
167	r400cl_ucp_combos_08	2002Oct4
PASS		
168	r400cl_ucp_combos_09	2002Oct4
PASS		
169	r400cl_ucp_combos_10	2002Oct4
PASS		
170	r400cl_ucp_combos_11	2002Oct4
PASS		
171	r400cl_ucp_combos_12	2002Oct4
PASS		
172	r400cl_ucp_combos_13	2002Oct4
PASS		
173	r400cl_ucp_combos_14	2002Oct4
PASS		
174	r400cl_ucp_combos_15	2002Oct4
PASS		
175	r400cl_ucp_combos_16	2002Oct4
PASS		
176	r400cl_ucp_combos_17	2002Oct4
PASS		
177	r400cl_ucp_combos_18	2002Oct4
PASS		
178	r400cl_ucp_combos_19	2002Oct4
PASS		
179	r400cl_ucp_combos_20	2002Oct4
PASS		
180	r400cl_ucp_combos_21	2002Oct4
PASS		
181	r400cl_ucp_combos_22	2002Oct4
PASS		
182	r400cl_ucp_combos_23	2002Oct4
PASS		
183	r400cl_ucp_combos_24	2002Oct4
PASS		
184	r400cl_ucp_combos_25	2002Oct4
PASS		
185	r400cl_ucp_combos_26	2002Oct4
PASS		
186	r400cl_ucp_combos_27	2002Oct4
PASS		
187	r400cl_ucp_combos_28	2002Oct4
PASS		
188	r400cl_ucp_combos_29	2002Oct4

PASS		
189	r400cl_ucp_combos_30	2002Oct4
PASS		
190	r400cl_ucp_pointlist_01	2002Oct4
PASS		
191	r400cl_vertex_reuse_clip_01	2002Oct4
PASS		
192	r400cl_vtx_kill_01	2002Oct4
PASS		
193	r400cl_vtx_kill_02	2002Oct4
PASS		
194	r400cl_w_eq_0	2002Oct4
PASS		
195	r400rbm_simple_triangle_01	2002Oct4
PASS		
196	r400sanity_persp_projection_01	2002Oct4
PASS		
197	r400sanity_vfd_texture_sample_01	2002Oct4
PASS		
198	r400sc_baryc_01	2002Oct4
PASS		
199	r400sc_bres_cntl_01	2002Sep7
PASS		
200	r400sc_bres_cntl_02	2002Oct4
PASS		
201	r400sc_bres_cntl_03	2002Oct4
PASS		
202	r400sc_bres_cntl_04	2002Oct4
PASS		
203	r400sc_bres_cntl_w2k_01	2002Oct4
PASS		
204	r400sc_bres_cntl_w9x_01	2002Oct4
PASS		
205	r400sc_clip_rect_01	2002Oct4
PASS		
206	r400sc_clip_rect_02	2002Oct4
PASS		
207	r400sc_clip_rect_03	2002Oct4
PASS		
208	r400sc_clip_rect_04	2002Oct4
PASS		
209	r400sc_clip_rect_fc_01	2002Oct4
PASS		
210	r400sc_diamond_exit_01	2002Oct4
PASS		
211	r400sc_diamond_exit_02	2002Oct4
PASS		
212	r400sc_diamond_exit_03	2002Oct4

PASS		
213	r400sc_diamond_exit_04	2002Oct4
PASS		
214	r400sc_diamond_exit_05	2002Oct4
PASS		
215	r400sc_jss_1x1_primtypes_01	2002Oct4
PASS		
216	r400sc_jss_1x2_01	2002Oct4
PASS		
217	r400sc_jss_1x2_02	2002Oct4
PASS		
218	r400sc_jss_1x2_primtypes_01	2002Oct4
PASS		
219	r400sc_jss_1x3_01	2002Oct4
PASS		
220	r400sc_jss_1x3_02	2002Oct4
PASS		
221	r400sc_jss_1x3_primtypes_01	2002Oct4
PASS		
222	r400sc_jss_1x4_01	2002Oct4
PASS		
223	r400sc_jss_1x4_02	2002Oct4
PASS		
224	r400sc_jss_1x4_primtypes_01	2002Oct4
PASS		
225	r400sc_jss_2x1_01	2002Oct4
PASS		
226	r400sc_jss_2x1_02	2002Oct4
PASS		
227	r400sc_jss_2x1_primtypes_01	2002Oct4
PASS		
228	r400sc_jss_2x2_01	2002Oct4
PASS		
229	r400sc_jss_2x2_02	2002Oct4
PASS		
230	r400sc_jss_2x2_primtypes_01	2002Oct4
PASS		
231	r400sc_jss_2x3_01	2002Oct4
PASS		
232	r400sc_jss_2x3_02	2002Oct4
PASS		
233	r400sc_jss_2x3_primtypes_01	2002Oct4
PASS		
234	r400sc_jss_2x4_01	2002Oct4
PASS		
235	r400sc_jss_2x4_02	2002Oct4
PASS		
236	r400sc_jss_2x4_primtypes_01	2002Oct4

PASS		
237	r400sc_jss_3x1_01	2002Oct4
PASS		
238	r400sc_jss_3x1_02	2002Oct4
PASS		
239	r400sc_jss_3x1_primtypes_01	2002Oct4
PASS		
240	r400sc_jss_3x2_01	2002Oct4
PASS		
241	r400sc_jss_3x2_02	2002Oct4
PASS		
242	r400sc_jss_3x2_primtypes_01	2002Oct4
PASS		
243	r400sc_jss_3x3_01	2002Oct4
PASS		
244	r400sc_jss_3x3_02	2002Oct4
PASS		
245	r400sc_jss_3x3_primtypes_01	2002Oct4
PASS		
246	r400sc_jss_3x4_01	2002Oct4
PASS		
247	r400sc_jss_3x4_02	2002Oct4
PASS		
248	r400sc_jss_3x4_03	2002Oct4
PASS		
249	r400sc_jss_3x4_primtypes_01	2002Oct4
PASS		
250	r400sc_jss_4x1_01	2002Oct4
PASS		
251	r400sc_jss_4x1_02	2002Oct4
PASS		
252	r400sc_jss_4x1_primtypes_01	2002Oct4
PASS		
253	r400sc_jss_4x2_01	2002Oct4
PASS		
254	r400sc_jss_4x2_02	2002Oct4
PASS		
255	r400sc_jss_4x2_primtypes_01	2002Oct4
PASS		
256	r400sc_jss_4x3_01	2002Oct4
PASS		
257	r400sc_jss_4x3_02	2002Oct4
PASS		
258	r400sc_jss_4x3_primtypes_01	2002Oct4
PASS		
259	r400sc_jss_4x4_01	2002Oct4
PASS		
260	r400sc_jss_4x4_02	2002Oct4

PASS		
261	r400sc_jss_4x4_03	2002Oct4
PASS		
262	r400sc_jss_4x4_aa_mask_01	2002Oct4
PASS		
263	r400sc_jss_4x4_aa_mask_02	2002Oct4
PASS		
264	r400sc_jss_4x4_fc_01	2002Oct4
PASS		
265	r400sc_jss_4x4_fc_02	2002Oct4
PASS		
266	r400sc_jss_4x4_max_dist_01	2002Oct4
PASS		
267	r400sc_jss_4x4_primitives_01	2002Oct4
PASS		
268	r400sc_line_dx10_eq_0_01	2002Oct4
PASS		
269	r400sc_line_dx10_ge_0_01	2002Oct4
PASS		
270	r400sc_line_dx10_lt_0_01	2002Oct4
PASS		
271	r400sc_line_dy10_eq_0_01	2002Oct4
PASS		
272	r400sc_line_dy10_ge_0_01	2002Oct4
PASS		
273	r400sc_line_dy10_lt_0_01	2002Oct4
PASS		
274	r400sc_line_expand_width_msa_8_01	2002Oct4
PASS		
275	r400sc_line_expand_width_msa_8_02	2002Oct4
PASS		
276	r400sc_line_expand_width_msa_8_03	2002Oct4
PASS		
277	r400sc_line_jss_3x4_01	2002Oct4
PASS		
278	r400sc_line_list_01	2002Oct4
PASS		
279	r400sc_line_list_02	2002Oct4
PASS		
280	r400sc_line_list_concentric_circle_01	2002Oct4
PASS		
281	r400sc_line_list_concentric_circle_02	2002Oct4
PASS		
282	r400sc_line_list_concentric_circle_03	2002Oct4
PASS		
283	r400sc_line_list_textured_01	2002Oct4
PASS		
284	r400sc_line_list_verify_st_01	2002Oct4

PASS		
285	r400sc_line_list_verify_st_01_bug	2002Oct4
PASS		
286	r400sc_line_msaa_8_01	2002Oct4
PASS		
287	r400sc_line_msaa_8_textured_01	2002Oct4
PASS		
288	r400sc_line_stipple_01	2002Oct4
PASS		
289	r400sc_line_stipple_02	2002Oct4
PASS		
290	r400sc_line_stipple_03	2002Oct4
PASS		
291	r400sc_line_stipple_04	2002Oct4
PASS		
292	r400sc_line_stipple_05	2002Oct4
PASS		
293	r400sc_line_stipple_06	2002Oct4
PASS		
294	r400sc_line_stipple_07	2002Oct4
PASS		
295	r400sc_line_stipple_08	2002Oct4
PASS		
296	r400sc_line_stipple_09	2002Oct4
PASS		
297	r400sc_line_stipple_10	2002Oct4
PASS		
298	r400sc_line_stipple_11	2002Oct4
PASS		
299	r400sc_line_stipple_12	2002Oct4
PASS		
300	r400sc_line_stipple_13	2002Oct4
PASS		
301	r400sc_line_stipple_14	2002Oct4
PASS		
302	r400sc_line_stipple_15	2002Oct4
PASS		
303	r400sc_line_stipple_16	2002Oct4
PASS		
304	r400sc_line_stipple_17	2002Oct4
PASS		
305	r400sc_line_stipple_18	2002Oct4
PASS		
306	r400sc_line_stipple_19	2002Oct4
PASS		
307	r400sc_line_stipple_20	2002Oct4
PASS		
308	r400sc_line_stipple_21	2002Oct4

PASS		
309	r400sc_line_stipple_22	2002Oct4
PASS		
310	r400sc_line_stipple_23	2002Oct4
PASS		
311	r400sc_line_stipple_fc_08	2002Oct4
PASS		
312	r400sc_line_strip_stipple_01	2002Oct4
PASS		
313	r400sc_msaa_1_01	2002Oct4
PASS		
314	r400sc_msaa_1_primitives_01	2002Oct4
PASS		
315	r400sc_msaa_1_rectangle_list_01	2002Oct4
PASS		
316	r400sc_msaa_1_rectangle_list_02	2002Oct4
PASS		
317	r400sc_msaa_1_rectangle_list_03	2002Oct4
PASS		
318	r400sc_msaa_1_rectangle_list_04	2002Oct4
PASS		
319	r400sc_msaa_1_rectangle_list_05	2002Oct4
PASS		
320	r400sc_msaa_1_rectangle_list_06	2002Oct4
PASS		
321	r400sc_msaa_1_rectangle_list_07	2002Oct4
PASS		
322	r400sc_msaa_1_rectangle_list_08	2002Oct4
PASS		
323	r400sc_msaa_1_zbuffer_rectangle_list_01	2002Oct4
PASS		
324	r400sc_msaa_1_zbuffer_rectangle_list_02	2002Oct4
PASS		
325	r400sc_msaa_2_primitives_01	2002Oct4
PASS		
326	r400sc_msaa_2_rectangle_list_01	2002Oct4
PASS		
327	r400sc_msaa_2_rectangle_list_02	2002Oct4
PASS		
328	r400sc_msaa_2_rectangle_list_03	2002Oct4
PASS		
329	r400sc_msaa_2_rectangle_list_04	2002Oct4
PASS		
330	r400sc_msaa_2_rectangle_list_05	2002Oct4
PASS		
331	r400sc_msaa_2_rectangle_list_06	2002Oct4
PASS		
332	r400sc_msaa_2_rectangle_list_07	2002Oct4

PASS		
333	r400sc_msaa_2_rectangle_list_08	2002Oct4
PASS		
334	r400sc_msaa_2_zbuffer_rectangle_list_01	2002Oct4
PASS		
335	r400sc_msaa_2_zbuffer_rectangle_list_02	2002Oct4
PASS		
336	r400sc_msaa_3_primitives_01	2002Oct4
PASS		
337	r400sc_msaa_3_rectangle_list_01	2002Oct4
PASS		
338	r400sc_msaa_3_rectangle_list_02	2002Oct4
PASS		
339	r400sc_msaa_3_rectangle_list_03	2002Oct4
PASS		
340	r400sc_msaa_3_rectangle_list_04	2002Oct4
PASS		
341	r400sc_msaa_3_rectangle_list_05	2002Oct4
PASS		
342	r400sc_msaa_3_rectangle_list_06	2002Oct4
PASS		
343	r400sc_msaa_3_rectangle_list_07	2002Oct4
PASS		
344	r400sc_msaa_3_rectangle_list_08	2002Oct4
PASS		
345	r400sc_msaa_3_zbuffer_rectangle_list_01	2002Oct4
PASS		
346	r400sc_msaa_3_zbuffer_rectangle_list_02	2002Oct4
PASS		
347	r400sc_msaa_4_01	2002Oct4
PASS		
348	r400sc_msaa_4_primitives_01	2002Oct4
PASS		
349	r400sc_msaa_4_rectangle_list_01	2002Oct4
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355	r400sc_msaa_4_rectangle_list_07	2002Oct4
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356	r400sc_msaa_4_rectangle_list_08	2002Oct4

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357	r400sc_msaa_4_zbuffer_rectangle_list_01	2002Oct4
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358	r400sc_msaa_4_zbuffer_rectangle_list_02	2002Oct4
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359	r400sc_msaa_6_01	2002Oct4
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360	r400sc_msaa_6_primitives_01	2002Oct4
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361	r400sc_msaa_6_rectangle_list_01	2002Oct4
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362	r400sc_msaa_6_rectangle_list_02	2002Oct4
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370	r400sc_msaa_6_zbuffer_rectangle_list_02	2002Oct4
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374	r400sc_msaa_8_04	2002Oct4
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375	r400sc_msaa_8_05	2002Oct4
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376	r400sc_msaa_8_aa_mask_01	2002Oct4
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377	r400sc_msaa_8_aa_mask_02	2002Oct4
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378	r400sc_msaa_8_aa_mask_fc_02	2002Oct4
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379	r400sc_msaa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct4
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380	r400sc_msaa_8_primitives_01	2002Oct4

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389	r400sc_msaa_8_zbuffer_rectangle_list_01	2002Oct4
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390	r400sc_msaa_8_zbuffer_rectangle_list_02	2002Oct4
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391	r400sc_msaa_X_primtypes_01_bug	2002Oct4
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392	r400sc_msaa_X_primtypes_02_bug	2002Oct4
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393	r400sc_multi_chip_prim_discard_00_07	2002Oct4
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394	r400sc_null_triangles_01	2002Oct4
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395	r400sc_null_triangles_fc_01	2002Oct4
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396	r400sc_pinwheel_01	2002Oct4
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397	r400sc_pinwheel_02	2002Oct4
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398	r400sc_pinwheel_02_bug	2002Oct4
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399	r400sc_point_jss_3x4_01	2002Oct4
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400	r400sc_point_list_01	2002Oct4
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406	r400sc_point_list_06_bug	2002Oct4
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407	r400sc_point_list_06_bug_02	2002Oct4
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412	r400sc_poly_offset_01	2002Oct4
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413	r400sc_poly_offset_02	2002Oct4
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423	r400sc_poly_offset_fc_01	2002Oct4
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424	r400sc_polygon_stipple_01	2002Oct4
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425	r400sc_polymode_tri_fill_01	2002Oct4
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426	r400sc_polymode_tri_fill_01_bug	2002Oct4
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427	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct4
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428	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct4

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434	r400sc_prsp_byc_intrp_ref_pix_08	2002Oct4
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436	r400sc_raster_fill_rule_02	2002Oct4
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437	r400sc_raster_fill_rule_03	2002Oct4
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439	r400sc_raster_fill_rule_05	2002Oct4
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440	r400sc_raster_fill_rule_fc_01	2002Oct4
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449	r400sc_scissor_rect_01	2002Oct4
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451	r400sc_scissor_rect_03	2002Oct4
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452	r400sc_scissor_rect_04	2002Oct4

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454	r400sc_scissor_rect_fc_01	2002Oct4
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455	r400sc_set_state_01	2002Oct4
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456	r400sc_sp_sample_cntl_01	2002Oct4
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462	r400sc_sp_sample_cntl_07	2002Oct4
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463	r400sc_sp_sample_cntl_08	2002Oct4
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464	r400sc_tri_16_par_64_dwords_01	2002Oct4
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465	r400sc_tri_8textures_01	2002Oct4
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466	r400sc_tri_8textures_02	2002Oct4
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467	r400sc_tri_walk_start_vertex_01	2002Oct4
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468	r400sc_tri_walk_start_vertex_02	2002Oct4
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469	r400sc_tri_walk_start_vertex_03	2002Oct4
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475	r400sc_tri_walk_start_vertex_09	2002Oct4
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478	r400sc_tri_walk_start_vertex_12	2002Oct4
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479	r400sc_tri_walk_start_vertex_13	2002Oct4
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481	r400sc_tri_walk_start_vertex_15	2002Oct4
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493	r400sc_zbuffer_rectangle_list_02	2002Oct4
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494	r400sc_zbuffer_rectangle_list_fc_02	2002Oct4
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495	r400sc_zbuffer_triangle_list_01	2002Oct4
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497	r400su_baryc_test_01	2002Oct4
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498	r400su_baryc_test_02	2002Oct4
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504 r400su_clip_pointsize_test_01 2002Oct4
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505 r400su_clip_pointttest_01 2002Oct4
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506 r400su_clip_pointttest_02 2002Oct4
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508 r400su_clip_pointttest_04 2002Oct4
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515 r400su_degentri_test_02 2002Oct4
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516 r400su_degentri_test_03 2002Oct4
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517 r400su_edge_flag_01 2002Oct4
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518 r400su_edge_flag_02 2002Oct4
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519 r400su_edgeflags_triangle_01 2002Oct4
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520 r400su_edgeflags_triangle_02 2002Oct4
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521 r400su_geom_sort_01 2002Oct4
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523 r400su_line_end_cap_width_functional_02 2002Oct4
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524 r400su_line_orientation_01 2002Oct4

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527	r400su_line_orientation_dx01_02	2002Oct4
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528	r400su_line_orientation_dy01_01	2002Oct4
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529	r400su_line_orientation_dy01_02	2002Oct4
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533	r400su_multi_context_01	2002Oct4
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534	r400su_multi_prim_01	2002Oct4
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535	r400su_multi_prim_02	2002Oct4
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536	r400su_parallel_orientation_all_01	2002Oct4
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537	r400su_parallel_orientation_all_02	2002Oct4
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538	r400su_pc_management_01	2002Oct4
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547	r400su_point_sprite_07	2002Oct4
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548	r400su_point_sprite_08	2002Oct4

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550	r400su_point_wl_h16_functional_01	2002Oct4
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552	r400su_pointsizepresent_02	2002Oct4
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554	r400su_polymode_lines_degen_triangle_02	2002Oct4
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556	r400su_polymode_lines_zero_area_triangle_02	2002Oct4
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558	r400su_polymode_points_degen_triangle_02	2002Oct4
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563	r400su_polymode_zero_area_triangle_02	2002Oct4
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565	r400su_polymode_zero_area_triangle_04	2002Oct4
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567	r400su_polymodefunctional_01	2002Oct4
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568	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct4
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592	r400su_z_grad_test_03	2002Oct4
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595	r400vgt_auto_index_line_strip_01	2002Oct4
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596	r400vgt_auto_index_points_01	2002Oct4

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597	r400vgt_auto_index_polygon_01	2002Oct4
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598	r400vgt_auto_index_primitives_01	2002Oct4
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603	r400vgt_auto_index_tri_list_01	2002Oct4
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604	r400vgt_auto_index_tri_strip_01	2002Oct4
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605	r400vgt_auto_index_tri_wflags_01	2002Oct4
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630	r400vgt_dma_swap_idx32_01	2002Oct4
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631	r400vgt_edgeflags_polygon_01	2002Oct4
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636	r400vgt_edgeflags_triangle_strip_01	2002Oct4
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637	r400vgt_edgeflags_triangle_wflags_01	2002Oct4
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638	r400vgt_event_handling_01	2002Sep26
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639	r400vgt_ext2int_index_line_list_01	2002Oct4
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650	r400vgt_ext2int_index_triangle_wflags_01	2002Oct4
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651	r400vgt_hos_LPatch_01	2002Oct4
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652	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Oct4
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653	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct4
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654	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct4
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655	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Oct4
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656	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Oct4
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657	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct4
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658	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct4
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659	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct4
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660	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct4
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661	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct4
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662	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct4
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663	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct4
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664	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct4
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665	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct4
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666	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct4
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667	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Oct4
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668	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct4

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669	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Oct4
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671	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Oct4
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672	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct4
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673	r400vgt_hos_PNT_edge_detection_01	2002Oct4
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674	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct4
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675	r400vgt_hos_RPatch_cp_02	2002Oct4
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676	r400vgt_hos_RPatch_lp_02	2002Oct4
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677	r400vgt_hos_TPatch_01	2002Oct4
PASS		
678	r400vgt_hos_TPatch_02	2002Oct4
INCOMPLETE		
679	r400vgt_hos_auto_index_line_list_01	2002Oct4
PASS		
680	r400vgt_hos_auto_index_quad_list_01	2002Oct4
PASS		
681	r400vgt_hos_auto_index_triangle_list_01	2002Oct4
PASS		
682	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Oct4
PASS		
683	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct4
PASS		
684	r400vgt_immed_index_line_list_01	2002Oct4
PASS		
685	r400vgt_immed_index_line_loop_01	2002Oct4
PASS		
686	r400vgt_immed_index_line_strip_01	2002Oct4
PASS		
687	r400vgt_immed_index_points_01	2002Oct4
PASS		
688	r400vgt_immed_index_polygon_01	2002Oct4
PASS		
689	r400vgt_immed_index_primitives_01	2002Oct4
PASS		
690	r400vgt_immed_index_quad_list_01	2002Oct4
PASS		
691	r400vgt_immed_index_quad_strip_01	2002Oct4
PASS		
692	r400vgt_immed_index_rectangle_list_01	2002Oct4

PASS		
693	r400vgt_immed_index_tri_fan_01	2002Oct4
PASS		
694	r400vgt_immed_index_tri_list_01	2002Oct4
PASS		
695	r400vgt_immed_index_tri_strip_01	2002Oct4
PASS		
696	r400vgt_immed_index_tri_wflags_01	2002Oct4
PASS		
697	r400vgt_index_dealloc_line_list_01	2002Oct4
PASS		
698	r400vgt_index_dealloc_points_01	2002Oct4
PASS		
699	r400vgt_index_dealloc_triangle_list_01	2002Oct4
PASS		
700	r400vgt_index_min_max_01	2002Oct4
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701	r400vgt_index_min_max_02	2002Oct4
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702	r400vgt_index_offset_01	2002Oct4
PASS		
703	r400vgt_index_offset_02	2002Oct4
PASS		
704	r400vgt_index_offset_03	2002Oct4
PASS		
705	r400vgt_index_offset_04	2002Oct4
PASS		
706	r400vgt_index_offset_05	2002Oct4
PASS		
707	r400vgt_index_offset_06	2002Oct4
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708	r400vgt_index_offset_07	2002Oct4
PASS		
709	r400vgt_index_offset_08	2002Oct4
PASS		
710	r400vgt_index_size_01	2002Oct4
PASS		
711	r400vgt_index_source_switch_01	2002Oct4
PASS		
712	r400vgt_line_list_01	2002Oct4
PASS		
713	r400vgt_line_list_02	2002Oct4
PASS		
714	r400vgt_line_loop_01	2002Oct4
PASS		
715	r400vgt_line_loop_02	2002Oct4
PASS		
716	r400vgt_line_strip_01	2002Oct4

PASS		
717	r400vgt_line_strip_02	2002Oct4
PASS		
718	r400vgt_multi_context_01	2002Oct4
PASS		
719	r400vgt_multi_context_02	2002Oct4
PASS		
720	r400vgt_multi_context_03	2002Oct4
PASS		
721	r400vgt_multi_context_04	2002Oct4
PASS		
722	r400vgt_multi_context_05	2002Oct4
PASS		
723	r400vgt_multi_context_06	2002Oct4
PASS		
724	r400vgt_multi_context_07	2002Oct4
PASS		
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PASS		
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727	r400vgt_multi_context_10	2002Oct4
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728	r400vgt_multi_context_11	2002Oct4
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729	r400vgt_multi_context_12	2002Oct4
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730	r400vgt_multi_prim_reset_index_all_01	2002Oct4
PASS		
731	r400vgt_multi_prim_reset_index_all_02	2002Oct4
PASS		
732	r400vgt_pass_thru_all_prims_01	2002Oct4
PASS		
733	r400vgt_point_list_01	2002Oct4
PASS		
734	r400vgt_point_list_02	2002Oct4
PASS		
735	r400vgt_polygon_01	2002Oct4
PASS		
736	r400vgt_polygon_02	2002Oct4
PASS		
737	r400vgt_provoking_vtx_all_01	2002Oct4
PASS		
738	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct4
PASS		
739	r400vgt_provoking_vtx_polygon_01	2002Oct4
PASS		
740	r400vgt_provoking_vtx_quad_list_01	2002Oct4

PASS			
741	r400vgt_provoking_vtx_quad_strip_01		2002Oct4
PASS			
742	r400vgt_provoking_vtx_tri_fan_01		2002Oct4
PASS			
743	r400vgt_provoking_vtx_tri_strip_01		2002Oct4
PASS			
744	r400vgt_quad_list_01		2002Oct4
PASS			
745	r400vgt_quad_list_02		2002Oct4
PASS			
746	r400vgt_quad_strip_01		2002Oct4
PASS			
747	r400vgt_quad_strip_02		2002Oct4
PASS			
748	r400vgt_rectangle_list_01		2002Oct4
PASS			
749	r400vgt_rectangle_list_02		2002Oct4
PASS			
750	r400vgt_reuse_depth_line_list_01		2002Oct4
PASS			
751	r400vgt_reuse_depth_point_list_01		2002Oct4
PASS			
752	r400vgt_reuse_depth_triangle_list_01		2002Oct4
PASS			
753	r400vgt_reuse_index_line_list_01		2002Oct4
PASS			
754	r400vgt_reuse_index_point_list_01		2002Oct4
PASS			
755	r400vgt_reuse_index_triangle_list_01		2002Oct4
PASS			
756	r400vgt_reuse_index_triangle_list_02		2002Oct4
PASS			
757	r400vgt_reuse_index_triangle_list_03		2002Oct4
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758	r400vgt_suppress_eop_01	0:0:1	2002Oct4
PASS			
759	r400vgt_suppress_eop_02	0:0:1	2002Oct4
PASS			
760	r400vgt_suppress_eop_03	0:0:1	2002Oct4
PASS			
761	r400vgt_suppress_eop_04	0:0:1	2002Oct4
PASS			
762	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
763	r400vgt_triangle_fan_01	0:0:1	2002Oct4
PASS			
764	r400vgt_triangle_fan_02	0:0:1	2002Oct4


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PASS
765 r400vgt_triangle_list_01          0:0:1    2002Oct4
PASS
766 r400vgt_triangle_list_02          0:0:1    2002Oct4
PASS
767 r400vgt_triangle_strip_01         0:0:1    2002Oct4
PASS
768 r400vgt_triangle_strip_02         0:0:1    2002Oct4
PASS
769 r400vgt_triangle_wflags_01        0:0:1    2002Oct4
PASS
770 r400vgt_triangle_wflags_02        0:0:1    2002Oct4
PASS
771 r400vgt_viz_query_01              0:0:1    2002Oct4
PASS
772 r400vgt_vtx_vector_packing_01     0:0:1    2002Oct4
PASS
773 r400vte_coverage_01               0:0:1    2002Oct4
PASS
774 r400vte_simple_point_01           0:0:1    2002Oct4
PASS
775 r400vte_simple_triangle_01        0:0:1    2002Oct4
PASS
776 r400vte_w0_fmt_01                 0:0:1    2002Oct4
PASS
777 r400vte_w0_fmt_03                 0:0:1    2002Oct4
PASS
778 r400vte_xy_fmt_01                 0:0:1    2002Oct4
PASS
779 r400vte_xy_fmt_mk_01_bug           0:0:1    2002Oct4
PASS
780 r400vte_xyz_scale_01               0:0:1    2002Oct4
PASS
781 r400vte_xyz_scale_02               0:0:1    2002Oct4
PASS
782 r400vte_z_fmt_01                  0:0:1    2002Oct4
PASS

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=====
Tests_Run_count = 782 Pass_count = 781 Fail_count = 0 Incomplete_count = 1
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```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Oct4
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Oct4
PASS			
3	cp_e2_one_blit		2002Oct4
PASS			
4	cp_e2_one_line		2002Oct4
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Oct4
PASS			
6	cp_e2paint_565		2002Oct4
PASS			
7	cp_e2paint_multi		2002Oct4
PASS			
8	cp_e2polyscanlines_brush		2002Oct4
PASS			
9	cp_e2polyscanlines_brush_mt		2002Oct4
PASS			
10	cp_e2smalltext_jcl		2002Oct4
PASS			
11	cp_simple_triangle		2002Oct4
PASS			
12	milestone_event_flush_tri1		2002Oct3
PASS			
13	milestone_event_flush_tri2		2002Oct3
PASS			
14	milestone_event_flush_tri3		2002Oct3
PASS			
15	milestone_event_flush_tri4		2002Oct3
PASS			
16	milestone_event_flush_tri5		2002Oct3
PASS			
17	milestone_event_flush_tri6		2002Oct3
PASS			
18	milestone_event_flush_tri7		2002Oct3
PASS			
19	milestone_event_flush_tri8		2002Oct3
PASS			
20	milestone_event_flush_tri9		2002Oct3

PASS		
21	milestone_event_randomized	2002Oct3
PASS		
22	milestone_event_standard	2002Oct3
PASS		
23	milestone_event_switch_flush_tri1	2002Oct3
PASS		
24	milestone_event_switch_flush_tri2	2002Oct3
PASS		
25	milestone_event_switch_flush_tri3	2002Oct3
PASS		
26	milestone_event_switch_flush_tri4	2002Oct3
PASS		
27	milestone_event_switch_flush_tri5	2002Oct3
PASS		
28	milestone_event_switch_flush_tri6	2002Oct3
PASS		
29	milestone_event_switch_flush_tri7	2002Oct3
PASS		
30	milestone_event_switch_flush_tri8	2002Oct3
PASS		
31	milestone_event_switch_flush_tri9	2002Oct3
PASS		
32	milestone_event_switch_tri2	2002Oct3
PASS		
33	milestone_event_switch_tri3	2002Oct3
PASS		
34	milestone_event_switch_tri4	2002Oct3
PASS		
35	milestone_event_switch_tri5	2002Oct3
PASS		
36	milestone_event_switch_tri6	2002Oct3
PASS		
37	milestone_event_switch_tri7	2002Oct3
PASS		
38	milestone_event_switch_tri8	2002Oct3
PASS		
39	milestone_event_switch_tri9	2002Oct3
PASS		
40	milestone_event_tri2	2002Oct3
PASS		
41	milestone_event_tri3	2002Oct3
PASS		
42	milestone_event_tri4	2002Oct3
PASS		
43	milestone_event_tri5	2002Oct3
PASS		
44	milestone_event_tri6	2002Oct3

PASS		
45	milestone_event_tri7	2002Oct3
PASS		
46	milestone_event_tri8	2002Oct3
PASS		
47	milestone_event_tri9	2002Oct3
PASS		
48	milestone_tri_random_rect	2002Oct3
PASS		
49	milestone_tri_random_tri	2002Oct3
PASS		
50	milestone_tri_randomized	2002Oct3
PASS		
51	milestone_tri_standard	2002Oct3
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52	milestone_tri_tril28_pix4	2002Oct3
PASS		
53	milestone_tri_tril_pix1	2002Oct3
PASS		
54	milestone_tri_tril_pix2	2002Oct3
PASS		
55	milestone_tri_tril_pix3	2002Oct3
PASS		
56	milestone_tri_tril_pix4	2002Oct3
PASS		
57	milestone_tri_tri24x24	2002Oct3
PASS		
58	milestone_tri_tri28x28	2002Oct3
PASS		
59	milestone_tri_tri2_pix1	2002Oct3
PASS		
60	milestone_tri_tri2_pix16	2002Oct3
PASS		
61	milestone_tri_tri2_pix4	2002Oct3
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62	milestone_tri_tri32_pix1	2002Oct3
PASS		
63	milestone_tri_tri32_pix4	2002Oct3
PASS		
64	milestone_tri_tri32x32	2002Oct3
PASS		
65	milestone_tri_tri36x36	2002Oct3
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66	milestone_tri_tri40x40	2002Oct3
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67	milestone_tri_tri44x44	2002Oct3
PASS		
68	milestone_tri_tri48x48	2002Oct3

PASS		
69	milestone_tri_tri52x52	2002Oct3
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70	milestone_tri_tri56x56	2002Oct3
PASS		
71	milestone_tri_tri60x60	2002Oct3
PASS		
72	milestone_tri_tri64x64	2002Oct3
PASS		
73	milestone_tri_tri8_pix4	2002Oct3
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74	primlib_1st_tri_june15	2002Oct4
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75	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct4
PASS		
76	primlib_gouraud_triangles_2_draw_passes	2002Oct4
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77	primlib_parameterized_simple_triangle	2002Oct4
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78	primlib_template_simple_triangle	2002Oct4
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79	primlib_tex_tri	2002Oct4
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80	r400cl_bary_texture_08_bug	2002Sep21
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81	r400cl_clip_10_verts_01	2002Oct4
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82	r400cl_clip_disable_01	2002Oct4
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83	r400cl_clip_space_dx_ogl_01	2002Oct4
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84	r400cl_clip_ucp_6bits_01	2002Oct4
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85	r400cl_cull_only_ena_01	2002Oct4
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86	r400cl_edgeflags_01	2002Oct4
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87	r400cl_edgeflags_01_bug	2002Oct4
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88	r400cl_edgeflags_frustum_bottom_01	2002Oct4
PASS		
89	r400cl_edgeflags_frustum_far_01	2002Oct4
PASS		
90	r400cl_edgeflags_frustum_left_01	2002Oct4
PASS		
91	r400cl_edgeflags_frustum_near_01	2002Oct4
PASS		
92	r400cl_edgeflags_frustum_right_01	2002Oct4

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93	r400cl_edgeflags_frustum_top_01	2002Oct4
PASS		
94	r400cl_edgeflags_gband_01	2002Oct4
PASS		
95	r400cl_edgeflags_gband_bottom_01	2002Oct4
PASS		
96	r400cl_edgeflags_gband_left_01	2002Oct4
PASS		
97	r400cl_edgeflags_gband_right_01	2002Oct4
PASS		
98	r400cl_edgeflags_gband_top_01	2002Oct4
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99	r400cl_frustum_01	2002Oct4
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100	r400cl_frustum_02	2002Oct4
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102	r400cl_frustum_04	2002Oct4
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126	r400cl_frustum_28	2002Oct4
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127	r400cl_frustum_all_vols_lines	2002Oct4
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128	r400cl_frustum_all_vols_tris	2002Oct4
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129	r400cl_frustum_lines_01	2002Oct4
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130	r400cl_frustum_lines_02	2002Oct4
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131	r400cl_frustum_lines_03	2002Oct4
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154	r400cl_gband_tcl_01	2002Oct4
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156	r400cl_pointlist_vertex_state_ucp_01	2002Oct4
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157	r400cl_polymode_line_fill_01_bug	2002Oct4
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158	r400cl_simple_triangle_01	2002Oct4
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159	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct4
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160	r400cl_ucp_combos_01	2002Oct4
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161	r400cl_ucp_combos_02	2002Oct4
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163	r400cl_ucp_combos_04	2002Oct4
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189	r400cl_ucp_combos_30	2002Oct4
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193	r400cl_vtx_kill_02	2002Oct4
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194	r400cl_w_eq_0	2002Oct4
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195	r400rbm_simple_triangle_01	2002Oct4
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196	r400sanity_persp_projection_01	2002Oct4
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197	r400sanity_vfd_texture_sample_01	2002Oct4
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198	r400sc_baryc_01	2002Oct4
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199	r400sc_bres_cntl_01	2002Sep7
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200	r400sc_bres_cntl_02	2002Oct4
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201	r400sc_bres_cntl_03	2002Oct4
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203	r400sc_bres_cntl_w2k_01	2002Oct4
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204	r400sc_bres_cntl_w9x_01	2002Oct4
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205	r400sc_clip_rect_01	2002Oct4
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206	r400sc_clip_rect_02	2002Oct4
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207	r400sc_clip_rect_03	2002Oct4
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208	r400sc_clip_rect_04	2002Oct4
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209	r400sc_clip_rect_fc_01	2002Oct4
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210	r400sc_diamond_exit_01	2002Oct4
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211	r400sc_diamond_exit_02	2002Oct4
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212	r400sc_diamond_exit_03	2002Oct4

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214	r400sc_diamond_exit_05	2002Oct4
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216	r400sc_jss_1x2_01	2002Oct4
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217	r400sc_jss_1x2_02	2002Oct4
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218	r400sc_jss_1x2_primtypes_01	2002Oct4
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219	r400sc_jss_1x3_01	2002Oct4
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220	r400sc_jss_1x3_02	2002Oct4
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221	r400sc_jss_1x3_primtypes_01	2002Oct4
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222	r400sc_jss_1x4_01	2002Oct4
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223	r400sc_jss_1x4_02	2002Oct4
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224	r400sc_jss_1x4_primtypes_01	2002Oct4
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225	r400sc_jss_2x1_01	2002Oct4
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226	r400sc_jss_2x1_02	2002Oct4
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227	r400sc_jss_2x1_primtypes_01	2002Oct4
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228	r400sc_jss_2x2_01	2002Oct4
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229	r400sc_jss_2x2_02	2002Oct4
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230	r400sc_jss_2x2_primtypes_01	2002Oct4
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231	r400sc_jss_2x3_01	2002Oct4
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232	r400sc_jss_2x3_02	2002Oct4
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233	r400sc_jss_2x3_primtypes_01	2002Oct4
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234	r400sc_jss_2x4_01	2002Oct4
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235	r400sc_jss_2x4_02	2002Oct4
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236	r400sc_jss_2x4_primtypes_01	2002Oct4

PASS		
237	r400sc_jss_3x1_01	2002Oct4
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238	r400sc_jss_3x1_02	2002Oct4
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239	r400sc_jss_3x1_primtypes_01	2002Oct4
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240	r400sc_jss_3x2_01	2002Oct4
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241	r400sc_jss_3x2_02	2002Oct4
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242	r400sc_jss_3x2_primtypes_01	2002Oct4
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243	r400sc_jss_3x3_01	2002Oct4
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244	r400sc_jss_3x3_02	2002Oct4
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247	r400sc_jss_3x4_02	2002Oct4
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248	r400sc_jss_3x4_03	2002Oct4
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249	r400sc_jss_3x4_primtypes_01	2002Oct4
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250	r400sc_jss_4x1_01	2002Oct4
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251	r400sc_jss_4x1_02	2002Oct4
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254	r400sc_jss_4x2_02	2002Oct4
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255	r400sc_jss_4x2_primtypes_01	2002Oct4
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256	r400sc_jss_4x3_01	2002Oct4
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258	r400sc_jss_4x3_primtypes_01	2002Oct4
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260	r400sc_jss_4x4_02	2002Oct4

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262	r400sc_jss_4x4_aa_mask_01	2002Oct4
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263	r400sc_jss_4x4_aa_mask_02	2002Oct4
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264	r400sc_jss_4x4_fc_01	2002Oct4
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265	r400sc_jss_4x4_fc_02	2002Oct4
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266	r400sc_jss_4x4_max_dist_01	2002Oct4
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267	r400sc_jss_4x4_primitives_01	2002Oct4
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268	r400sc_line_dx10_eq_0_01	2002Oct4
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269	r400sc_line_dx10_ge_0_01	2002Oct4
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270	r400sc_line_dx10_lt_0_01	2002Oct4
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271	r400sc_line_dy10_eq_0_01	2002Oct4
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272	r400sc_line_dy10_ge_0_01	2002Oct4
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273	r400sc_line_dy10_lt_0_01	2002Oct4
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274	r400sc_line_expand_width_msa_8_01	2002Oct4
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275	r400sc_line_expand_width_msa_8_02	2002Oct4
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276	r400sc_line_expand_width_msa_8_03	2002Oct4
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277	r400sc_line_jss_3x4_01	2002Oct4
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278	r400sc_line_list_01	2002Oct4
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279	r400sc_line_list_02	2002Oct4
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280	r400sc_line_list_concentric_circle_01	2002Oct4
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281	r400sc_line_list_concentric_circle_02	2002Oct4
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282	r400sc_line_list_concentric_circle_03	2002Oct4
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283	r400sc_line_list_textured_01	2002Oct4
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284	r400sc_line_list_verify_st_01	2002Oct4

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285	r400sc_line_list_verify_st_01_bug	2002Oct4
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286	r400sc_line_msaa_8_01	2002Oct4
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287	r400sc_line_msaa_8_textured_01	2002Oct4
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288	r400sc_line_stipple_01	2002Oct4
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289	r400sc_line_stipple_02	2002Oct4
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312	r400sc_line_strip_stipple_01	2002Oct4
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313	r400sc_msa_1_01	2002Oct4
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324	r400sc_msa_1_zbuffer_rectangle_list_02	2002Oct4
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358	r400sc_msaa_4_zbuffer_rectangle_list_02	2002Oct4
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376	r400sc_msaa_8_aa_mask_01	2002Oct4
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377	r400sc_msaa_8_aa_mask_02	2002Oct4
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378	r400sc_msaa_8_aa_mask_fc_02	2002Oct4
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379	r400sc_msaa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct4
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380	r400sc_msaa_8_primitives_01	2002Oct4

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389	r400sc_msaa_8_zbuffer_rectangle_list_01	2002Oct4
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390	r400sc_msaa_8_zbuffer_rectangle_list_02	2002Oct4
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392	r400sc_msaa_X_primitives_02_bug	2002Oct4
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393	r400sc_multi_chip_prim_discard_00_07	2002Oct4
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394	r400sc_null_triangles_01	2002Oct4
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395	r400sc_null_triangles_fc_01	2002Oct4
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396	r400sc_pinwheel_01	2002Oct4
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397	r400sc_pinwheel_02	2002Oct4
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398	r400sc_pinwheel_02_bug	2002Oct4
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399	r400sc_point_jss_3x4_01	2002Oct4
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406	r400sc_point_list_06_bug	2002Oct4
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423	r400sc_poly_offset_fc_01	2002Oct4
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424	r400sc_polygon_stipple_01	2002Oct4
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425	r400sc_polymode_tri_fill_01	2002Oct4
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426	r400sc_polymode_tri_fill_01_bug	2002Oct4
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427	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct4
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428	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct4

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434	r400sc_prsp_byc_intrp_ref_pix_08	2002Oct4
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437	r400sc_raster_fill_rule_03	2002Oct4
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449	r400sc_scissor_rect_01	2002Oct4
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454	r400sc_scissor_rect_fc_01	2002Oct4
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455	r400sc_set_state_01	2002Oct4
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456	r400sc_sp_sample_cntl_01	2002Oct4
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457	r400sc_sp_sample_cntl_02	2002Oct4
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458	r400sc_sp_sample_cntl_03	2002Oct4
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459	r400sc_sp_sample_cntl_04	2002Oct4
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462	r400sc_sp_sample_cntl_07	2002Oct4
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463	r400sc_sp_sample_cntl_08	2002Oct4
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464	r400sc_tri_16_par_64_dwords_01	2002Oct4
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465	r400sc_tri_8textures_01	2002Oct4
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466	r400sc_tri_8textures_02	2002Oct4
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467	r400sc_tri_walk_start_vertex_01	2002Oct4
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468	r400sc_tri_walk_start_vertex_02	2002Oct4
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469	r400sc_tri_walk_start_vertex_03	2002Oct4
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470	r400sc_tri_walk_start_vertex_04	2002Oct4
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482	r400sc_tri_walk_start_vertex_16	2002Oct4
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491	r400sc_zbuffer_point_list_01	2002Oct4
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492	r400sc_zbuffer_rectangle_list_01	2002Oct4
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493	r400sc_zbuffer_rectangle_list_02	2002Oct4
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494	r400sc_zbuffer_rectangle_list_fc_02	2002Oct4
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495	r400sc_zbuffer_triangle_list_01	2002Oct4
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496	r400su_4tri_text_offscreen_01	2002Oct4
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497	r400su_baryc_test_01	2002Oct4
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502 r400su_clip_baryc_test_02 2002Oct4
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503 r400su_clip_edgeflag_polymode_01 2002Oct4
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504 r400su_clip_pointsize_test_01 2002Oct4
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505 r400su_clip_pointtest_01 2002Oct4
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506 r400su_clip_pointtest_02 2002Oct4
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510 r400su_clipline_01 2002Oct4
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515 r400su_degentri_test_02 2002Oct4
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516 r400su_degentri_test_03 2002Oct4
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517 r400su_edge_flag_01 2002Oct4
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518 r400su_edge_flag_02 2002Oct4
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519 r400su_edgeflags_triangle_01 2002Oct4
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520 r400su_edgeflags_triangle_02 2002Oct4
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521 r400su_geom_sort_01 2002Oct4
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524 r400su_line_orientation_01 2002Oct4

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528	r400su_line_orientation_dy01_01	2002Oct4
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529	r400su_line_orientation_dy01_02	2002Oct4
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535	r400su_multi_prim_02	2002Oct4
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537	r400su_parallel_orientation_all_02	2002Oct4
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538	r400su_pc_management_01	2002Oct4
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548	r400su_point_sprite_08	2002Oct4

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554	r400su_polymode_lines_degen_triangle_02	2002Oct4
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565	r400su_polymode_zero_area_triangle_04	2002Oct4
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566	r400su_polymodeculling_01	2002Oct4
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567	r400su_polymodefunctional_01	2002Oct4
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568	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct4
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569	r400su_provoking_vtx_edgeflags_triangle_02	2002Oct4
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571	r400su_provoking_vtx_edgeflags_triangle_04	2002Oct4
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572	r400su_provoking_vtx_line_01	2002Oct4

PASS		
573	r400su_provoking_vtx_point_01	2002Oct4
PASS		
574	r400su_provoking_vtx_polymode_rectangle_01	2002Oct4
PASS		
575	r400su_provoking_vtx_rectangle_01	2002Oct4
PASS		
576	r400su_provoking_vtx_triangle_01	2002Oct4
PASS		
577	r400su_rand_line_01	2002Oct4
PASS		
578	r400su_rand_point_01	2002Oct4
PASS		
579	r400su_rand_tri_01	2002Oct4
PASS		
580	r400su_rectangle_list_01	2002Oct4
PASS		
581	r400su_sliver_01	2002Oct4
PASS		
582	r400su_triarea_test_01	2002Oct4
PASS		
583	r400su_triarea_test_02	2002Oct4
PASS		
584	r400su_triarea_test_03	2002Oct4
PASS		
585	r400su_triarea_test_04	2002Oct4
PASS		
586	r400su_vertexsortingfunctional_01	2002Oct4
PASS		
587	r400su_w_grad_test_01	2002Oct4
PASS		
588	r400su_w_grad_test_02	2002Oct4
PASS		
589	r400su_w_grad_test_03	2002Oct4
PASS		
590	r400su_z_grad_test_01	2002Oct4
PASS		
591	r400su_z_grad_test_02	2002Oct4
PASS		
592	r400su_z_grad_test_03	2002Oct4
PASS		
593	r400vgt_auto_index_line_list_01	2002Oct4
PASS		
594	r400vgt_auto_index_line_loop_01	2002Oct4
PASS		
595	r400vgt_auto_index_line_strip_01	2002Oct4
PASS		
596	r400vgt_auto_index_points_01	2002Oct4

PASS		
597	r400vgt_auto_index_polygon_01	2002Oct4
PASS		
598	r400vgt_auto_index_primitives_01	2002Oct4
PASS		
599	r400vgt_auto_index_quad_list_01	2002Oct4
PASS		
600	r400vgt_auto_index_quad_strip_01	2002Oct4
PASS		
601	r400vgt_auto_index_rectangle_list_01	2002Oct4
PASS		
602	r400vgt_auto_index_tri_fan_01	2002Oct4
PASS		
603	r400vgt_auto_index_tri_list_01	2002Oct4
PASS		
604	r400vgt_auto_index_tri_strip_01	2002Oct4
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605	r400vgt_auto_index_tri_wflags_01	2002Oct4
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606	r400vgt_dma_engine_01	2002Oct4
PASS		
607	r400vgt_dma_engine_02	2002Oct4
PASS		
608	r400vgt_dma_engine_03	2002Oct4
PASS		
609	r400vgt_dma_engine_04	2002Oct4
PASS		
610	r400vgt_dma_engine_05	2002Oct4
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611	r400vgt_dma_engine_06	2002Oct4
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612	r400vgt_dma_engine_07	2002Oct4
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613	r400vgt_dma_engine_08	2002Oct4
PASS		
614	r400vgt_dma_engine_09	2002Oct4
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615	r400vgt_dma_engine_10	2002Oct4
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616	r400vgt_dma_index_line_list_01	2002Oct4
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617	r400vgt_dma_index_line_loop_01	2002Oct4
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618	r400vgt_dma_index_line_strip_01	2002Oct4
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619	r400vgt_dma_index_points_01	2002Oct4
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620	r400vgt_dma_index_polygon_01	2002Oct4

PASS		
621	r400vgt_dma_index_primitives_01	2002Oct4
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623	r400vgt_dma_index_quad_strip_01	2002Oct4
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624	r400vgt_dma_index_rectangle_list_01	2002Oct4
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625	r400vgt_dma_index_tri_fan_01	2002Oct4
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626	r400vgt_dma_index_tri_list_01	2002Oct4
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627	r400vgt_dma_index_tri_strip_01	2002Oct4
PASS		
628	r400vgt_dma_index_tri_wflags_01	2002Oct4
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629	r400vgt_dma_swap_idx16_01	2002Oct4
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630	r400vgt_dma_swap_idx32_01	2002Oct4
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631	r400vgt_edgeflags_polygon_01	2002Oct4
PASS		
632	r400vgt_edgeflags_quad_list_01	2002Oct4
PASS		
633	r400vgt_edgeflags_quad_strip_01	2002Oct4
PASS		
634	r400vgt_edgeflags_triangle_fan_01	2002Oct4
PASS		
635	r400vgt_edgeflags_triangle_list_01	2002Oct4
PASS		
636	r400vgt_edgeflags_triangle_strip_01	2002Oct4
PASS		
637	r400vgt_edgeflags_triangle_wflags_01	2002Oct4
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638	r400vgt_event_handling_01	2002Sep26
PASS		
639	r400vgt_ext2int_index_line_list_01	2002Oct4
PASS		
640	r400vgt_ext2int_index_line_loop_01	2002Oct4
PASS		
641	r400vgt_ext2int_index_line_strip_01	2002Oct4
PASS		
642	r400vgt_ext2int_index_points_01	2002Oct4
PASS		
643	r400vgt_ext2int_index_polygon_01	2002Oct4
PASS		
644	r400vgt_ext2int_index_quad_list_01	2002Oct4

PASS		
645	r400vgt_ext2int_index_quad_strip_01	2002Oct4
PASS		
646	r400vgt_ext2int_index_rectangle_list_01	2002Oct4
PASS		
647	r400vgt_ext2int_index_triangle_fan_01	2002Oct4
PASS		
648	r400vgt_ext2int_index_triangle_list_01	2002Oct4
PASS		
649	r400vgt_ext2int_index_triangle_strip_01	2002Oct4
PASS		
650	r400vgt_ext2int_index_triangle_wflags_01	2002Oct4
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651	r400vgt_hos_LPatch_01	2002Oct4
PASS		
652	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Oct4
PASS		
653	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct4
PASS		
654	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct4
PASS		
655	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Oct4
PASS		
656	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Oct4
PASS		
657	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct4
PASS		
658	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct4
PASS		
659	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct4
PASS		
660	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct4
PASS		
661	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct4
PASS		
662	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct4
PASS		
663	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct4
PASS		
664	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct4
PASS		
665	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct4
PASS		
666	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct4
PASS		
667	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Oct4
PASS		
668	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct4

PASS		
669	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Oct4
PASS		
670	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Oct4
PASS		
671	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Oct4
PASS		
672	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct4
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673	r400vgt_hos_PNT_edge_detection_01	2002Oct4
PASS		
674	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct4
PASS		
675	r400vgt_hos_RPatch_cp_02	2002Oct4
PASS		
676	r400vgt_hos_RPatch_lp_02	2002Oct4
PASS		
677	r400vgt_hos_TPatch_01	2002Oct4
PASS		
678	r400vgt_hos_TPatch_02	2002Oct4
INCOMPLETE		
679	r400vgt_hos_auto_index_line_list_01	2002Oct4
PASS		
680	r400vgt_hos_auto_index_quad_list_01	2002Oct4
PASS		
681	r400vgt_hos_auto_index_triangle_list_01	2002Oct4
PASS		
682	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Oct4
PASS		
683	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct4
PASS		
684	r400vgt_immed_index_line_list_01	2002Oct4
PASS		
685	r400vgt_immed_index_line_loop_01	2002Oct4
PASS		
686	r400vgt_immed_index_line_strip_01	2002Oct4
PASS		
687	r400vgt_immed_index_points_01	2002Oct4
PASS		
688	r400vgt_immed_index_polygon_01	2002Oct4
PASS		
689	r400vgt_immed_index_primitives_01	2002Oct4
PASS		
690	r400vgt_immed_index_quad_list_01	2002Oct4
PASS		
691	r400vgt_immed_index_quad_strip_01	2002Oct4
PASS		
692	r400vgt_immed_index_rectangle_list_01	2002Oct4

PASS		
693	r400vgt_immed_index_tri_fan_01	2002Oct4
PASS		
694	r400vgt_immed_index_tri_list_01	2002Oct4
PASS		
695	r400vgt_immed_index_tri_strip_01	2002Oct4
PASS		
696	r400vgt_immed_index_tri_wflags_01	2002Oct4
PASS		
697	r400vgt_index_dealloc_line_list_01	2002Oct4
PASS		
698	r400vgt_index_dealloc_points_01	2002Oct4
PASS		
699	r400vgt_index_dealloc_triangle_list_01	2002Oct4
PASS		
700	r400vgt_index_min_max_01	2002Oct4
PASS		
701	r400vgt_index_min_max_02	2002Oct4
PASS		
702	r400vgt_index_offset_01	2002Oct4
PASS		
703	r400vgt_index_offset_02	2002Oct4
PASS		
704	r400vgt_index_offset_03	2002Oct4
PASS		
705	r400vgt_index_offset_04	2002Oct4
PASS		
706	r400vgt_index_offset_05	2002Oct4
PASS		
707	r400vgt_index_offset_06	2002Oct4
PASS		
708	r400vgt_index_offset_07	2002Oct4
PASS		
709	r400vgt_index_offset_08	2002Oct4
PASS		
710	r400vgt_index_size_01	2002Oct4
PASS		
711	r400vgt_index_source_switch_01	2002Oct4
PASS		
712	r400vgt_line_list_01	2002Oct4
PASS		
713	r400vgt_line_list_02	2002Oct4
PASS		
714	r400vgt_line_loop_01	2002Oct4
PASS		
715	r400vgt_line_loop_02	2002Oct4
PASS		
716	r400vgt_line_strip_01	2002Oct4

PASS		
717	r400vgt_line_strip_02	2002Oct4
PASS		
718	r400vgt_multi_context_01	2002Oct4
PASS		
719	r400vgt_multi_context_02	2002Oct4
PASS		
720	r400vgt_multi_context_03	2002Oct4
PASS		
721	r400vgt_multi_context_04	2002Oct4
PASS		
722	r400vgt_multi_context_05	2002Oct4
PASS		
723	r400vgt_multi_context_06	2002Oct4
PASS		
724	r400vgt_multi_context_07	2002Oct4
PASS		
725	r400vgt_multi_context_08	2002Oct4
PASS		
726	r400vgt_multi_context_09	2002Oct4
PASS		
727	r400vgt_multi_context_10	2002Oct4
PASS		
728	r400vgt_multi_context_11	2002Oct4
PASS		
729	r400vgt_multi_context_12	2002Oct4
PASS		
730	r400vgt_multi_prim_reset_index_all_01	2002Oct4
PASS		
731	r400vgt_multi_prim_reset_index_all_02	2002Oct4
PASS		
732	r400vgt_pass_thru_all_prims_01	2002Oct4
PASS		
733	r400vgt_point_list_01	2002Oct4
PASS		
734	r400vgt_point_list_02	2002Oct4
PASS		
735	r400vgt_polygon_01	2002Oct4
PASS		
736	r400vgt_polygon_02	2002Oct4
PASS		
737	r400vgt_provoking_vtx_all_01	2002Oct4
PASS		
738	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct4
PASS		
739	r400vgt_provoking_vtx_polygon_01	2002Oct4
PASS		
740	r400vgt_provoking_vtx_quad_list_01	2002Oct4

PASS			
741	r400vgt_provoking_vtx_quad_strip_01		2002Oct4
PASS			
742	r400vgt_provoking_vtx_tri_fan_01		2002Oct4
PASS			
743	r400vgt_provoking_vtx_tri_strip_01		2002Oct4
PASS			
744	r400vgt_quad_list_01		2002Oct4
PASS			
745	r400vgt_quad_list_02		2002Oct4
PASS			
746	r400vgt_quad_strip_01		2002Oct4
PASS			
747	r400vgt_quad_strip_02		2002Oct4
PASS			
748	r400vgt_rectangle_list_01		2002Oct4
PASS			
749	r400vgt_rectangle_list_02		2002Oct4
PASS			
750	r400vgt_reuse_depth_line_list_01		2002Oct4
PASS			
751	r400vgt_reuse_depth_point_list_01		2002Oct4
PASS			
752	r400vgt_reuse_depth_triangle_list_01		2002Oct4
PASS			
753	r400vgt_reuse_index_line_list_01		2002Oct4
PASS			
754	r400vgt_reuse_index_point_list_01		2002Oct4
PASS			
755	r400vgt_reuse_index_triangle_list_01		2002Oct4
PASS			
756	r400vgt_reuse_index_triangle_list_02		2002Oct4
PASS			
757	r400vgt_reuse_index_triangle_list_03		2002Oct4
PASS			
758	r400vgt_suppress_eop_01	0:0:1	2002Oct4
PASS			
759	r400vgt_suppress_eop_02	0:0:1	2002Oct4
PASS			
760	r400vgt_suppress_eop_03	0:0:1	2002Oct4
PASS			
761	r400vgt_suppress_eop_04	0:0:1	2002Oct4
PASS			
762	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
763	r400vgt_triangle_fan_01	0:0:1	2002Oct4
PASS			
764	r400vgt_triangle_fan_02	0:0:1	2002Oct4

```

PASS
765 r400vgt_triangle_list_01          0:0:1    2002Oct4
PASS
766 r400vgt_triangle_list_02          0:0:1    2002Oct4
PASS
767 r400vgt_triangle_strip_01         0:0:1    2002Oct4
PASS
768 r400vgt_triangle_strip_02         0:0:1    2002Oct4
PASS
769 r400vgt_triangle_wflags_01        0:0:1    2002Oct4
PASS
770 r400vgt_triangle_wflags_02        0:0:1    2002Oct4
PASS
771 r400vgt_viz_query_01              0:0:1    2002Oct4
PASS
772 r400vgt_vtx_vector_packing_01     0:0:1    2002Oct4
PASS
773 r400vte_coverage_01               0:0:1    2002Oct4
PASS
774 r400vte_simple_point_01           0:0:1    2002Oct4
PASS
775 r400vte_simple_triangle_01        0:0:1    2002Oct4
PASS
776 r400vte_w0_fmt_01                 0:0:1    2002Oct4
PASS
777 r400vte_w0_fmt_03                 0:0:1    2002Oct4
PASS
778 r400vte_xy_fmt_01                 0:0:1    2002Oct4
PASS
779 r400vte_xy_fmt_mk_01_bug          0:0:1    2002Oct4
PASS
780 r400vte_xyz_scale_01              0:0:1    2002Oct4
PASS
781 r400vte_xyz_scale_02              0:0:1    2002Oct4
PASS
782 r400vte_z_fmt_01                  0:0:1    2002Oct4
PASS

```

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=====
Tests_Run_count = 782 Pass_count = 781 Fail_count = 0 Incomplete_count = 1
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep20
PASS			
2	cp_e2blit_brush_mt_ropcc		2002Sep20
PASS			
3	cp_e2_hostdata_blt_pntr_8888		2002Sep20
PASS			
4	cp_e2_one_blit		2002Sep20
PASS			
5	cp_e2_one_line		2002Sep20
PASS			
6	cp_e2paint_565		2002Sep20
PASS			
7	cp_e2paint_multi		2002Sep20
PASS			
8	cp_e2polyscanlines_brush		2002Sep20
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep20
PASS			
10	cp_e2smalltext_jcl		2002Sep20
PASS			
11	cp_simple_triangle		2002Sep20
PASS			
12	primlib_1st_tri_june15		2002Sep20
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep20
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep20
PASS			
15	primlib_parameterized_simple_triangle		2002Sep20
PASS			
16	primlib_template_simple_triangle		2002Sep20
PASS			
17	primlib_tex_tri		2002Sep20
PASS			
18	r400cl_bary_texture_08_bug		2002Sep20
PASS			
19	r400cl_clip_10_verts_01		2002Sep20
PASS			
20	r400cl_clip_disable_01		2002Sep20

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep20
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep20
PASS		
23	r400cl_cull_only_ena_01	2002Sep20
PASS		
24	r400cl_edgeflags_01	2002Sep20
PASS		
25	r400cl_edgeflags_01_bug	2002Sep20
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep20
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep20
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep20
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep20
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep20
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep20
PASS		
32	r400cl_edgeflags_gband_01	2002Sep20
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep20
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep20
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep20
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep20
PASS		
37	r400cl_frustum_01	2002Sep20
PASS		
38	r400cl_frustum_02	2002Sep20
PASS		
39	r400cl_frustum_03	2002Sep20
PASS		
40	r400cl_frustum_04	2002Sep20
PASS		
41	r400cl_frustum_05	2002Sep20
PASS		
42	r400cl_frustum_06	2002Sep20
PASS		
43	r400cl_frustum_07	2002Sep20
PASS		
44	r400cl_frustum_08	2002Sep20

PASS		
45	r400cl_frustum_09	2002Sep20
PASS		
46	r400cl_frustum_10	2002Sep20
PASS		
47	r400cl_frustum_11	2002Sep20
PASS		
48	r400cl_frustum_12	2002Sep20
PASS		
49	r400cl_frustum_13	2002Sep20
PASS		
50	r400cl_frustum_14	2002Sep20
PASS		
51	r400cl_frustum_15	2002Sep20
PASS		
52	r400cl_frustum_16	2002Sep20
PASS		
53	r400cl_frustum_17	2002Sep20
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54	r400cl_frustum_18	2002Sep20
PASS		
55	r400cl_frustum_19	2002Sep20
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56	r400cl_frustum_20	2002Sep20
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57	r400cl_frustum_21	2002Sep20
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58	r400cl_frustum_22	2002Sep20
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59	r400cl_frustum_23	2002Sep20
PASS		
60	r400cl_frustum_24	2002Sep20
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61	r400cl_frustum_25	2002Sep20
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62	r400cl_frustum_26	2002Sep20
PASS		
63	r400cl_frustum_27	2002Sep20
PASS		
64	r400cl_frustum_28	2002Sep20
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep20
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep20
PASS		
67	r400cl_frustum_lines_01	2002Sep20
PASS		
68	r400cl_frustum_lines_02	2002Sep20

PASS		
69	r400cl_frustum_lines_03	2002Sep20
PASS		
70	r400cl_frustum_lines_04	2002Sep20
PASS		
71	r400cl_frustum_lines_05	2002Sep20
PASS		
72	r400cl_frustum_lines_06	2002Sep20
PASS		
73	r400cl_frustum_lines_07	2002Sep20
PASS		
74	r400cl_frustum_lines_08	2002Sep20
PASS		
75	r400cl_frustum_lines_09	2002Sep20
PASS		
76	r400cl_frustum_lines_10	2002Sep20
PASS		
77	r400cl_frustum_lines_11	2002Sep20
PASS		
78	r400cl_frustum_lines_12	2002Sep20
PASS		
79	r400cl_frustum_lines_13	2002Sep20
PASS		
80	r400cl_frustum_lines_14	2002Sep20
PASS		
81	r400cl_frustum_lines_15	2002Sep20
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82	r400cl_frustum_lines_16	2002Sep20
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83	r400cl_frustum_lines_17	2002Sep20
PASS		
84	r400cl_frustum_lines_18	2002Sep20
PASS		
85	r400cl_frustum_lines_19	2002Sep20
PASS		
86	r400cl_frustum_lines_20	2002Sep20
PASS		
87	r400cl_frustum_lines_21	2002Sep20
PASS		
88	r400cl_frustum_lines_22	2002Sep20
PASS		
89	r400cl_frustum_lines_23	2002Sep20
PASS		
90	r400cl_frustum_lines_24	2002Sep20
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91	r400cl_gband_04	2002Sep20
PASS		
92	r400cl_gband_tcl_01	2002Sep20

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93	r400cl_nan_kill_01_bug	2002Sep20
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Sep20
PASS		
95	r400cl_polymode_line_fill_01_bug	2002Sep20
PASS		
96	r400cl_simple_triangle_01	2002Sep20
PASS		
97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep20
PASS		
98	r400cl_ucp_combos_01	2002Sep20
PASS		
99	r400cl_ucp_combos_02	2002Sep20
PASS		
100	r400cl_ucp_combos_03	2002Sep20
PASS		
101	r400cl_ucp_combos_04	2002Sep20
PASS		
102	r400cl_ucp_combos_05	2002Sep20
PASS		
103	r400cl_ucp_combos_06	2002Sep20
PASS		
104	r400cl_ucp_combos_07	2002Sep20
PASS		
105	r400cl_ucp_combos_08	2002Sep20
PASS		
106	r400cl_ucp_combos_09	2002Sep20
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107	r400cl_ucp_combos_10	2002Sep20
PASS		
108	r400cl_ucp_combos_11	2002Sep20
PASS		
109	r400cl_ucp_combos_12	2002Sep20
PASS		
110	r400cl_ucp_combos_13	2002Sep20
PASS		
111	r400cl_ucp_combos_14	2002Sep20
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447	r400su_point_sprite_02	2002Sep20
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448	r400su_point_sprite_03	2002Sep20
PASS		
449	r400su_point_sprite_04	2002Sep20
PASS		
450	r400su_point_sprite_05	2002Sep20
PASS		
451	r400su_point_sprite_06	2002Sep20
PASS		
452	r400su_point_sprite_07	2002Sep20

PASS		
453	r400su_point_sprite_08	2002Sep20
PASS		
454	r400su_point_wl6_h1_functional_01	2002Sep20
PASS		
455	r400su_point_wl_h16_functional_01	2002Sep20
PASS		
456	r400su_polymodeculling_01	2002Sep20
PASS		
457	r400su_polymodefunctional_01	2002Sep20
PASS		
458	r400su_polymode_lines_degen_triangle_01	2002Sep20
PASS		
459	r400su_polymode_lines_degen_triangle_02	2002Sep20
PASS		
460	r400su_polymode_lines_zero_area_triangle_01	2002Sep20
PASS		
461	r400su_polymode_lines_zero_area_triangle_02	2002Sep20
PASS		
462	r400su_polymode_points_degen_triangle_01	2002Sep20
PASS		
463	r400su_polymode_points_degen_triangle_02	2002Sep20
PASS		
464	r400su_polymode_points_zero_area_triangle_01	2002Sep20
PASS		
465	r400su_polymode_points_zero_area_triangle_02	2002Sep20
PASS		
466	r400su_polymode_rectangle_01	2002Sep20
PASS		
467	r400su_polymode_zero_area_triangle_01	2002Sep20
PASS		
468	r400su_polymode_zero_area_triangle_02	2002Sep20
PASS		
469	r400su_polymode_zero_area_triangle_03	2002Sep20
PASS		
470	r400su_polymode_zero_area_triangle_04	2002Sep20
PASS		
471	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep20
PASS		
472	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep20
PASS		
473	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep20
PASS		
474	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep20
PASS		
475	r400su_provoking_vtx_line_01	2002Sep20
PASS		
476	r400su_provoking_vtx_point_01	2002Sep20

PASS		
477	r400su_provoking_vtx_polymode_rectangle_01	2002Sep20
PASS		
478	r400su_provoking_vtx_rectangle_01	2002Sep20
PASS		
479	r400su_provoking_vtx_triangle_01	2002Sep20
PASS		
480	r400su_rand_line_01	2002Sep20
PASS		
481	r400su_rand_point_01	2002Sep20
PASS		
482	r400su_rand_tri_01	2002Sep20
PASS		
483	r400su_rectangle_list_01	2002Sep20
PASS		
484	r400su_sliver_01	2002Sep20
PASS		
485	r400su_triarea_test_01	2002Sep20
PASS		
486	r400su_triarea_test_02	2002Sep20
PASS		
487	r400su_triarea_test_03	2002Sep20
PASS		
488	r400su_triarea_test_04	2002Sep20
PASS		
489	r400su_vertexsortingfunctional_01	2002Sep20
PASS		
490	r400vgt_auto_index_line_list_01	2002Sep20
PASS		
491	r400vgt_auto_index_line_loop_01	2002Sep20
PASS		
492	r400vgt_auto_index_line_strip_01	2002Sep20
PASS		
493	r400vgt_auto_index_points_01	2002Sep20
PASS		
494	r400vgt_auto_index_polygon_01	2002Sep20
PASS		
495	r400vgt_auto_index_primitives_01	2002Sep20
PASS		
496	r400vgt_auto_index_quad_list_01	2002Sep20
PASS		
497	r400vgt_auto_index_quad_strip_01	2002Sep20
PASS		
498	r400vgt_auto_index_rectangle_list_01	2002Sep20
PASS		
499	r400vgt_auto_index_tri_fan_01	2002Sep20
PASS		
500	r400vgt_auto_index_tri_list_01	2002Sep20

PASS		
501	r400vgt_auto_index_tri_strip_01	2002Sep20
PASS		
502	r400vgt_auto_index_tri_wflags_01	2002Sep20
PASS		
503	r400vgt_dma_engine_01	2002Sep20
PASS		
504	r400vgt_dma_engine_02	2002Sep20
PASS		
505	r400vgt_dma_engine_03	2002Sep20
PASS		
506	r400vgt_dma_engine_04	2002Sep20
PASS		
507	r400vgt_dma_engine_05	2002Sep20
PASS		
508	r400vgt_dma_engine_06	2002Sep20
PASS		
509	r400vgt_dma_engine_07	2002Sep20
PASS		
510	r400vgt_dma_engine_08	2002Sep20
PASS		
511	r400vgt_dma_engine_09	2002Sep20
PASS		
512	r400vgt_dma_engine_10	2002Sep20
PASS		
513	r400vgt_dma_index_line_list_01	2002Sep20
PASS		
514	r400vgt_dma_index_line_loop_01	2002Sep20
PASS		
515	r400vgt_dma_index_line_strip_01	2002Sep20
PASS		
516	r400vgt_dma_index_points_01	2002Sep20
PASS		
517	r400vgt_dma_index_polygon_01	2002Sep20
PASS		
518	r400vgt_dma_index_primitives_01	2002Sep20
PASS		
519	r400vgt_dma_index_quad_list_01	2002Sep20
PASS		
520	r400vgt_dma_index_quad_strip_01	2002Sep20
PASS		
521	r400vgt_dma_index_rectangle_list_01	2002Sep20
PASS		
522	r400vgt_dma_index_tri_fan_01	2002Sep20
PASS		
523	r400vgt_dma_index_tri_list_01	2002Sep20
PASS		
524	r400vgt_dma_index_tri_strip_01	2002Sep20

PASS		
525	r400vgt_dma_index_tri_wflags_01	2002Sep20
PASS		
526	r400vgt_dma_swap_indx16_01	2002Sep20
PASS		
527	r400vgt_dma_swap_indx32_01	2002Sep20
PASS		
528	r400vgt_edgeflags_polygon_01	2002Sep20
PASS		
529	r400vgt_edgeflags_quad_list_01	2002Sep20
PASS		
530	r400vgt_edgeflags_quad_strip_01	2002Sep20
PASS		
531	r400vgt_edgeflags_triangle_fan_01	2002Sep20
PASS		
532	r400vgt_edgeflags_triangle_list_01	2002Sep20
PASS		
533	r400vgt_edgeflags_triangle_strip_01	2002Sep20
PASS		
534	r400vgt_edgeflags_triangle_wflags_01	2002Sep20
PASS		
535	r400vgt_ext2int_index_line_list_01	2002Sep20
PASS		
536	r400vgt_ext2int_index_line_loop_01	2002Sep20
PASS		
537	r400vgt_ext2int_index_line_strip_01	2002Sep20
PASS		
538	r400vgt_ext2int_index_points_01	2002Sep20
PASS		
539	r400vgt_ext2int_index_polygon_01	2002Sep20
PASS		
540	r400vgt_ext2int_index_quad_list_01	2002Sep20
PASS		
541	r400vgt_ext2int_index_quad_strip_01	2002Sep20
PASS		
542	r400vgt_ext2int_index_triangle_fan_01	2002Sep20
PASS		
543	r400vgt_ext2int_index_triangle_list_01	2002Sep20
PASS		
544	r400vgt_ext2int_index_triangle_strip_01	2002Sep20
PASS		
545	r400vgt_hos_auto_index_line_list_01	2002Sep20
PASS		
546	r400vgt_hos_auto_index_quad_list_01	2002Sep20
PASS		
547	r400vgt_hos_auto_index_triangle_list_01	2002Sep20
PASS		
548	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep20

PASS
549 r400vgt_hos_LPatch_01 2002Sep20
PASS
550 r400vgt_hos_PNL_cp_ln_cont_no_projection_01 2002Sep20
PASS
551 r400vgt_hos_PNL_lp_ln_cont_no_projection_01 2002Sep20
PASS
552 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01 2002Sep20
PASS
553 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02 2002Sep20
PASS
554 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01 2002Sep20
PASS
555 r400vgt_hos_PNQ_lp_cont_no_projection_01 2002Sep20
PASS
556 r400vgt_hos_PNT_cont_cp_qn_complex_01 2002Sep20
PASS
557 r400vgt_hos_PNT_cont_cp_qn_precision_01 2002Sep20
PASS
558 r400vgt_hos_PNT_cont_cp_qn_precision_02 2002Sep20
PASS
559 r400vgt_hos_PNT_cp_qn_cont_light_texture_01 2002Sep20
PASS
560 r400vgt_hos_PNT_cp_qn_cont_light_texture_02 2002Sep20
PASS
561 r400vgt_hos_PNT_cp_qn_cont_light_texture_03 2002Sep20
PASS
562 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01 2002Sep20
PASS
563 r400vgt_hos_PNT_cp_qn_cont_no_projection_01 2002Sep20
PASS
564 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01 2002Sep20
PASS
565 r400vgt_hos_PNT_disc_cp_qn_complex_01 2002Sep20
PASS
566 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Sep20
PASS
567 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Sep20
PASS
568 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Sep20
PASS
569 r400vgt_hos_PNT_disc_cp_qn_precision_02 2002Sep20
PASS
570 r400vgt_hos_PNT_edge_detection_01 2002Sep20
PASS
571 r400vgt_hos_PNT_lp_cont_no_projection_01 2002Sep20
PASS
572 r400vgt_hos_PNTQL_cp_qn_cont_stress_01 2002Sep20

PASS		
573	r400vgt_hos_RPatch_cp_02	2002Sep20
PASS		
574	r400vgt_hos_RPatch_lp_02	2002Sep20
PASS		
575	r400vgt_immed_index_line_list_01	2002Sep20
PASS		
576	r400vgt_immed_index_line_loop_01	2002Sep20
PASS		
577	r400vgt_immed_index_line_strip_01	2002Sep20
PASS		
578	r400vgt_immed_index_points_01	2002Sep20
PASS		
579	r400vgt_immed_index_polygon_01	2002Sep20
PASS		
580	r400vgt_immed_index_primitives_01	2002Sep20
PASS		
581	r400vgt_immed_index_quad_list_01	2002Sep20
PASS		
582	r400vgt_immed_index_quad_strip_01	2002Sep20
PASS		
583	r400vgt_immed_index_rectangle_list_01	2002Sep20
PASS		
584	r400vgt_immed_index_tri_fan_01	2002Sep20
PASS		
585	r400vgt_immed_index_tri_list_01	2002Sep20
PASS		
586	r400vgt_immed_index_tri_strip_01	2002Sep20
PASS		
587	r400vgt_immed_index_tri_wflags_01	2002Sep20
PASS		
588	r400vgt_index_dealloc_line_list_01	2002Sep20
PASS		
589	r400vgt_index_dealloc_points_01	2002Sep20
PASS		
590	r400vgt_index_dealloc_triangle_list_01	2002Sep20
PASS		
591	r400vgt_index_min_max_01	2002Sep20
PASS		
592	r400vgt_index_min_max_02	2002Sep20
PASS		
593	r400vgt_index_offset_01	2002Sep20
PASS		
594	r400vgt_index_offset_02	2002Sep20
PASS		
595	r400vgt_index_offset_03	2002Sep20
PASS		
596	r400vgt_index_size_01	2002Sep20

PASS		
597	r400vgt_index_source_switch_01	2002Sep20
PASS		
598	r400vgt_line_list_01	2002Sep20
PASS		
599	r400vgt_line_list_02	2002Sep20
PASS		
600	r400vgt_line_loop_01	2002Sep20
PASS		
601	r400vgt_line_loop_02	2002Sep20
PASS		
602	r400vgt_line_strip_01	2002Sep20
PASS		
603	r400vgt_line_strip_02	2002Sep20
PASS		
604	r400vgt_multi_context_01	2002Sep20
PASS		
605	r400vgt_multi_context_02	2002Sep20
PASS		
606	r400vgt_multi_context_03	2002Sep20
PASS		
607	r400vgt_multi_context_04	2002Sep20
PASS		
608	r400vgt_multi_context_05	2002Sep20
PASS		
609	r400vgt_multi_context_06	2002Sep20
PASS		
610	r400vgt_multi_context_07	2002Sep20
PASS		
611	r400vgt_multi_context_08	2002Sep20
PASS		
612	r400vgt_multi_context_09	2002Sep20
PASS		
613	r400vgt_multi_context_10	2002Sep20
PASS		
614	r400vgt_multi_context_11	2002Sep20
PASS		
615	r400vgt_multi_context_12	2002Sep20
PASS		
616	r400vgt_multi_prim_reset_index_all_01	2002Sep20
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
617	r400vgt_pass_thru_all_prims_01	2002Sep20
PASS		
618	r400vgt_point_list_01	2002Sep20
PASS		
619	r400vgt_point_list_02	2002Sep20
PASS		
620	r400vgt_polygon_01	2002Sep20

PASS		
621	r400vgt_polygon_02	2002Sep20
PASS		
622	r400vgt_provoking_vtx_all_01	2002Sep20
PASS		
623	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep20
PASS		
624	r400vgt_provoking_vtx_polygon_01	2002Sep20
PASS		
625	r400vgt_provoking_vtx_quad_list_01	2002Sep20
PASS		
626	r400vgt_provoking_vtx_quad_strip_01	2002Sep20
PASS		
627	r400vgt_provoking_vtx_tri_fan_01	2002Sep20
PASS		
628	r400vgt_provoking_vtx_tri_strip_01	2002Sep20
PASS		
629	r400vgt_quad_list_01	2002Sep20
PASS		
630	r400vgt_quad_strip_01	2002Sep20
PASS		
631	r400vgt_rectangle_list_01	2002Sep20
PASS		
632	r400vgt_reuse_depth_line_list_01	2002Sep20
PASS		
633	r400vgt_reuse_depth_point_list_01	2002Sep20
PASS		
634	r400vgt_reuse_depth_triangle_list_01	2002Sep20
PASS		
635	r400vgt_reuse_index_line_list_01	2002Sep20
PASS		
636	r400vgt_reuse_index_point_list_01	2002Sep20
PASS		
637	r400vgt_reuse_index_triangle_list_01	2002Sep20
PASS		
638	r400vgt_reuse_index_triangle_list_02	2002Sep20
PASS		
639	r400vgt_reuse_index_triangle_list_03	2002Sep20
PASS		
640	r400vgt_suppress_eop_01	2002Sep20
PASS		
641	r400vgt_suppress_eop_02	2002Sep20
PASS		
642	r400vgt_suppress_eop_03	2002Sep20
PASS		
643	r400vgt_suppress_eop_04	2002Sep20
PASS		
644	r400vgt_triangle_fan_01	2002Sep20

```

PASS
645 r400vgt_triangle_fan_02 2002Sep20
PASS
646 r400vgt_triangle_list_01 2002Sep20
PASS
647 r400vgt_triangle_strip_01 2002Sep20
PASS
648 r400vgt_triangle_strip_02 2002Sep20
PASS
649 r400vgt_triangle_wflags_01 2002Sep20
PASS
650 r400vgt_viz_query_01 2002Sep20
PASS
651 r400vgt_vtx_vector_packing_01 2002Sep20
PASS
652 r400vte_coverage_01 2002Sep20
PASS
653 r400vte_simple_point_01 2002Sep20
PASS
654 r400vte_simple_triangle_01 2002Sep20
PASS
655 r400vte_w0_fmt_01 2002Sep20
PASS
656 r400vte_w0_fmt_03 2002Sep20
PASS
657 r400vte_xy_fmt_01 2002Sep20
PASS
658 r400vte_xy_fmt_mk_01_bug 2002Sep20
PASS
659 r400vte_xyz_scale_01 2002Sep20
PASS
660 r400vte_xyz_scale_02 2002Sep20
PASS
661 r400vte_z_fmt_01 2002Sep20
PASS

```

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=====
Tests_Run_count = 661 Pass_count = 660 Fail_count = 1 Incomplete_count = 0
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Oct  9 22:16:29 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              50          50          50          100
              RBBM             1           1           1           100
              VGT             181         181         181          100
              SC              301         301         299           99
              CL              187         187         146           78
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              PARAMETER         62          62          62           100
              SU              107         107         103           96
              SANITY             2           2           2           100
+++++
              TOTAL            907         907         860           94
+++++

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Sep 20  9:05:37 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             162         162         161           99
              SC              269         269         269          100
              CL              115         115         115          100
              VTE              10          10          10          100
              SPECIALTY         6           6           6           100
              SU               85          85          85          100
              SANITY            2           2           2           100
+++++
              TOTAL            661         661         660           99
+++++

```



```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Aug 15  3:01:34 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          124          96
              SC           133          133           0           0
              CL           80           80           0           0
              VTE           9            9            9          100
SPECIALTY     SU           64          64          64          100
              SANITY       2            2            2          100
+++++
              TOTAL        423          423          205          48
+++++

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Oct  1 21:29:17 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             175         175         175          100
              SC              286         286         285           99
              CL              115         115         115          100
              VTE              10          10          10          100
              SPECIALTY         6           6           6           100
              SU               91          91          91          100
              SANITY            2           2           2           100
+++++
              TOTAL            697         697         696           99
+++++

```

=====
Regression Report For VGT - Project = r400 Created on Sun Oct 6 21:23:51 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc		2002Oct6
PASS			
2	cp_dma_interrupt		2002Oct6
PASS			
3	cp_dma_m2m_01		2002Oct6
PASS			
4	cp_dma_m2m_02		2002Oct6
PASS			
5	cp_dma_m2m_03		2002Oct6
PASS			
6	cp_dma_m2m_04		2002Oct6
PASS			
7	cp_dma_m2r_01		2002Oct6
PASS			
8	cp_dma_m2r_02		2002Oct6
PASS			
9	cp_dma_m2r_03		2002Oct6
PASS			
10	cp_dma_m2r_04		2002Oct6
PASS			
11	cp_dma_m2r_r2m		2002Oct6
PASS			
12	cp_dma_pio_simple		2002Oct6
PASS			
13	cp_dma_pio_stress		2002Oct6
PASS			
14	cp_dma_piobm_stress		2002Oct6
PASS			
15	cp_dma_r2m_01		2002Oct6
PASS			
16	cp_dma_r2m_02		2002Oct6
PASS			
17	cp_dma_r2m_03		2002Oct6
PASS			
18	cp_dma_r2m_04		2002Oct6
PASS			
19	cp_dma_r2r_01		2002Oct6
PASS			
20	cp_dma_r2r_02		2002Oct6

PASS		
21	cp_dma_r2r_03	20020ct6
PASS		
22	cp_dma_r2r_r2m	20020ct6
PASS		
23	cp_dma_r2r_r2m_m2m	20020ct6
PASS		
24	cp_dma_r2r_r2m_m2m_r2m	20020ct6
PASS		
25	cp_dma_simple	20020ct6
PASS		
26	cp_e2_hostdata_blt_pntr_8888	20020ct6
PASS		
27	cp_e2_one_blit	20020ct6
PASS		
28	cp_e2_one_line	20020ct6
PASS		
29	cp_e2blit_brush_mt_ropcc	20020ct6
PASS		
30	cp_e2paint_565	20020ct6
PASS		
31	cp_e2paint_multi	20020ct6
PASS		
32	cp_e2polyscanlines_brush	20020ct6
PASS		
33	cp_e2polyscanlines_brush_mt	20020ct6
PASS		
34	cp_e2smalltext_jcl	20020ct6
PASS		
35	cp_im_load_indirect	20020ct6
PASS		
36	cp_push_aper_indirect1	20020ct6
PASS		
37	cp_push_aper_primary	20020ct6
PASS		
38	cp_queue_avail_01	20020ct6
PASS		
39	cp_queue_avail_02	20020ct6
PASS		
40	cp_queue_avail_03	20020ct6
PASS		
41	cp_queue_avail_04	20020ct6
PASS		
42	cp_queue_avail_05	20020ct6
PASS		
43	cp_queue_avail_06	20020ct6
PASS		
44	cp_queue_avail_07	20020ct6

PASS		
45	cp_queue_avail_08	2002Oct6
PASS		
46	cp_queue_avail_09	2002Oct6
PASS		
47	cp_queue_avail_10	2002Oct6
PASS		
48	cp_queue_avail_11	2002Oct6
PASS		
49	cp_queue_avail_12	2002Oct6
PASS		
50	cp_simple_triangle	2002Oct6
PASS		
51	milestone_event_flush_tri1	2002Oct3
PASS		
52	milestone_event_flush_tri2	2002Oct3
PASS		
53	milestone_event_flush_tri3	2002Oct3
PASS		
54	milestone_event_flush_tri4	2002Oct3
PASS		
55	milestone_event_flush_tri5	2002Oct3
PASS		
56	milestone_event_flush_tri6	2002Oct3
PASS		
57	milestone_event_flush_tri7	2002Oct3
PASS		
58	milestone_event_flush_tri8	2002Oct3
PASS		
59	milestone_event_flush_tri9	2002Oct3
PASS		
60	milestone_event_randomized	2002Oct3
PASS		
61	milestone_event_standard	2002Oct3
PASS		
62	milestone_event_switch_flush_tri1	2002Oct3
PASS		
63	milestone_event_switch_flush_tri2	2002Oct3
PASS		
64	milestone_event_switch_flush_tri3	2002Oct3
PASS		
65	milestone_event_switch_flush_tri4	2002Oct3
PASS		
66	milestone_event_switch_flush_tri5	2002Oct3
PASS		
67	milestone_event_switch_flush_tri6	2002Oct3
PASS		
68	milestone_event_switch_flush_tri7	2002Oct3

PASS		
69	milestone_event_switch_flush_tri8	2002Oct3
PASS		
70	milestone_event_switch_flush_tri9	2002Oct3
PASS		
71	milestone_event_switch_tri2	2002Oct3
PASS		
72	milestone_event_switch_tri3	2002Oct3
PASS		
73	milestone_event_switch_tri4	2002Oct3
PASS		
74	milestone_event_switch_tri5	2002Oct3
PASS		
75	milestone_event_switch_tri6	2002Oct3
PASS		
76	milestone_event_switch_tri7	2002Oct3
PASS		
77	milestone_event_switch_tri8	2002Oct3
PASS		
78	milestone_event_switch_tri9	2002Oct3
PASS		
79	milestone_event_tri2	2002Oct3
PASS		
80	milestone_event_tri3	2002Oct3
PASS		
81	milestone_event_tri4	2002Oct3
PASS		
82	milestone_event_tri5	2002Oct3
PASS		
83	milestone_event_tri6	2002Oct3
PASS		
84	milestone_event_tri7	2002Oct3
PASS		
85	milestone_event_tri8	2002Oct3
PASS		
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117	primlib_template_simple_triangle	2002Oct6
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122	r400cl_clip_space_dx_ogl_01	2002Oct6
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123	r400cl_clip_ucp_6bits_01	2002Oct6
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124	r400cl_cull_only_ena_01	2002Oct6
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126	r400cl_edgeflags_01_bug	2002Oct6
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193	r400cl_gband_tcl_01	2002Oct6
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422	r400sc_msa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct6
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432	r400sc_msa_8_zbuffer_rectangle_list_01	2002Oct6
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433	r400sc_msa_8_zbuffer_rectangle_list_02	2002Oct6
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434	r400sc_msa_X_primitives_01_bug	2002Oct6
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435	r400sc_msa_X_primitives_02_bug	2002Oct6
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436	r400sc_multi_chip_prim_discard_00_07	2002Oct6
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437	r400sc_multi_chip_tile_discard_00_07	2002Oct6
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438	r400sc_null_triangles_01	2002Oct6
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439	r400sc_null_triangles_fc_01	2002Oct6
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444	r400sc_point_jss_3x4_01	2002Oct6
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445	r400sc_point_list_01	2002Oct6
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469	r400sc_polygon_stipple_01	2002Oct6
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472	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct6
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473	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct6
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547	r400su_clip_baryc_test_02	2002Oct6
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695	r400vgt_ext2int_index_triangle_wflags_01	2002Oct6
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709	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct6
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711	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct6
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744	r400vgt_index_dealloc_triangle_list_01	2002Oct6
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745	r400vgt_index_min_max_01	2002Oct6
PASS		
746	r400vgt_index_min_max_02	2002Oct6
PASS		
747	r400vgt_index_offset_01	2002Oct6
PASS		
748	r400vgt_index_offset_02	2002Oct6
PASS		
749	r400vgt_index_offset_03	2002Oct6
PASS		
750	r400vgt_index_offset_04	2002Oct6
PASS		
751	r400vgt_index_offset_05	2002Oct6
PASS		
752	r400vgt_index_offset_06	2002Oct6
PASS		
753	r400vgt_index_offset_07	2002Oct6
PASS		
754	r400vgt_index_offset_08	2002Oct6
PASS		
755	r400vgt_index_size_01	2002Oct6
PASS		
756	r400vgt_index_source_switch_01	2002Oct6
PASS		
757	r400vgt_line_list_01	2002Oct6
PASS		
758	r400vgt_line_list_02	2002Oct6
PASS		
759	r400vgt_line_loop_01	2002Oct6
PASS		
760	r400vgt_line_loop_02	2002Oct6
PASS		
761	r400vgt_line_strip_01	2002Oct6
PASS		
762	r400vgt_line_strip_02	2002Oct6
PASS		
763	r400vgt_multi_context_01	2002Oct6
PASS		
764	r400vgt_multi_context_02	2002Oct6

PASS		
765	r400vgt_multi_context_03	2002Oct6
PASS		
766	r400vgt_multi_context_04	2002Oct6
PASS		
767	r400vgt_multi_context_05	2002Oct6
PASS		
768	r400vgt_multi_context_06	2002Oct6
PASS		
769	r400vgt_multi_context_07	2002Oct6
PASS		
770	r400vgt_multi_context_08	2002Oct6
PASS		
771	r400vgt_multi_context_09	2002Oct6
PASS		
772	r400vgt_multi_context_10	2002Oct6
PASS		
773	r400vgt_multi_context_11	2002Oct6
PASS		
774	r400vgt_multi_context_12	2002Oct6
PASS		
775	r400vgt_multi_prim_reset_index_all_01	2002Oct6
PASS		
776	r400vgt_multi_prim_reset_index_all_02	2002Oct6
PASS		
777	r400vgt_pass_thru_all_prims_01	2002Oct6
PASS		
778	r400vgt_point_list_01	2002Oct6
PASS		
779	r400vgt_point_list_02	2002Oct6
PASS		
780	r400vgt_polygon_01	2002Oct6
PASS		
781	r400vgt_polygon_02	2002Oct6
PASS		
782	r400vgt_provoking_vtx_all_01	2002Oct6
PASS		
783	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct6
PASS		
784	r400vgt_provoking_vtx_polygon_01	2002Oct6
PASS		
785	r400vgt_provoking_vtx_quad_list_01	2002Oct6
PASS		
786	r400vgt_provoking_vtx_quad_strip_01	2002Oct6
PASS		
787	r400vgt_provoking_vtx_tri_fan_01	2002Oct6
PASS		
788	r400vgt_provoking_vtx_tri_strip_01	2002Oct6

PASS			
789	r400vgt_quad_list_01		2002Oct6
PASS			
790	r400vgt_quad_list_02		2002Oct6
PASS			
791	r400vgt_quad_strip_01		2002Oct6
PASS			
792	r400vgt_quad_strip_02		2002Oct6
PASS			
793	r400vgt_rectangle_list_01		2002Oct6
PASS			
794	r400vgt_rectangle_list_02		2002Oct6
PASS			
795	r400vgt_reuse_depth_line_list_01		2002Oct6
PASS			
796	r400vgt_reuse_depth_point_list_01		2002Oct6
PASS			
797	r400vgt_reuse_depth_triangle_list_01		2002Oct6
PASS			
798	r400vgt_reuse_index_line_list_01		2002Oct6
PASS			
799	r400vgt_reuse_index_point_list_01		2002Oct6
PASS			
800	r400vgt_reuse_index_triangle_list_01		2002Oct6
PASS			
801	r400vgt_reuse_index_triangle_list_02		2002Oct6
PASS			
802	r400vgt_reuse_index_triangle_list_03		2002Oct6
PASS			
803	r400vgt_suppress_eop_01	0:0:1	2002Oct6
PASS			
804	r400vgt_suppress_eop_02	0:0:1	2002Oct6
PASS			
805	r400vgt_suppress_eop_03	0:0:1	2002Oct6
PASS			
806	r400vgt_suppress_eop_04	0:0:1	2002Oct6
PASS			
807	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
808	r400vgt_triangle_fan_01	0:0:1	2002Oct6
PASS			
809	r400vgt_triangle_fan_02	0:0:1	2002Oct6
PASS			
810	r400vgt_triangle_list_01	0:0:1	2002Oct6
PASS			
811	r400vgt_triangle_list_02	0:0:1	2002Oct6
PASS			
812	r400vgt_triangle_strip_01	0:0:1	2002Oct6

```

PASS
813 r400vgt_triangle_strip_02          0:0:1  2002Oct6
PASS
814 r400vgt_triangle_wflags_01        0:0:1  2002Oct6
PASS
815 r400vgt_triangle_wflags_02        0:0:1  2002Oct6
PASS
816 r400vgt_viz_query_01              0:0:1  2002Oct6
PASS
817 r400vgt_vtx_vector_packing_01     0:0:1  2002Oct6
PASS
818 r400vte_coverage_01               0:0:1  2002Oct6
PASS
819 r400vte_simple_point_01           0:0:1  2002Oct6
PASS
820 r400vte_simple_triangle_01        0:0:1  2002Oct6
PASS
821 r400vte_w0_fmt_01                 0:0:1  2002Oct6
PASS
822 r400vte_w0_fmt_03                 0:0:1  2002Oct6
PASS
823 r400vte_xy_fmt_01                 0:0:1  2002Oct6
PASS
824 r400vte_xy_fmt_mk_01_bug          0:0:1  2002Oct6
PASS
825 r400vte_xyz_scale_01              0:0:1  2002Oct6
PASS
826 r400vte_xyz_scale_02              0:0:1  2002Oct6
PASS
827 r400vte_z_fmt_01                  0:0:1  2002Oct6
PASS

```

```

=====
Tests_Run_count = 827  Pass_count = 826  Fail_count = 0  Incomplete_count = 1
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Sep 10  3:24:14 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          151          151          151      100
              SC           235          235          233       99
              CL           110          110          110      100
              VTE           9           9            9       100
SPECIALTY    SU            82          82          82       100
              SANITY       2           2            2       100
+++++
              TOTAL        595          595          593       99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc	0:0:3	2002Oct15
PASS			
2	cp_dma_interrupt	0:0:2	2002Oct15
PASS			
3	cp_dma_m2m_01	0:0:1	2002Oct15
PASS			
4	cp_dma_m2m_02	0:0:1	2002Oct15
PASS			
5	cp_dma_m2m_03	0:0:1	2002Oct15
PASS			
6	cp_dma_m2m_04	0:0:1	2002Oct15
PASS			
7	cp_dma_m2r_01	0:0:1	2002Oct15
PASS			
8	cp_dma_m2r_02	0:0:2	2002Oct15
PASS			
9	cp_dma_m2r_03	0:0:1	2002Oct15
PASS			
10	cp_dma_m2r_04	0:0:1	2002Oct15
PASS			
11	cp_dma_m2r_r2m	0:0:1	2002Oct15
PASS			
12	cp_dma_pio_simple	0:0:1	2002Oct15
PASS			
13	cp_dma_pio_stress	0:0:1	2002Oct15
PASS			
14	cp_dma_piobm_stress	0:0:1	2002Oct15
PASS			
15	cp_dma_r2m_01	0:0:1	2002Oct15
PASS			
16	cp_dma_r2m_02	0:0:2	2002Oct15
PASS			
17	cp_dma_r2m_03	0:0:2	2002Oct15
PASS			
18	cp_dma_r2m_04	0:0:2	2002Oct15
PASS			
19	cp_dma_r2r_01	0:0:1	2002Oct15
PASS			
20	cp_dma_r2r_02	0:0:1	2002Oct15

PASS			
21	cp_dma_r2r_03	0:0:2	2002Oct15
PASS			
22	cp_dma_r2r_r2m	0:0:1	2002Oct15
PASS			
23	cp_dma_r2r_r2m_m2m	0:0:2	2002Oct15
PASS			
24	cp_dma_r2r_r2m_m2m_r2m	0:0:1	2002Oct15
PASS			
25	cp_dma_simple	0:0:1	2002Oct15
PASS			
26	cp_e2_hostdata_blt_pntr_8888	0:0:1	2002Oct15
PASS			
27	cp_e2_one_blit	0:0:0	2002Oct15
PASS			
28	cp_e2_one_line	0:0:2	2002Oct15
PASS			
29	cp_e2blit_brush_mt_ropcc	0:0:1	2002Oct15
PASS			
30	cp_e2paint_565	0:0:1	2002Oct15
PASS			
31	cp_e2paint_multi	0:0:2	2002Oct15
PASS			
32	cp_e2polyscanlines_brush	0:0:1	2002Oct15
PASS			
33	cp_e2polyscanlines_brush_mt	0:0:1	2002Oct15
PASS			
34	cp_e2smalltext_jcl	0:0:3	2002Oct15
PASS			
35	cp_im_load_indirect	0:0:3	2002Oct15
PASS			
36	cp_push_aper_indirect1	0:0:3	2002Oct15
PASS			
37	cp_push_aper_primary	0:0:3	2002Oct15
PASS			
38	cp_queue_avail_01	0:0:3	2002Oct15
PASS			
39	cp_queue_avail_02	0:0:3	2002Oct15
PASS			
40	cp_queue_avail_03	0:0:3	2002Oct15
PASS			
41	cp_queue_avail_04	0:0:3	2002Oct15
PASS			
42	cp_queue_avail_05	0:0:3	2002Oct15
PASS			
43	cp_queue_avail_06	0:0:3	2002Oct15
PASS			
44	cp_queue_avail_07	0:0:3	2002Oct15

PASS			
45	cp_queue_avail_08	0:0:3	2002Oct15
PASS			
46	cp_queue_avail_09	0:0:3	2002Oct15
PASS			
47	cp_queue_avail_10	0:0:3	2002Oct15
PASS			
48	cp_queue_avail_11	0:0:3	2002Oct15
PASS			
49	cp_queue_avail_12	0:0:3	2002Oct15
PASS			
50	cp_simple_triangle	0:0:3	2002Oct15
PASS			
51	milestone_event_flush_tri1	0:0:3	2002Oct15
PASS			
52	milestone_event_flush_tri2	0:0:3	2002Oct15
PASS			
53	milestone_event_flush_tri3	0:0:3	2002Oct15
PASS			
54	milestone_event_flush_tri4	0:0:3	2002Oct15
PASS			
55	milestone_event_flush_tri5	0:0:3	2002Oct15
PASS			
56	milestone_event_flush_tri6	0:0:3	2002Oct15
PASS			
57	milestone_event_flush_tri7	0:0:3	2002Oct15
PASS			
58	milestone_event_flush_tri8	0:0:3	2002Oct15
PASS			
59	milestone_event_flush_tri9	0:0:3	2002Oct15
PASS			
60	milestone_event_randomized	0:0:3	2002Oct15
PASS			
61	milestone_event_standard	0:0:3	2002Oct15
PASS			
62	milestone_event_switch_flush_tri1	0:0:3	2002Oct15
PASS			
63	milestone_event_switch_flush_tri2	0:0:3	2002Oct15
PASS			
64	milestone_event_switch_flush_tri3	0:0:3	2002Oct15
PASS			
65	milestone_event_switch_flush_tri4	0:0:3	2002Oct15
PASS			
66	milestone_event_switch_flush_tri5	0:0:3	2002Oct15
PASS			
67	milestone_event_switch_flush_tri6	0:0:3	2002Oct15
PASS			
68	milestone_event_switch_flush_tri7	0:0:3	2002Oct15

PASS			
69	milestone_event_switch_flush_tri8	0:0:3	2002Oct15
PASS			
70	milestone_event_switch_flush_tri9	0:0:3	2002Oct15
PASS			
71	milestone_event_switch_tri2	0:0:3	2002Oct15
PASS			
72	milestone_event_switch_tri3	0:0:3	2002Oct15
PASS			
73	milestone_event_switch_tri4	0:0:3	2002Oct15
PASS			
74	milestone_event_switch_tri5	0:0:3	2002Oct15
PASS			
75	milestone_event_switch_tri6	0:0:3	2002Oct15
PASS			
76	milestone_event_switch_tri7	0:0:3	2002Oct15
PASS			
77	milestone_event_switch_tri8	0:0:3	2002Oct15
PASS			
78	milestone_event_switch_tri9	0:0:3	2002Oct15
PASS			
79	milestone_event_tri2	0:0:3	2002Oct15
PASS			
80	milestone_event_tri3	0:0:3	2002Oct15
PASS			
81	milestone_event_tri4	0:0:3	2002Oct15
PASS			
82	milestone_event_tri5	0:0:3	2002Oct15
PASS			
83	milestone_event_tri6	0:0:3	2002Oct15
PASS			
84	milestone_event_tri7	0:0:3	2002Oct15
PASS			
85	milestone_event_tri8	0:0:3	2002Oct15
PASS			
86	milestone_event_tri9	0:0:3	2002Oct15
PASS			
87	milestone_tri_random_rect	0:0:3	2002Oct15
PASS			
88	milestone_tri_random_tri	0:0:3	2002Oct15
PASS			
89	milestone_tri_randomized	0:0:3	2002Oct15
PASS			
90	milestone_tri_standard	0:0:3	2002Oct15
PASS			
91	milestone_tri_tril128_pix4	0:0:3	2002Oct15
PASS			
92	milestone_tri_tril_pix1	0:0:3	2002Oct15

PASS			
93	milestone_tri_tri1_pix2	0:0:3	2002Oct15
PASS			
94	milestone_tri_tri1_pix3	0:0:3	2002Oct15
PASS			
95	milestone_tri_tri1_pix4	0:0:3	2002Oct15
PASS			
96	milestone_tri_tri24x24	0:0:3	2002Oct15
PASS			
97	milestone_tri_tri28x28	0:0:3	2002Oct15
PASS			
98	milestone_tri_tri2_pix1	0:0:3	2002Oct15
PASS			
99	milestone_tri_tri2_pix16	0:0:3	2002Oct15
PASS			
100	milestone_tri_tri2_pix4	0:0:3	2002Oct15
PASS			
101	milestone_tri_tri32_pix1	0:0:3	2002Oct15
PASS			
102	milestone_tri_tri32_pix4	0:0:3	2002Oct15
PASS			
103	milestone_tri_tri32x32	0:0:3	2002Oct15
PASS			
104	milestone_tri_tri36x36	0:0:3	2002Oct15
PASS			
105	milestone_tri_tri40x40	0:0:3	2002Oct15
PASS			
106	milestone_tri_tri44x44	0:0:3	2002Oct15
PASS			
107	milestone_tri_tri48x48	0:0:3	2002Oct15
PASS			
108	milestone_tri_tri52x52	0:0:3	2002Oct15
PASS			
109	milestone_tri_tri56x56	0:0:3	2002Oct15
PASS			
110	milestone_tri_tri60x60	0:0:3	2002Oct15
PASS			
111	milestone_tri_tri64x64	0:0:3	2002Oct15
PASS			
112	milestone_tri_tri8_pix4	0:0:3	2002Oct15
PASS			
113	primlib_1st_tri_june15	0:0:3	2002Oct15
PASS			
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	0:0:3	2002Oct15
PASS			
115	primlib_gouraud_triangles_2_draw_passes	0:0:3	2002Oct15
PASS			
116	primlib_parameterized_simple_triangle	0:0:3	2002Oct15

PASS			
117	primlib_template_simple_triangle	0:0:3	2002Oct15
PASS			
118	primlib_tex_tri	0:0:3	2002Oct15
PASS			
119	r400cl_bary_texture_08_bug	0:0:3	2002Sep21
PASS			
120	r400cl_clip_10_verts_01	0:0:3	2002Oct15
PASS			
121	r400cl_clip_disable_01	0:0:3	2002Oct15
PASS			
122	r400cl_clip_space_dx_ogl_01	0:0:3	2002Oct15
PASS			
123	r400cl_clip_ucp_6bits_01	0:0:3	2002Oct15
PASS			
124	r400cl_cull_only_ena_01	0:0:3	2002Oct15
PASS			
125	r400cl_edgeflags_01	0:0:3	2002Oct15
PASS			
126	r400cl_edgeflags_01_bug	0:0:3	2002Oct9
PASS			
127	r400cl_edgeflags_frustum_bottom_01	0:0:3	2002Oct15
PASS			
128	r400cl_edgeflags_frustum_far_01	0:0:3	2002Oct15
PASS			
129	r400cl_edgeflags_frustum_left_01	0:0:3	2002Oct15
PASS			
130	r400cl_edgeflags_frustum_near_01	0:0:3	2002Oct15
PASS			
131	r400cl_edgeflags_frustum_right_01	0:0:3	2002Oct15
PASS			
132	r400cl_edgeflags_frustum_top_01	0:0:3	2002Oct15
PASS			
133	r400cl_edgeflags_gband_01	0:0:3	2002Oct15
PASS			
134	r400cl_edgeflags_gband_bottom_01	0:0:3	2002Oct15
PASS			
135	r400cl_edgeflags_gband_left_01	0:0:3	2002Oct15
PASS			
136	r400cl_edgeflags_gband_right_01	0:0:3	2002Oct15
PASS			
137	r400cl_edgeflags_gband_top_01	0:0:3	2002Oct15
PASS			
138	r400cl_frustum_01	0:0:3	2002Oct15
PASS			
139	r400cl_frustum_02	0:0:3	2002Oct15
PASS			
140	r400cl_frustum_03	0:0:3	2002Oct15

PASS			
141	r400cl_frustum_04	0:0:3	2002Oct15
PASS			
142	r400cl_frustum_05	0:0:3	2002Oct15
PASS			
143	r400cl_frustum_06	0:0:3	2002Oct15
PASS			
144	r400cl_frustum_07	0:0:3	2002Oct15
PASS			
145	r400cl_frustum_08	0:0:3	2002Oct15
PASS			
146	r400cl_frustum_09	0:0:3	2002Oct15
PASS			
147	r400cl_frustum_10	0:0:3	2002Oct15
PASS			
148	r400cl_frustum_11	0:0:3	2002Oct15
PASS			
149	r400cl_frustum_12	0:0:3	2002Oct15
PASS			
150	r400cl_frustum_13	0:0:3	2002Oct15
PASS			
151	r400cl_frustum_14	0:0:3	2002Oct15
PASS			
152	r400cl_frustum_15	0:0:3	2002Oct15
PASS			
153	r400cl_frustum_16	0:0:3	2002Oct15
PASS			
154	r400cl_frustum_17	0:0:3	2002Oct15
PASS			
155	r400cl_frustum_18	0:0:3	2002Oct15
PASS			
156	r400cl_frustum_19	0:0:3	2002Oct15
PASS			
157	r400cl_frustum_20	0:0:3	2002Oct15
PASS			
158	r400cl_frustum_21	0:0:3	2002Oct15
PASS			
159	r400cl_frustum_22	0:0:3	2002Oct15
PASS			
160	r400cl_frustum_23	0:0:3	2002Oct15
PASS			
161	r400cl_frustum_24	0:0:3	2002Oct15
PASS			
162	r400cl_frustum_25	0:0:3	2002Oct15
PASS			
163	r400cl_frustum_26	0:0:3	2002Oct15
PASS			
164	r400cl_frustum_27	0:0:3	2002Oct15

PASS			
165	r400cl_frustum_28	0:0:3	2002Oct15
PASS			
166	r400cl_frustum_29	0:0:3	2002Oct15
PASS			
167	r400cl_frustum_30	0:0:3	2002Oct15
PASS			
168	r400cl_frustum_31	0:0:3	2002Oct15
PASS			
169	r400cl_frustum_32	0:0:3	2002Oct15
PASS			
170	r400cl_frustum_33	0:0:3	2002Oct15
PASS			
171	r400cl_frustum_34	0:0:3	2002Oct15
PASS			
172	r400cl_frustum_35	0:0:3	2002Oct15
PASS			
173	r400cl_frustum_36	0:0:3	2002Oct15
PASS			
174	r400cl_frustum_37	0:0:3	2002Oct15
PASS			
175	r400cl_frustum_38	0:0:3	2002Oct15
PASS			
176	r400cl_frustum_39	0:0:3	2002Oct15
PASS			
177	r400cl_frustum_40	0:0:3	2002Oct15
PASS			
178	r400cl_frustum_all_vols_lines	0:0:3	2002Oct15
PASS			
179	r400cl_frustum_all_vols_tris	0:0:3	2002Oct15
PASS			
180	r400cl_frustum_lines_01	0:0:3	2002Oct15
PASS			
181	r400cl_frustum_lines_02	0:0:3	2002Oct15
PASS			
182	r400cl_frustum_lines_03	0:0:3	2002Oct15
PASS			
183	r400cl_frustum_lines_04	0:0:3	2002Oct15
PASS			
184	r400cl_frustum_lines_05	0:0:3	2002Oct15
PASS			
185	r400cl_frustum_lines_06	0:0:3	2002Oct15
PASS			
186	r400cl_frustum_lines_07	0:0:3	2002Oct15
PASS			
187	r400cl_frustum_lines_08	0:0:3	2002Oct15
PASS			
188	r400cl_frustum_lines_09	0:0:3	2002Oct15

PASS			
189	r400cl_frustum_lines_10	0:0:3	2002Oct15
PASS			
190	r400cl_frustum_lines_101	0:0:3	2002Oct15
PASS			
191	r400cl_frustum_lines_102	0:0:3	2002Oct15
PASS			
192	r400cl_frustum_lines_103	0:0:3	2002Oct15
PASS			
193	r400cl_frustum_lines_104	0:0:3	2002Oct15
PASS			
194	r400cl_frustum_lines_105	0:0:3	2002Oct15
PASS			
195	r400cl_frustum_lines_106	0:0:3	2002Oct15
PASS			
196	r400cl_frustum_lines_107	0:0:3	2002Oct15
PASS			
197	r400cl_frustum_lines_108	0:0:3	2002Oct15
PASS			
198	r400cl_frustum_lines_11	0:0:3	2002Oct15
PASS			
199	r400cl_frustum_lines_12	0:0:3	2002Oct15
PASS			
200	r400cl_frustum_lines_13	0:0:3	2002Oct15
PASS			
201	r400cl_frustum_lines_14	0:0:3	2002Oct15
PASS			
202	r400cl_frustum_lines_15	0:0:3	2002Oct15
PASS			
203	r400cl_frustum_lines_16	0:0:3	2002Oct15
PASS			
204	r400cl_frustum_lines_17	0:0:3	2002Oct15
PASS			
205	r400cl_frustum_lines_18	0:0:3	2002Oct15
PASS			
206	r400cl_frustum_lines_19	0:0:3	2002Oct15
PASS			
207	r400cl_frustum_lines_20	0:0:3	2002Oct15
PASS			
208	r400cl_frustum_lines_21	0:0:3	2002Oct15
PASS			
209	r400cl_frustum_lines_22	0:0:3	2002Oct15
PASS			
210	r400cl_frustum_lines_23	0:0:3	2002Oct15
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478	r400sc_msa_6_rectangle_list_04	0:0:3	2002Oct15
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479	r400sc_msa_6_rectangle_list_05	0:0:3	2002Oct15
PASS			
480	r400sc_msa_6_rectangle_list_06	0:0:3	2002Oct15
PASS			
481	r400sc_msa_6_rectangle_list_07	0:0:3	2002Oct15
PASS			
482	r400sc_msa_6_rectangle_list_08	0:0:3	2002Oct15
PASS			
483	r400sc_msa_6_zbuffer_rectangle_list_01	0:0:3	2002Oct15
PASS			
484	r400sc_msa_6_zbuffer_rectangle_list_02	0:0:3	2002Oct15
PASS			
485	r400sc_msa_8_01	0:0:3	2002Oct15
PASS			
486	r400sc_msa_8_02	0:0:3	2002Oct15
PASS			
487	r400sc_msa_8_03	0:0:3	2002Oct15
PASS			
488	r400sc_msa_8_04	0:0:3	2002Oct15
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489	r400sc_msa_8_05	0:0:3	2002Oct15
PASS			
490	r400sc_msa_8_aa_mask_01	0:0:3	2002Oct15
PASS			
491	r400sc_msa_8_aa_mask_02	0:0:3	2002Oct15
PASS			
492	r400sc_msa_8_aa_mask_fc_02	0:0:3	2002Oct15
PASS			
493	r400sc_msa_8_hos_PNT_cont_cp_qn_complex_01	0:0:3	2002Oct15
PASS			
494	r400sc_msa_8_primtypes_01	0:0:3	2002Oct15
PASS			
495	r400sc_msa_8_rectangle_list_01	0:0:3	2002Oct15
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496	r400sc_msa_8_rectangle_list_02	0:0:3	2002Oct15
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497	r400sc_msa_8_rectangle_list_03	0:0:3	2002Oct15
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498	r400sc_msa_8_rectangle_list_04	0:0:3	2002Oct15
PASS			
499	r400sc_msa_8_rectangle_list_05	0:0:3	2002Oct15
PASS			
500	r400sc_msa_8_rectangle_list_06	0:0:3	2002Oct15

PASS			
501	r400sc_msa_8_rectangle_list_07	0:0:3	2002Oct15
PASS			
502	r400sc_msa_8_rectangle_list_08	0:0:3	2002Oct15
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503	r400sc_msa_8_zbuffer_rectangle_list_01	0:0:3	2002Oct15
PASS			
504	r400sc_msa_8_zbuffer_rectangle_list_02	0:0:3	2002Oct15
PASS			
505	r400sc_msa_X_primitives_01_bug	0:0:3	2002Oct9
PASS			
506	r400sc_msa_X_primitives_02_bug	0:0:3	2002Oct9
PASS			
507	r400sc_multi_chip_prim_discard_00_07	0:0:3	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
508	r400sc_multi_chip_prim_discard_00_07_standard	0:0:3	2002Oct15
PASS			
509	r400sc_multi_chip_prim_discard_00_07_test00	0:0:3	2002Oct15
PASS			
510	r400sc_multi_chip_prim_discard_00_07_test01	0:0:3	2002Oct15
PASS			
511	r400sc_multi_chip_prim_discard_00_07_test02	0:0:3	2002Oct15
PASS			
512	r400sc_multi_chip_prim_discard_00_07_test03	0:0:3	2002Oct15
PASS			
513	r400sc_multi_chip_prim_discard_00_07_test04	0:0:3	2002Oct15
PASS			
514	r400sc_multi_chip_prim_discard_00_07_test05	0:0:3	2002Oct15
PASS			
515	r400sc_multi_chip_prim_discard_00_07_test06	0:0:3	2002Oct15
PASS			
516	r400sc_multi_chip_tile_discard_00_07	0:0:3	2002Oct7
PASS			
517	r400sc_multi_chip_tile_discard_00_07_standard	0:0:3	2002Oct15
INCOMPLETE			
518	r400sc_multi_chip_tile_discard_00_07_test00	0:0:3	2002Oct15
INCOMPLETE			
519	r400sc_multi_chip_tile_discard_00_07_test01	0:0:3	2002Oct15
INCOMPLETE			
520	r400sc_multi_chip_tile_discard_00_07_test02	0:0:3	2002Oct15
INCOMPLETE			
521	r400sc_multi_chip_tile_discard_00_07_test03	0:0:3	2002Oct15
INCOMPLETE			
522	r400sc_multi_chip_tile_discard_00_07_test04	0:0:3	2002Oct15
INCOMPLETE			
523	r400sc_multi_chip_tile_discard_00_07_test05	0:0:3	2002Oct15
INCOMPLETE			
524	r400sc_multi_chip_tile_discard_00_07_test06	0:0:3	2002Oct15

INCOMPLETE

525	r400sc_null_triangles_01	0:0:3	2002Oct15
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526	r400sc_null_triangles_fc_01	0:0:3	2002Oct15
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527	r400sc_packed_color_01	0:0:2	2002Oct15
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528	r400sc_parameterized_template_01_sc_0000	0:0:2	2002Oct15
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529	r400sc_parameterized_template_01_sc_0001	0:0:2	2002Oct15
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530	r400sc_parameterized_template_01_sc_0002	0:0:2	2002Oct15
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531	r400sc_parameterized_template_01_sc_0003	0:0:2	2002Oct15
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532	r400sc_parameterized_template_01_sc_0004	0:0:2	2002Oct15
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533	r400sc_parameterized_template_01_sc_0005	0:0:2	2002Oct15
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534	r400sc_parameterized_template_01_sc_0006	0:0:2	2002Oct15
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535	r400sc_parameterized_template_01_standard	0:0:2	2002Oct15
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536	r400sc_perf_01	0:0:2	2002Oct15
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537	r400sc_perf_02	0:0:2	2002Oct15
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538	r400sc_perf_03	0:0:2	2002Oct15
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539	r400sc_pinwheel_01	0:0:2	2002Oct15
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540	r400sc_pinwheel_02	0:0:2	2002Oct15
	PASS		
541	r400sc_pinwheel_02_bug	0:0:2	2002Oct9
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542	r400sc_point_jss_3x4_01	0:0:2	2002Oct15
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543	r400sc_point_list_01	0:0:2	2002Oct15
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544	r400sc_point_list_02	0:0:2	2002Oct15
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545	r400sc_point_list_03	0:0:2	2002Oct15
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546	r400sc_point_list_04	0:0:2	2002Oct15
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547	r400sc_point_list_05	0:0:2	2002Oct15
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548	r400sc_point_list_06	0:0:2	2002Oct15

PASS			
549	r400sc_point_list_06_bug	0:0:2	2002Oct9
PASS			
550	r400sc_point_list_06_bug_02	0:0:2	2002Oct9
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551	r400sc_point_list_07	0:0:2	2002Oct15
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552	r400sc_point_list_08	0:0:2	2002Oct15
PASS			
553	r400sc_point_list_09	0:0:2	2002Oct15
PASS			
554	r400sc_point_msa_8_01	0:0:2	2002Oct15
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555	r400sc_poly_offset_01	0:0:2	2002Oct15
PASS			
556	r400sc_poly_offset_02	0:0:2	2002Oct15
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557	r400sc_poly_offset_03	0:0:2	2002Oct15
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558	r400sc_poly_offset_04	0:0:2	2002Oct15
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559	r400sc_poly_offset_05	0:0:2	2002Oct15
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560	r400sc_poly_offset_06	0:0:2	2002Oct15
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561	r400sc_poly_offset_07	0:0:2	2002Oct15
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562	r400sc_poly_offset_08	0:0:2	2002Oct15
PASS			
563	r400sc_poly_offset_09	0:0:2	2002Oct15
PASS			
564	r400sc_poly_offset_10	0:0:2	2002Oct15
PASS			
565	r400sc_poly_offset_10_bug	0:0:2	2002Oct9
PASS			
566	r400sc_poly_offset_fc_01	0:0:2	2002Oct15
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567	r400sc_polygon_stipple_01	0:0:2	2002Oct15
PASS			
568	r400sc_polymode_tri_fill_01	0:0:2	2002Oct15
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569	r400sc_polymode_tri_fill_01_bug	0:0:2	2002Oct9
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570	r400sc_prsp_byc_intrp_ref_pix_01	0:0:2	2002Oct15
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571	r400sc_prsp_byc_intrp_ref_pix_02	0:0:2	2002Oct15
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572	r400sc_prsp_byc_intrp_ref_pix_03	0:0:2	2002Oct15

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573	r400sc_prsp_byc_intrp_ref_pix_04	0:0:2	2002Oct15
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574	r400sc_prsp_byc_intrp_ref_pix_05	0:0:2	2002Oct15
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575	r400sc_prsp_byc_intrp_ref_pix_06	0:0:2	2002Oct15
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576	r400sc_prsp_byc_intrp_ref_pix_07	0:0:2	2002Oct15
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577	r400sc_prsp_byc_intrp_ref_pix_08	0:0:2	2002Oct15
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578	r400sc_raster_fill_rule_01	0:0:2	2002Oct15
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579	r400sc_raster_fill_rule_02	0:0:2	2002Oct15
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580	r400sc_raster_fill_rule_03	0:0:2	2002Oct15
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581	r400sc_raster_fill_rule_04	0:0:2	2002Oct15
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582	r400sc_raster_fill_rule_05	0:0:2	2002Oct15
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583	r400sc_raster_fill_rule_fc_01	0:0:2	2002Oct15
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584	r400sc_rectangle_list_01	0:0:2	2002Oct15
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585	r400sc_rectangle_list_02	0:0:2	2002Oct15
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586	r400sc_rectangle_list_03	0:0:2	2002Oct15
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587	r400sc_rectangle_list_04	0:0:2	2002Oct15
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588	r400sc_rectangle_list_05	0:0:2	2002Oct15
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589	r400sc_rectangle_list_06	0:0:2	2002Oct15
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590	r400sc_rectangle_list_07	0:0:2	2002Oct15
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591	r400sc_rectangle_list_08	0:0:2	2002Oct15
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592	r400sc_scissor_rect_01	0:0:2	2002Oct15
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593	r400sc_scissor_rect_02	0:0:2	2002Oct15
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594	r400sc_scissor_rect_03	0:0:2	2002Oct15
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595	r400sc_scissor_rect_04	0:0:2	2002Oct15
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596	r400sc_scissor_rect_05	0:0:2	2002Oct15

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597	r400sc_scissor_rect_fc_01	0:0:2	2002Oct15
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598	r400sc_set_state_01	0:0:2	2002Oct15
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599	r400sc_sp_sample_cntl_01	0:0:2	2002Oct15
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600	r400sc_sp_sample_cntl_02	0:0:2	2002Oct15
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601	r400sc_sp_sample_cntl_03	0:0:2	2002Oct15
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602	r400sc_sp_sample_cntl_04	0:0:2	2002Oct15
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603	r400sc_sp_sample_cntl_05	0:0:2	2002Oct15
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604	r400sc_sp_sample_cntl_06	0:0:2	2002Oct15
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605	r400sc_sp_sample_cntl_07	0:0:2	2002Oct15
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606	r400sc_sp_sample_cntl_08	0:0:2	2002Oct15
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607	r400sc_tri_16_par_64_dwords_01	0:0:2	2002Oct15
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608	r400sc_tri_8textures_01	0:0:2	2002Oct15
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609	r400sc_tri_8textures_02	0:0:2	2002Oct15
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610	r400sc_tri_walk_start_vertex_01	0:0:2	2002Oct15
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611	r400sc_tri_walk_start_vertex_02	0:0:2	2002Oct15
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612	r400sc_tri_walk_start_vertex_03	0:0:2	2002Oct15
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613	r400sc_tri_walk_start_vertex_04	0:0:2	2002Oct15
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614	r400sc_tri_walk_start_vertex_05	0:0:2	2002Oct15
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615	r400sc_tri_walk_start_vertex_06	0:0:2	2002Oct15
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616	r400sc_tri_walk_start_vertex_07	0:0:2	2002Oct15
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617	r400sc_tri_walk_start_vertex_08	0:0:2	2002Oct15
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618	r400sc_tri_walk_start_vertex_09	0:0:2	2002Oct15
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619	r400sc_tri_walk_start_vertex_10	0:0:2	2002Oct15
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620	r400sc_tri_walk_start_vertex_11	0:0:2	2002Oct15

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621	r400sc_tri_walk_start_vertex_12	0:0:2	2002Oct15
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622	r400sc_tri_walk_start_vertex_13	0:0:2	2002Oct15
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623	r400sc_tri_walk_start_vertex_14	0:0:2	2002Oct15
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624	r400sc_tri_walk_start_vertex_15	0:0:2	2002Oct15
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625	r400sc_tri_walk_start_vertex_16	0:0:2	2002Oct15
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626	r400sc_triangle_stipple_01	0:0:2	2002Oct15
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627	r400sc_window_offset_01	0:0:2	2002Oct15
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628	r400sc_window_offset_02	0:0:2	2002Oct15
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629	r400sc_window_offset_03	0:0:2	2002Oct15
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630	r400sc_window_offset_04	0:0:2	2002Oct15
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631	r400sc_window_offset_05	0:0:2	2002Oct15
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632	r400sc_window_offset_fc_01	0:0:2	2002Oct15
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633	r400sc_zbuffer_line_list_01	0:0:2	2002Oct15
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634	r400sc_zbuffer_point_list_01	0:0:2	2002Oct15
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635	r400sc_zbuffer_rectangle_list_01	0:0:2	2002Oct15
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636	r400sc_zbuffer_rectangle_list_02	0:0:2	2002Oct15
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637	r400sc_zbuffer_rectangle_list_fc_02	0:0:2	2002Oct15
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638	r400sc_zbuffer_triangle_list_01	0:0:2	2002Oct15
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639	r400su_4tri_text_offscreen_01	0:0:2	2002Oct15
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640	r400su_baryc_test_01	0:0:2	2002Oct15
PASS			
641	r400su_baryc_test_02	0:0:2	2002Oct15
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642	r400su_baryc_test_03	0:0:2	2002Oct15
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643	r400su_baryc_test_04	0:0:2	2002Oct15
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644	r400su_clip_baryc_test_01	0:0:2	2002Oct15

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645	r400su_clip_baryc_test_02	0:0:2	2002Oct15
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646	r400su_clip_edgeflag_polymode_01	0:0:2	2002Oct15
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647	r400su_clip_pointsize_test_01	0:0:2	2002Oct15
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648	r400su_clip_pointtest_01	0:0:2	2002Oct15
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649	r400su_clip_pointtest_02	0:0:2	2002Oct15
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650	r400su_clip_pointtest_03	0:0:2	2002Oct15
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651	r400su_clip_pointtest_04	0:0:2	2002Oct15
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652	r400su_clip_polymode_test_01	0:0:2	2002Oct15
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653	r400su_clipline_01	0:0:2	2002Oct15
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654	r400su_clippoint_01	0:0:2	2002Oct15
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655	r400su_clipvertexsortingfunctional_01	0:0:2	2002Oct15
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656	r400su_cullingfunctional_01	0:0:2	2002Oct15
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657	r400su_degentri_test_01	0:0:2	2002Oct15
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658	r400su_degentri_test_02	0:0:2	2002Oct15
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659	r400su_degentri_test_03	0:0:2	2002Oct15
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660	r400su_degentri_test_04	0:0:2	2002Oct15
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661	r400su_edge_flag_01	0:0:2	2002Oct15
PASS			
662	r400su_edge_flag_02	0:0:2	2002Oct15
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663	r400su_edgeflags_triangle_01	0:0:2	2002Oct15
PASS			
664	r400su_edgeflags_triangle_02	0:0:2	2002Oct15
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665	r400su_geom_sort_01	0:0:2	2002Oct15
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666	r400su_line_clip_end_cap_01	0:0:2	2002Oct15
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667	r400su_line_clip_orientation_01	0:0:2	2002Oct15
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668	r400su_line_clip_orientation_02	0:0:2	2002Oct15

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669	r400su_line_clip_x_major_01	0:0:2	2002Oct15
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670	r400su_line_end_cap_functional_01	0:0:2	2002Oct15
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671	r400su_line_end_cap_width_functional_02	0:0:2	2002Oct15
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672	r400su_line_orientation_01	0:0:2	2002Oct15
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673	r400su_line_orientation_02	0:0:2	2002Oct15
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674	r400su_line_orientation_dx01_01	0:0:2	2002Oct15
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675	r400su_line_orientation_dx01_02	0:0:2	2002Oct15
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676	r400su_line_orientation_dy01_01	0:0:2	2002Oct15
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677	r400su_line_orientation_dy01_02	0:0:2	2002Oct15
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678	r400su_line_test_01	0:0:2	2002Oct15
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679	r400su_line_test_02	0:0:2	2002Oct15
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680	r400su_line_x_major_01	0:0:2	2002Oct15
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681	r400su_line_x_major_02	0:0:2	2002Oct15
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682	r400su_line_y_major_01	0:0:2	2002Oct15
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683	r400su_line_y_major_02	0:0:2	2002Oct15
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684	r400su_longstrip_01	0:0:2	2002Oct15
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685	r400su_multi_context_01	0:0:2	2002Oct15
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686	r400su_multi_prim_01	0:0:2	2002Oct15
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687	r400su_multi_prim_02	0:0:2	2002Oct15
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688	r400su_parallel_orientation_all_01	0:0:2	2002Oct15
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689	r400su_parallel_orientation_all_02	0:0:2	2002Oct15
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690	r400su_pc_management_01	0:0:2	2002Oct15
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691	r400su_pc_management_02	0:0:2	2002Oct15
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692	r400su_pc_management_03	0:0:2	2002Oct15

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693	r400su_point_sprite_01	0:0:2	2002Oct15
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694	r400su_point_sprite_02	0:0:2	2002Oct15
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695	r400su_point_sprite_03	0:0:2	2002Oct15
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696	r400su_point_sprite_04	0:0:2	2002Oct15
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697	r400su_point_sprite_05	0:0:2	2002Oct15
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698	r400su_point_sprite_06	0:0:2	2002Oct15
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699	r400su_point_sprite_07	0:0:2	2002Oct15
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700	r400su_point_sprite_08	0:0:2	2002Oct15
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701	r400su_point_wl6_h1_functional_01	0:0:2	2002Oct15
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702	r400su_point_wl_h16_functional_01	0:0:2	2002Oct15
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703	r400su_pointsizepresent_01	0:0:2	2002Oct15
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704	r400su_pointsizepresent_02	0:0:2	2002Oct15
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705	r400su_polymode_lines_degen_triangle_01	0:0:2	2002Oct15
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706	r400su_polymode_lines_degen_triangle_02	0:0:2	2002Oct15
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707	r400su_polymode_lines_zero_area_triangle_01	0:0:2	2002Oct15
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708	r400su_polymode_lines_zero_area_triangle_02	0:0:2	2002Oct15
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709	r400su_polymode_points_degen_triangle_01	0:0:2	2002Oct15
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710	r400su_polymode_points_degen_triangle_02	0:0:2	2002Oct15
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711	r400su_polymode_points_zero_area_triangle_01	0:0:2	2002Oct15
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712	r400su_polymode_points_zero_area_triangle_02	0:0:2	2002Oct15
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713	r400su_polymode_rectangle_01	0:0:2	2002Oct15
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714	r400su_polymode_zero_area_triangle_01	0:0:2	2002Oct15
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715	r400su_polymode_zero_area_triangle_02	0:0:2	2002Oct15
PASS			
716	r400su_polymode_zero_area_triangle_03	0:0:2	2002Oct15

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717	r400su_polymode_zero_area_triangle_04	0:0:2	2002Oct15
PASS			
718	r400su_polymodeculling_01	0:0:2	2002Oct15
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719	r400su_polymodefunctional_01	0:0:2	2002Oct15
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720	r400su_provoking_vtx_edgeflags_triangle_01	0:0:2	2002Oct15
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721	r400su_provoking_vtx_edgeflags_triangle_02	0:0:2	2002Oct15
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722	r400su_provoking_vtx_edgeflags_triangle_03	0:0:2	2002Oct15
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723	r400su_provoking_vtx_edgeflags_triangle_04	0:0:2	2002Oct15
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724	r400su_provoking_vtx_line_01	0:0:2	2002Oct15
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725	r400su_provoking_vtx_point_01	0:0:2	2002Oct15
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726	r400su_provoking_vtx_polymode_rectangle_01	0:0:2	2002Oct15
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727	r400su_provoking_vtx_rectangle_01	0:0:2	2002Oct15
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728	r400su_provoking_vtx_triangle_01	0:0:2	2002Oct15
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729	r400su_rand_line_01	0:0:2	2002Oct15
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730	r400su_rand_point_01	0:0:2	2002Oct15
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731	r400su_rand_tri_01	0:0:2	2002Oct15
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732	r400su_rectangle_01	0:0:2	2002Oct15
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733	r400su_rectangle_list_01	0:0:2	2002Oct15
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734	r400su_sliver_01	0:0:2	2002Oct15
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735	r400su_triarea_test_01	0:0:2	2002Oct15
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736	r400su_triarea_test_02	0:0:2	2002Oct15
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737	r400su_triarea_test_03	0:0:2	2002Oct15
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738	r400su_triarea_test_04	0:0:2	2002Oct15
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739	r400su_vertexsortingfunctional_01	0:0:2	2002Oct15
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740	r400su_w_grad_test_01	0:0:2	2002Oct15

PASS			
741	r400su_w_grad_test_02	0:0:2	2002Oct15
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742	r400su_w_grad_test_03	0:0:2	2002Oct15
PASS			
743	r400su_z_grad_test_01	0:0:2	2002Oct15
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744	r400su_z_grad_test_02	0:0:2	2002Oct15
PASS			
745	r400su_z_grad_test_03	0:0:2	2002Oct15
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746	r400su_zero_area_test_01	0:0:2	2002Oct15
PASS			
747	r400su_zero_area_test_02	0:0:2	2002Oct15
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748	r400su_zero_area_test_03	0:0:2	2002Oct15
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749	r400su_zero_area_test_04	0:0:2	2002Oct15
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750	r400vgt_auto_index_line_list_01	0:0:2	2002Oct15
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751	r400vgt_auto_index_line_loop_01	0:0:2	2002Oct15
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752	r400vgt_auto_index_line_strip_01	0:0:2	2002Oct15
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753	r400vgt_auto_index_points_01	0:0:2	2002Oct15
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754	r400vgt_auto_index_polygon_01	0:0:2	2002Oct15
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755	r400vgt_auto_index_primitives_01	0:0:2	2002Oct15
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756	r400vgt_auto_index_quad_list_01	0:0:2	2002Oct15
PASS			
757	r400vgt_auto_index_quad_strip_01	0:0:2	2002Oct15
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758	r400vgt_auto_index_rectangle_list_01	0:0:2	2002Oct15
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759	r400vgt_auto_index_tri_fan_01	0:0:2	2002Oct15
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760	r400vgt_auto_index_tri_list_01	0:0:2	2002Oct15
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761	r400vgt_auto_index_tri_strip_01	0:0:2	2002Oct15
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762	r400vgt_auto_index_tri_wflags_01	0:0:2	2002Oct15
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763	r400vgt_dma_engine_01	0:0:2	2002Oct15
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771	r400vgt_dma_engine_09	0:0:2	2002Oct15
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773	r400vgt_dma_index_line_list_01	0:0:2	2002Oct15
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774	r400vgt_dma_index_line_loop_01	0:0:2	2002Oct15
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775	r400vgt_dma_index_line_strip_01	0:0:2	2002Oct15
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776	r400vgt_dma_index_points_01	0:0:2	2002Oct15
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785	r400vgt_dma_index_tri_wflags_01	0:0:2	2002Oct15
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786	r400vgt_dma_swap_idx16_01	0:0:2	2002Oct15
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787	r400vgt_dma_swap_idx32_01	0:0:2	2002Oct15
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788	r400vgt_edgeflags_polygon_01	0:0:2	2002Oct15

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789	r400vgt_edgeflags_quad_list_01	0:0:2	2002Oct15
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791	r400vgt_edgeflags_triangle_fan_01	0:0:2	2002Oct15
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792	r400vgt_edgeflags_triangle_list_01	0:0:2	2002Oct15
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793	r400vgt_edgeflags_triangle_strip_01	0:0:2	2002Oct15
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794	r400vgt_edgeflags_triangle_wflags_01	0:0:2	2002Oct15
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795	r400vgt_event_handling_01	0:0:2	2002Oct15
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796	r400vgt_event_handling_02	0:0:2	2002Oct15
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797	r400vgt_event_handling_03	0:0:2	2002Oct15
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798	r400vgt_event_handling_04	0:0:2	2002Oct15
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801	r400vgt_ext2int_index_line_strip_01	0:0:2	2002Oct15
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805	r400vgt_ext2int_index_quad_strip_01	0:0:2	2002Oct15
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806	r400vgt_ext2int_index_rectangle_list_01	0:0:2	2002Oct15
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807	r400vgt_ext2int_index_triangle_fan_01	0:0:2	2002Oct15
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808	r400vgt_ext2int_index_triangle_list_01	0:0:2	2002Oct15
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810	r400vgt_ext2int_index_triangle_wflags_01	0:0:2	2002Oct15
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812	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	0:0:2	2002Oct15

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813	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	0:0:2	2002Oct15
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814	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	0:0:2	2002Oct15
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815	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	0:0:2	2002Oct15
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816	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	0:0:2	2002Oct15
PASS			
817	r400vgt_hos_PNQ_lp_cont_no_projection_01	0:0:2	2002Oct15
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818	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	0:0:2	2002Oct15
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819	r400vgt_hos_PNT_cont_cp_qn_complex_01	0:0:2	2002Oct15
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820	r400vgt_hos_PNT_cont_cp_qn_precision_01	0:0:2	2002Oct15
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821	r400vgt_hos_PNT_cont_cp_qn_precision_02	0:0:2	2002Oct15
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822	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	0:0:2	2002Oct15
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823	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	0:0:2	2002Oct15
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824	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	0:0:2	2002Oct15
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825	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	0:0:2	2002Oct15
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826	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	0:0:2	2002Oct15
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827	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	0:0:2	2002Oct15
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828	r400vgt_hos_PNT_disc_cp_qn_complex_01	0:0:2	2002Oct15
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829	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	0:0:2	2002Oct15
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830	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	0:0:2	2002Oct15
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831	r400vgt_hos_PNT_disc_cp_qn_precision_01	0:0:2	2002Oct15
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832	r400vgt_hos_PNT_disc_cp_qn_precision_02	0:0:2	2002Oct15
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833	r400vgt_hos_PNT_edge_detection_01	0:0:2	2002Oct15
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834	r400vgt_hos_PNT_lp_cont_no_projection_01	0:0:2	2002Oct15
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835	r400vgt_hos_RPatch_cp_02	0:0:2	2002Oct15
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836	r400vgt_hos_RPatch_lp_02	0:0:2	2002Oct15

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837	r400vgt_hos_TPatch_01	0:0:2	2002Oct15
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838	r400vgt_hos_TPatch_02	0:0:2	2002Oct15
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840	r400vgt_hos_auto_index_quad_list_01	0:0:2	2002Oct15
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841	r400vgt_hos_auto_index_triangle_list_01	0:0:2	2002Oct15
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842	r400vgt_hos_cubic_pos_pnt_discrete_01	0:0:2	2002Oct15
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843	r400vgt_hos_simple_linear_PNT_discrete_01	0:0:2	2002Oct15
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844	r400vgt_immed_index_line_list_01	0:0:2	2002Oct15
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845	r400vgt_immed_index_line_loop_01	0:0:2	2002Oct15
PASS			
846	r400vgt_immed_index_line_strip_01	0:0:2	2002Oct15
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847	r400vgt_immed_index_points_01	0:0:2	2002Oct15
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848	r400vgt_immed_index_polygon_01	0:0:2	2002Oct15
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849	r400vgt_immed_index_primitives_01	0:0:2	2002Oct15
PASS			
850	r400vgt_immed_index_quad_list_01	0:0:2	2002Oct15
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851	r400vgt_immed_index_quad_strip_01	0:0:2	2002Oct15
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852	r400vgt_immed_index_rectangle_list_01	0:0:2	2002Oct15
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853	r400vgt_immed_index_tri_fan_01	0:0:2	2002Oct15
PASS			
854	r400vgt_immed_index_tri_list_01	0:0:2	2002Oct15
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855	r400vgt_immed_index_tri_strip_01	0:0:2	2002Oct15
PASS			
856	r400vgt_immed_index_tri_wflags_01	0:0:2	2002Oct15
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857	r400vgt_index_dealloc_line_list_01	0:0:2	2002Oct15
PASS			
858	r400vgt_index_dealloc_points_01	0:0:2	2002Oct15
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859	r400vgt_index_dealloc_triangle_list_01	0:0:2	2002Oct15
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860	r400vgt_index_min_max_01	0:0:2	2002Oct15

PASS			
861	r400vgt_index_min_max_02	0:0:2	2002Oct15
PASS			
862	r400vgt_index_min_max_03	0:0:2	2002Oct15
PASS			
863	r400vgt_index_min_max_04	0:0:2	2002Oct15
PASS			
864	r400vgt_index_offset_01	0:0:2	2002Oct15
PASS			
865	r400vgt_index_offset_02	0:0:2	2002Oct15
PASS			
866	r400vgt_index_offset_03	0:0:2	2002Oct15
PASS			
867	r400vgt_index_offset_04	0:0:2	2002Oct15
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868	r400vgt_index_offset_05	0:0:2	2002Oct15
PASS			
869	r400vgt_index_offset_06	0:0:2	2002Oct15
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870	r400vgt_index_offset_07	0:0:2	2002Oct15
PASS			
871	r400vgt_index_offset_08	0:0:2	2002Oct15
PASS			
872	r400vgt_index_size_01	0:0:2	2002Oct15
PASS			
873	r400vgt_index_size_02	0:0:2	2002Oct15
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874	r400vgt_index_source_switch_01	0:0:2	2002Oct15
PASS			
875	r400vgt_line_list_01	0:0:2	2002Oct15
PASS			
876	r400vgt_line_list_02	0:0:2	2002Oct15
PASS			
877	r400vgt_line_loop_01	0:0:2	2002Oct15
PASS			
878	r400vgt_line_loop_02	0:0:2	2002Oct15
PASS			
879	r400vgt_line_strip_01	0:0:2	2002Oct15
PASS			
880	r400vgt_line_strip_02	0:0:2	2002Oct15
PASS			
881	r400vgt_multi_context_01	0:0:2	2002Oct15
PASS			
882	r400vgt_multi_context_02	0:0:2	2002Oct15
PASS			
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PASS			
884	r400vgt_multi_context_04	0:0:2	2002Oct15

PASS			
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PASS			
886	r400vgt_multi_context_06	0:0:2	2002Oct15
PASS			
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PASS			
888	r400vgt_multi_context_08	0:0:2	2002Oct15
PASS			
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PASS			
890	r400vgt_multi_context_10	0:0:2	2002Oct15
PASS			
891	r400vgt_multi_context_11	0:0:2	2002Oct15
PASS			
892	r400vgt_multi_context_12	0:0:2	2002Oct15
PASS			
893	r400vgt_multi_prim_reset_index_all_01	0:0:2	2002Oct15
PASS			
894	r400vgt_multi_prim_reset_index_all_02	0:0:2	2002Oct15
PASS			
895	r400vgt_pass_thru_all_prims_01	0:0:2	2002Oct15
PASS			
896	r400vgt_point_list_01	0:0:2	2002Oct15
PASS			
897	r400vgt_point_list_02	0:0:2	2002Oct15
PASS			
898	r400vgt_polygon_01	0:0:2	2002Oct15
PASS			
899	r400vgt_polygon_02	0:0:2	2002Oct15
PASS			
900	r400vgt_provoking_vtx_all_01	0:0:2	2002Oct15
PASS			
901	r400vgt_provoking_vtx_edgeflags_all_01	0:0:2	2002Oct15
PASS			
902	r400vgt_provoking_vtx_polygon_01	0:0:2	2002Oct15
PASS			
903	r400vgt_provoking_vtx_quad_list_01	0:0:2	2002Oct15
PASS			
904	r400vgt_provoking_vtx_quad_strip_01	0:0:2	2002Oct15
PASS			
905	r400vgt_provoking_vtx_tri_fan_01	0:0:2	2002Oct15
PASS			
906	r400vgt_provoking_vtx_tri_strip_01	0:0:2	2002Oct15
PASS			
907	r400vgt_quad_list_01	0:0:2	2002Oct15
PASS			
908	r400vgt_quad_list_02	0:0:2	2002Oct15

PASS			
909	r400vgt_quad_strip_01	0:0:2	2002Oct15
PASS			
910	r400vgt_quad_strip_02	0:0:2	2002Oct15
PASS			
911	r400vgt_rectangle_list_01	0:0:2	2002Oct15
PASS			
912	r400vgt_rectangle_list_02	0:0:2	2002Oct15
PASS			
913	r400vgt_reuse_depth_line_list_01	0:0:2	2002Oct15
PASS			
914	r400vgt_reuse_depth_point_list_01	0:0:2	2002Oct15
PASS			
915	r400vgt_reuse_depth_triangle_list_01	0:0:2	2002Oct15
PASS			
916	r400vgt_reuse_index_line_list_01	0:0:2	2002Oct15
PASS			
917	r400vgt_reuse_index_point_list_01	0:0:2	2002Oct15
PASS			
918	r400vgt_reuse_index_triangle_list_01	0:0:2	2002Oct15
PASS			
919	r400vgt_reuse_index_triangle_list_02	0:0:2	2002Oct15
PASS			
920	r400vgt_reuse_index_triangle_list_03	0:0:2	2002Oct15
PASS			
921	r400vgt_suppress_eop_01	0:0:1	2002Oct15
PASS			
922	r400vgt_suppress_eop_02	0:0:1	2002Oct15
PASS			
923	r400vgt_suppress_eop_03	0:0:1	2002Oct15
PASS			
924	r400vgt_suppress_eop_04	0:0:1	2002Oct15
PASS			
925	r400vgt_suppress_eop_05	0:0:1	2002Oct15
PASS			
926	r400vgt_triangle_fan_01	0:0:1	2002Oct15
PASS			
927	r400vgt_triangle_fan_02	0:0:1	2002Oct15
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928	r400vgt_triangle_list_01	0:0:1	2002Oct15
PASS			
929	r400vgt_triangle_list_02	0:0:1	2002Oct15
PASS			
930	r400vgt_triangle_strip_01	0:0:1	2002Oct15
PASS			
931	r400vgt_triangle_strip_02	0:0:1	2002Oct15
PASS			
932	r400vgt_triangle_wflags_01	0:0:1	2002Oct15

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PASS
933 r400vgt_triangle_wflags_02          0:0:1    2002Oct15
PASS
934 r400vgt_viz_query_01                0:0:1    2002Oct15
PASS
935 r400vgt_vtx_vector_packing_01      0:0:1    2002Oct15
PASS
936 r400vte_coverage_01                 0:0:1    2002Oct15
PASS
937 r400vte_simple_point_01            0:0:1    2002Oct15
PASS
938 r400vte_simple_triangle_01         0:0:1    2002Oct15
PASS
939 r400vte_w0_fmt_01                   0:0:1    2002Oct15
PASS
940 r400vte_w0_fmt_03                   0:0:1    2002Oct15
PASS
941 r400vte_xy_fmt_01                   0:0:1    2002Oct15
PASS
942 r400vte_xy_fmt_mk_01_bug            0:0:1    2002Oct9
PASS
943 r400vte_xyz_scale_01                0:0:1    2002Oct15
PASS
944 r400vte_xyz_scale_02                0:0:1    2002Oct15
PASS
945 r400vte_z_fmt_01                    0:0:1    2002Oct15
PASS

```

```

=====
Tests_Run_count = 945 Pass_count = 935 Fail_count = 1 Incomplete_count = 9
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```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep19
PASS			
2	cp_e2blit_brush_mt_ropcc		2002Sep19
PASS			
3	cp_e2_hostdata_blt_pntr_8888		2002Sep19
PASS			
4	cp_e2_one_blit		2002Sep19
PASS			
5	cp_e2_one_line		2002Sep19
PASS			
6	cp_e2paint_565		2002Sep19
PASS			
7	cp_e2paint_multi		2002Sep19
PASS			
8	cp_e2polyscanlines_brush		2002Sep19
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep19
PASS			
10	cp_e2smalltext_jcl		2002Sep19
PASS			
11	cp_simple_triangle		2002Sep19
PASS			
12	primlib_1st_tri_june15		2002Sep19
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep19
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep19
PASS			
15	primlib_parameterized_simple_triangle		2002Sep19
PASS			
16	primlib_template_simple_triangle		2002Sep19
PASS			
17	primlib_tex_tri		2002Sep19
PASS			
18	r400cl_bary_texture_08_bug		2002Sep19
PASS			
19	r400cl_clip_10_verts_01		2002Sep19
PASS			
20	r400cl_clip_disable_01		2002Sep19

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep19
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep19
PASS		
23	r400cl_cull_only_ena_01	2002Sep19
PASS		
24	r400cl_edgeflags_01	2002Sep19
PASS		
25	r400cl_edgeflags_01_bug	2002Sep19
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep19
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep19
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep19
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep19
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep19
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep19
PASS		
32	r400cl_edgeflags_gband_01	2002Sep19
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep19
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep19
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep19
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep19
PASS		
37	r400cl_frustum_01	2002Sep19
PASS		
38	r400cl_frustum_02	2002Sep19
PASS		
39	r400cl_frustum_03	2002Sep19
PASS		
40	r400cl_frustum_04	2002Sep19
PASS		
41	r400cl_frustum_05	2002Sep19
PASS		
42	r400cl_frustum_06	2002Sep19
PASS		
43	r400cl_frustum_07	2002Sep19
PASS		
44	r400cl_frustum_08	2002Sep19

PASS		
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PASS		
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63	r400cl_frustum_27	2002Sep19
PASS		
64	r400cl_frustum_28	2002Sep19
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep19
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep19
PASS		
67	r400cl_frustum_lines_01	2002Sep19
PASS		
68	r400cl_frustum_lines_02	2002Sep19

PASS		
69	r400cl_frustum_lines_03	2002Sep19
PASS		
70	r400cl_frustum_lines_04	2002Sep19
PASS		
71	r400cl_frustum_lines_05	2002Sep19
PASS		
72	r400cl_frustum_lines_06	2002Sep19
PASS		
73	r400cl_frustum_lines_07	2002Sep19
PASS		
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405	r400su_4tri_text_offscreen_01	2002Sep19
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406	r400su_baryc_test_01	2002Sep19
PASS		
407	r400su_baryc_test_02	2002Sep19
PASS		
408	r400su_baryc_test_03	2002Sep19
PASS		
409	r400su_baryc_test_04	2002Sep19
PASS		
410	r400su_clip_baryc_test_01	2002Sep19
PASS		
411	r400su_clip_baryc_test_02	2002Sep19
PASS		
412	r400su_clip_edgflag_polymode_01	2002Sep19
PASS		
413	r400su_clipline_01	2002Sep19
PASS		
414	r400su_clippoint_01	2002Sep19
PASS		
415	r400su_clip_pointsize_test_01	2002Sep19
PASS		
416	r400su_clip_pointttest_01	2002Sep19
PASS		
417	r400su_clip_pointttest_02	2002Sep19
PASS		
418	r400su_clip_pointttest_03	2002Sep19
PASS		
419	r400su_clip_pointttest_04	2002Sep19
PASS		
420	r400su_clip_polymode_test_01	2002Sep19
PASS		
421	r400su_clipvertexasortingfunctional_01	2002Sep19
PASS		
422	r400su_cullingfunctional_01	2002Sep19
PASS		
423	r400su_degentri_test_01	2002Sep19
PASS		
424	r400su_degentri_test_02	2002Sep19
PASS		
425	r400su_degentri_test_03	2002Sep19
PASS		
426	r400su_edge_flag_01	2002Sep19
PASS		
427	r400su_edge_flag_02	2002Sep19
PASS		
428	r400su_edgflags_triangle_01	2002Sep19

PASS		
429	r400su_edgeflags_triangle_02	2002Sep19
PASS		
430	r400su_geom_sort_01	2002Sep19
PASS		
431	r400su_line_end_cap_functional_01	2002Sep19
PASS		
432	r400su_line_end_cap_width_functional_02	2002Sep19
PASS		
433	r400su_line_test_01	2002Sep19
PASS		
434	r400su_line_test_02	2002Sep19
PASS		
435	r400su_longstrip_01	2002Sep19
PASS		
436	r400su_multi_context_01	2002Sep19
PASS		
437	r400su_multi_prim_01	2002Sep19
PASS		
438	r400su_multi_prim_02	2002Sep19
PASS		
439	r400su_parallel_orientation_all_01	2002Sep19
PASS		
440	r400su_parallel_orientation_all_02	2002Sep19
PASS		
441	r400su_pc_management_01	2002Sep19
PASS		
442	r400su_pc_management_02	2002Sep19
PASS		
443	r400su_pc_management_03	2002Sep19
PASS		
444	r400su_pointsizepresent_01	2002Sep19
PASS		
445	r400su_pointsizepresent_02	2002Sep19
PASS		
446	r400su_point_sprite_01	2002Sep19
PASS		
447	r400su_point_sprite_02	2002Sep19
PASS		
448	r400su_point_sprite_03	2002Sep19
PASS		
449	r400su_point_sprite_04	2002Sep19
PASS		
450	r400su_point_sprite_05	2002Sep19
PASS		
451	r400su_point_sprite_06	2002Sep19
PASS		
452	r400su_point_sprite_07	2002Sep19

PASS		
453	r400su_point_sprite_08	2002Sep19
PASS		
454	r400su_point_wl6_h1_functional_01	2002Sep19
PASS		
455	r400su_point_wl_h16_functional_01	2002Sep19
PASS		
456	r400su_polymodeculling_01	2002Sep19
PASS		
457	r400su_polymodefunctional_01	2002Sep19
PASS		
458	r400su_polymode_lines_degen_triangle_01	2002Sep19
PASS		
459	r400su_polymode_lines_degen_triangle_02	2002Sep19
PASS		
460	r400su_polymode_lines_zero_area_triangle_01	2002Sep19
PASS		
461	r400su_polymode_lines_zero_area_triangle_02	2002Sep19
PASS		
462	r400su_polymode_points_degen_triangle_01	2002Sep19
PASS		
463	r400su_polymode_points_degen_triangle_02	2002Sep19
PASS		
464	r400su_polymode_points_zero_area_triangle_01	2002Sep19
PASS		
465	r400su_polymode_points_zero_area_triangle_02	2002Sep19
PASS		
466	r400su_polymode_rectangle_01	2002Sep19
PASS		
467	r400su_polymode_zero_area_triangle_01	2002Sep19
PASS		
468	r400su_polymode_zero_area_triangle_02	2002Sep19
PASS		
469	r400su_polymode_zero_area_triangle_03	2002Sep19
PASS		
470	r400su_polymode_zero_area_triangle_04	2002Sep19
PASS		
471	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep19
PASS		
472	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep19
PASS		
473	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep19
PASS		
474	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep19
PASS		
475	r400su_provoking_vtx_line_01	2002Sep19
PASS		
476	r400su_provoking_vtx_point_01	2002Sep19

PASS		
477	r400su_provoking_vtx_polymode_rectangle_01	2002Sep19
PASS		
478	r400su_provoking_vtx_rectangle_01	2002Sep19
PASS		
479	r400su_provoking_vtx_triangle_01	2002Sep19
PASS		
480	r400su_rand_line_01	2002Sep19
PASS		
481	r400su_rand_point_01	2002Sep19
PASS		
482	r400su_rand_tri_01	2002Sep19
PASS		
483	r400su_rectangle_list_01	2002Sep19
PASS		
484	r400su_sliver_01	2002Sep19
PASS		
485	r400su_triarea_test_01	2002Sep19
PASS		
486	r400su_triarea_test_02	2002Sep19
PASS		
487	r400su_triarea_test_03	2002Sep19
PASS		
488	r400su_triarea_test_04	2002Sep19
PASS		
489	r400su_vertexsortingfunctional_01	2002Sep19
PASS		
490	r400vgt_auto_index_line_list_01	2002Sep19
PASS		
491	r400vgt_auto_index_line_loop_01	2002Sep19
PASS		
492	r400vgt_auto_index_line_strip_01	2002Sep19
PASS		
493	r400vgt_auto_index_points_01	2002Sep19
PASS		
494	r400vgt_auto_index_polygon_01	2002Sep19
PASS		
495	r400vgt_auto_index_primitives_01	2002Sep19
PASS		
496	r400vgt_auto_index_quad_list_01	2002Sep19
PASS		
497	r400vgt_auto_index_quad_strip_01	2002Sep19
PASS		
498	r400vgt_auto_index_rectangle_list_01	2002Sep19
PASS		
499	r400vgt_auto_index_tri_fan_01	2002Sep19
PASS		
500	r400vgt_auto_index_tri_list_01	2002Sep19

PASS		
501	r400vgt_auto_index_tri_strip_01	2002Sep19
PASS		
502	r400vgt_auto_index_tri_wflags_01	2002Sep19
PASS		
503	r400vgt_dma_engine_01	2002Sep19
PASS		
504	r400vgt_dma_engine_02	2002Sep19
PASS		
505	r400vgt_dma_engine_03	2002Sep19
PASS		
506	r400vgt_dma_engine_04	2002Sep19
PASS		
507	r400vgt_dma_engine_05	2002Sep19
PASS		
508	r400vgt_dma_engine_06	2002Sep19
PASS		
509	r400vgt_dma_engine_07	2002Sep19
PASS		
510	r400vgt_dma_engine_08	2002Sep19
PASS		
511	r400vgt_dma_engine_09	2002Sep19
PASS		
512	r400vgt_dma_engine_10	2002Sep19
PASS		
513	r400vgt_dma_index_line_list_01	2002Sep19
PASS		
514	r400vgt_dma_index_line_loop_01	2002Sep19
PASS		
515	r400vgt_dma_index_line_strip_01	2002Sep19
PASS		
516	r400vgt_dma_index_points_01	2002Sep19
PASS		
517	r400vgt_dma_index_polygon_01	2002Sep19
PASS		
518	r400vgt_dma_index_primitives_01	2002Sep19
PASS		
519	r400vgt_dma_index_quad_list_01	2002Sep19
PASS		
520	r400vgt_dma_index_quad_strip_01	2002Sep19
PASS		
521	r400vgt_dma_index_rectangle_list_01	2002Sep19
PASS		
522	r400vgt_dma_index_tri_fan_01	2002Sep19
PASS		
523	r400vgt_dma_index_tri_list_01	2002Sep19
PASS		
524	r400vgt_dma_index_tri_strip_01	2002Sep19

PASS		
525	r400vgt_dma_index_tri_wflags_01	2002Sep19
PASS		
526	r400vgt_dma_swap_indx16_01	2002Sep19
PASS		
527	r400vgt_dma_swap_indx32_01	2002Sep19
PASS		
528	r400vgt_edgeflags_polygon_01	2002Sep19
PASS		
529	r400vgt_edgeflags_quad_list_01	2002Sep19
PASS		
530	r400vgt_edgeflags_quad_strip_01	2002Sep19
PASS		
531	r400vgt_edgeflags_triangle_fan_01	2002Sep19
PASS		
532	r400vgt_edgeflags_triangle_list_01	2002Sep19
PASS		
533	r400vgt_edgeflags_triangle_strip_01	2002Sep19
PASS		
534	r400vgt_edgeflags_triangle_wflags_01	2002Sep19
PASS		
535	r400vgt_ext2int_index_line_list_01	2002Sep19
PASS		
536	r400vgt_ext2int_index_line_loop_01	2002Sep19
PASS		
537	r400vgt_ext2int_index_line_strip_01	2002Sep19
PASS		
538	r400vgt_ext2int_index_points_01	2002Sep19
PASS		
539	r400vgt_ext2int_index_polygon_01	2002Sep19
PASS		
540	r400vgt_ext2int_index_quad_list_01	2002Sep19
PASS		
541	r400vgt_ext2int_index_quad_strip_01	2002Sep19
PASS		
542	r400vgt_ext2int_index_triangle_fan_01	2002Sep19
PASS		
543	r400vgt_ext2int_index_triangle_list_01	2002Sep19
PASS		
544	r400vgt_ext2int_index_triangle_strip_01	2002Sep19
PASS		
545	r400vgt_hos_auto_index_line_list_01	2002Sep19
PASS		
546	r400vgt_hos_auto_index_quad_list_01	2002Sep19
PASS		
547	r400vgt_hos_auto_index_triangle_list_01	2002Sep19
PASS		
548	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep19

PASS
549 r400vgt_hos_LPatch_01 2002Sep19
PASS
550 r400vgt_hos_PNL_cp_ln_cont_no_projection_01 2002Sep19
PASS
551 r400vgt_hos_PNL_lp_ln_cont_no_projection_01 2002Sep19
PASS
552 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01 2002Sep19
PASS
553 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02 2002Sep19
PASS
554 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01 2002Sep19
PASS
555 r400vgt_hos_PNQ_lp_cont_no_projection_01 2002Sep19
PASS
556 r400vgt_hos_PNT_cont_cp_qn_complex_01 2002Sep19
PASS
557 r400vgt_hos_PNT_cont_cp_qn_precision_01 2002Sep19
PASS
558 r400vgt_hos_PNT_cont_cp_qn_precision_02 2002Sep19
PASS
559 r400vgt_hos_PNT_cp_qn_cont_light_texture_01 2002Sep19
PASS
560 r400vgt_hos_PNT_cp_qn_cont_light_texture_02 2002Sep19
PASS
561 r400vgt_hos_PNT_cp_qn_cont_light_texture_03 2002Sep19
PASS
562 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01 2002Sep19
PASS
563 r400vgt_hos_PNT_cp_qn_cont_no_projection_01 2002Sep19
PASS
564 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01 2002Sep19
PASS
565 r400vgt_hos_PNT_disc_cp_qn_complex_01 2002Sep19
PASS
566 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Sep19
PASS
567 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Sep19
PASS
568 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Sep19
PASS
569 r400vgt_hos_PNT_disc_cp_qn_precision_02 2002Sep19
PASS
570 r400vgt_hos_PNT_edge_detection_01 2002Sep19
PASS
571 r400vgt_hos_PNT_lp_cont_no_projection_01 2002Sep19
PASS
572 r400vgt_hos_PNTQL_cp_qn_cont_stress_01 2002Sep19

PASS		
573	r400vgt_hos_RPatch_cp_02	2002Sep19
PASS		
574	r400vgt_hos_RPatch_lp_02	2002Sep19
PASS		
575	r400vgt_immed_index_line_list_01	2002Sep19
PASS		
576	r400vgt_immed_index_line_loop_01	2002Sep19
PASS		
577	r400vgt_immed_index_line_strip_01	2002Sep19
PASS		
578	r400vgt_immed_index_points_01	2002Sep19
PASS		
579	r400vgt_immed_index_polygon_01	2002Sep19
PASS		
580	r400vgt_immed_index_primitives_01	2002Sep19
PASS		
581	r400vgt_immed_index_quad_list_01	2002Sep19
PASS		
582	r400vgt_immed_index_quad_strip_01	2002Sep19
PASS		
583	r400vgt_immed_index_rectangle_list_01	2002Sep19
PASS		
584	r400vgt_immed_index_tri_fan_01	2002Sep19
PASS		
585	r400vgt_immed_index_tri_list_01	2002Sep19
PASS		
586	r400vgt_immed_index_tri_strip_01	2002Sep19
PASS		
587	r400vgt_immed_index_tri_wflags_01	2002Sep19
PASS		
588	r400vgt_index_dealloc_line_list_01	2002Sep19
PASS		
589	r400vgt_index_dealloc_points_01	2002Sep19
PASS		
590	r400vgt_index_dealloc_triangle_list_01	2002Sep19
PASS		
591	r400vgt_index_min_max_01	2002Sep19
PASS		
592	r400vgt_index_min_max_02	2002Sep19
PASS		
593	r400vgt_index_offset_01	2002Sep19
PASS		
594	r400vgt_index_offset_02	2002Sep19
PASS		
595	r400vgt_index_offset_03	2002Sep19
PASS		
596	r400vgt_index_size_01	2002Sep19

PASS		
597	r400vgt_index_source_switch_01	2002Sep19
PASS		
598	r400vgt_line_list_01	2002Sep19
PASS		
599	r400vgt_line_list_02	2002Sep19
PASS		
600	r400vgt_line_loop_01	2002Sep19
PASS		
601	r400vgt_line_loop_02	2002Sep19
PASS		
602	r400vgt_line_strip_01	2002Sep19
PASS		
603	r400vgt_line_strip_02	2002Sep19
PASS		
604	r400vgt_multi_context_01	2002Sep19
PASS		
605	r400vgt_multi_context_02	2002Sep19
PASS		
606	r400vgt_multi_context_03	2002Sep19
PASS		
607	r400vgt_multi_context_04	2002Sep19
PASS		
608	r400vgt_multi_context_05	2002Sep19
PASS		
609	r400vgt_multi_context_06	2002Sep19
PASS		
610	r400vgt_multi_context_07	2002Sep19
PASS		
611	r400vgt_multi_context_08	2002Sep19
PASS		
612	r400vgt_multi_context_09	2002Sep19
PASS		
613	r400vgt_multi_context_10	2002Sep19
PASS		
614	r400vgt_multi_context_11	2002Sep19
PASS		
615	r400vgt_multi_context_12	2002Sep19
PASS		
616	r400vgt_multi_prim_reset_index_all_01	2002Sep19
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
617	r400vgt_pass_thru_all_prims_01	2002Sep19
PASS		
618	r400vgt_point_list_01	2002Sep19
PASS		
619	r400vgt_point_list_02	2002Sep19
PASS		
620	r400vgt_polygon_01	2002Sep19

PASS		
621	r400vgt_polygon_02	2002Sep19
PASS		
622	r400vgt_provoking_vtx_all_01	2002Sep19
PASS		
623	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep19
PASS		
624	r400vgt_provoking_vtx_polygon_01	2002Sep19
PASS		
625	r400vgt_provoking_vtx_quad_list_01	2002Sep19
PASS		
626	r400vgt_provoking_vtx_quad_strip_01	2002Sep19
PASS		
627	r400vgt_provoking_vtx_tri_fan_01	2002Sep19
PASS		
628	r400vgt_provoking_vtx_tri_strip_01	2002Sep19
PASS		
629	r400vgt_quad_list_01	2002Sep19
PASS		
630	r400vgt_quad_strip_01	2002Sep19
PASS		
631	r400vgt_rectangle_list_01	2002Sep19
PASS		
632	r400vgt_reuse_depth_line_list_01	2002Sep19
PASS		
633	r400vgt_reuse_depth_point_list_01	2002Sep19
PASS		
634	r400vgt_reuse_depth_triangle_list_01	2002Sep19
PASS		
635	r400vgt_reuse_index_line_list_01	2002Sep19
PASS		
636	r400vgt_reuse_index_point_list_01	2002Sep19
PASS		
637	r400vgt_reuse_index_triangle_list_01	2002Sep19
PASS		
638	r400vgt_reuse_index_triangle_list_02	2002Sep19
PASS		
639	r400vgt_reuse_index_triangle_list_03	2002Sep19
PASS		
640	r400vgt_suppress_eop_01	2002Sep19
PASS		
641	r400vgt_suppress_eop_02	2002Sep19
PASS		
642	r400vgt_suppress_eop_03	2002Sep19
PASS		
643	r400vgt_suppress_eop_04	2002Sep19
PASS		
644	r400vgt_triangle_fan_01	2002Sep19

PASS
645 r400vgt_triangle_fan_02 2002Sep19
PASS
646 r400vgt_triangle_list_01 2002Sep19
PASS
647 r400vgt_triangle_strip_01 2002Sep19
PASS
648 r400vgt_triangle_strip_02 2002Sep19
PASS
649 r400vgt_triangle_wflags_01 2002Sep19
PASS
650 r400vgt_viz_query_01 2002Sep19
PASS
651 r400vgt_vtx_vector_packing_01 2002Sep19
PASS
652 r400vte_coverage_01 2002Sep19
PASS
653 r400vte_simple_point_01 2002Sep19
PASS
654 r400vte_simple_triangle_01 2002Sep19
PASS
655 r400vte_w0_fmt_01 2002Sep19
PASS
656 r400vte_w0_fmt_03 2002Sep19
PASS
657 r400vte_xy_fmt_01 2002Sep19
PASS
658 r400vte_xy_fmt_mk_01_bug 2002Sep19
PASS
659 r400vte_xyz_scale_01 2002Sep19
PASS
660 r400vte_xyz_scale_02 2002Sep19
PASS
661 r400vte_z_fmt_01 2002Sep19
PASS

=====
Tests_Run_count = 661 Pass_count = 660 Fail_count = 1 Incomplete_count = 0
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Sep 21 15:00:27 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             168         168         156           92
              SC              273         273         272           99
              CL              115         115         115           100
              VTE              10          10           8            80
              SPECIALTY         6           6           5            83
              SU               85          85          83           97
              SANITY            2           2           2           100
+++++
              TOTAL            671         671         653           97
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_1st_tri_june15		2002Aug14
	PASS		
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug14
	PASS		
3	primlib_gouraud_triangles_2_draw_passes		2002Aug14
	PASS		
4	primlib_parameterized_simple_triangle		2002Aug14
	PASS		
5	primlib_template_simple_triangle		2002Aug14
	PASS		
6	primlib_tex_tri		2002Aug14
	PASS		
7	r400cl_clip_10_verts_01		2002Aug14
	INCOMPLETE		
8	r400cl_clip_disable_01		2002Aug14
	INCOMPLETE		
9	r400cl_clip_space_dx_ogl_01		2002Aug14
	INCOMPLETE		
10	r400cl_clip_ucp_6bits_01		2002Aug14
	INCOMPLETE		
11	r400cl_cull_only_ena_01		2002Aug14
	INCOMPLETE		
12	r400cl_edgeflags_01		2002Aug14
	INCOMPLETE		
13	r400cl_edgeflags_frustum_bottom_01		2002Aug14
	INCOMPLETE		
14	r400cl_edgeflags_frustum_far_01		2002Aug14
	INCOMPLETE		
15	r400cl_edgeflags_frustum_left_01		2002Aug14
	INCOMPLETE		
16	r400cl_edgeflags_frustum_near_01		2002Aug14
	INCOMPLETE		
17	r400cl_edgeflags_frustum_right_01		2002Aug14
	INCOMPLETE		
18	r400cl_edgeflags_frustum_top_01		2002Aug14
	INCOMPLETE		
19	r400cl_edgeflags_gband_01		2002Aug14
	INCOMPLETE		
20	r400cl_edgeflags_gband_bottom_01		2002Aug14

INCOMPLETE		
21	r400cl_edgeflags_gband_left_01	2002Aug14
INCOMPLETE		
22	r400cl_edgeflags_gband_right_01	2002Aug14
INCOMPLETE		
23	r400cl_edgeflags_gband_top_01	2002Aug14
INCOMPLETE		
24	r400cl_frustum_01	2002Aug14
INCOMPLETE		
25	r400cl_frustum_02	2002Aug14
INCOMPLETE		
26	r400cl_frustum_03	2002Aug14
INCOMPLETE		
27	r400cl_frustum_04	2002Aug14
INCOMPLETE		
28	r400cl_frustum_05	2002Aug14
INCOMPLETE		
29	r400cl_frustum_06	2002Aug14
INCOMPLETE		
30	r400cl_frustum_07	2002Aug14
INCOMPLETE		
31	r400cl_frustum_08	2002Aug14
INCOMPLETE		
32	r400cl_frustum_09	2002Aug14
INCOMPLETE		
33	r400cl_frustum_10	2002Aug14
INCOMPLETE		
34	r400cl_frustum_11	2002Aug14
INCOMPLETE		
35	r400cl_frustum_12	2002Aug14
INCOMPLETE		
36	r400cl_frustum_13	2002Aug14
INCOMPLETE		
37	r400cl_frustum_14	2002Aug14
INCOMPLETE		
38	r400cl_frustum_15	2002Aug14
INCOMPLETE		
39	r400cl_frustum_16	2002Aug14
INCOMPLETE		
40	r400cl_frustum_17	2002Aug14
INCOMPLETE		
41	r400cl_frustum_18	2002Aug14
INCOMPLETE		
42	r400cl_frustum_19	2002Aug14
INCOMPLETE		
43	r400cl_frustum_20	2002Aug14
INCOMPLETE		
44	r400cl_frustum_21	2002Aug14

INCOMPLETE		
45	r400cl_frustum_22	2002Aug14
INCOMPLETE		
46	r400cl_frustum_23	2002Aug14
INCOMPLETE		
47	r400cl_frustum_24	2002Aug14
INCOMPLETE		
48	r400cl_frustum_25	2002Aug14
INCOMPLETE		
49	r400cl_frustum_26	2002Aug14
INCOMPLETE		
50	r400cl_frustum_27	2002Aug14
INCOMPLETE		
51	r400cl_frustum_28	2002Aug14
INCOMPLETE		
52	r400cl_frustum_lines_01	2002Aug14
INCOMPLETE		
53	r400cl_frustum_lines_02	2002Aug14
INCOMPLETE		
54	r400cl_frustum_lines_03	2002Aug14
INCOMPLETE		
55	r400cl_frustum_lines_04	2002Aug14
INCOMPLETE		
56	r400cl_frustum_lines_05	2002Aug14
INCOMPLETE		
57	r400cl_frustum_lines_06	2002Aug14
INCOMPLETE		
58	r400cl_frustum_lines_07	2002Aug14
INCOMPLETE		
59	r400cl_frustum_lines_08	2002Aug14
INCOMPLETE		
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77	r400cl_gband_tcl_01	2002Aug14
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88	r400sanity_vfd_texture_sample_01	2002Aug14
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346	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug14
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348	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug14
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367	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug14
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371	r400vgt_immed_index_points_01	2002Aug14
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372	r400vgt_immed_index_polygon_01	2002Aug14
PASS		
373	r400vgt_immed_index_primitives_01	2002Aug14
PASS		
374	r400vgt_immed_index_quad_list_01	2002Aug14
PASS		
375	r400vgt_immed_index_quad_strip_01	2002Aug14
PASS		
376	r400vgt_immed_index_rectangle_list_01	2002Aug14
PASS		
377	r400vgt_immed_index_tri_fan_01	2002Aug14
PASS		
378	r400vgt_immed_index_tri_list_01	2002Aug14
PASS		
379	r400vgt_immed_index_tri_strip_01	2002Aug14
PASS		
380	r400vgt_immed_index_tri_wflags_01	2002Aug14

PASS		
381	r400vgt_index_dealloc_line_list_01	2002Aug14
PASS		
382	r400vgt_index_dealloc_points_01	2002Aug14
PASS		
383	r400vgt_index_dealloc_triangle_list_01	2002Aug14
PASS		
384	r400vgt_index_min_max_01	2002Aug14
PASS		
385	r400vgt_index_min_max_02	2002Aug14
PASS		
386	r400vgt_index_offset_01	2002Aug14
PASS		
387	r400vgt_index_offset_02	2002Aug14
PASS		
388	r400vgt_index_offset_03	2002Aug14
PASS		
389	r400vgt_index_size_01	2002Aug14
PASS		
390	r400vgt_index_source_switch_01	2002Aug14
PASS		
391	r400vgt_line_list_01	2002Aug14
PASS		
392	r400vgt_line_loop_01	2002Aug14
PASS		
393	r400vgt_line_strip_01	2002Aug14
PASS		
394	r400vgt_pass_thru_all_prims_01	2002Aug14
PASS		
395	r400vgt_point_list_01	2002Aug14
PASS		
396	r400vgt_polygon_01	2002Aug14
PASS		
397	r400vgt_provoking_vtx_all_01	2002Aug14
PASS		
398	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug14
PASS		
399	r400vgt_quad_list_01	2002Aug14
PASS		
400	r400vgt_quad_strip_01	2002Aug14
PASS		
401	r400vgt_rectangle_list_01	2002Aug14
PASS		
402	r400vgt_reuse_depth_line_list_01	2002Aug14
PASS		
403	r400vgt_reuse_depth_point_list_01	2002Aug14
PASS		
404	r400vgt_reuse_depth_triangle_list_01	2002Aug14

PASS
405 r400vgt_reuse_index_line_list_01 2002Aug14
PASS
406 r400vgt_reuse_index_point_list_01 2002Aug14
PASS
407 r400vgt_reuse_index_triangle_list_01 2002Aug14
PASS
408 r400vgt_reuse_index_triangle_list_02 2002Aug14
PASS
409 r400vgt_reuse_index_triangle_list_03 2002Aug14
PASS
410 r400vgt_triangle_fan_01 2002Aug14
PASS
411 r400vgt_triangle_list_01 2002Aug14
PASS
412 r400vgt_triangle_strip_01 2002Aug14
PASS
413 r400vgt_triangle_wflags_01 2002Aug14
PASS
414 r400vgt_vtx_vector_packing_01 2002Aug14
PASS
415 r400vte_coverage_01 2002Aug14
PASS
416 r400vte_simple_point_01 2002Aug14
PASS
417 r400vte_simple_triangle_01 2002Aug14
PASS
418 r400vte_w0_fmt_01 2002Aug14
PASS
419 r400vte_w0_fmt_03 2002Aug14
PASS
420 r400vte_xy_fmt_01 2002Aug14
PASS
421 r400vte_xyz_scale_01 2002Aug14
PASS
422 r400vte_xyz_scale_02 2002Aug14
PASS
423 r400vte_z_fmt_01 2002Aug14
PASS

=====
Tests_Run_count = 423 Pass_count = 206 Fail_count = 0 Incomplete_count = 217
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug24
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug24
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug24
PASS			
4	primlib_parameterized_simple_triangle		2002Aug24
PASS			
5	primlib_template_simple_triangle		2002Aug24
PASS			
6	primlib_tex_tri		2002Aug24
PASS			
7	r400cl_clip_10_verts_01		2002Aug24
PASS			
8	r400cl_clip_disable_01		2002Aug24
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug24
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug24
PASS			
11	r400cl_cull_only_ena_01		2002Aug24
PASS			
12	r400cl_edgeflags_01		2002Aug24
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug24
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug24
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug24
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug24
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug24
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug24
PASS			
19	r400cl_edgeflags_gband_01		2002Aug24
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Aug24

PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug24
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug24
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug24
PASS		
24	r400cl_frustum_01	2002Aug24
PASS		
25	r400cl_frustum_02	2002Aug24
PASS		
26	r400cl_frustum_03	2002Aug24
PASS		
27	r400cl_frustum_04	2002Aug24
PASS		
28	r400cl_frustum_05	2002Aug24
PASS		
29	r400cl_frustum_06	2002Aug24
PASS		
30	r400cl_frustum_07	2002Aug24
PASS		
31	r400cl_frustum_08	2002Aug24
PASS		
32	r400cl_frustum_09	2002Aug24
PASS		
33	r400cl_frustum_10	2002Aug24
PASS		
34	r400cl_frustum_11	2002Aug24
PASS		
35	r400cl_frustum_12	2002Aug24
PASS		
36	r400cl_frustum_13	2002Aug24
PASS		
37	r400cl_frustum_14	2002Aug24
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38	r400cl_frustum_15	2002Aug24
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39	r400cl_frustum_16	2002Aug24
PASS		
40	r400cl_frustum_17	2002Aug24
PASS		
41	r400cl_frustum_18	2002Aug24
PASS		
42	r400cl_frustum_19	2002Aug24
PASS		
43	r400cl_frustum_20	2002Aug24
PASS		
44	r400cl_frustum_21	2002Aug24

PASS		
45	r400cl_frustum_22	2002Aug24
PASS		
46	r400cl_frustum_23	2002Aug24
PASS		
47	r400cl_frustum_24	2002Aug24
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48	r400cl_frustum_25	2002Aug24
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49	r400cl_frustum_26	2002Aug24
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50	r400cl_frustum_27	2002Aug24
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51	r400cl_frustum_28	2002Aug24
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52	r400cl_frustum_lines_01	2002Aug24
PASS		
53	r400cl_frustum_lines_02	2002Aug24
PASS		
54	r400cl_frustum_lines_03	2002Aug24
PASS		
55	r400cl_frustum_lines_04	2002Aug24
PASS		
56	r400cl_frustum_lines_05	2002Aug24
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57	r400cl_frustum_lines_06	2002Aug24
PASS		
58	r400cl_frustum_lines_07	2002Aug24
PASS		
59	r400cl_frustum_lines_08	2002Aug24
PASS		
60	r400cl_frustum_lines_09	2002Aug24
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63	r400cl_frustum_lines_12	2002Aug24
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64	r400cl_frustum_lines_13	2002Aug24
PASS		
65	r400cl_frustum_lines_14	2002Aug24
PASS		
66	r400cl_frustum_lines_15	2002Aug24
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67	r400cl_frustum_lines_16	2002Aug24
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68	r400cl_frustum_lines_17	2002Aug24

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70	r400cl_frustum_lines_19	2002Aug24
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71	r400cl_frustum_lines_20	2002Aug24
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72	r400cl_frustum_lines_21	2002Aug24
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73	r400cl_frustum_lines_22	2002Aug24
PASS		
74	r400cl_frustum_lines_23	2002Aug24
PASS		
75	r400cl_frustum_lines_24	2002Aug24
PASS		
76	r400cl_gband_04	2002Aug24
PASS		
77	r400cl_gband_tcl_01	2002Aug24
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78	r400cl_simple_triangle_01	2002Aug24
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79	r400cl_ucp_combos_01	2002Aug24
PASS		
80	r400cl_ucp_combos_02	2002Aug24
PASS		
81	r400cl_ucp_combos_03	2002Aug24
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82	r400cl_ucp_combos_04	2002Aug24
PASS		
83	r400cl_ucp_pointlist_01	2002Aug24
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84	r400cl_vertex_reuse_clip_01	2002Aug24
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85	r400cl_vtx_kill_01	2002Aug24
PASS		
86	r400cl_vtx_kill_02	2002Aug24
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87	r400cl_w_eq_0	2002Aug24
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88	r400sanity_persp_projection_01	2002Aug24
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89	r400sanity_vfd_texture_sample_01	2002Aug24
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91	r400sc_bres_cntl_02	2002Aug24
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92	r400sc_bres_cntl_03	2002Aug24

PASS		
93	r400sc_bres_cntl_04	2002Aug24
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94	r400sc_clip_rect_01	2002Aug24
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95	r400sc_clip_rect_02	2002Aug24
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96	r400sc_clip_rect_03	2002Aug24
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97	r400sc_clip_rect_04	2002Aug24
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98	r400sc_clip_rect_fc_01	2002Aug24
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99	r400sc_diamond_exit_01	2002Aug24
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100	r400sc_diamond_exit_02	2002Aug24
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101	r400sc_diamond_exit_03	2002Aug24
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102	r400sc_diamond_exit_04	2002Aug24
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103	r400sc_diamond_exit_05	2002Aug24
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104	r400sc_jss_1x2_01	2002Aug24
PASS		
105	r400sc_jss_1x2_02	2002Aug24
PASS		
106	r400sc_jss_1x3_01	2002Aug24
PASS		
107	r400sc_jss_1x3_02	2002Aug24
PASS		
108	r400sc_jss_1x4_01	2002Aug24
PASS		
109	r400sc_jss_1x4_02	2002Aug24
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110	r400sc_jss_2x1_01	2002Aug24
PASS		
111	r400sc_jss_2x1_02	2002Aug24
PASS		
112	r400sc_jss_2x2_01	2002Aug24
PASS		
113	r400sc_jss_2x2_02	2002Aug24
PASS		
114	r400sc_jss_2x3_01	2002Aug24
PASS		
115	r400sc_jss_2x3_02	2002Aug24
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116	r400sc_jss_2x4_01	2002Aug24

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117 r400sc_jss_2x4_02 2002Aug24
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118 r400sc_jss_3x1_01 2002Aug24
PASS
119 r400sc_jss_3x1_02 2002Aug24
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120 r400sc_jss_3x2_01 2002Aug24
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121 r400sc_jss_3x2_02 2002Aug24
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122 r400sc_jss_3x3_01 2002Aug24
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123 r400sc_jss_3x3_02 2002Aug24
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124 r400sc_jss_3x4_01 2002Aug24
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125 r400sc_jss_3x4_02 2002Aug24
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126 r400sc_jss_3x4_03 2002Aug24
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127 r400sc_jss_4x1_01 2002Aug24
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128 r400sc_jss_4x1_02 2002Aug24
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129 r400sc_jss_4x2_01 2002Aug24
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130 r400sc_jss_4x2_02 2002Aug24
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131 r400sc_jss_4x3_01 2002Aug24
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132 r400sc_jss_4x3_02 2002Aug24
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133 r400sc_jss_4x4_01 2002Aug24
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134 r400sc_jss_4x4_02 2002Aug24
PASS
135 r400sc_jss_4x4_aa_mask_01 2002Aug24
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136 r400sc_jss_4x4_aa_mask_02 2002Aug24
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137 r400sc_jss_4x4_fc_01 2002Aug24
PASS
138 r400sc_jss_4x4_max_dist_01 2002Aug24
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139 r400sc_line_dx10_eq_0_01 2002Aug24
PASS
140 r400sc_line_dx10_ge_0_01 2002Aug24

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141	r400sc_line_dx10_lt_0_01	2002Aug24
PASS		
142	r400sc_line_dy10_eq_0_01	2002Aug24
PASS		
143	r400sc_line_dy10_ge_0_01	2002Aug24
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144	r400sc_line_dy10_lt_0_01	2002Aug24
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145	r400sc_line_expand_width_msa_8_01	2002Aug24
PASS		
146	r400sc_line_expand_width_msa_8_02	2002Aug24
PASS		
147	r400sc_line_expand_width_msa_8_03	2002Aug24
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148	r400sc_line_jss_3x4_01	2002Aug24
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149	r400sc_line_list_01	2002Aug24
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150	r400sc_line_list_concentric_circle_01	2002Aug24
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151	r400sc_line_list_concentric_circle_02	2002Aug24
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152	r400sc_line_list_concentric_circle_03	2002Aug24
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153	r400sc_line_msa_8_01	2002Aug24
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154	r400sc_line_stipple_01	2002Aug24
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155	r400sc_line_stipple_02	2002Aug24
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156	r400sc_line_stipple_03	2002Aug24
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157	r400sc_line_stipple_04	2002Aug24
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158	r400sc_line_stipple_05	2002Aug24
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159	r400sc_line_stipple_06	2002Aug24
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160	r400sc_line_stipple_07	2002Aug24
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161	r400sc_line_stipple_08	2002Aug24
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162	r400sc_line_stipple_09	2002Aug24
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163	r400sc_line_stipple_fc_08	2002Aug24
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164	r400sc_line_strip_stipple_01	2002Aug24

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165	r400sc_msaa_1_01	2002Aug24
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166	r400sc_msaa_4_01	2002Aug24
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167	r400sc_msaa_6_01	2002Aug24
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168	r400sc_msaa_8_01	2002Aug24
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169	r400sc_msaa_8_aa_mask_01	2002Aug24
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170	r400sc_msaa_8_aa_mask_02	2002Aug24
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171	r400sc_msaa_8_aa_mask_fc_02	2002Aug24
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172	r400sc_null_triangles_01	2002Aug24
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173	r400sc_null_triangles_fc_01	2002Aug24
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174	r400sc_point_jss_3x4_01	2002Aug24
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175	r400sc_point_list_01	2002Aug24
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176	r400sc_point_list_02	2002Aug24
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177	r400sc_point_list_03	2002Aug24
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178	r400sc_point_list_04	2002Aug24
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179	r400sc_point_list_05	2002Aug24
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180	r400sc_point_list_06	2002Aug24
INCOMPLETE		
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182	r400sc_point_list_08	2002Aug24
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183	r400sc_point_list_09	2002Aug24
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184	r400sc_point_msaa_8_01	2002Aug24
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185	r400sc_poly_offset_01	2002Aug24
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186	r400sc_poly_offset_02	2002Aug24
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187	r400sc_poly_offset_fc_01	2002Aug24
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188	r400sc_polygon_stipple_01	2002Aug24

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189	r400sc_prsp_byc_intrp_ref_pix_01	2002Aug24
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190	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug24
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191	r400sc_prsp_byc_intrp_ref_pix_03	2002Aug24
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192	r400sc_prsp_byc_intrp_ref_pix_04	2002Aug24
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193	r400sc_prsp_byc_intrp_ref_pix_05	2002Aug24
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194	r400sc_prsp_byc_intrp_ref_pix_06	2002Aug24
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195	r400sc_prsp_byc_intrp_ref_pix_07	2002Aug24
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196	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug24
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198	r400sc_raster_fill_rule_02	2002Aug24
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199	r400sc_raster_fill_rule_03	2002Aug24
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200	r400sc_raster_fill_rule_04	2002Aug24
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201	r400sc_raster_fill_rule_05	2002Aug24
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202	r400sc_raster_fill_rule_fc_01	2002Aug24
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203	r400sc_rectangle_list_01	2002Aug24
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204	r400sc_rectangle_list_02	2002Aug24
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205	r400sc_rectangle_list_03	2002Aug24
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206	r400sc_rectangle_list_04	2002Aug24
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207	r400sc_rectangle_list_05	2002Aug24
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208	r400sc_rectangle_list_06	2002Aug24
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209	r400sc_rectangle_list_07	2002Aug24
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210	r400sc_rectangle_list_08	2002Aug24
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212	r400sc_scissor_rect_02	2002Aug24

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213	r400sc_scissor_rect_03	2002Aug24
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214	r400sc_scissor_rect_04	2002Aug24
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215	r400sc_scissor_rect_05	2002Aug24
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216	r400sc_scissor_rect_fc_01	2002Aug24
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217	r400sc_set_state_01	2002Aug24
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218	r400sc_tri_walk_start_vertex_01	2002Aug24
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219	r400sc_tri_walk_start_vertex_02	2002Aug24
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220	r400sc_tri_walk_start_vertex_03	2002Aug24
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221	r400sc_tri_walk_start_vertex_04	2002Aug24
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222	r400sc_tri_walk_start_vertex_05	2002Aug24
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223	r400sc_tri_walk_start_vertex_06	2002Aug24
PASS		
224	r400sc_tri_walk_start_vertex_07	2002Aug24
PASS		
225	r400sc_tri_walk_start_vertex_08	2002Aug24
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226	r400sc_tri_walk_start_vertex_09	2002Aug24
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227	r400sc_tri_walk_start_vertex_10	2002Aug24
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228	r400sc_tri_walk_start_vertex_11	2002Aug24
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229	r400sc_tri_walk_start_vertex_12	2002Aug24
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230	r400sc_tri_walk_start_vertex_13	2002Aug24
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231	r400sc_tri_walk_start_vertex_14	2002Aug24
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232	r400sc_tri_walk_start_vertex_15	2002Aug24
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233	r400sc_tri_walk_start_vertex_16	2002Aug24
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234	r400sc_triangle_stipple_01	2002Aug24
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235	r400sc_window_offset_01	2002Aug24
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236	r400sc_window_offset_fc_01	2002Aug24

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237	r400sc_zbuffer_line_list_01	2002Aug24
PASS		
238	r400sc_zbuffer_point_list_01	2002Aug24
PASS		
239	r400sc_zbuffer_rectangle_list_01	2002Aug24
PASS		
240	r400sc_zbuffer_rectangle_list_02	2002Aug24
PASS		
241	r400sc_zbuffer_rectangle_list_fc_02	2002Aug24
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242	r400sc_zbuffer_triangle_list_01	2002Aug24
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243	r400su_4tri_text_offscreen_01	2002Aug24
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244	r400su_baryc_test_01	2002Aug24
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245	r400su_baryc_test_02	2002Aug24
PASS		
246	r400su_baryc_test_03	2002Aug24
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247	r400su_baryc_test_04	2002Aug24
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248	r400su_clip_edgeflag_polymode_01	2002Aug24
PASS		
249	r400su_clip_pointsize_test_01	2002Aug24
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250	r400su_clip_pointtest_01	2002Aug24
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251	r400su_clip_pointtest_02	2002Aug24
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252	r400su_clip_pointtest_03	2002Aug24
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253	r400su_clip_pointtest_04	2002Aug24
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254	r400su_clip_polymode_test_01	2002Aug24
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255	r400su_clipline_01	2002Aug24
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256	r400su_clippoint_01	2002Aug24
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257	r400su_clipvertexsortingfunctional_01	2002Aug24
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258	r400su_cullingfunctional_01	2002Aug24
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259	r400su_degentri_test_01	2002Aug24
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260	r400su_degentri_test_02	2002Aug24

PASS		
261	r400su_degentri_test_03	2002Aug24
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262	r400su_edge_flag_01	2002Aug24
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263	r400su_edge_flag_02	2002Aug24
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264	r400su_edgeflags_triangle_01	2002Aug24
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265	r400su_edgeflags_triangle_02	2002Aug24
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266	r400su_geom_sort_01	2002Aug24
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267	r400su_line_end_cap_functional_01	2002Aug24
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268	r400su_line_end_cap_width_functional_02	2002Aug24
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269	r400su_line_test_01	2002Aug24
PASS		
270	r400su_line_test_02	2002Aug24
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271	r400su_longstrip_01	2002Aug24
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272	r400su_multi_prim_01	2002Aug24
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273	r400su_parallel_orientation_all_01	2002Aug24
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274	r400su_parallel_orientation_all_02	2002Aug24
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275	r400su_pc_management_01	2002Aug24
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276	r400su_pc_management_02	2002Aug24
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277	r400su_pc_management_03	2002Aug24
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278	r400su_point_sprite_01	2002Aug24
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279	r400su_point_sprite_02	2002Aug24
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280	r400su_point_sprite_03	2002Aug24
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281	r400su_point_sprite_04	2002Aug24
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282	r400su_point_sprite_05	2002Aug24
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283	r400su_point_sprite_06	2002Aug24
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284	r400su_point_wl6_hl_functional_01	2002Aug24

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285	r400su_point_wl_h16_functional_01	2002Aug24
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286	r400su_pointsizepresent_01	2002Aug24
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287	r400su_pointsizepresent_02	2002Aug24
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288	r400su_polymode_lines_degen_triangle_01	2002Aug24
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289	r400su_polymode_lines_degen_triangle_02	2002Aug24
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290	r400su_polymode_lines_zero_area_triangle_01	2002Aug24
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291	r400su_polymode_lines_zero_area_triangle_02	2002Aug24
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292	r400su_polymode_points_degen_triangle_01	2002Aug24
PASS		
293	r400su_polymode_points_degen_triangle_02	2002Aug24
PASS		
294	r400su_polymode_points_zero_area_triangle_01	2002Aug24
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295	r400su_polymode_points_zero_area_triangle_02	2002Aug24
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296	r400su_polymode_rectangle_01	2002Aug24
PASS		
297	r400su_polymode_zero_area_triangle_01	2002Aug24
PASS		
298	r400su_polymode_zero_area_triangle_02	2002Aug24
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299	r400su_polymode_zero_area_triangle_03	2002Aug24
PASS		
300	r400su_polymode_zero_area_triangle_04	2002Aug24
PASS		
301	r400su_polymodeculling_01	2002Aug24
PASS		
302	r400su_polymodefunctional_01	2002Aug24
PASS		
303	r400su_provoking_vtx_edgeflags_triangle_01	2002Aug24
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304	r400su_provoking_vtx_edgeflags_triangle_02	2002Aug24
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305	r400su_provoking_vtx_edgeflags_triangle_03	2002Aug24
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306	r400su_provoking_vtx_edgeflags_triangle_04	2002Aug24
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307	r400su_provoking_vtx_line_01	2002Aug24
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308	r400su_provoking_vtx_point_01	2002Aug24

PASS		
309	r400su_provoking_vtx_polymode_rectangle_01	2002Aug24
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310	r400su_provoking_vtx_rectangle_01	2002Aug24
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311	r400su_provoking_vtx_triangle_01	2002Aug24
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312	r400su_rand_line_01	2002Aug24
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313	r400su_rand_point_01	2002Aug24
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314	r400su_rand_tri_01	2002Aug24
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315	r400su_rectangle_list_01	2002Aug24
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316	r400su_sliver_01	2002Aug24
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317	r400su_triarea_test_01	2002Aug24
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318	r400su_triarea_test_02	2002Aug24
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319	r400su_triarea_test_03	2002Aug24
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320	r400su_triarea_test_04	2002Aug24
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321	r400su_vertexsortingfunctional_01	2002Aug24
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322	r400vgt_auto_index_line_list_01	2002Aug24
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323	r400vgt_auto_index_line_loop_01	2002Aug24
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324	r400vgt_auto_index_line_strip_01	2002Aug24
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325	r400vgt_auto_index_points_01	2002Aug24
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326	r400vgt_auto_index_polygon_01	2002Aug24
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327	r400vgt_auto_index_primitives_01	2002Aug24
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328	r400vgt_auto_index_quad_list_01	2002Aug24
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329	r400vgt_auto_index_quad_strip_01	2002Aug24
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330	r400vgt_auto_index_rectangle_list_01	2002Aug24
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331	r400vgt_auto_index_tri_fan_01	2002Aug24
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332	r400vgt_auto_index_tri_list_01	2002Aug24

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333	r400vgt_auto_index_tri_strip_01	2002Aug24
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334	r400vgt_auto_index_tri_wflags_01	2002Aug24
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335	r400vgt_dma_engine_01	2002Aug24
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336	r400vgt_dma_engine_02	2002Aug24
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337	r400vgt_dma_engine_03	2002Aug24
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343	r400vgt_dma_engine_09	2002Aug24
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344	r400vgt_dma_engine_10	2002Aug24
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345	r400vgt_dma_index_line_list_01	2002Aug24
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346	r400vgt_dma_index_line_loop_01	2002Aug24
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347	r400vgt_dma_index_line_strip_01	2002Aug24
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348	r400vgt_dma_index_points_01	2002Aug24
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349	r400vgt_dma_index_polygon_01	2002Aug24
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350	r400vgt_dma_index_primitives_01	2002Aug24
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351	r400vgt_dma_index_quad_list_01	2002Aug24
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352	r400vgt_dma_index_quad_strip_01	2002Aug24
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354	r400vgt_dma_index_tri_fan_01	2002Aug24
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355	r400vgt_dma_index_tri_list_01	2002Aug24
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356	r400vgt_dma_index_tri_strip_01	2002Aug24

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357	r400vgt_dma_index_tri_wflags_01	2002Aug24
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358	r400vgt_dma_swap_indx16_01	2002Aug24
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359	r400vgt_dma_swap_indx32_01	2002Aug24
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360	r400vgt_edgeflags_polygon_01	2002Aug24
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361	r400vgt_edgeflags_quad_list_01	2002Aug24
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362	r400vgt_edgeflags_quad_strip_01	2002Aug24
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363	r400vgt_edgeflags_triangle_fan_01	2002Aug24
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364	r400vgt_edgeflags_triangle_list_01	2002Aug24
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365	r400vgt_edgeflags_triangle_strip_01	2002Aug24
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366	r400vgt_edgeflags_triangle_wflags_01	2002Aug24
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367	r400vgt_ext2int_index_line_list_01	2002Aug24
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368	r400vgt_ext2int_index_line_loop_01	2002Aug24
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369	r400vgt_ext2int_index_line_strip_01	2002Aug24
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371	r400vgt_ext2int_index_polygon_01	2002Aug24
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372	r400vgt_ext2int_index_quad_list_01	2002Aug24
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373	r400vgt_ext2int_index_quad_strip_01	2002Aug24
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374	r400vgt_ext2int_index_triangle_fan_01	2002Aug24
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375	r400vgt_ext2int_index_triangle_list_01	2002Aug24
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376	r400vgt_ext2int_index_triangle_strip_01	2002Aug24
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377	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug24
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378	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug24
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379	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug24
PASS		
380	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug24

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381 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01 2002Aug24
PASS
382 r400vgt_hos_PNQ_lp_cont_no_projection_01 2002Aug24
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383 r400vgt_hos_PNT_cont_cp_qn_complex_01 2002Aug24
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384 r400vgt_hos_PNT_cont_cp_qn_precision_01 2002Aug24
PASS
385 r400vgt_hos_PNT_cont_cp_qn_precision_02 2002Aug24
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386 r400vgt_hos_PNT_cp_qn_cont_light_texture_01 2002Aug24
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387 r400vgt_hos_PNT_cp_qn_cont_light_texture_02 2002Aug24
PASS
388 r400vgt_hos_PNT_cp_qn_cont_light_texture_03 2002Aug24
PASS
389 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01 2002Aug24
PASS
390 r400vgt_hos_PNT_cp_qn_cont_no_projection_01 2002Aug24
PASS
391 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01 2002Aug24
PASS
392 r400vgt_hos_PNT_disc_cp_qn_complex_01 2002Aug24
PASS
393 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Aug24
PASS
394 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Aug24
PASS
395 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Aug24
PASS
396 r400vgt_hos_PNT_disc_cp_qn_precision_02 2002Aug24
PASS
397 r400vgt_hos_PNT_edge_detection_01 2002Aug24
PASS
398 r400vgt_hos_PNT_lp_cont_no_projection_01 2002Aug24
PASS
399 r400vgt_hos_auto_index_line_list_01 2002Aug24
PASS
400 r400vgt_hos_auto_index_quad_list_01 2002Aug24
PASS
401 r400vgt_hos_auto_index_triangle_list_01 2002Aug24
PASS
402 r400vgt_hos_cubic_pos_pnt_discrete_01 2002Aug24
PASS
403 r400vgt_hos_simple_linear_PNT_discrete_01 2002Aug24
PASS
404 r400vgt_immed_index_line_list_01 2002Aug24

PASS		
405	r400vgt_immed_index_line_loop_01	2002Aug24
PASS		
406	r400vgt_immed_index_line_strip_01	2002Aug24
PASS		
407	r400vgt_immed_index_points_01	2002Aug24
PASS		
408	r400vgt_immed_index_polygon_01	2002Aug24
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409	r400vgt_immed_index_primitives_01	2002Aug24
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410	r400vgt_immed_index_quad_list_01	2002Aug24
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411	r400vgt_immed_index_quad_strip_01	2002Aug24
PASS		
412	r400vgt_immed_index_rectangle_list_01	2002Aug24
PASS		
413	r400vgt_immed_index_tri_fan_01	2002Aug24
PASS		
414	r400vgt_immed_index_tri_list_01	2002Aug24
PASS		
415	r400vgt_immed_index_tri_strip_01	2002Aug24
PASS		
416	r400vgt_immed_index_tri_wflags_01	2002Aug24
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417	r400vgt_index_dealloc_line_list_01	2002Aug24
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418	r400vgt_index_dealloc_points_01	2002Aug24
PASS		
419	r400vgt_index_dealloc_triangle_list_01	2002Aug24
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420	r400vgt_index_min_max_01	2002Aug24
PASS		
421	r400vgt_index_min_max_02	2002Aug24
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422	r400vgt_index_offset_01	2002Aug24
PASS		
423	r400vgt_index_offset_02	2002Aug24
PASS		
424	r400vgt_index_offset_03	2002Aug24
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425	r400vgt_index_size_01	2002Aug24
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426	r400vgt_index_source_switch_01	2002Aug24
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427	r400vgt_line_list_01	2002Aug24
PASS		
428	r400vgt_line_loop_01	2002Aug24

PASS		
429	r400vgt_line_strip_01	2002Aug24
PASS		
430	r400vgt_multi_context_01	2002Aug24
PASS		
431	r400vgt_multi_context_02	2002Aug24
PASS		
432	r400vgt_multi_context_03	2002Aug24
PASS		
433	r400vgt_multi_context_04	2002Aug24
PASS		
434	r400vgt_multi_context_05	2002Aug24
PASS		
435	r400vgt_multi_context_06	2002Aug24
PASS		
436	r400vgt_multi_context_07	2002Aug24
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438	r400vgt_multi_context_09	2002Aug24
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439	r400vgt_multi_context_10	2002Aug24
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440	r400vgt_multi_context_11	2002Aug24
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441	r400vgt_multi_context_12	2002Aug24
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442	r400vgt_pass_thru_all_prims_01	2002Aug24
PASS		
443	r400vgt_point_list_01	2002Aug24
PASS		
444	r400vgt_polygon_01	2002Aug24
PASS		
445	r400vgt_provoking_vtx_all_01	2002Aug24
PASS		
446	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug24
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447	r400vgt_quad_list_01	2002Aug24
PASS		
448	r400vgt_quad_strip_01	2002Aug24
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449	r400vgt_rectangle_list_01	2002Aug24
PASS		
450	r400vgt_reuse_depth_line_list_01	2002Aug24
PASS		
451	r400vgt_reuse_depth_point_list_01	2002Aug24
PASS		
452	r400vgt_reuse_depth_triangle_list_01	2002Aug24

PASS
453 r400vgt_reuse_index_line_list_01 2002Aug24
PASS
454 r400vgt_reuse_index_point_list_01 2002Aug24
PASS
455 r400vgt_reuse_index_triangle_list_01 2002Aug24
PASS
456 r400vgt_reuse_index_triangle_list_02 2002Aug24
PASS
457 r400vgt_reuse_index_triangle_list_03 2002Aug24
PASS
458 r400vgt_triangle_fan_01 2002Aug24
PASS
459 r400vgt_triangle_list_01 2002Aug24
PASS
460 r400vgt_triangle_strip_01 2002Aug24
PASS
461 r400vgt_triangle_wflags_01 2002Aug24
PASS
462 r400vgt_vtx_vector_packing_01 2002Aug24
PASS
463 r400vte_coverage_01 2002Aug24
PASS
464 r400vte_simple_point_01 2002Aug24
PASS
465 r400vte_simple_triangle_01 2002Aug24
PASS
466 r400vte_w0_fmt_01 2002Aug24
PASS
467 r400vte_w0_fmt_03 2002Aug24
PASS
468 r400vte_xy_fmt_01 2002Aug24
PASS
469 r400vte_xyz_scale_01 2002Aug24
PASS
470 r400vte_xyz_scale_02 2002Aug24
PASS
471 r400vte_z_fmt_01 2002Aug24
PASS

=====
Tests_Run_count = 471 Pass_count = 469 Fail_count = 0 Incomplete_count = 2
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No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep9
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep9
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep9
PASS			
4	primlib_parameterized_simple_triangle		2002Sep9
PASS			
5	primlib_template_simple_triangle		2002Sep9
PASS			
6	primlib_tex_tri		2002Sep9
PASS			
7	r400cl_clip_10_verts_01		2002Sep8
PASS			
8	r400cl_clip_disable_01		2002Sep8
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep8
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep8
PASS			
11	r400cl_cull_only_ena_01		2002Sep8
PASS			
12	r400cl_edgeflags_01		2002Sep8
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep8
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep8
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep8
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep8
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep8
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep8
PASS			
19	r400cl_edgeflags_gband_01		2002Sep8
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep8

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep8
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep8
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep8
PASS		
24	r400cl_frustum_01	2002Sep8
PASS		
25	r400cl_frustum_02	2002Sep8
PASS		
26	r400cl_frustum_03	2002Sep8
PASS		
27	r400cl_frustum_04	2002Sep8
PASS		
28	r400cl_frustum_05	2002Sep8
PASS		
29	r400cl_frustum_06	2002Sep8
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30	r400cl_frustum_07	2002Sep8
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31	r400cl_frustum_08	2002Sep8
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32	r400cl_frustum_09	2002Sep8
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33	r400cl_frustum_10	2002Sep8
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34	r400cl_frustum_11	2002Sep8
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35	r400cl_frustum_12	2002Sep8
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36	r400cl_frustum_13	2002Sep8
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37	r400cl_frustum_14	2002Sep8
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38	r400cl_frustum_15	2002Sep8
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39	r400cl_frustum_16	2002Sep8
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40	r400cl_frustum_17	2002Sep8
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41	r400cl_frustum_18	2002Sep8
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42	r400cl_frustum_19	2002Sep8
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43	r400cl_frustum_20	2002Sep8
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44	r400cl_frustum_21	2002Sep8

PASS		
45	r400cl_frustum_22	2002Sep8
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46	r400cl_frustum_23	2002Sep8
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50	r400cl_frustum_27	2002Sep8
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51	r400cl_frustum_28	2002Sep8
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52	r400cl_frustum_all_vols_lines	2002Sep8
PASS		
53	r400cl_frustum_all_vols_tris	2002Sep8
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54	r400cl_frustum_lines_01	2002Sep8
PASS		
55	r400cl_frustum_lines_02	2002Sep8
PASS		
56	r400cl_frustum_lines_03	2002Sep8
PASS		
57	r400cl_frustum_lines_04	2002Sep8
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63	r400cl_frustum_lines_10	2002Sep8
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64	r400cl_frustum_lines_11	2002Sep8
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65	r400cl_frustum_lines_12	2002Sep8
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68	r400cl_frustum_lines_15	2002Sep8

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74	r400cl_frustum_lines_21	2002Sep8
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77	r400cl_frustum_lines_24	2002Sep8
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79	r400cl_gband_tcl_01	2002Sep8
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81	r400cl_simple_triangle_01	2002Sep8
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82	r400cl_ucp_combos_01	2002Sep8
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83	r400cl_ucp_combos_02	2002Sep8
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84	r400cl_ucp_combos_03	2002Sep8
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88	r400cl_ucp_combos_07	2002Sep8
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90	r400cl_ucp_combos_09	2002Sep8
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108	r400cl_ucp_combos_27	2002Sep8
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109	r400cl_ucp_combos_28	2002Sep8
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110	r400cl_ucp_combos_29	2002Sep8
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111	r400cl_ucp_combos_30	2002Sep8
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112	r400cl_ucp_pointlist_01	2002Sep8
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113	r400cl_vertex_reuse_clip_01	2002Sep8
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114	r400cl_vtx_kill_01	2002Sep8
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115	r400cl_vtx_kill_02	2002Sep8
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116	r400cl_w_eq_0	2002Sep8

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117	r400sanity_persp_projection_01	2002Sep9
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118	r400sanity_vfd_texture_sample_01	2002Sep9
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119	r400sc_bres_cntl_01	2002Sep7
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120	r400sc_bres_cntl_02	2002Sep8
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121	r400sc_bres_cntl_03	2002Sep8
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122	r400sc_bres_cntl_04	2002Sep8
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123	r400sc_bres_cntl_w2k_01	2002Sep8
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124	r400sc_bres_cntl_w9x_01	2002Sep8
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125	r400sc_clip_rect_01	2002Sep8
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126	r400sc_clip_rect_02	2002Sep8
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127	r400sc_clip_rect_03	2002Sep8
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128	r400sc_clip_rect_04	2002Sep8
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129	r400sc_clip_rect_fc_01	2002Sep8
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130	r400sc_diamond_exit_01	2002Sep8
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131	r400sc_diamond_exit_02	2002Sep8
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132	r400sc_diamond_exit_03	2002Sep8
PASS		
133	r400sc_diamond_exit_04	2002Sep8
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134	r400sc_diamond_exit_05	2002Sep8
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135	r400sc_jss_1x2_01	2002Sep8
PASS		
136	r400sc_jss_1x2_02	2002Sep8
PASS		
137	r400sc_jss_1x3_01	2002Sep8
PASS		
138	r400sc_jss_1x3_02	2002Sep8
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139	r400sc_jss_1x4_01	2002Sep8
PASS		
140	r400sc_jss_1x4_02	2002Sep8

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141	r400sc_jss_2x1_01	2002Sep8
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142	r400sc_jss_2x1_02	2002Sep8
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143	r400sc_jss_2x2_01	2002Sep8
PASS		
144	r400sc_jss_2x2_02	2002Sep8
PASS		
145	r400sc_jss_2x3_01	2002Sep8
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146	r400sc_jss_2x3_02	2002Sep8
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147	r400sc_jss_2x4_01	2002Sep8
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148	r400sc_jss_2x4_02	2002Sep8
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149	r400sc_jss_3x1_01	2002Sep8
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150	r400sc_jss_3x1_02	2002Sep8
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151	r400sc_jss_3x2_01	2002Sep8
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152	r400sc_jss_3x2_02	2002Sep8
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153	r400sc_jss_3x3_01	2002Sep8
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154	r400sc_jss_3x3_02	2002Sep8
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155	r400sc_jss_3x4_01	2002Sep8
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156	r400sc_jss_3x4_02	2002Sep8
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157	r400sc_jss_3x4_03	2002Sep8
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158	r400sc_jss_4x1_01	2002Sep8
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159	r400sc_jss_4x1_02	2002Sep8
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160	r400sc_jss_4x2_01	2002Sep8
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161	r400sc_jss_4x2_02	2002Sep8
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162	r400sc_jss_4x3_01	2002Sep8
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163	r400sc_jss_4x3_02	2002Sep8
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164	r400sc_jss_4x4_01	2002Sep8

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165	r400sc_jss_4x4_02	2002Sep8
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166	r400sc_jss_4x4_03	2002Sep8
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167	r400sc_jss_4x4_aa_mask_01	2002Sep8
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168	r400sc_jss_4x4_aa_mask_02	2002Sep8
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169	r400sc_jss_4x4_fc_01	2002Sep8
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170	r400sc_jss_4x4_fc_02	2002Sep8
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171	r400sc_jss_4x4_max_dist_01	2002Sep8
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172	r400sc_line_dx10_eq_0_01	2002Sep8
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173	r400sc_line_dx10_ge_0_01	2002Sep8
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174	r400sc_line_dx10_lt_0_01	2002Sep8
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175	r400sc_line_dy10_eq_0_01	2002Sep8
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176	r400sc_line_dy10_ge_0_01	2002Sep8
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177	r400sc_line_dy10_lt_0_01	2002Sep8
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178	r400sc_line_expand_width_msa_8_01	2002Sep8
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179	r400sc_line_expand_width_msa_8_02	2002Sep8
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180	r400sc_line_expand_width_msa_8_03	2002Sep8
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181	r400sc_line_jss_3x4_01	2002Sep8
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182	r400sc_line_list_01	2002Sep8
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183	r400sc_line_list_02	2002Sep8
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184	r400sc_line_list_concentric_circle_01	2002Sep8
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185	r400sc_line_list_concentric_circle_02	2002Sep8
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186	r400sc_line_list_concentric_circle_03	2002Sep8
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187	r400sc_line_list_textured_01	2002Sep8
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188	r400sc_line_list_verify_st_01	2002Sep8

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189	r400sc_line_msa_8_01	2002Sep8
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190	r400sc_line_msa_8_textured_01	2002Sep8
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191	r400sc_line_stipple_01	2002Sep8
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192	r400sc_line_stipple_02	2002Sep8
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193	r400sc_line_stipple_03	2002Sep8
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205	r400sc_line_stipple_fc_08	2002Sep8
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206	r400sc_line_strip_stipple_01	2002Sep8
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207	r400sc_msa_1_01	2002Sep8
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217	r400sc_msa_1_zbuffer_rectangle_list_02	2002Sep8
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236	r400sc_msa_3_zbuffer_rectangle_list_01	2002Sep8

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237	r400sc_msaa_3_zbuffer_rectangle_list_02	2002Sep8
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248	r400sc_msaa_4_zbuffer_rectangle_list_02	2002Sep8
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259	r400sc_msaa_6_zbuffer_rectangle_list_02	2002Sep8
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265	r400sc_msa_8_aa_mask_01	2002Sep8
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266	r400sc_msa_8_aa_mask_02	2002Sep8
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267	r400sc_msa_8_aa_mask_fc_02	2002Sep8
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276	r400sc_msa_8_zbuffer_rectangle_list_01	2002Sep8
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277	r400sc_msa_8_zbuffer_rectangle_list_02	2002Sep8
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278	r400sc_null_triangles_01	2002Sep8
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279	r400sc_null_triangles_fc_01	2002Sep8
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280	r400sc_pinwheel_02	2002Sep8
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281	r400sc_point_jss_3x4_01	2002Sep8
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282	r400sc_point_list_01	2002Sep8
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283	r400sc_point_list_02	2002Sep8
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284	r400sc_point_list_03	2002Sep8

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289	r400sc_point_list_08	2002Sep8
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290	r400sc_point_list_09	2002Sep8
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291	r400sc_point_msa_8_01	2002Sep8
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292	r400sc_polygon_stipple_01	2002Sep8
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293	r400sc_poly_offset_01	2002Sep8
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294	r400sc_poly_offset_02	2002Sep8
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295	r400sc_poly_offset_fc_01	2002Sep8
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296	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep8
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297	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep8
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305	r400sc_raster_fill_rule_02	2002Sep8
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306	r400sc_raster_fill_rule_03	2002Sep8
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307	r400sc_raster_fill_rule_04	2002Sep8
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308	r400sc_raster_fill_rule_05	2002Sep8

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309	r400sc_raster_fill_rule_fc_01	2002Sep8
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317	r400sc_rectangle_list_08	2002Sep8
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319	r400sc_scissor_rect_02	2002Sep8
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322	r400sc_scissor_rect_05	2002Sep8
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323	r400sc_scissor_rect_fc_01	2002Sep8
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324	r400sc_set_state_01	2002Sep8
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325	r400sc_tri_16_par_64_dwords_01	2002Sep8
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326	r400sc_tri_8textures_01	2002Sep8
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327	r400sc_tri_8textures_02	2002Sep8
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328	r400sc_triangle_stipple_01	2002Sep8
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329	r400sc_tri_walk_start_vertex_01	2002Sep8
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330	r400sc_tri_walk_start_vertex_02	2002Sep8
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331	r400sc_tri_walk_start_vertex_03	2002Sep8
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332	r400sc_tri_walk_start_vertex_04	2002Sep8

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334	r400sc_tri_walk_start_vertex_06	2002Sep8
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335	r400sc_tri_walk_start_vertex_07	2002Sep8
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336	r400sc_tri_walk_start_vertex_08	2002Sep8
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337	r400sc_tri_walk_start_vertex_09	2002Sep8
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339	r400sc_tri_walk_start_vertex_11	2002Sep8
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340	r400sc_tri_walk_start_vertex_12	2002Sep8
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341	r400sc_tri_walk_start_vertex_13	2002Sep8
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342	r400sc_tri_walk_start_vertex_14	2002Sep8
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343	r400sc_tri_walk_start_vertex_15	2002Sep8
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344	r400sc_tri_walk_start_vertex_16	2002Sep8
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345	r400sc_window_offset_01	2002Sep8
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346	r400sc_window_offset_fc_01	2002Sep8
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348	r400sc_zbuffer_point_list_01	2002Sep8
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349	r400sc_zbuffer_rectangle_list_01	2002Sep8
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350	r400sc_zbuffer_rectangle_list_02	2002Sep8
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351	r400sc_zbuffer_rectangle_list_fc_02	2002Sep8
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352	r400sc_zbuffer_triangle_list_01	2002Sep8
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353	r400su_4tri_text_offscreen_01	2002Sep9
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354	r400su_baryc_test_01	2002Sep9
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355	r400su_baryc_test_02	2002Sep9
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356	r400su_baryc_test_03	2002Sep9

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358 r400su_clip_baryc_test_01 2002Sep9
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359 r400su_clip_edgeflag_polymode_01 2002Sep9
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360 r400su_clipline_01 2002Sep9
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361 r400su_clippoint_01 2002Sep9
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363 r400su_clip_pointttest_01 2002Sep9
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364 r400su_clip_pointttest_02 2002Sep9
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365 r400su_clip_pointttest_03 2002Sep9
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366 r400su_clip_pointttest_04 2002Sep9
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367 r400su_clip_polymode_test_01 2002Sep9
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368 r400su_clipvertexsortingfunctional_01 2002Sep9
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369 r400su_cullingfunctional_01 2002Sep9
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370 r400su_degentri_test_01 2002Sep9
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371 r400su_degentri_test_02 2002Sep9
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372 r400su_degentri_test_03 2002Sep9
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373 r400su_edge_flag_01 2002Sep9
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374 r400su_edge_flag_02 2002Sep9
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375 r400su_edgeflags_triangle_01 2002Sep9
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376 r400su_edgeflags_triangle_02 2002Sep9
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377 r400su_geom_sort_01 2002Sep9
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378 r400su_line_end_cap_functional_01 2002Sep9
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379 r400su_line_end_cap_width_functional_02 2002Sep9
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380 r400su_line_test_01 2002Sep9

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385	r400su_parallel_orientation_all_02	2002Sep9
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386	r400su_pc_management_01	2002Sep9
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390	r400su_pointsizepresent_02	2002Sep9
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397	r400su_point_sprite_07	2002Sep9
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398	r400su_point_sprite_08	2002Sep9
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400	r400su_point_wl_h16_functional_01	2002Sep9
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402	r400su_polymodefunctional_01	2002Sep9
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415	r400su_polymode_zero_area_triangle_04	2002Sep9
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417	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep9
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433	r400su_triarea_test_04	2002Sep9
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435	r400vgt_auto_index_line_list_01	2002Sep8
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436	r400vgt_auto_index_line_loop_01	2002Sep8
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437	r400vgt_auto_index_line_strip_01	2002Sep8
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438	r400vgt_auto_index_points_01	2002Sep8
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439	r400vgt_auto_index_polygon_01	2002Sep8
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440	r400vgt_auto_index_primitives_01	2002Sep8
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441	r400vgt_auto_index_quad_list_01	2002Sep8
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445	r400vgt_auto_index_tri_list_01	2002Sep8
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446	r400vgt_auto_index_tri_strip_01	2002Sep8
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447	r400vgt_auto_index_tri_wflags_01	2002Sep8
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470	r400vgt_dma_index_tri_wflags_01	2002Sep8
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472	r400vgt_dma_swap_idx32_01	2002Sep8
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473	r400vgt_edgeflags_polygon_01	2002Sep8
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474	r400vgt_edgeflags_quad_list_01	2002Sep8
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475	r400vgt_edgeflags_quad_strip_01	2002Sep8
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477	r400vgt_edgeflags_triangle_list_01	2002Sep8
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478	r400vgt_edgeflags_triangle_strip_01	2002Sep8
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479	r400vgt_edgeflags_triangle_wflags_01	2002Sep8
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488	r400vgt_ext2int_index_triangle_list_01	2002Sep8
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489	r400vgt_ext2int_index_triangle_strip_01	2002Sep8
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490	r400vgt_hos_auto_index_line_list_01	2002Sep8
PASS		
491	r400vgt_hos_auto_index_quad_list_01	2002Sep9
PASS		
492	r400vgt_hos_auto_index_triangle_list_01	2002Sep8
PASS		
493	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep8
PASS		
494	r400vgt_hos_LPatch_01	2002Sep9
PASS		
495	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep8
PASS		
496	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep8
PASS		
497	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep8
PASS		
498	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep8
PASS		
499	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep8
PASS		
500	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep8

PASS		
501	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep9
PASS		
502	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep8
PASS		
503	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep9
PASS		
504	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep8
PASS		
505	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep8
PASS		
506	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep8
PASS		
507	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep8
PASS		
508	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep8
PASS		
509	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep8
PASS		
510	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep8
PASS		
511	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep8
PASS		
512	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep8
PASS		
513	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep8
PASS		
514	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep9
PASS		
515	r400vgt_hos_PNT_edge_detection_01	2002Sep9
PASS		
516	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep8
PASS		
517	r400vgt_hos_RPatch_cp_02	2002Sep9
PASS		
518	r400vgt_hos_RPatch_lp_02	2002Sep9
PASS		
519	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep9
PASS		
520	r400vgt_hos_TPatch_01	2002Sep9
PASS		
521	r400vgt_hos_TPatch_02	2002Sep9
PASS		
522	r400vgt_immed_index_line_list_01	2002Sep9
PASS		
523	r400vgt_immed_index_line_loop_01	2002Sep9
PASS		
524	r400vgt_immed_index_line_strip_01	2002Sep9

PASS		
525	r400vgt_immed_index_points_01	2002Sep9
PASS		
526	r400vgt_immed_index_polygon_01	2002Sep9
PASS		
527	r400vgt_immed_index_primitives_01	2002Sep9
PASS		
528	r400vgt_immed_index_quad_list_01	2002Sep9
PASS		
529	r400vgt_immed_index_quad_strip_01	2002Sep9
PASS		
530	r400vgt_immed_index_rectangle_list_01	2002Sep9
PASS		
531	r400vgt_immed_index_tri_fan_01	2002Sep9
PASS		
532	r400vgt_immed_index_tri_list_01	2002Sep9
PASS		
533	r400vgt_immed_index_tri_strip_01	2002Sep9
PASS		
534	r400vgt_immed_index_tri_wflags_01	2002Sep9
PASS		
535	r400vgt_index_dealloc_line_list_01	2002Sep9
PASS		
536	r400vgt_index_dealloc_points_01	2002Sep9
PASS		
537	r400vgt_index_dealloc_triangle_list_01	2002Sep9
PASS		
538	r400vgt_index_min_max_01	2002Sep9
PASS		
539	r400vgt_index_min_max_02	2002Sep9
PASS		
540	r400vgt_index_offset_01	2002Sep9
PASS		
541	r400vgt_index_offset_02	2002Sep9
PASS		
542	r400vgt_index_offset_03	2002Sep9
PASS		
543	r400vgt_index_size_01	2002Sep9
PASS		
544	r400vgt_index_source_switch_01	2002Sep9
PASS		
545	r400vgt_line_list_01	2002Sep9
PASS		
546	r400vgt_line_loop_01	2002Sep9
PASS		
547	r400vgt_line_strip_01	2002Sep9
PASS		
548	r400vgt_multi_context_01	2002Sep9

PASS		
549	r400vgt_multi_context_02	2002Sep9
PASS		
550	r400vgt_multi_context_03	2002Sep9
PASS		
551	r400vgt_multi_context_04	2002Sep9
PASS		
552	r400vgt_multi_context_05	2002Sep9
PASS		
553	r400vgt_multi_context_06	2002Sep9
PASS		
554	r400vgt_multi_context_07	2002Sep9
PASS		
555	r400vgt_multi_context_08	2002Sep9
PASS		
556	r400vgt_multi_context_09	2002Sep9
PASS		
557	r400vgt_multi_context_10	2002Sep9
PASS		
558	r400vgt_multi_context_11	2002Sep9
PASS		
559	r400vgt_multi_context_12	2002Sep9
PASS		
560	r400vgt_pass_thru_all_prims_01	2002Sep9
PASS		
561	r400vgt_point_list_01	2002Sep9
PASS		
562	r400vgt_polygon_01	2002Sep9
PASS		
563	r400vgt_provoking_vtx_all_01	2002Sep9
PASS		
564	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep9
PASS		
565	r400vgt_quad_list_01	2002Sep9
PASS		
566	r400vgt_quad_strip_01	2002Sep9
PASS		
567	r400vgt_rectangle_list_01	2002Sep9
PASS		
568	r400vgt_reuse_depth_line_list_01	2002Sep9
PASS		
569	r400vgt_reuse_depth_point_list_01	2002Sep9
PASS		
570	r400vgt_reuse_depth_triangle_list_01	2002Sep9
PASS		
571	r400vgt_reuse_index_line_list_01	2002Sep9
PASS		
572	r400vgt_reuse_index_point_list_01	2002Sep9

```

PASS
573 r400vgt_reuse_index_triangle_list_01 2002Sep9
PASS
574 r400vgt_reuse_index_triangle_list_02 2002Sep9
PASS
575 r400vgt_reuse_index_triangle_list_03 2002Sep9
PASS
576 r400vgt_suppress_eop_01 2002Sep9
PASS
577 r400vgt_suppress_eop_02 2002Sep9
PASS
578 r400vgt_suppress_eop_03 2002Sep9
PASS
579 r400vgt_suppress_eop_04 2002Sep9
PASS
580 r400vgt_triangle_fan_01 2002Sep9
PASS
581 r400vgt_triangle_list_01 2002Sep9
PASS
582 r400vgt_triangle_strip_01 2002Sep9
PASS
583 r400vgt_triangle_wflags_01 2002Sep9
PASS
584 r400vgt_viz_query_01 2002Sep9
PASS
585 r400vgt_vtx_vector_packing_01 2002Sep9
PASS
586 r400vte_coverage_01 2002Sep9
PASS
587 r400vte_simple_point_01 2002Sep9
PASS
588 r400vte_simple_triangle_01 2002Sep9
PASS
589 r400vte_w0_fmt_01 2002Sep9
PASS
590 r400vte_w0_fmt_03 2002Sep9
PASS
591 r400vte_xy_fmt_01 2002Sep9
PASS
592 r400vte_xyz_scale_01 2002Sep9
PASS
593 r400vte_xyz_scale_02 2002Sep9
PASS
594 r400vte_z_fmt_01 2002Sep9
PASS

```

```

=====
Tests_Run_count = 594 Pass_count = 593 Fail_count = 0 Incomplete_count = 1
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep 26  0:58:15 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             172         172         170           98
              SC              276         276         276           100
              CL              115         115         115           100
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              SU               85          85          85           100
              SANITY            2           2           2           100
+++++
              TOTAL            678         678         676           99
+++++

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep  2  7:39:09 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          147          147          147      100
              SC           161          161          160       99
              CL           108          108          108      100
              VTE           9           9           9       100
SPECIALTY    SU           81           81           81      100
              SANITY       2           2           2       100
+++++
              TOTAL        514          514          513       99
+++++

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sun Sep 29 22:14:43 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             175         175         173           98
              SC              284         284         284           100
              CL              115         115         115           100
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              SU               91          91          91           100
              SANITY            2           2           2           100
+++++
              TOTAL            695         695         693           99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc	0:0:3	2002Oct12
PASS			
2	cp_dma_interrupt	0:0:2	2002Oct12
PASS			
3	cp_dma_m2m_01	0:0:1	2002Oct12
PASS			
4	cp_dma_m2m_02	0:0:1	2002Oct12
PASS			
5	cp_dma_m2m_03	0:0:1	2002Oct12
PASS			
6	cp_dma_m2m_04	0:0:1	2002Oct12
PASS			
7	cp_dma_m2r_01	0:0:1	2002Oct12
PASS			
8	cp_dma_m2r_02	0:0:2	2002Oct12
PASS			
9	cp_dma_m2r_03	0:0:1	2002Oct12
PASS			
10	cp_dma_m2r_04	0:0:1	2002Oct12
PASS			
11	cp_dma_m2r_r2m	0:0:1	2002Oct12
PASS			
12	cp_dma_pio_simple	0:0:1	2002Oct12
PASS			
13	cp_dma_pio_stress	0:0:1	2002Oct12
PASS			
14	cp_dma_piobm_stress	0:0:1	2002Oct12
PASS			
15	cp_dma_r2m_01	0:0:1	2002Oct12
PASS			
16	cp_dma_r2m_02	0:0:2	2002Oct12
PASS			
17	cp_dma_r2m_03	0:0:2	2002Oct12
PASS			
18	cp_dma_r2m_04	0:0:2	2002Oct12
PASS			
19	cp_dma_r2r_01	0:0:1	2002Oct12
PASS			
20	cp_dma_r2r_02	0:0:1	2002Oct12

PASS			
21	cp_dma_r2r_03	0:0:2	2002Oct12
PASS			
22	cp_dma_r2r_r2m	0:0:1	2002Oct12
PASS			
23	cp_dma_r2r_r2m_m2m	0:0:2	2002Oct12
PASS			
24	cp_dma_r2r_r2m_m2m_r2m	0:0:1	2002Oct12
PASS			
25	cp_dma_simple	0:0:1	2002Oct12
PASS			
26	cp_e2_hostdata_blt_pntr_8888	0:0:1	2002Oct12
PASS			
27	cp_e2_one_blit	0:0:0	2002Oct12
PASS			
28	cp_e2_one_line	0:0:2	2002Oct12
PASS			
29	cp_e2blit_brush_mt_ropcc	0:0:1	2002Oct12
PASS			
30	cp_e2paint_565	0:0:1	2002Oct12
PASS			
31	cp_e2paint_multi	0:0:2	2002Oct12
PASS			
32	cp_e2polyscanlines_brush	0:0:1	2002Oct12
PASS			
33	cp_e2polyscanlines_brush_mt	0:0:1	2002Oct12
PASS			
34	cp_e2smalltext_jcl	0:0:3	2002Oct12
PASS			
35	cp_im_load_indirect	0:0:3	2002Oct12
PASS			
36	cp_push_aper_indirect1	0:0:3	2002Oct12
PASS			
37	cp_push_aper_primary	0:0:3	2002Oct12
PASS			
38	cp_queue_avail_01	0:0:3	2002Oct12
PASS			
39	cp_queue_avail_02	0:0:3	2002Oct12
PASS			
40	cp_queue_avail_03	0:0:3	2002Oct12
PASS			
41	cp_queue_avail_04	0:0:3	2002Oct12
PASS			
42	cp_queue_avail_05	0:0:3	2002Oct12
PASS			
43	cp_queue_avail_06	0:0:3	2002Oct12
PASS			
44	cp_queue_avail_07	0:0:3	2002Oct12

PASS			
45	cp_queue_avail_08	0:0:3	2002Oct12
PASS			
46	cp_queue_avail_09	0:0:3	2002Oct12
PASS			
47	cp_queue_avail_10	0:0:3	2002Oct12
PASS			
48	cp_queue_avail_11	0:0:3	2002Oct12
PASS			
49	cp_queue_avail_12	0:0:3	2002Oct12
PASS			
50	cp_simple_triangle	0:0:3	2002Oct12
PASS			
51	milestone_event_flush_tri1	0:0:3	2002Oct11
PASS			
52	milestone_event_flush_tri2	0:0:3	2002Oct11
PASS			
53	milestone_event_flush_tri3	0:0:3	2002Oct11
PASS			
54	milestone_event_flush_tri4	0:0:3	2002Oct11
PASS			
55	milestone_event_flush_tri5	0:0:3	2002Oct11
PASS			
56	milestone_event_flush_tri6	0:0:3	2002Oct11
PASS			
57	milestone_event_flush_tri7	0:0:3	2002Oct11
PASS			
58	milestone_event_flush_tri8	0:0:3	2002Oct11
PASS			
59	milestone_event_flush_tri9	0:0:3	2002Oct11
PASS			
60	milestone_event_randomized	0:0:3	2002Oct11
PASS			
61	milestone_event_standard	0:0:3	2002Oct11
PASS			
62	milestone_event_switch_flush_tri1	0:0:3	2002Oct11
PASS			
63	milestone_event_switch_flush_tri2	0:0:3	2002Oct11
PASS			
64	milestone_event_switch_flush_tri3	0:0:3	2002Oct11
PASS			
65	milestone_event_switch_flush_tri4	0:0:3	2002Oct11
PASS			
66	milestone_event_switch_flush_tri5	0:0:3	2002Oct11
PASS			
67	milestone_event_switch_flush_tri6	0:0:3	2002Oct11
PASS			
68	milestone_event_switch_flush_tri7	0:0:3	2002Oct11

PASS			
69	milestone_event_switch_flush_tri8	0:0:3	2002Oct11
PASS			
70	milestone_event_switch_flush_tri9	0:0:3	2002Oct11
PASS			
71	milestone_event_switch_tri2	0:0:3	2002Oct11
PASS			
72	milestone_event_switch_tri3	0:0:3	2002Oct11
PASS			
73	milestone_event_switch_tri4	0:0:3	2002Oct11
PASS			
74	milestone_event_switch_tri5	0:0:3	2002Oct11
PASS			
75	milestone_event_switch_tri6	0:0:3	2002Oct11
PASS			
76	milestone_event_switch_tri7	0:0:3	2002Oct11
PASS			
77	milestone_event_switch_tri8	0:0:3	2002Oct11
PASS			
78	milestone_event_switch_tri9	0:0:3	2002Oct11
PASS			
79	milestone_event_tri2	0:0:3	2002Oct11
PASS			
80	milestone_event_tri3	0:0:3	2002Oct11
PASS			
81	milestone_event_tri4	0:0:3	2002Oct11
PASS			
82	milestone_event_tri5	0:0:3	2002Oct11
PASS			
83	milestone_event_tri6	0:0:3	2002Oct11
PASS			
84	milestone_event_tri7	0:0:3	2002Oct11
PASS			
85	milestone_event_tri8	0:0:3	2002Oct11
PASS			
86	milestone_event_tri9	0:0:3	2002Oct11
PASS			
87	milestone_tri_random_rect	0:0:3	2002Oct11
PASS			
88	milestone_tri_random_tri	0:0:3	2002Oct11
PASS			
89	milestone_tri_randomized	0:0:3	2002Oct11
PASS			
90	milestone_tri_standard	0:0:3	2002Oct11
PASS			
91	milestone_tri_tril128_pix4	0:0:3	2002Oct11
PASS			
92	milestone_tri_tril_pix1	0:0:3	2002Oct11

PASS			
93	milestone_tri_tri1_pix2	0:0:3	2002Oct11
PASS			
94	milestone_tri_tri1_pix3	0:0:3	2002Oct11
PASS			
95	milestone_tri_tri1_pix4	0:0:3	2002Oct11
PASS			
96	milestone_tri_tri24x24	0:0:3	2002Oct11
PASS			
97	milestone_tri_tri28x28	0:0:3	2002Oct11
PASS			
98	milestone_tri_tri2_pix1	0:0:3	2002Oct11
PASS			
99	milestone_tri_tri2_pix16	0:0:3	2002Oct11
PASS			
100	milestone_tri_tri2_pix4	0:0:3	2002Oct11
PASS			
101	milestone_tri_tri32_pix1	0:0:3	2002Oct11
PASS			
102	milestone_tri_tri32_pix4	0:0:3	2002Oct11
PASS			
103	milestone_tri_tri32x32	0:0:3	2002Oct11
PASS			
104	milestone_tri_tri36x36	0:0:3	2002Oct11
PASS			
105	milestone_tri_tri40x40	0:0:3	2002Oct11
PASS			
106	milestone_tri_tri44x44	0:0:3	2002Oct11
PASS			
107	milestone_tri_tri48x48	0:0:3	2002Oct11
PASS			
108	milestone_tri_tri52x52	0:0:3	2002Oct11
PASS			
109	milestone_tri_tri56x56	0:0:3	2002Oct11
PASS			
110	milestone_tri_tri60x60	0:0:3	2002Oct11
PASS			
111	milestone_tri_tri64x64	0:0:3	2002Oct11
PASS			
112	milestone_tri_tri8_pix4	0:0:3	2002Oct11
PASS			
113	primlib_1st_tri_june15	0:0:3	2002Oct12
PASS			
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	0:0:3	2002Oct12
PASS			
115	primlib_gouraud_triangles_2_draw_passes	0:0:3	2002Oct12
PASS			
116	primlib_parameterized_simple_triangle	0:0:3	2002Oct12

PASS			
117	primlib_template_simple_triangle	0:0:3	2002Oct12
PASS			
118	primlib_tex_tri	0:0:3	2002Oct12
PASS			
119	r400cl_bary_texture_08_bug	0:0:3	2002Sep21
PASS			
120	r400cl_clip_10_verts_01	0:0:3	2002Oct12
PASS			
121	r400cl_clip_disable_01	0:0:3	2002Oct12
PASS			
122	r400cl_clip_space_dx_ogl_01	0:0:3	2002Oct12
PASS			
123	r400cl_clip_ucp_6bits_01	0:0:3	2002Oct12
PASS			
124	r400cl_cull_only_ena_01	0:0:3	2002Oct12
PASS			
125	r400cl_edgeflags_01	0:0:3	2002Oct12
PASS			
126	r400cl_edgeflags_01_bug	0:0:3	2002Oct9
PASS			
127	r400cl_edgeflags_frustum_bottom_01	0:0:3	2002Oct12
PASS			
128	r400cl_edgeflags_frustum_far_01	0:0:3	2002Oct12
PASS			
129	r400cl_edgeflags_frustum_left_01	0:0:3	2002Oct12
PASS			
130	r400cl_edgeflags_frustum_near_01	0:0:3	2002Oct12
PASS			
131	r400cl_edgeflags_frustum_right_01	0:0:3	2002Oct12
PASS			
132	r400cl_edgeflags_frustum_top_01	0:0:3	2002Oct12
PASS			
133	r400cl_edgeflags_gband_01	0:0:3	2002Oct12
PASS			
134	r400cl_edgeflags_gband_bottom_01	0:0:3	2002Oct12
PASS			
135	r400cl_edgeflags_gband_left_01	0:0:3	2002Oct12
PASS			
136	r400cl_edgeflags_gband_right_01	0:0:3	2002Oct12
PASS			
137	r400cl_edgeflags_gband_top_01	0:0:3	2002Oct12
PASS			
138	r400cl_frustum_01	0:0:3	2002Oct12
PASS			
139	r400cl_frustum_02	0:0:3	2002Oct12
PASS			
140	r400cl_frustum_03	0:0:3	2002Oct12

PASS			
141	r400cl_frustum_04	0:0:3	2002Oct12
PASS			
142	r400cl_frustum_05	0:0:3	2002Oct12
PASS			
143	r400cl_frustum_06	0:0:3	2002Oct12
PASS			
144	r400cl_frustum_07	0:0:3	2002Oct12
PASS			
145	r400cl_frustum_08	0:0:3	2002Oct12
PASS			
146	r400cl_frustum_09	0:0:3	2002Oct12
PASS			
147	r400cl_frustum_10	0:0:3	2002Oct12
PASS			
148	r400cl_frustum_11	0:0:3	2002Oct12
PASS			
149	r400cl_frustum_12	0:0:3	2002Oct12
PASS			
150	r400cl_frustum_13	0:0:3	2002Oct12
PASS			
151	r400cl_frustum_14	0:0:3	2002Oct12
PASS			
152	r400cl_frustum_15	0:0:3	2002Oct12
PASS			
153	r400cl_frustum_16	0:0:3	2002Oct12
PASS			
154	r400cl_frustum_17	0:0:3	2002Oct12
PASS			
155	r400cl_frustum_18	0:0:3	2002Oct12
PASS			
156	r400cl_frustum_19	0:0:3	2002Oct12
PASS			
157	r400cl_frustum_20	0:0:3	2002Oct12
PASS			
158	r400cl_frustum_21	0:0:3	2002Oct12
PASS			
159	r400cl_frustum_22	0:0:3	2002Oct12
PASS			
160	r400cl_frustum_23	0:0:3	2002Oct12
PASS			
161	r400cl_frustum_24	0:0:3	2002Oct12
PASS			
162	r400cl_frustum_25	0:0:3	2002Oct12
PASS			
163	r400cl_frustum_26	0:0:3	2002Oct12
PASS			
164	r400cl_frustum_27	0:0:3	2002Oct12

PASS			
165	r400cl_frustum_28	0:0:3	2002Oct12
PASS			
166	r400cl_frustum_29	0:0:3	2002Oct12
PASS			
167	r400cl_frustum_30	0:0:3	2002Oct12
PASS			
168	r400cl_frustum_31	0:0:3	2002Oct12
PASS			
169	r400cl_frustum_32	0:0:3	2002Oct12
PASS			
170	r400cl_frustum_33	0:0:3	2002Oct12
PASS			
171	r400cl_frustum_34	0:0:3	2002Oct12
PASS			
172	r400cl_frustum_35	0:0:3	2002Oct12
PASS			
173	r400cl_frustum_36	0:0:3	2002Oct12
PASS			
174	r400cl_frustum_37	0:0:3	2002Oct12
PASS			
175	r400cl_frustum_38	0:0:3	2002Oct12
PASS			
176	r400cl_frustum_39	0:0:3	2002Oct12
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448	r400sc_msaa_2_zbuffer_rectangle_list_01	0:0:3	2002Oct12
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449	r400sc_msaa_2_zbuffer_rectangle_list_02	0:0:3	2002Oct12
PASS			
450	r400sc_msaa_3_primitives_01	0:0:3	2002Oct12
PASS			
451	r400sc_msaa_3_rectangle_list_01	0:0:3	2002Oct12
PASS			
452	r400sc_msaa_3_rectangle_list_02	0:0:3	2002Oct12

PASS			
453	r400sc_msaa_3_rectangle_list_03	0:0:3	2002Oct12
PASS			
454	r400sc_msaa_3_rectangle_list_04	0:0:3	2002Oct12
PASS			
455	r400sc_msaa_3_rectangle_list_05	0:0:3	2002Oct12
PASS			
456	r400sc_msaa_3_rectangle_list_06	0:0:3	2002Oct12
PASS			
457	r400sc_msaa_3_rectangle_list_07	0:0:3	2002Oct12
PASS			
458	r400sc_msaa_3_rectangle_list_08	0:0:3	2002Oct12
PASS			
459	r400sc_msaa_3_zbuffer_rectangle_list_01	0:0:3	2002Oct12
PASS			
460	r400sc_msaa_3_zbuffer_rectangle_list_02	0:0:3	2002Oct12
PASS			
461	r400sc_msaa_4_01	0:0:3	2002Oct12
PASS			
462	r400sc_msaa_4_primitives_01	0:0:3	2002Oct12
PASS			
463	r400sc_msaa_4_rectangle_list_01	0:0:3	2002Oct12
PASS			
464	r400sc_msaa_4_rectangle_list_02	0:0:3	2002Oct12
PASS			
465	r400sc_msaa_4_rectangle_list_03	0:0:3	2002Oct12
PASS			
466	r400sc_msaa_4_rectangle_list_04	0:0:3	2002Oct12
PASS			
467	r400sc_msaa_4_rectangle_list_05	0:0:3	2002Oct12
PASS			
468	r400sc_msaa_4_rectangle_list_06	0:0:3	2002Oct12
PASS			
469	r400sc_msaa_4_rectangle_list_07	0:0:3	2002Oct12
PASS			
470	r400sc_msaa_4_rectangle_list_08	0:0:3	2002Oct12
PASS			
471	r400sc_msaa_4_zbuffer_rectangle_list_01	0:0:3	2002Oct12
PASS			
472	r400sc_msaa_4_zbuffer_rectangle_list_02	0:0:3	2002Oct12
PASS			
473	r400sc_msaa_6_01	0:0:3	2002Oct12
PASS			
474	r400sc_msaa_6_primitives_01	0:0:3	2002Oct12
PASS			
475	r400sc_msaa_6_rectangle_list_01	0:0:3	2002Oct12
PASS			
476	r400sc_msaa_6_rectangle_list_02	0:0:3	2002Oct12

PASS			
477	r400sc_msaa_6_rectangle_list_03	0:0:3	2002Oct12
PASS			
478	r400sc_msaa_6_rectangle_list_04	0:0:3	2002Oct12
PASS			
479	r400sc_msaa_6_rectangle_list_05	0:0:3	2002Oct12
PASS			
480	r400sc_msaa_6_rectangle_list_06	0:0:3	2002Oct12
PASS			
481	r400sc_msaa_6_rectangle_list_07	0:0:3	2002Oct12
PASS			
482	r400sc_msaa_6_rectangle_list_08	0:0:3	2002Oct12
PASS			
483	r400sc_msaa_6_zbuffer_rectangle_list_01	0:0:3	2002Oct12
PASS			
484	r400sc_msaa_6_zbuffer_rectangle_list_02	0:0:3	2002Oct12
PASS			
485	r400sc_msaa_8_01	0:0:3	2002Oct12
PASS			
486	r400sc_msaa_8_02	0:0:3	2002Oct12
PASS			
487	r400sc_msaa_8_03	0:0:3	2002Oct12
PASS			
488	r400sc_msaa_8_04	0:0:3	2002Oct12
PASS			
489	r400sc_msaa_8_05	0:0:3	2002Oct12
PASS			
490	r400sc_msaa_8_aa_mask_01	0:0:3	2002Oct12
PASS			
491	r400sc_msaa_8_aa_mask_02	0:0:3	2002Oct12
PASS			
492	r400sc_msaa_8_aa_mask_fc_02	0:0:3	2002Oct12
PASS			
493	r400sc_msaa_8_hos_PNT_cont_cp_qn_complex_01	0:0:3	2002Oct12
PASS			
494	r400sc_msaa_8_primitives_01	0:0:3	2002Oct12
PASS			
495	r400sc_msaa_8_rectangle_list_01	0:0:3	2002Oct12
PASS			
496	r400sc_msaa_8_rectangle_list_02	0:0:3	2002Oct12
PASS			
497	r400sc_msaa_8_rectangle_list_03	0:0:3	2002Oct12
PASS			
498	r400sc_msaa_8_rectangle_list_04	0:0:3	2002Oct12
PASS			
499	r400sc_msaa_8_rectangle_list_05	0:0:3	2002Oct12
PASS			
500	r400sc_msaa_8_rectangle_list_06	0:0:3	2002Oct12

PASS			
501	r400sc_msa_8_rectangle_list_07	0:0:3	2002Oct12
PASS			
502	r400sc_msa_8_rectangle_list_08	0:0:3	2002Oct12
PASS			
503	r400sc_msa_8_zbuffer_rectangle_list_01	0:0:3	2002Oct12
PASS			
504	r400sc_msa_8_zbuffer_rectangle_list_02	0:0:3	2002Oct12
PASS			
505	r400sc_msa_X_primitives_01_bug	0:0:3	2002Oct9
PASS			
506	r400sc_msa_X_primitives_02_bug	0:0:3	2002Oct9
PASS			
507	r400sc_multi_chip_prim_discard_00_07	0:0:3	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
508	r400sc_multi_chip_prim_discard_00_07_standard	0:0:3	2002Oct11
PASS			
509	r400sc_multi_chip_prim_discard_00_07_test00	0:0:3	2002Oct11
INCOMPLETE			
510	r400sc_multi_chip_prim_discard_00_07_test01	0:0:3	2002Oct11
PASS			
511	r400sc_multi_chip_prim_discard_00_07_test02	0:0:3	2002Oct11
INCOMPLETE			
512	r400sc_multi_chip_prim_discard_00_07_test03	0:0:3	2002Oct11
PASS			
513	r400sc_multi_chip_prim_discard_00_07_test04	0:0:3	2002Oct11
INCOMPLETE			
514	r400sc_multi_chip_prim_discard_00_07_test05	0:0:3	2002Oct11
INCOMPLETE			
515	r400sc_multi_chip_prim_discard_00_07_test06	0:0:3	2002Oct11
PASS			
516	r400sc_multi_chip_tile_discard_00_07	0:0:3	2002Oct7
PASS			
517	r400sc_multi_chip_tile_discard_00_07_standard	0:0:3	2002Oct11
PASS			
518	r400sc_multi_chip_tile_discard_00_07_test00	0:0:3	2002Oct11
PASS			
519	r400sc_multi_chip_tile_discard_00_07_test01	0:0:3	2002Oct11
PASS			
520	r400sc_multi_chip_tile_discard_00_07_test02	0:0:3	2002Oct11
PASS			
521	r400sc_multi_chip_tile_discard_00_07_test03	0:0:3	2002Oct11
PASS			
522	r400sc_multi_chip_tile_discard_00_07_test04	0:0:3	2002Oct11
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp VgtPaClipS_VgtSide.cmp		
compare file empty			
523	r400sc_multi_chip_tile_discard_00_07_test05	0:0:3	2002Oct11
INCOMPLETE			

524	r400sc_multi_chip_tile_discard_00_07_test06	0:0:3	2002Oct11
	INCOMPLETE		
525	r400sc_null_triangles_01	0:0:3	2002Oct12
	PASS		
526	r400sc_null_triangles_fc_01	0:0:3	2002Oct12
	PASS		
527	r400sc_packed_color_01	0:0:3	2002Oct12
	PASS		
528	r400sc_perf_01	0:0:3	2002Oct12
	PASS		
529	r400sc_perf_02	0:0:3	2002Oct12
	PASS		
530	r400sc_perf_03	0:0:3	2002Oct12
	PASS		
531	r400sc_pinwheel_01	0:0:3	2002Oct12
	PASS		
532	r400sc_pinwheel_02	0:0:3	2002Oct12
	PASS		
533	r400sc_pinwheel_02_bug	0:0:3	2002Oct9
	PASS		
534	r400sc_point_jss_3x4_01	0:0:3	2002Oct12
	PASS		
535	r400sc_point_list_01	0:0:3	2002Oct12
	PASS		
536	r400sc_point_list_02	0:0:3	2002Oct12
	PASS		
537	r400sc_point_list_03	0:0:3	2002Oct12
	PASS		
538	r400sc_point_list_04	0:0:3	2002Oct12
	PASS		
539	r400sc_point_list_05	0:0:3	2002Oct12
	PASS		
540	r400sc_point_list_06	0:0:3	2002Oct12
	PASS		
541	r400sc_point_list_06_bug	0:0:3	2002Oct9
	PASS		
542	r400sc_point_list_06_bug_02	0:0:3	2002Oct9
	PASS		
543	r400sc_point_list_07	0:0:3	2002Oct12
	PASS		
544	r400sc_point_list_08	0:0:3	2002Oct12
	PASS		
545	r400sc_point_list_09	0:0:3	2002Oct12
	PASS		
546	r400sc_point_msa_8_01	0:0:3	2002Oct12
	PASS		
547	r400sc_poly_offset_01	0:0:3	2002Oct12
	PASS		

548	r400sc_poly_offset_02	0:0:3	2002Oct12
	PASS		
549	r400sc_poly_offset_03	0:0:3	2002Oct12
	PASS		
550	r400sc_poly_offset_04	0:0:3	2002Oct12
	PASS		
551	r400sc_poly_offset_05	0:0:3	2002Oct12
	PASS		
552	r400sc_poly_offset_06	0:0:3	2002Oct12
	PASS		
553	r400sc_poly_offset_07	0:0:3	2002Oct12
	PASS		
554	r400sc_poly_offset_08	0:0:3	2002Oct12
	PASS		
555	r400sc_poly_offset_09	0:0:3	2002Oct12
	PASS		
556	r400sc_poly_offset_10	0:0:3	2002Oct12
	PASS		
557	r400sc_poly_offset_10_bug	0:0:3	2002Oct9
	PASS		
558	r400sc_poly_offset_fc_01	0:0:3	2002Oct12
	PASS		
559	r400sc_polygon_stipple_01	0:0:3	2002Oct12
	PASS		
560	r400sc_polymode_tri_fill_01	0:0:3	2002Oct12
	PASS		
561	r400sc_polymode_tri_fill_01_bug	0:0:3	2002Oct9
	PASS		
562	r400sc_prsp_byc_intrp_ref_pix_01	0:0:3	2002Oct12
	PASS		
563	r400sc_prsp_byc_intrp_ref_pix_02	0:0:3	2002Oct12
	PASS		
564	r400sc_prsp_byc_intrp_ref_pix_03	0:0:3	2002Oct12
	PASS		
565	r400sc_prsp_byc_intrp_ref_pix_04	0:0:3	2002Oct12
	PASS		
566	r400sc_prsp_byc_intrp_ref_pix_05	0:0:3	2002Oct12
	PASS		
567	r400sc_prsp_byc_intrp_ref_pix_06	0:0:3	2002Oct12
	PASS		
568	r400sc_prsp_byc_intrp_ref_pix_07	0:0:3	2002Oct12
	PASS		
569	r400sc_prsp_byc_intrp_ref_pix_08	0:0:3	2002Oct12
	PASS		
570	r400sc_raster_fill_rule_01	0:0:3	2002Oct12
	PASS		
571	r400sc_raster_fill_rule_02	0:0:3	2002Oct12
	PASS		

572	r400sc_raster_fill_rule_03	0:0:3	2002Oct12
	PASS		
573	r400sc_raster_fill_rule_04	0:0:3	2002Oct12
	PASS		
574	r400sc_raster_fill_rule_05	0:0:3	2002Oct12
	PASS		
575	r400sc_raster_fill_rule_fc_01	0:0:3	2002Oct12
	PASS		
576	r400sc_rectangle_list_01	0:0:3	2002Oct12
	PASS		
577	r400sc_rectangle_list_02	0:0:3	2002Oct12
	PASS		
578	r400sc_rectangle_list_03	0:0:3	2002Oct12
	PASS		
579	r400sc_rectangle_list_04	0:0:3	2002Oct12
	PASS		
580	r400sc_rectangle_list_05	0:0:3	2002Oct12
	PASS		
581	r400sc_rectangle_list_06	0:0:3	2002Oct12
	PASS		
582	r400sc_rectangle_list_07	0:0:3	2002Oct12
	PASS		
583	r400sc_rectangle_list_08	0:0:3	2002Oct12
	PASS		
584	r400sc_scissor_rect_01	0:0:3	2002Oct12
	PASS		
585	r400sc_scissor_rect_02	0:0:3	2002Oct12
	PASS		
586	r400sc_scissor_rect_03	0:0:3	2002Oct12
	PASS		
587	r400sc_scissor_rect_04	0:0:3	2002Oct12
	PASS		
588	r400sc_scissor_rect_05	0:0:3	2002Oct12
	PASS		
589	r400sc_scissor_rect_fc_01	0:0:3	2002Oct12
	PASS		
590	r400sc_set_state_01	0:0:3	2002Oct12
	PASS		
591	r400sc_sp_sample_cntl_01	0:0:3	2002Oct12
	PASS		
592	r400sc_sp_sample_cntl_02	0:0:3	2002Oct12
	PASS		
593	r400sc_sp_sample_cntl_03	0:0:3	2002Oct12
	PASS		
594	r400sc_sp_sample_cntl_04	0:0:3	2002Oct12
	PASS		
595	r400sc_sp_sample_cntl_05	0:0:3	2002Oct12
	PASS		

596	r400sc_sp_sample_cntl_06	0:0:3	2002Oct12
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597	r400sc_sp_sample_cntl_07	0:0:3	2002Oct12
	PASS		
598	r400sc_sp_sample_cntl_08	0:0:3	2002Oct12
	PASS		
599	r400sc_tri_16_par_64_dwords_01	0:0:3	2002Oct12
	PASS		
600	r400sc_tri_8textures_01	0:0:3	2002Oct12
	PASS		
601	r400sc_tri_8textures_02	0:0:3	2002Oct12
	PASS		
602	r400sc_tri_walk_start_vertex_01	0:0:3	2002Oct12
	PASS		
603	r400sc_tri_walk_start_vertex_02	0:0:3	2002Oct12
	PASS		
604	r400sc_tri_walk_start_vertex_03	0:0:3	2002Oct12
	PASS		
605	r400sc_tri_walk_start_vertex_04	0:0:3	2002Oct12
	PASS		
606	r400sc_tri_walk_start_vertex_05	0:0:3	2002Oct12
	PASS		
607	r400sc_tri_walk_start_vertex_06	0:0:3	2002Oct12
	PASS		
608	r400sc_tri_walk_start_vertex_07	0:0:3	2002Oct12
	PASS		
609	r400sc_tri_walk_start_vertex_08	0:0:3	2002Oct12
	PASS		
610	r400sc_tri_walk_start_vertex_09	0:0:3	2002Oct12
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611	r400sc_tri_walk_start_vertex_10	0:0:3	2002Oct12
	PASS		
612	r400sc_tri_walk_start_vertex_11	0:0:3	2002Oct12
	PASS		
613	r400sc_tri_walk_start_vertex_12	0:0:3	2002Oct12
	PASS		
614	r400sc_tri_walk_start_vertex_13	0:0:3	2002Oct12
	PASS		
615	r400sc_tri_walk_start_vertex_14	0:0:3	2002Oct12
	PASS		
616	r400sc_tri_walk_start_vertex_15	0:0:3	2002Oct12
	PASS		
617	r400sc_tri_walk_start_vertex_16	0:0:3	2002Oct12
	PASS		
618	r400sc_triangle_stipple_01	0:0:3	2002Oct12
	PASS		
619	r400sc_window_offset_01	0:0:3	2002Oct12
	PASS		

620	r400sc_window_offset_02	0:0:3	2002Oct12
	PASS		
621	r400sc_window_offset_03	0:0:3	2002Oct12
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622	r400sc_window_offset_04	0:0:3	2002Oct12
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623	r400sc_window_offset_05	0:0:3	2002Oct12
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624	r400sc_window_offset_fc_01	0:0:3	2002Oct12
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625	r400sc_zbuffer_line_list_01	0:0:3	2002Oct12
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626	r400sc_zbuffer_point_list_01	0:0:3	2002Oct12
	PASS		
627	r400sc_zbuffer_rectangle_list_01	0:0:3	2002Oct12
	PASS		
628	r400sc_zbuffer_rectangle_list_02	0:0:3	2002Oct12
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629	r400sc_zbuffer_rectangle_list_fc_02	0:0:3	2002Oct12
	PASS		
630	r400sc_zbuffer_triangle_list_01	0:0:3	2002Oct12
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631	r400su_4tri_text_offscreen_01	0:0:3	2002Oct12
	PASS		
632	r400su_baryc_test_01	0:0:3	2002Oct12
	PASS		
633	r400su_baryc_test_02	0:0:3	2002Oct12
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634	r400su_baryc_test_03	0:0:3	2002Oct12
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635	r400su_baryc_test_04	0:0:3	2002Oct12
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636	r400su_clip_baryc_test_01	0:0:3	2002Oct12
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637	r400su_clip_baryc_test_02	0:0:3	2002Oct12
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638	r400su_clip_edgeflag_polymode_01	0:0:3	2002Oct12
	PASS		
639	r400su_clip_pointsize_test_01	0:0:3	2002Oct12
	PASS		
640	r400su_clip_pointtest_01	0:0:3	2002Oct12
	PASS		
641	r400su_clip_pointtest_02	0:0:3	2002Oct12
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642	r400su_clip_pointtest_03	0:0:3	2002Oct12
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643	r400su_clip_pointtest_04	0:0:3	2002Oct12
	PASS		

644	r400su_clip_polymode_test_01	0:0:3	2002Oct12
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645	r400su_clipline_01	0:0:3	2002Oct12
	PASS		
646	r400su_clippoint_01	0:0:3	2002Oct12
	PASS		
647	r400su_clipvertexsortingfunctional_01	0:0:3	2002Oct12
	PASS		
648	r400su_cullingfunctional_01	0:0:3	2002Oct12
	PASS		
649	r400su_degentri_test_01	0:0:3	2002Oct12
	PASS		
650	r400su_degentri_test_02	0:0:3	2002Oct12
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651	r400su_degentri_test_03	0:0:3	2002Oct12
	PASS		
652	r400su_degentri_test_04	0:0:3	2002Oct12
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653	r400su_edge_flag_01	0:0:3	2002Oct12
	PASS		
654	r400su_edge_flag_02	0:0:3	2002Oct12
	PASS		
655	r400su_edgeflags_triangle_01	0:0:3	2002Oct12
	PASS		
656	r400su_edgeflags_triangle_02	0:0:3	2002Oct12
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657	r400su_geom_sort_01	0:0:3	2002Oct12
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658	r400su_line_end_cap_functional_01	0:0:3	2002Oct12
	PASS		
659	r400su_line_end_cap_width_functional_02	0:0:3	2002Oct12
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660	r400su_line_orientation_01	0:0:3	2002Oct12
	PASS		
661	r400su_line_orientation_02	0:0:3	2002Oct12
	PASS		
662	r400su_line_orientation_dx01_01	0:0:3	2002Oct12
	PASS		
663	r400su_line_orientation_dx01_02	0:0:3	2002Oct12
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664	r400su_line_orientation_dy01_01	0:0:3	2002Oct12
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665	r400su_line_orientation_dy01_02	0:0:3	2002Oct12
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666	r400su_line_test_01	0:0:3	2002Oct12
	PASS		
667	r400su_line_test_02	0:0:3	2002Oct12
	PASS		

668	r400su_line_x_major_01	0:0:3	2002Oct12
	PASS		
669	r400su_line_x_major_02	0:0:3	2002Oct12
	PASS		
670	r400su_line_y_major_01	0:0:3	2002Oct12
	PASS		
671	r400su_line_y_major_02	0:0:3	2002Oct12
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672	r400su_longstrip_01	0:0:3	2002Oct12
	PASS		
673	r400su_multi_context_01	0:0:3	2002Oct12
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674	r400su_multi_prim_01	0:0:3	2002Oct12
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675	r400su_multi_prim_02	0:0:3	2002Oct12
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676	r400su_parallel_orientation_all_01	0:0:3	2002Oct12
	PASS		
677	r400su_parallel_orientation_all_02	0:0:3	2002Oct12
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678	r400su_pc_management_01	0:0:3	2002Oct12
	PASS		
679	r400su_pc_management_02	0:0:3	2002Oct12
	PASS		
680	r400su_pc_management_03	0:0:3	2002Oct12
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681	r400su_point_sprite_01	0:0:3	2002Oct12
	PASS		
682	r400su_point_sprite_02	0:0:3	2002Oct12
	PASS		
683	r400su_point_sprite_03	0:0:3	2002Oct12
	PASS		
684	r400su_point_sprite_04	0:0:3	2002Oct12
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685	r400su_point_sprite_05	0:0:3	2002Oct12
	PASS		
686	r400su_point_sprite_06	0:0:3	2002Oct12
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687	r400su_point_sprite_07	0:0:3	2002Oct12
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688	r400su_point_sprite_08	0:0:3	2002Oct12
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689	r400su_point_wl6_h1_functional_01	0:0:3	2002Oct12
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690	r400su_point_wl_h16_functional_01	0:0:3	2002Oct12
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691	r400su_pointsizepresent_01	0:0:3	2002Oct12
	PASS		

692	r400su_pointsizepresent_02	0:0:3	2002Oct12
	PASS		
693	r400su_polymode_lines_degen_triangle_01	0:0:3	2002Oct12
	PASS		
694	r400su_polymode_lines_degen_triangle_02	0:0:3	2002Oct12
	PASS		
695	r400su_polymode_lines_zero_area_triangle_01	0:0:3	2002Oct12
	PASS		
696	r400su_polymode_lines_zero_area_triangle_02	0:0:3	2002Oct12
	PASS		
697	r400su_polymode_points_degen_triangle_01	0:0:3	2002Oct12
	PASS		
698	r400su_polymode_points_degen_triangle_02	0:0:3	2002Oct12
	PASS		
699	r400su_polymode_points_zero_area_triangle_01	0:0:3	2002Oct12
	PASS		
700	r400su_polymode_points_zero_area_triangle_02	0:0:3	2002Oct12
	PASS		
701	r400su_polymode_rectangle_01	0:0:3	2002Oct12
	PASS		
702	r400su_polymode_zero_area_triangle_01	0:0:3	2002Oct12
	PASS		
703	r400su_polymode_zero_area_triangle_02	0:0:3	2002Oct12
	PASS		
704	r400su_polymode_zero_area_triangle_03	0:0:3	2002Oct12
	PASS		
705	r400su_polymode_zero_area_triangle_04	0:0:3	2002Oct12
	PASS		
706	r400su_polymodeculling_01	0:0:3	2002Oct12
	PASS		
707	r400su_polymodefunctional_01	0:0:3	2002Oct12
	PASS		
708	r400su_provoking_vtx_edgeflags_triangle_01	0:0:3	2002Oct12
	PASS		
709	r400su_provoking_vtx_edgeflags_triangle_02	0:0:3	2002Oct12
	PASS		
710	r400su_provoking_vtx_edgeflags_triangle_03	0:0:3	2002Oct12
	PASS		
711	r400su_provoking_vtx_edgeflags_triangle_04	0:0:3	2002Oct12
	PASS		
712	r400su_provoking_vtx_line_01	0:0:3	2002Oct12
	PASS		
713	r400su_provoking_vtx_point_01	0:0:3	2002Oct12
	PASS		
714	r400su_provoking_vtx_polymode_rectangle_01	0:0:3	2002Oct12
	PASS		
715	r400su_provoking_vtx_rectangle_01	0:0:3	2002Oct12
	PASS		

716	r400su_provoking_vtx_triangle_01	0:0:3	2002Oct12
	PASS		
717	r400su_rand_line_01	0:0:3	2002Oct12
	PASS		
718	r400su_rand_point_01	0:0:3	2002Oct12
	PASS		
719	r400su_rand_tri_01	0:0:3	2002Oct12
	PASS		
720	r400su_rectangle_01	0:0:3	2002Oct12
	PASS		
721	r400su_rectangle_list_01	0:0:3	2002Oct12
	PASS		
722	r400su_sliver_01	0:0:3	2002Oct12
	PASS		
723	r400su_triarea_test_01	0:0:3	2002Oct12
	PASS		
724	r400su_triarea_test_02	0:0:3	2002Oct12
	PASS		
725	r400su_triarea_test_03	0:0:3	2002Oct12
	PASS		
726	r400su_triarea_test_04	0:0:3	2002Oct12
	PASS		
727	r400su_vertexsortingfunctional_01	0:0:3	2002Oct12
	PASS		
728	r400su_w_grad_test_01	0:0:3	2002Oct12
	PASS		
729	r400su_w_grad_test_02	0:0:3	2002Oct12
	PASS		
730	r400su_w_grad_test_03	0:0:3	2002Oct12
	PASS		
731	r400su_z_grad_test_01	0:0:3	2002Oct12
	PASS		
732	r400su_z_grad_test_02	0:0:3	2002Oct12
	PASS		
733	r400su_z_grad_test_03	0:0:3	2002Oct12
	PASS		
734	r400su_zero_area_test_01	0:0:3	2002Oct12
	PASS		
735	r400su_zero_area_test_02	0:0:3	2002Oct12
	PASS		
736	r400su_zero_area_test_03	0:0:3	2002Oct12
	PASS		
737	r400su_zero_area_test_04	0:0:3	2002Oct12
	PASS		
738	r400vgt_auto_index_line_list_01	0:0:3	2002Oct12
	PASS		
739	r400vgt_auto_index_line_loop_01	0:0:3	2002Oct12
	PASS		

740	r400vgt_auto_index_line_strip_01	0:0:3	2002Oct12
	PASS		
741	r400vgt_auto_index_points_01	0:0:3	2002Oct12
	PASS		
742	r400vgt_auto_index_polygon_01	0:0:3	2002Oct12
	PASS		
743	r400vgt_auto_index_primitives_01	0:0:3	2002Oct12
	PASS		
744	r400vgt_auto_index_quad_list_01	0:0:3	2002Oct12
	PASS		
745	r400vgt_auto_index_quad_strip_01	0:0:3	2002Oct12
	PASS		
746	r400vgt_auto_index_rectangle_list_01	0:0:3	2002Oct12
	PASS		
747	r400vgt_auto_index_tri_fan_01	0:0:3	2002Oct12
	PASS		
748	r400vgt_auto_index_tri_list_01	0:0:3	2002Oct12
	PASS		
749	r400vgt_auto_index_tri_strip_01	0:0:3	2002Oct12
	PASS		
750	r400vgt_auto_index_tri_wflags_01	0:0:3	2002Oct12
	PASS		
751	r400vgt_dma_engine_01	0:0:3	2002Oct12
	PASS		
752	r400vgt_dma_engine_02	0:0:3	2002Oct12
	PASS		
753	r400vgt_dma_engine_03	0:0:3	2002Oct12
	PASS		
754	r400vgt_dma_engine_04	0:0:3	2002Oct12
	PASS		
755	r400vgt_dma_engine_05	0:0:3	2002Oct12
	PASS		
756	r400vgt_dma_engine_06	0:0:3	2002Oct12
	PASS		
757	r400vgt_dma_engine_07	0:0:3	2002Oct12
	PASS		
758	r400vgt_dma_engine_08	0:0:3	2002Oct12
	PASS		
759	r400vgt_dma_engine_09	0:0:3	2002Oct12
	PASS		
760	r400vgt_dma_engine_10	0:0:3	2002Oct12
	PASS		
761	r400vgt_dma_index_line_list_01	0:0:3	2002Oct12
	PASS		
762	r400vgt_dma_index_line_loop_01	0:0:3	2002Oct12
	PASS		
763	r400vgt_dma_index_line_strip_01	0:0:3	2002Oct12
	PASS		

764	r400vgt_dma_index_points_01	0:0:3	2002Oct12
	PASS		
765	r400vgt_dma_index_polygon_01	0:0:3	2002Oct12
	PASS		
766	r400vgt_dma_index_primitives_01	0:0:3	2002Oct12
	PASS		
767	r400vgt_dma_index_quad_list_01	0:0:3	2002Oct12
	PASS		
768	r400vgt_dma_index_quad_strip_01	0:0:3	2002Oct12
	PASS		
769	r400vgt_dma_index_rectangle_list_01	0:0:3	2002Oct12
	PASS		
770	r400vgt_dma_index_tri_fan_01	0:0:3	2002Oct12
	PASS		
771	r400vgt_dma_index_tri_list_01	0:0:3	2002Oct12
	PASS		
772	r400vgt_dma_index_tri_strip_01	0:0:3	2002Oct12
	PASS		
773	r400vgt_dma_index_tri_wflags_01	0:0:3	2002Oct12
	PASS		
774	r400vgt_dma_swap_idx16_01	0:0:3	2002Oct12
	PASS		
775	r400vgt_dma_swap_idx32_01	0:0:3	2002Oct12
	PASS		
776	r400vgt_edgeflags_polygon_01	0:0:3	2002Oct12
	PASS		
777	r400vgt_edgeflags_quad_list_01	0:0:3	2002Oct12
	PASS		
778	r400vgt_edgeflags_quad_strip_01	0:0:3	2002Oct12
	PASS		
779	r400vgt_edgeflags_triangle_fan_01	0:0:3	2002Oct12
	PASS		
780	r400vgt_edgeflags_triangle_list_01	0:0:3	2002Oct12
	PASS		
781	r400vgt_edgeflags_triangle_strip_01	0:0:3	2002Oct12
	PASS		
782	r400vgt_edgeflags_triangle_wflags_01	0:0:3	2002Oct12
	PASS		
783	r400vgt_event_handling_01	0:0:3	2002Sep26
	PASS		
784	r400vgt_ext2int_index_line_list_01	0:0:3	2002Oct12
	PASS		
785	r400vgt_ext2int_index_line_loop_01	0:0:3	2002Oct12
	PASS		
786	r400vgt_ext2int_index_line_strip_01	0:0:3	2002Oct12
	PASS		
787	r400vgt_ext2int_index_points_01	0:0:3	2002Oct12
	PASS		

788	r400vgt_ext2int_index_polygon_01	0:0:3	2002Oct12
	PASS		
789	r400vgt_ext2int_index_quad_list_01	0:0:3	2002Oct12
	PASS		
790	r400vgt_ext2int_index_quad_strip_01	0:0:3	2002Oct12
	PASS		
791	r400vgt_ext2int_index_rectangle_list_01	0:0:3	2002Oct12
	PASS		
792	r400vgt_ext2int_index_triangle_fan_01	0:0:3	2002Oct12
	PASS		
793	r400vgt_ext2int_index_triangle_list_01	0:0:3	2002Oct12
	PASS		
794	r400vgt_ext2int_index_triangle_strip_01	0:0:3	2002Oct12
	PASS		
795	r400vgt_ext2int_index_triangle_wflags_01	0:0:3	2002Oct12
	PASS		
796	r400vgt_hos_LPatch_01	0:0:3	2002Oct12
	PASS		
797	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	0:0:3	2002Oct12
	PASS		
798	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	0:0:3	2002Oct12
	PASS		
799	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	0:0:3	2002Oct12
	PASS		
800	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	0:0:3	2002Oct12
	PASS		
801	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	0:0:3	2002Oct12
	PASS		
802	r400vgt_hos_PNQ_lp_cont_no_projection_01	0:0:3	2002Oct12
	PASS		
803	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	0:0:3	2002Oct12
	PASS		
804	r400vgt_hos_PNT_cont_cp_qn_complex_01	0:0:3	2002Oct12
	PASS		
805	r400vgt_hos_PNT_cont_cp_qn_precision_01	0:0:3	2002Oct12
	PASS		
806	r400vgt_hos_PNT_cont_cp_qn_precision_02	0:0:3	2002Oct12
	PASS		
807	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	0:0:3	2002Oct12
	PASS		
808	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	0:0:3	2002Oct12
	PASS		
809	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	0:0:3	2002Oct12
	PASS		
810	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	0:0:3	2002Oct12
	PASS		
811	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	0:0:3	2002Oct12
	PASS		

812	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	0:0:3	2002Oct12
	PASS		
813	r400vgt_hos_PNT_disc_cp_qn_complex_01	0:0:3	2002Oct12
	PASS		
814	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	0:0:3	2002Oct12
	PASS		
815	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	0:0:3	2002Oct12
	PASS		
816	r400vgt_hos_PNT_disc_cp_qn_precision_01	0:0:3	2002Oct12
	PASS		
817	r400vgt_hos_PNT_disc_cp_qn_precision_02	0:0:3	2002Oct12
	PASS		
818	r400vgt_hos_PNT_edge_detection_01	0:0:3	2002Oct12
	PASS		
819	r400vgt_hos_PNT_lp_cont_no_projection_01	0:0:3	2002Oct12
	PASS		
820	r400vgt_hos_RPatch_cp_02	0:0:3	2002Oct12
	PASS		
821	r400vgt_hos_RPatch_lp_02	0:0:3	2002Oct12
	PASS		
822	r400vgt_hos_TPatch_01	0:0:3	2002Oct12
	PASS		
823	r400vgt_hos_TPatch_02	0:0:3	2002Oct12
	PASS		
824	r400vgt_hos_auto_index_line_list_01	0:0:3	2002Oct12
	PASS		
825	r400vgt_hos_auto_index_quad_list_01	0:0:3	2002Oct12
	PASS		
826	r400vgt_hos_auto_index_triangle_list_01	0:0:3	2002Oct12
	PASS		
827	r400vgt_hos_cubic_pos_pnt_discrete_01	0:0:3	2002Oct12
	PASS		
828	r400vgt_hos_simple_linear_PNT_discrete_01	0:0:3	2002Oct12
	PASS		
829	r400vgt_immed_index_line_list_01	0:0:3	2002Oct12
	PASS		
830	r400vgt_immed_index_line_loop_01	0:0:3	2002Oct12
	PASS		
831	r400vgt_immed_index_line_strip_01	0:0:3	2002Oct12
	PASS		
832	r400vgt_immed_index_points_01	0:0:3	2002Oct12
	PASS		
833	r400vgt_immed_index_polygon_01	0:0:3	2002Oct12
	PASS		
834	r400vgt_immed_index_primitives_01	0:0:3	2002Oct12
	PASS		
835	r400vgt_immed_index_quad_list_01	0:0:3	2002Oct12
	PASS		

836	r400vgt_immed_index_quad_strip_01	0:0:3	2002Oct12
	PASS		
837	r400vgt_immed_index_rectangle_list_01	0:0:3	2002Oct12
	PASS		
838	r400vgt_immed_index_tri_fan_01	0:0:3	2002Oct12
	PASS		
839	r400vgt_immed_index_tri_list_01	0:0:3	2002Oct12
	PASS		
840	r400vgt_immed_index_tri_strip_01	0:0:3	2002Oct12
	PASS		
841	r400vgt_immed_index_tri_wflags_01	0:0:3	2002Oct12
	PASS		
842	r400vgt_index_dealloc_line_list_01	0:0:3	2002Oct12
	PASS		
843	r400vgt_index_dealloc_points_01	0:0:3	2002Oct12
	PASS		
844	r400vgt_index_dealloc_triangle_list_01	0:0:3	2002Oct12
	PASS		
845	r400vgt_index_min_max_01	0:0:3	2002Oct12
	PASS		
846	r400vgt_index_min_max_02	0:0:3	2002Oct12
	PASS		
847	r400vgt_index_min_max_03	0:0:3	2002Oct12
	PASS		
848	r400vgt_index_min_max_04	0:0:3	2002Oct12
	PASS		
849	r400vgt_index_offset_01	0:0:3	2002Oct12
	PASS		
850	r400vgt_index_offset_02	0:0:3	2002Oct12
	PASS		
851	r400vgt_index_offset_03	0:0:3	2002Oct12
	PASS		
852	r400vgt_index_offset_04	0:0:3	2002Oct12
	PASS		
853	r400vgt_index_offset_05	0:0:3	2002Oct12
	PASS		
854	r400vgt_index_offset_06	0:0:3	2002Oct12
	PASS		
855	r400vgt_index_offset_07	0:0:3	2002Oct12
	PASS		
856	r400vgt_index_offset_08	0:0:3	2002Oct12
	PASS		
857	r400vgt_index_size_01	0:0:3	2002Oct12
	PASS		
858	r400vgt_index_size_02	0:0:3	2002Oct12
	PASS		
859	r400vgt_index_source_switch_01	0:0:3	2002Oct12
	PASS		

860	r400vgt_line_list_01	0:0:3	2002Oct12
	PASS		
861	r400vgt_line_list_02	0:0:3	2002Oct12
	PASS		
862	r400vgt_line_loop_01	0:0:3	2002Oct12
	PASS		
863	r400vgt_line_loop_02	0:0:3	2002Oct12
	PASS		
864	r400vgt_line_strip_01	0:0:3	2002Oct12
	PASS		
865	r400vgt_line_strip_02	0:0:3	2002Oct12
	PASS		
866	r400vgt_multi_context_01	0:0:3	2002Oct12
	PASS		
867	r400vgt_multi_context_02	0:0:3	2002Oct12
	PASS		
868	r400vgt_multi_context_03	0:0:3	2002Oct12
	PASS		
869	r400vgt_multi_context_04	0:0:3	2002Oct12
	PASS		
870	r400vgt_multi_context_05	0:0:3	2002Oct12
	PASS		
871	r400vgt_multi_context_06	0:0:3	2002Oct12
	PASS		
872	r400vgt_multi_context_07	0:0:3	2002Oct12
	PASS		
873	r400vgt_multi_context_08	0:0:3	2002Oct12
	PASS		
874	r400vgt_multi_context_09	0:0:3	2002Oct12
	PASS		
875	r400vgt_multi_context_10	0:0:3	2002Oct12
	PASS		
876	r400vgt_multi_context_11	0:0:3	2002Oct12
	PASS		
877	r400vgt_multi_context_12	0:0:3	2002Oct12
	PASS		
878	r400vgt_multi_prim_reset_index_all_01	0:0:3	2002Oct12
	PASS		
879	r400vgt_multi_prim_reset_index_all_02	0:0:3	2002Oct12
	PASS		
880	r400vgt_pass_thru_all_prims_01	0:0:3	2002Oct12
	PASS		
881	r400vgt_point_list_01	0:0:3	2002Oct12
	PASS		
882	r400vgt_point_list_02	0:0:3	2002Oct12
	PASS		
883	r400vgt_polygon_01	0:0:3	2002Oct12
	PASS		

884	r400vgt_polygon_02	0:0:3	2002Oct12
	PASS		
885	r400vgt_provoking_vtx_all_01	0:0:3	2002Oct12
	PASS		
886	r400vgt_provoking_vtx_edgeflags_all_01	0:0:3	2002Oct12
	PASS		
887	r400vgt_provoking_vtx_polygon_01	0:0:3	2002Oct12
	PASS		
888	r400vgt_provoking_vtx_quad_list_01	0:0:3	2002Oct12
	PASS		
889	r400vgt_provoking_vtx_quad_strip_01	0:0:3	2002Oct12
	PASS		
890	r400vgt_provoking_vtx_tri_fan_01	0:0:3	2002Oct12
	PASS		
891	r400vgt_provoking_vtx_tri_strip_01	0:0:3	2002Oct12
	PASS		
892	r400vgt_quad_list_01	0:0:3	2002Oct12
	PASS		
893	r400vgt_quad_list_02	0:0:3	2002Oct12
	PASS		
894	r400vgt_quad_strip_01	0:0:3	2002Oct12
	PASS		
895	r400vgt_quad_strip_02	0:0:3	2002Oct12
	PASS		
896	r400vgt_rectangle_list_01	0:0:3	2002Oct12
	PASS		
897	r400vgt_rectangle_list_02	0:0:3	2002Oct12
	PASS		
898	r400vgt_reuse_depth_line_list_01	0:0:3	2002Oct12
	PASS		
899	r400vgt_reuse_depth_point_list_01	0:0:3	2002Oct12
	PASS		
900	r400vgt_reuse_depth_triangle_list_01	0:0:3	2002Oct12
	PASS		
901	r400vgt_reuse_index_line_list_01	0:0:3	2002Oct12
	PASS		
902	r400vgt_reuse_index_point_list_01	0:0:3	2002Oct12
	PASS		
903	r400vgt_reuse_index_triangle_list_01	0:0:3	2002Oct12
	PASS		
904	r400vgt_reuse_index_triangle_list_02	0:0:3	2002Oct12
	PASS		
905	r400vgt_reuse_index_triangle_list_03	0:0:3	2002Oct12
	PASS		
906	r400vgt_suppress_eop_01	0:0:1	2002Oct12
	PASS		
907	r400vgt_suppress_eop_02	0:0:1	2002Oct12
	PASS		

908	r400vgt_suppress_eop_03	0:0:1	2002Oct12
	PASS		
909	r400vgt_suppress_eop_04	0:0:1	2002Oct12
	PASS		
910	r400vgt_suppress_eop_05	0:0:1	2002Sep26
	PASS		
911	r400vgt_triangle_fan_01	0:0:1	2002Oct12
	PASS		
912	r400vgt_triangle_fan_02	0:0:1	2002Oct12
	PASS		
913	r400vgt_triangle_list_01	0:0:1	2002Oct12
	PASS		
914	r400vgt_triangle_list_02	0:0:1	2002Oct12
	PASS		
915	r400vgt_triangle_strip_01	0:0:1	2002Oct12
	PASS		
916	r400vgt_triangle_strip_02	0:0:1	2002Oct12
	PASS		
917	r400vgt_triangle_wflags_01	0:0:1	2002Oct12
	PASS		
918	r400vgt_triangle_wflags_02	0:0:1	2002Oct12
	PASS		
919	r400vgt_viz_query_01	0:0:1	2002Oct12
	PASS		
920	r400vgt_vtx_vector_packing_01	0:0:1	2002Oct12
	PASS		
921	r400vte_coverage_01	0:0:1	2002Oct12
	PASS		
922	r400vte_simple_point_01	0:0:1	2002Oct12
	PASS		
923	r400vte_simple_triangle_01	0:0:1	2002Oct12
	PASS		
924	r400vte_w0_fmt_01	0:0:1	2002Oct12
	PASS		
925	r400vte_w0_fmt_03	0:0:1	2002Oct12
	PASS		
926	r400vte_xy_fmt_01	0:0:1	2002Oct12
	PASS		
927	r400vte_xy_fmt_mk_01_bug	0:0:1	2002Oct9
	PASS		
928	r400vte_xyz_scale_01	0:0:1	2002Oct12
	PASS		
929	r400vte_xyz_scale_02	0:0:1	2002Oct12
	PASS		
930	r400vte_z_fmt_01	0:0:1	2002Oct12
	PASS		

=====
Tests_Run_count = 930 Pass_count = 921 Fail_count = 2 Incomplete_count = 7

=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Mon Oct  7 21:50:04 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              50          50          49          98
              RBBM             1           1           1          100
              VGT             180         180          83          46
              SC              300         300         265          88
              CL              119         119         117          98
              VTE              10          10           9          90
              SPECIALTY         6           6           6          100
              PARAMETER         62          62          62          100
              SU               97          97          92          94
              SANITY            2           2           2          100
+++++
              TOTAL            827         827         687          83
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug31
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug31
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug31
PASS			
4	primlib_parameterized_simple_triangle		2002Aug31
PASS			
5	primlib_template_simple_triangle		2002Aug31
PASS			
6	primlib_tex_tri		2002Aug31
PASS			
7	r400cl_clip_10_verts_01		2002Aug31
PASS			
8	r400cl_clip_disable_01		2002Aug31
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug31
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug31
PASS			
11	r400cl_cull_only_ena_01		2002Aug31
PASS			
12	r400cl_edgeflags_01		2002Aug31
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug31
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug31
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug31
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug31
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug31
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug31
PASS			
19	r400cl_edgeflags_gband_01		2002Aug31
PASS			
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368	r400vgt_dma_engine_01	2002Aug31
PASS		
369	r400vgt_dma_engine_02	2002Aug31
PASS		
370	r400vgt_dma_engine_03	2002Aug31
PASS		
371	r400vgt_dma_engine_04	2002Aug31
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372	r400vgt_dma_engine_05	2002Aug31
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374	r400vgt_dma_engine_07	2002Aug31
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375	r400vgt_dma_engine_08	2002Aug31
PASS		
376	r400vgt_dma_engine_09	2002Aug31
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377	r400vgt_dma_engine_10	2002Aug31
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378	r400vgt_dma_index_line_list_01	2002Aug31
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379	r400vgt_dma_index_line_loop_01	2002Aug31
PASS		
380	r400vgt_dma_index_line_strip_01	2002Aug31

PASS
381 r400vgt_dma_index_points_01 2002Aug31
PASS
382 r400vgt_dma_index_polygon_01 2002Aug31
PASS
383 r400vgt_dma_index_primitives_01 2002Aug31
PASS
384 r400vgt_dma_index_quad_list_01 2002Aug31
PASS
385 r400vgt_dma_index_quad_strip_01 2002Aug31
PASS
386 r400vgt_dma_index_rectangle_list_01 2002Aug31
PASS
387 r400vgt_dma_index_tri_fan_01 2002Aug31
PASS
388 r400vgt_dma_index_tri_list_01 2002Aug31
PASS
389 r400vgt_dma_index_tri_strip_01 2002Aug31
PASS
390 r400vgt_dma_index_tri_wflags_01 2002Aug31
PASS
391 r400vgt_dma_swap_indx16_01 2002Aug31
PASS
392 r400vgt_dma_swap_indx32_01 2002Aug31
PASS
393 r400vgt_edgeflags_polygon_01 2002Aug31
PASS
394 r400vgt_edgeflags_quad_list_01 2002Aug31
PASS
395 r400vgt_edgeflags_quad_strip_01 2002Aug31
PASS
396 r400vgt_edgeflags_triangle_fan_01 2002Aug31
PASS
397 r400vgt_edgeflags_triangle_list_01 2002Aug31
PASS
398 r400vgt_edgeflags_triangle_strip_01 2002Aug31
PASS
399 r400vgt_edgeflags_triangle_wflags_01 2002Aug31
PASS
400 r400vgt_ext2int_index_line_list_01 2002Aug31
PASS
401 r400vgt_ext2int_index_line_loop_01 2002Aug31
PASS
402 r400vgt_ext2int_index_line_strip_01 2002Aug31
PASS
403 r400vgt_ext2int_index_points_01 2002Aug31
PASS
404 r400vgt_ext2int_index_polygon_01 2002Aug31

PASS		
405	r400vgt_ext2int_index_quad_list_01	2002Aug31
PASS		
406	r400vgt_ext2int_index_quad_strip_01	2002Aug31
PASS		
407	r400vgt_ext2int_index_triangle_fan_01	2002Aug31
PASS		
408	r400vgt_ext2int_index_triangle_list_01	2002Aug31
PASS		
409	r400vgt_ext2int_index_triangle_strip_01	2002Aug31
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410	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug31
PASS		
411	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug31
PASS		
412	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug31
PASS		
413	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug31
PASS		
414	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug31
PASS		
415	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug31
PASS		
416	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug31
PASS		
417	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug31
PASS		
418	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug31
PASS		
419	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug31
PASS		
420	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug31
PASS		
421	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug31
PASS		
422	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug31
PASS		
423	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug31
PASS		
424	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug31
PASS		
425	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug31
PASS		
426	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug31
PASS		
427	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug31
PASS		
428	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug31

PASS		
429	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug31
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430	r400vgt_hos_PNT_edge_detection_01	2002Aug31
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431	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug31
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432	r400vgt_hos_auto_index_line_list_01	2002Aug31
PASS		
433	r400vgt_hos_auto_index_quad_list_01	2002Aug31
PASS		
434	r400vgt_hos_auto_index_triangle_list_01	2002Aug31
PASS		
435	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug31
PASS		
436	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug31
PASS		
437	r400vgt_immed_index_line_list_01	2002Aug31
PASS		
438	r400vgt_immed_index_line_loop_01	2002Aug31
PASS		
439	r400vgt_immed_index_line_strip_01	2002Aug31
PASS		
440	r400vgt_immed_index_points_01	2002Aug31
PASS		
441	r400vgt_immed_index_polygon_01	2002Aug31
PASS		
442	r400vgt_immed_index_primitives_01	2002Aug31
PASS		
443	r400vgt_immed_index_quad_list_01	2002Aug31
PASS		
444	r400vgt_immed_index_quad_strip_01	2002Aug31
PASS		
445	r400vgt_immed_index_rectangle_list_01	2002Aug31
PASS		
446	r400vgt_immed_index_tri_fan_01	2002Aug31
PASS		
447	r400vgt_immed_index_tri_list_01	2002Aug31
PASS		
448	r400vgt_immed_index_tri_strip_01	2002Aug31
PASS		
449	r400vgt_immed_index_tri_wflags_01	2002Aug31
PASS		
450	r400vgt_index_dealloc_line_list_01	2002Aug31
PASS		
451	r400vgt_index_dealloc_points_01	2002Aug31
PASS		
452	r400vgt_index_dealloc_triangle_list_01	2002Aug31

PASS		
453	r400vgt_index_min_max_01	2002Aug31
PASS		
454	r400vgt_index_min_max_02	2002Aug31
PASS		
455	r400vgt_index_offset_01	2002Aug31
PASS		
456	r400vgt_index_offset_02	2002Aug31
PASS		
457	r400vgt_index_offset_03	2002Aug31
PASS		
458	r400vgt_index_size_01	2002Aug31
PASS		
459	r400vgt_index_source_switch_01	2002Aug31
PASS		
460	r400vgt_line_list_01	2002Aug31
PASS		
461	r400vgt_line_loop_01	2002Aug31
PASS		
462	r400vgt_line_strip_01	2002Aug31
PASS		
463	r400vgt_multi_context_01	2002Aug31
PASS		
464	r400vgt_multi_context_02	2002Aug31
PASS		
465	r400vgt_multi_context_03	2002Aug31
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466	r400vgt_multi_context_04	2002Aug31
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467	r400vgt_multi_context_05	2002Aug31
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468	r400vgt_multi_context_06	2002Aug31
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469	r400vgt_multi_context_07	2002Aug31
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470	r400vgt_multi_context_08	2002Aug31
PASS		
471	r400vgt_multi_context_09	2002Aug31
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472	r400vgt_multi_context_10	2002Aug31
PASS		
473	r400vgt_multi_context_11	2002Aug31
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474	r400vgt_multi_context_12	2002Aug31
PASS		
475	r400vgt_pass_thru_all_prims_01	2002Aug31
PASS		
476	r400vgt_point_list_01	2002Aug31

PASS		
477	r400vgt_polygon_01	2002Aug31
PASS		
478	r400vgt_provoking_vtx_all_01	2002Aug31
PASS		
479	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug31
PASS		
480	r400vgt_quad_list_01	2002Aug31
PASS		
481	r400vgt_quad_strip_01	2002Aug31
PASS		
482	r400vgt_rectangle_list_01	2002Aug31
PASS		
483	r400vgt_reuse_depth_line_list_01	2002Aug31
PASS		
484	r400vgt_reuse_depth_point_list_01	2002Aug31
PASS		
485	r400vgt_reuse_depth_triangle_list_01	2002Aug31
PASS		
486	r400vgt_reuse_index_line_list_01	2002Aug31
PASS		
487	r400vgt_reuse_index_point_list_01	2002Aug31
PASS		
488	r400vgt_reuse_index_triangle_list_01	2002Aug31
PASS		
489	r400vgt_reuse_index_triangle_list_02	2002Aug31
PASS		
490	r400vgt_reuse_index_triangle_list_03	2002Aug31
PASS		
491	r400vgt_triangle_fan_01	2002Aug31
PASS		
492	r400vgt_triangle_list_01	2002Aug31
PASS		
493	r400vgt_triangle_strip_01	2002Aug31
PASS		
494	r400vgt_triangle_wflags_01	2002Aug31
PASS		
495	r400vgt_vtx_vector_packing_01	2002Aug31
PASS		
496	r400vte_coverage_01	2002Aug31
PASS		
497	r400vte_simple_point_01	2002Aug31
PASS		
498	r400vte_simple_triangle_01	2002Aug31
PASS		
499	r400vte_w0_fmt_01	2002Aug31
PASS		
500	r400vte_w0_fmt_03	2002Aug31

PASS
501 r400vte_xy_fmt_01 2002Aug31
PASS
502 r400vte_xyz_scale_01 2002Aug31
PASS
503 r400vte_xyz_scale_02 2002Aug31
PASS
504 r400vte_z_fmt_01 2002Aug31
PASS

=====
Tests_Run_count = 504 Pass_count = 503 Fail_count = 0 Incomplete_count = 1
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Sep  6  4:51:58 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          147          147          147          100
              SC           165          165          165          100
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY    SU           81           81           81          100
              SANITY       2            2            2           100
+++++
              TOTAL        518          518          518          100
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Aug 17  3:00:41 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          0          0
              SC           133          133          0          0
              CL           80           80           0          0
              VTE          9            9            0          0
SPECIALTY    SU           64           64           0          0
              SANITY       2            2            0          0
+++++
              TOTAL        423          423          0          0
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep4
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep4
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep4
PASS			
4	primlib_parameterized_simple_triangle		2002Sep4
PASS			
5	primlib_template_simple_triangle		2002Sep4
PASS			
6	primlib_tex_tri		2002Sep4
PASS			
7	r400cl_clip_10_verts_01		2002Sep4
PASS			
8	r400cl_clip_disable_01		2002Sep4
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep4
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep4
PASS			
11	r400cl_cull_only_ena_01		2002Sep4
PASS			
12	r400cl_edgeflags_01		2002Sep4
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep4
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep4
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep4
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep4
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep4
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep4
PASS			
19	r400cl_edgeflags_gband_01		2002Sep4
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep4

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep4
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep4
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep4
PASS		
24	r400cl_frustum_01	2002Sep4
PASS		
25	r400cl_frustum_02	2002Sep4
PASS		
26	r400cl_frustum_03	2002Sep4
PASS		
27	r400cl_frustum_04	2002Sep4
PASS		
28	r400cl_frustum_05	2002Sep4
PASS		
29	r400cl_frustum_06	2002Sep4
PASS		
30	r400cl_frustum_07	2002Sep4
PASS		
31	r400cl_frustum_08	2002Sep4
PASS		
32	r400cl_frustum_09	2002Sep4
PASS		
33	r400cl_frustum_10	2002Sep4
PASS		
34	r400cl_frustum_11	2002Sep4
PASS		
35	r400cl_frustum_12	2002Sep4
PASS		
36	r400cl_frustum_13	2002Sep4
PASS		
37	r400cl_frustum_14	2002Sep4
PASS		
38	r400cl_frustum_15	2002Sep4
PASS		
39	r400cl_frustum_16	2002Sep4
PASS		
40	r400cl_frustum_17	2002Sep4
PASS		
41	r400cl_frustum_18	2002Sep4
PASS		
42	r400cl_frustum_19	2002Sep4
PASS		
43	r400cl_frustum_20	2002Sep4
PASS		
44	r400cl_frustum_21	2002Sep4

PASS		
45	r400cl_frustum_22	2002Sep4
PASS		
46	r400cl_frustum_23	2002Sep4
PASS		
47	r400cl_frustum_24	2002Sep4
PASS		
48	r400cl_frustum_25	2002Sep4
PASS		
49	r400cl_frustum_26	2002Sep4
PASS		
50	r400cl_frustum_27	2002Sep4
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51	r400cl_frustum_28	2002Sep4
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52	r400cl_frustum_lines_01	2002Sep4
PASS		
53	r400cl_frustum_lines_02	2002Sep4
PASS		
54	r400cl_frustum_lines_03	2002Sep4
PASS		
55	r400cl_frustum_lines_04	2002Sep4
PASS		
56	r400cl_frustum_lines_05	2002Sep4
PASS		
57	r400cl_frustum_lines_06	2002Sep4
PASS		
58	r400cl_frustum_lines_07	2002Sep4
PASS		
59	r400cl_frustum_lines_08	2002Sep4
PASS		
60	r400cl_frustum_lines_09	2002Sep4
PASS		
61	r400cl_frustum_lines_10	2002Sep4
PASS		
62	r400cl_frustum_lines_11	2002Sep4
PASS		
63	r400cl_frustum_lines_12	2002Sep4
PASS		
64	r400cl_frustum_lines_13	2002Sep4
PASS		
65	r400cl_frustum_lines_14	2002Sep4
PASS		
66	r400cl_frustum_lines_15	2002Sep4
PASS		
67	r400cl_frustum_lines_16	2002Sep4
PASS		
68	r400cl_frustum_lines_17	2002Sep4

PASS		
69	r400cl_frustum_lines_18	2002Sep4
PASS		
70	r400cl_frustum_lines_19	2002Sep4
PASS		
71	r400cl_frustum_lines_20	2002Sep4
PASS		
72	r400cl_frustum_lines_21	2002Sep4
PASS		
73	r400cl_frustum_lines_22	2002Sep4
PASS		
74	r400cl_frustum_lines_23	2002Sep4
PASS		
75	r400cl_frustum_lines_24	2002Sep4
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76	r400cl_gband_04	2002Sep4
PASS		
77	r400cl_gband_tcl_01	2002Sep4
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78	r400cl_pointlist_vertex_state_ucp_01	2002Sep4
PASS		
79	r400cl_simple_triangle_01	2002Sep4
PASS		
80	r400cl_ucp_combos_01	2002Sep4
PASS		
81	r400cl_ucp_combos_02	2002Sep4
PASS		
82	r400cl_ucp_combos_03	2002Sep4
PASS		
83	r400cl_ucp_combos_04	2002Sep4
PASS		
84	r400cl_ucp_combos_05	2002Sep4
PASS		
85	r400cl_ucp_combos_06	2002Sep4
PASS		
86	r400cl_ucp_combos_07	2002Sep4
PASS		
87	r400cl_ucp_combos_08	2002Sep4
PASS		
88	r400cl_ucp_combos_09	2002Sep4
PASS		
89	r400cl_ucp_combos_10	2002Sep4
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90	r400cl_ucp_combos_11	2002Sep4
PASS		
91	r400cl_ucp_combos_12	2002Sep4
PASS		
92	r400cl_ucp_combos_13	2002Sep4

PASS		
93	r400cl_ucp_combos_14	2002Sep4
PASS		
94	r400cl_ucp_combos_15	2002Sep4
PASS		
95	r400cl_ucp_combos_16	2002Sep4
PASS		
96	r400cl_ucp_combos_17	2002Sep4
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97	r400cl_ucp_combos_18	2002Sep4
PASS		
98	r400cl_ucp_combos_19	2002Sep4
PASS		
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PASS		
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PASS		
101	r400cl_ucp_combos_22	2002Sep4
PASS		
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PASS		
103	r400cl_ucp_combos_24	2002Sep4
PASS		
104	r400cl_ucp_combos_25	2002Sep4
PASS		
105	r400cl_ucp_combos_26	2002Sep4
PASS		
106	r400cl_ucp_combos_27	2002Sep4
PASS		
107	r400cl_ucp_combos_28	2002Sep4
PASS		
108	r400cl_ucp_combos_29	2002Sep4
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109	r400cl_ucp_combos_30	2002Sep4
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110	r400cl_ucp_pointlist_01	2002Sep4
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111	r400cl_vertex_reuse_clip_01	2002Sep4
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112	r400cl_vtx_kill_01	2002Sep4
PASS		
113	r400cl_vtx_kill_02	2002Sep4
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114	r400cl_w_eq_0	2002Sep4
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115	r400sanity_persp_projection_01	2002Sep4
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116	r400sanity_vfd_texture_sample_01	2002Sep4

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117	r400sc_bres_cntl_01	2002Sep4
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118	r400sc_bres_cntl_02	2002Sep4
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119	r400sc_bres_cntl_03	2002Sep4
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120	r400sc_bres_cntl_04	2002Sep4
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123	r400sc_clip_rect_03	2002Sep4
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124	r400sc_clip_rect_04	2002Sep4
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125	r400sc_clip_rect_fc_01	2002Sep4
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126	r400sc_diamond_exit_01	2002Sep4
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127	r400sc_diamond_exit_02	2002Sep4
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128	r400sc_diamond_exit_03	2002Sep4
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129	r400sc_diamond_exit_04	2002Sep4
PASS		
130	r400sc_diamond_exit_05	2002Sep4
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131	r400sc_jss_1x2_01	2002Sep4
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132	r400sc_jss_1x2_02	2002Sep4
PASS		
133	r400sc_jss_1x3_01	2002Sep4
PASS		
134	r400sc_jss_1x3_02	2002Sep4
PASS		
135	r400sc_jss_1x4_01	2002Sep4
PASS		
136	r400sc_jss_1x4_02	2002Sep4
PASS		
137	r400sc_jss_2x1_01	2002Sep4
PASS		
138	r400sc_jss_2x1_02	2002Sep4
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139	r400sc_jss_2x2_01	2002Sep4
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140	r400sc_jss_2x2_02	2002Sep4

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142	r400sc_jss_2x3_02	2002Sep4
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143	r400sc_jss_2x4_01	2002Sep4
PASS		
144	r400sc_jss_2x4_02	2002Sep4
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145	r400sc_jss_3x1_01	2002Sep4
PASS		
146	r400sc_jss_3x1_02	2002Sep4
PASS		
147	r400sc_jss_3x2_01	2002Sep4
PASS		
148	r400sc_jss_3x2_02	2002Sep4
PASS		
149	r400sc_jss_3x3_01	2002Sep4
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150	r400sc_jss_3x3_02	2002Sep4
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151	r400sc_jss_3x4_01	2002Sep4
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152	r400sc_jss_3x4_02	2002Sep4
PASS		
153	r400sc_jss_3x4_03	2002Sep4
PASS		
154	r400sc_jss_4x1_01	2002Sep4
PASS		
155	r400sc_jss_4x1_02	2002Sep4
PASS		
156	r400sc_jss_4x2_01	2002Sep4
PASS		
157	r400sc_jss_4x2_02	2002Sep4
PASS		
158	r400sc_jss_4x3_01	2002Sep4
PASS		
159	r400sc_jss_4x3_02	2002Sep4
PASS		
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161	r400sc_jss_4x4_02	2002Sep4
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162	r400sc_jss_4x4_03	2002Sep4
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164	r400sc_jss_4x4_aa_mask_02	2002Sep4

PASS		
165	r400sc_jss_4x4_fc_01	2002Sep4
PASS		
166	r400sc_jss_4x4_fc_02	2002Sep4
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168	r400sc_line_dx10_eq_0_01	2002Sep4
PASS		
169	r400sc_line_dx10_ge_0_01	2002Sep4
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170	r400sc_line_dx10_lt_0_01	2002Sep4
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171	r400sc_line_dy10_eq_0_01	2002Sep4
PASS		
172	r400sc_line_dy10_ge_0_01	2002Sep4
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173	r400sc_line_dy10_lt_0_01	2002Sep4
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174	r400sc_line_expand_width_msa_8_01	2002Sep4
PASS		
175	r400sc_line_expand_width_msa_8_02	2002Sep4
PASS		
176	r400sc_line_expand_width_msa_8_03	2002Sep4
PASS		
177	r400sc_line_jss_3x4_01	2002Sep4
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178	r400sc_line_list_01	2002Sep4
PASS		
179	r400sc_line_list_02	2002Sep4
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180	r400sc_line_list_concentric_circle_01	2002Sep4
PASS		
181	r400sc_line_list_concentric_circle_02	2002Sep4
PASS		
182	r400sc_line_list_concentric_circle_03	2002Sep4
PASS		
183	r400sc_line_list_textured_01	2002Sep4
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184	r400sc_line_list_verify_st_01	2002Sep4
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185	r400sc_line_msa_8_01	2002Sep4
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187	r400sc_line_stipple_02	2002Sep4
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192	r400sc_line_stipple_07	2002Sep4
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193	r400sc_line_stipple_08	2002Sep4
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194	r400sc_line_stipple_09	2002Sep4
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195	r400sc_line_stipple_fc_08	2002Sep4
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196	r400sc_line_strip_stipple_01	2002Sep4
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197	r400sc_msaa_1_01	2002Sep4
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198	r400sc_msaa_4_01	2002Sep4
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205	r400sc_msaa_8_aa_mask_01	2002Sep4
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206	r400sc_msaa_8_aa_mask_02	2002Sep4
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207	r400sc_msaa_8_aa_mask_fc_02	2002Sep4
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208	r400sc_null_triangles_01	2002Sep4
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209	r400sc_null_triangles_fc_01	2002Sep4
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210	r400sc_point_jss_3x4_01	2002Sep4
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223	r400sc_poly_offset_fc_01	2002Sep4
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224	r400sc_polygon_stipple_01	2002Sep4
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225	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep4
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235	r400sc_raster_fill_rule_03	2002Sep4
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248	r400sc_scissor_rect_02	2002Sep4
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249	r400sc_scissor_rect_03	2002Sep4
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252	r400sc_scissor_rect_fc_01	2002Sep4
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253	r400sc_set_state_01	2002Sep4
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254	r400sc_tri_16_par_64_dwords_01	2002Sep4
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255	r400sc_tri_8textures_01	2002Sep4
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256	r400sc_tri_8textures_02	2002Sep4
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257	r400sc_tri_walk_start_vertex_01	2002Sep4
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258	r400sc_tri_walk_start_vertex_02	2002Sep4
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289	r400su_clip_pointtest_01	2002Sep4
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513 r400vte_w0_fmt_01 2002Sep4
PASS
514 r400vte_w0_fmt_03 2002Sep4
PASS
515 r400vte_xy_fmt_01 2002Sep4
PASS
516 r400vte_xyz_scale_01 2002Sep4
PASS
517 r400vte_xyz_scale_02 2002Sep4
PASS
518 r400vte_z_fmt_01 2002Sep4
PASS

=====
Tests_Run_count = 518 Pass_count = 515 Fail_count = 2 Incomplete_count = 1
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sun Sep  1  9:47:29 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          147          147          147          100
              SC           161          161          160           99
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY    SU           81           81           81          100
              SANITY       2           2           2           100
+++++
              TOTAL        514          514          513           99
+++++

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sun Aug 25  3:57:24 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141          100
              SC           154          154          152           98
              CL           81           81           81          100
              VTE           9           9            9          100
SPECIALTY    SU           79           79           79          100
              SANITY       2            2            1           50
+++++
              TOTAL        472          472          469           99
+++++

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Sep  3  8:13:11 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          147          147          147          100
              SC           161          161          160           99
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY
              SU           81           81           81          100
              SANITY       2            2            2           100
+++++
              TOTAL        514          514          513           99
+++++

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Aug 22  7:04:12 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          125          96
              SC           133          133          133          100
              CL           80           80           80           100
              VTE           9           9            9           100
SPECIALTY    SU           64          64          64           100
              SANITY       2           2            1           50
+++++
              TOTAL        423          423          418          98
+++++

```


=====
Regression Report For VGT - Project = r400 Created on Sat Sep 14 11:04:43 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep14
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep14
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep14
PASS			
4	primlib_parameterized_simple_triangle		2002Sep14
PASS			
5	primlib_template_simple_triangle		2002Sep14
PASS			
6	primlib_tex_tri		2002Sep14
PASS			
7	r400cl_clip_10_verts_01		2002Sep14
PASS			
8	r400cl_clip_disable_01		2002Sep14
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep14
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep14
PASS			
11	r400cl_cull_only_ena_01		2002Sep14
PASS			
12	r400cl_edgeflags_01		2002Sep14
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep14
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep14
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep14
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep14
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep14
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep14
PASS			
19	r400cl_edgeflags_gband_01		2002Sep14
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep14

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep14
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep14
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep14
PASS		
24	r400cl_frustum_01	2002Sep14
PASS		
25	r400cl_frustum_02	2002Sep14
PASS		
26	r400cl_frustum_03	2002Sep14
PASS		
27	r400cl_frustum_04	2002Sep14
PASS		
28	r400cl_frustum_05	2002Sep14
PASS		
29	r400cl_frustum_06	2002Sep14
PASS		
30	r400cl_frustum_07	2002Sep14
PASS		
31	r400cl_frustum_08	2002Sep14
PASS		
32	r400cl_frustum_09	2002Sep14
PASS		
33	r400cl_frustum_10	2002Sep14
PASS		
34	r400cl_frustum_11	2002Sep14
PASS		
35	r400cl_frustum_12	2002Sep14
PASS		
36	r400cl_frustum_13	2002Sep14
PASS		
37	r400cl_frustum_14	2002Sep14
PASS		
38	r400cl_frustum_15	2002Sep14
PASS		
39	r400cl_frustum_16	2002Sep14
PASS		
40	r400cl_frustum_17	2002Sep14
PASS		
41	r400cl_frustum_18	2002Sep14
PASS		
42	r400cl_frustum_19	2002Sep14
PASS		
43	r400cl_frustum_20	2002Sep14
PASS		
44	r400cl_frustum_21	2002Sep14

PASS		
45	r400cl_frustum_22	2002Sep14
PASS		
46	r400cl_frustum_23	2002Sep14
PASS		
47	r400cl_frustum_24	2002Sep14
PASS		
48	r400cl_frustum_25	2002Sep14
PASS		
49	r400cl_frustum_26	2002Sep14
PASS		
50	r400cl_frustum_27	2002Sep14
PASS		
51	r400cl_frustum_28	2002Sep14
PASS		
52	r400cl_frustum_all_vols_lines	2002Sep14
PASS		
53	r400cl_frustum_all_vols_tris	2002Sep14
PASS		
54	r400cl_frustum_lines_01	2002Sep14
PASS		
55	r400cl_frustum_lines_02	2002Sep14
PASS		
56	r400cl_frustum_lines_03	2002Sep14
PASS		
57	r400cl_frustum_lines_04	2002Sep14
PASS		
58	r400cl_frustum_lines_05	2002Sep14
PASS		
59	r400cl_frustum_lines_06	2002Sep14
PASS		
60	r400cl_frustum_lines_07	2002Sep14
PASS		
61	r400cl_frustum_lines_08	2002Sep14
PASS		
62	r400cl_frustum_lines_09	2002Sep14
PASS		
63	r400cl_frustum_lines_10	2002Sep14
PASS		
64	r400cl_frustum_lines_11	2002Sep14
PASS		
65	r400cl_frustum_lines_12	2002Sep14
PASS		
66	r400cl_frustum_lines_13	2002Sep14
PASS		
67	r400cl_frustum_lines_14	2002Sep14
PASS		
68	r400cl_frustum_lines_15	2002Sep14

PASS		
69	r400cl_frustum_lines_16	2002Sep14
PASS		
70	r400cl_frustum_lines_17	2002Sep14
PASS		
71	r400cl_frustum_lines_18	2002Sep14
PASS		
72	r400cl_frustum_lines_19	2002Sep14
PASS		
73	r400cl_frustum_lines_20	2002Sep14
PASS		
74	r400cl_frustum_lines_21	2002Sep14
PASS		
75	r400cl_frustum_lines_22	2002Sep14
PASS		
76	r400cl_frustum_lines_23	2002Sep14
PASS		
77	r400cl_frustum_lines_24	2002Sep14
PASS		
78	r400cl_gband_04	2002Sep14
PASS		
79	r400cl_gband_tcl_01	2002Sep14
PASS		
80	r400cl_pointlist_vertex_state_ucp_01	2002Sep14
PASS		
81	r400cl_simple_triangle_01	2002Sep14
PASS		
82	r400cl_ucp_combos_01	2002Sep14
PASS		
83	r400cl_ucp_combos_02	2002Sep14
PASS		
84	r400cl_ucp_combos_03	2002Sep14
PASS		
85	r400cl_ucp_combos_04	2002Sep14
PASS		
86	r400cl_ucp_combos_05	2002Sep14
PASS		
87	r400cl_ucp_combos_06	2002Sep14
PASS		
88	r400cl_ucp_combos_07	2002Sep14
PASS		
89	r400cl_ucp_combos_08	2002Sep14
PASS		
90	r400cl_ucp_combos_09	2002Sep14
PASS		
91	r400cl_ucp_combos_10	2002Sep14
PASS		
92	r400cl_ucp_combos_11	2002Sep14

PASS		
93	r400cl_ucp_combos_12	2002Sep14
PASS		
94	r400cl_ucp_combos_13	2002Sep14
PASS		
95	r400cl_ucp_combos_14	2002Sep14
PASS		
96	r400cl_ucp_combos_15	2002Sep14
PASS		
97	r400cl_ucp_combos_16	2002Sep14
PASS		
98	r400cl_ucp_combos_17	2002Sep14
PASS		
99	r400cl_ucp_combos_18	2002Sep14
PASS		
100	r400cl_ucp_combos_19	2002Sep14
PASS		
101	r400cl_ucp_combos_20	2002Sep14
PASS		
102	r400cl_ucp_combos_21	2002Sep14
PASS		
103	r400cl_ucp_combos_22	2002Sep14
PASS		
104	r400cl_ucp_combos_23	2002Sep14
PASS		
105	r400cl_ucp_combos_24	2002Sep14
PASS		
106	r400cl_ucp_combos_25	2002Sep14
PASS		
107	r400cl_ucp_combos_26	2002Sep14
PASS		
108	r400cl_ucp_combos_27	2002Sep14
PASS		
109	r400cl_ucp_combos_28	2002Sep14
PASS		
110	r400cl_ucp_combos_29	2002Sep14
PASS		
111	r400cl_ucp_combos_30	2002Sep14
PASS		
112	r400cl_ucp_pointlist_01	2002Sep14
PASS		
113	r400cl_vertex_reuse_clip_01	2002Sep14
PASS		
114	r400cl_vtx_kill_01	2002Sep14
PASS		
115	r400cl_vtx_kill_02	2002Sep14
PASS		
116	r400cl_w_eq_0	2002Sep14

PASS		
117	r400sanity_persp_projection_01	2002Sep14
PASS		
118	r400sanity_vfd_texture_sample_01	2002Sep14
PASS		
119	r400sc_bres_cntl_01	2002Sep7
PASS		
120	r400sc_bres_cntl_02	2002Sep14
PASS		
121	r400sc_bres_cntl_03	2002Sep14
PASS		
122	r400sc_bres_cntl_04	2002Sep14
PASS		
123	r400sc_bres_cntl_w2k_01	2002Sep14
PASS		
124	r400sc_bres_cntl_w9x_01	2002Sep14
PASS		
125	r400sc_clip_rect_01	2002Sep14
PASS		
126	r400sc_clip_rect_02	2002Sep14
PASS		
127	r400sc_clip_rect_03	2002Sep14
PASS		
128	r400sc_clip_rect_04	2002Sep14
PASS		
129	r400sc_clip_rect_fc_01	2002Sep14
PASS		
130	r400sc_diamond_exit_01	2002Sep14
PASS		
131	r400sc_diamond_exit_02	2002Sep14
PASS		
132	r400sc_diamond_exit_03	2002Sep14
PASS		
133	r400sc_diamond_exit_04	2002Sep14
PASS		
134	r400sc_diamond_exit_05	2002Sep14
PASS		
135	r400sc_jss_1x2_01	2002Sep14
PASS		
136	r400sc_jss_1x2_02	2002Sep14
PASS		
137	r400sc_jss_1x3_01	2002Sep14
PASS		
138	r400sc_jss_1x3_02	2002Sep14
PASS		
139	r400sc_jss_1x4_01	2002Sep14
PASS		
140	r400sc_jss_1x4_02	2002Sep14

PASS		
141	r400sc_jss_2x1_01	2002Sep14
PASS		
142	r400sc_jss_2x1_02	2002Sep14
PASS		
143	r400sc_jss_2x2_01	2002Sep14
PASS		
144	r400sc_jss_2x2_02	2002Sep14
PASS		
145	r400sc_jss_2x3_01	2002Sep14
PASS		
146	r400sc_jss_2x3_02	2002Sep14
PASS		
147	r400sc_jss_2x4_01	2002Sep14
PASS		
148	r400sc_jss_2x4_02	2002Sep14
PASS		
149	r400sc_jss_3x1_01	2002Sep14
PASS		
150	r400sc_jss_3x1_02	2002Sep14
PASS		
151	r400sc_jss_3x2_01	2002Sep14
PASS		
152	r400sc_jss_3x2_02	2002Sep14
PASS		
153	r400sc_jss_3x3_01	2002Sep14
PASS		
154	r400sc_jss_3x3_02	2002Sep14
PASS		
155	r400sc_jss_3x4_01	2002Sep14
PASS		
156	r400sc_jss_3x4_02	2002Sep14
PASS		
157	r400sc_jss_3x4_03	2002Sep14
PASS		
158	r400sc_jss_4x1_01	2002Sep14
PASS		
159	r400sc_jss_4x1_02	2002Sep14
PASS		
160	r400sc_jss_4x2_01	2002Sep14
PASS		
161	r400sc_jss_4x2_02	2002Sep14
PASS		
162	r400sc_jss_4x3_01	2002Sep14
PASS		
163	r400sc_jss_4x3_02	2002Sep14
PASS		
164	r400sc_jss_4x4_01	2002Sep14

PASS		
165	r400sc_jss_4x4_02	2002Sep14
PASS		
166	r400sc_jss_4x4_03	2002Sep14
PASS		
167	r400sc_jss_4x4_aa_mask_01	2002Sep14
PASS		
168	r400sc_jss_4x4_aa_mask_02	2002Sep14
PASS		
169	r400sc_jss_4x4_fc_01	2002Sep14
PASS		
170	r400sc_jss_4x4_fc_02	2002Sep14
PASS		
171	r400sc_jss_4x4_max_dist_01	2002Sep14
PASS		
172	r400sc_line_dx10_eq_0_01	2002Sep14
PASS		
173	r400sc_line_dx10_ge_0_01	2002Sep14
PASS		
174	r400sc_line_dx10_lt_0_01	2002Sep14
PASS		
175	r400sc_line_dy10_eq_0_01	2002Sep14
PASS		
176	r400sc_line_dy10_ge_0_01	2002Sep14
PASS		
177	r400sc_line_dy10_lt_0_01	2002Sep14
PASS		
178	r400sc_line_expand_width_msa_8_01	2002Sep14
PASS		
179	r400sc_line_expand_width_msa_8_02	2002Sep14
PASS		
180	r400sc_line_expand_width_msa_8_03	2002Sep14
PASS		
181	r400sc_line_jss_3x4_01	2002Sep14
PASS		
182	r400sc_line_list_01	2002Sep14
PASS		
183	r400sc_line_list_02	2002Sep14
PASS		
184	r400sc_line_list_concentric_circle_01	2002Sep14
PASS		
185	r400sc_line_list_concentric_circle_02	2002Sep14
PASS		
186	r400sc_line_list_concentric_circle_03	2002Sep14
PASS		
187	r400sc_line_list_textured_01	2002Sep14
PASS		
188	r400sc_line_list_verify_st_01	2002Sep14

PASS		
189	r400sc_line_msaa_8_01	2002Sep14
PASS		
190	r400sc_line_msaa_8_textured_01	2002Sep14
PASS		
191	r400sc_line_stipple_01	2002Sep14
PASS		
192	r400sc_line_stipple_02	2002Sep14
PASS		
193	r400sc_line_stipple_03	2002Sep14
PASS		
194	r400sc_line_stipple_04	2002Sep14
PASS		
195	r400sc_line_stipple_05	2002Sep14
PASS		
196	r400sc_line_stipple_06	2002Sep14
PASS		
197	r400sc_line_stipple_07	2002Sep14
PASS		
198	r400sc_line_stipple_08	2002Sep14
PASS		
199	r400sc_line_stipple_09	2002Sep14
PASS		
200	r400sc_line_stipple_10	2002Sep14
PASS		
201	r400sc_line_stipple_11	2002Sep14
PASS		
202	r400sc_line_stipple_12	2002Sep14
PASS		
203	r400sc_line_stipple_13	2002Sep14
PASS		
204	r400sc_line_stipple_14	2002Sep14
PASS		
205	r400sc_line_stipple_fc_08	2002Sep14
PASS		
206	r400sc_line_strip_stipple_01	2002Sep14
PASS		
207	r400sc_msaa_1_01	2002Sep14
PASS		
208	r400sc_msaa_1_rectangle_list_01	2002Sep14
PASS		
209	r400sc_msaa_1_rectangle_list_02	2002Sep14
PASS		
210	r400sc_msaa_1_rectangle_list_03	2002Sep14
PASS		
211	r400sc_msaa_1_rectangle_list_04	2002Sep14
PASS		
212	r400sc_msaa_1_rectangle_list_05	2002Sep14

PASS		
213	r400sc_msa_1_rectangle_list_06	2002Sep14
PASS		
214	r400sc_msa_1_rectangle_list_07	2002Sep14
PASS		
215	r400sc_msa_1_rectangle_list_08	2002Sep14
PASS		
216	r400sc_msa_1_zbuffer_rectangle_list_01	2002Sep14
PASS		
217	r400sc_msa_1_zbuffer_rectangle_list_02	2002Sep14
PASS		
218	r400sc_msa_2_rectangle_list_01	2002Sep14
PASS		
219	r400sc_msa_2_rectangle_list_02	2002Sep14
PASS		
220	r400sc_msa_2_rectangle_list_03	2002Sep14
PASS		
221	r400sc_msa_2_rectangle_list_04	2002Sep14
PASS		
222	r400sc_msa_2_rectangle_list_05	2002Sep14
PASS		
223	r400sc_msa_2_rectangle_list_06	2002Sep14
PASS		
224	r400sc_msa_2_rectangle_list_07	2002Sep14
PASS		
225	r400sc_msa_2_rectangle_list_08	2002Sep14
PASS		
226	r400sc_msa_2_zbuffer_rectangle_list_01	2002Sep14
PASS		
227	r400sc_msa_2_zbuffer_rectangle_list_02	2002Sep14
PASS		
228	r400sc_msa_3_rectangle_list_01	2002Sep14
PASS		
229	r400sc_msa_3_rectangle_list_02	2002Sep14
PASS		
230	r400sc_msa_3_rectangle_list_03	2002Sep14
PASS		
231	r400sc_msa_3_rectangle_list_04	2002Sep14
PASS		
232	r400sc_msa_3_rectangle_list_05	2002Sep14
PASS		
233	r400sc_msa_3_rectangle_list_06	2002Sep14
PASS		
234	r400sc_msa_3_rectangle_list_07	2002Sep14
PASS		
235	r400sc_msa_3_rectangle_list_08	2002Sep14
PASS		
236	r400sc_msa_3_zbuffer_rectangle_list_01	2002Sep14

PASS		
237	r400sc_msaa_3_zbuffer_rectangle_list_02	2002Sep14
PASS		
238	r400sc_msaa_4_01	2002Sep14
PASS		
239	r400sc_msaa_4_rectangle_list_01	2002Sep14
PASS		
240	r400sc_msaa_4_rectangle_list_02	2002Sep14
PASS		
241	r400sc_msaa_4_rectangle_list_03	2002Sep14
PASS		
242	r400sc_msaa_4_rectangle_list_04	2002Sep14
PASS		
243	r400sc_msaa_4_rectangle_list_05	2002Sep14
PASS		
244	r400sc_msaa_4_rectangle_list_06	2002Sep14
PASS		
245	r400sc_msaa_4_rectangle_list_07	2002Sep14
PASS		
246	r400sc_msaa_4_rectangle_list_08	2002Sep14
PASS		
247	r400sc_msaa_4_zbuffer_rectangle_list_01	2002Sep14
PASS		
248	r400sc_msaa_4_zbuffer_rectangle_list_02	2002Sep14
PASS		
249	r400sc_msaa_6_01	2002Sep14
PASS		
250	r400sc_msaa_6_rectangle_list_01	2002Sep14
PASS		
251	r400sc_msaa_6_rectangle_list_02	2002Sep14
PASS		
252	r400sc_msaa_6_rectangle_list_03	2002Sep14
PASS		
253	r400sc_msaa_6_rectangle_list_04	2002Sep14
PASS		
254	r400sc_msaa_6_rectangle_list_05	2002Sep14
PASS		
255	r400sc_msaa_6_rectangle_list_06	2002Sep14
PASS		
256	r400sc_msaa_6_rectangle_list_07	2002Sep14
PASS		
257	r400sc_msaa_6_rectangle_list_08	2002Sep14
PASS		
258	r400sc_msaa_6_zbuffer_rectangle_list_01	2002Sep14
PASS		
259	r400sc_msaa_6_zbuffer_rectangle_list_02	2002Sep14
PASS		
260	r400sc_msaa_8_01	2002Sep14

PASS		
261	r400sc_msa_8_02	2002Sep14
PASS		
262	r400sc_msa_8_03	2002Sep14
PASS		
263	r400sc_msa_8_04	2002Sep14
PASS		
264	r400sc_msa_8_05	2002Sep14
PASS		
265	r400sc_msa_8_aa_mask_01	2002Sep14
PASS		
266	r400sc_msa_8_aa_mask_02	2002Sep14
PASS		
267	r400sc_msa_8_aa_mask_fc_02	2002Sep14
PASS		
268	r400sc_msa_8_rectangle_list_01	2002Sep14
PASS		
269	r400sc_msa_8_rectangle_list_02	2002Sep14
PASS		
270	r400sc_msa_8_rectangle_list_03	2002Sep14
PASS		
271	r400sc_msa_8_rectangle_list_04	2002Sep14
PASS		
272	r400sc_msa_8_rectangle_list_05	2002Sep14
PASS		
273	r400sc_msa_8_rectangle_list_06	2002Sep14
PASS		
274	r400sc_msa_8_rectangle_list_07	2002Sep14
PASS		
275	r400sc_msa_8_rectangle_list_08	2002Sep14
PASS		
276	r400sc_msa_8_zbuffer_rectangle_list_01	2002Sep14
PASS		
277	r400sc_msa_8_zbuffer_rectangle_list_02	2002Sep14
PASS		
278	r400sc_null_triangles_01	2002Sep14
PASS		
279	r400sc_null_triangles_fc_01	2002Sep14
PASS		
280	r400sc_pinwheel_02	2002Sep14
PASS		
281	r400sc_point_jss_3x4_01	2002Sep14
PASS		
282	r400sc_point_list_01	2002Sep14
PASS		
283	r400sc_point_list_02	2002Sep14
PASS		
284	r400sc_point_list_03	2002Sep14

PASS		
285	r400sc_point_list_04	2002Sep14
PASS		
286	r400sc_point_list_05	2002Sep14
PASS		
287	r400sc_point_list_06	2002Aug28
INCOMPLETE		
288	r400sc_point_list_07	2002Sep14
PASS		
289	r400sc_point_list_08	2002Sep14
PASS		
290	r400sc_point_list_09	2002Sep14
PASS		
291	r400sc_point_msa_8_01	2002Sep14
PASS		
292	r400sc_polygon_stipple_01	2002Sep14
PASS		
293	r400sc_polymode_tri_fill_01	2002Sep14
PASS		
294	r400sc_poly_offset_01	2002Sep14
PASS		
295	r400sc_poly_offset_02	2002Sep14
PASS		
296	r400sc_poly_offset_fc_01	2002Sep14
PASS		
297	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep14
PASS		
298	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep14
PASS		
299	r400sc_prsp_byc_intrp_ref_pix_03	2002Sep14
PASS		
300	r400sc_prsp_byc_intrp_ref_pix_04	2002Sep14
PASS		
301	r400sc_prsp_byc_intrp_ref_pix_05	2002Sep14
PASS		
302	r400sc_prsp_byc_intrp_ref_pix_06	2002Sep14
PASS		
303	r400sc_prsp_byc_intrp_ref_pix_07	2002Sep14
PASS		
304	r400sc_prsp_byc_intrp_ref_pix_08	2002Sep14
PASS		
305	r400sc_raster_fill_rule_01	2002Sep14
PASS		
306	r400sc_raster_fill_rule_02	2002Sep14
PASS		
307	r400sc_raster_fill_rule_03	2002Sep14
PASS		
308	r400sc_raster_fill_rule_04	2002Sep14

PASS		
309	r400sc_raster_fill_rule_05	2002Sep14
PASS		
310	r400sc_raster_fill_rule_fc_01	2002Sep14
PASS		
311	r400sc_rectangle_list_01	2002Sep14
PASS		
312	r400sc_rectangle_list_02	2002Sep14
PASS		
313	r400sc_rectangle_list_03	2002Sep14
PASS		
314	r400sc_rectangle_list_04	2002Sep14
PASS		
315	r400sc_rectangle_list_05	2002Sep14
PASS		
316	r400sc_rectangle_list_06	2002Sep14
PASS		
317	r400sc_rectangle_list_07	2002Sep14
PASS		
318	r400sc_rectangle_list_08	2002Sep14
PASS		
319	r400sc_scissor_rect_01	2002Sep14
PASS		
320	r400sc_scissor_rect_02	2002Sep14
PASS		
321	r400sc_scissor_rect_03	2002Sep14
PASS		
322	r400sc_scissor_rect_04	2002Sep14
PASS		
323	r400sc_scissor_rect_05	2002Sep14
PASS		
324	r400sc_scissor_rect_fc_01	2002Sep14
PASS		
325	r400sc_set_state_01	2002Sep14
PASS		
326	r400sc_sp_sample_cntl_01	2002Sep14
PASS		
327	r400sc_sp_sample_cntl_02	2002Sep14
PASS		
328	r400sc_sp_sample_cntl_03	2002Sep14
PASS		
329	r400sc_sp_sample_cntl_04	2002Sep14
PASS		
330	r400sc_tri_16_par_64_dwords_01	2002Sep14
PASS		
331	r400sc_tri_8textures_01	2002Sep14
PASS		
332	r400sc_tri_8textures_02	2002Sep14

PASS		
333	r400sc_triangle_stipple_01	2002Sep14
PASS		
334	r400sc_tri_walk_start_vertex_01	2002Sep14
PASS		
335	r400sc_tri_walk_start_vertex_02	2002Sep14
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336	r400sc_tri_walk_start_vertex_03	2002Sep14
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337	r400sc_tri_walk_start_vertex_04	2002Sep14
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338	r400sc_tri_walk_start_vertex_05	2002Sep14
PASS		
339	r400sc_tri_walk_start_vertex_06	2002Sep14
PASS		
340	r400sc_tri_walk_start_vertex_07	2002Sep14
PASS		
341	r400sc_tri_walk_start_vertex_08	2002Sep14
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342	r400sc_tri_walk_start_vertex_09	2002Sep14
PASS		
343	r400sc_tri_walk_start_vertex_10	2002Sep14
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344	r400sc_tri_walk_start_vertex_11	2002Sep14
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345	r400sc_tri_walk_start_vertex_12	2002Sep14
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346	r400sc_tri_walk_start_vertex_13	2002Sep14
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347	r400sc_tri_walk_start_vertex_14	2002Sep14
PASS		
348	r400sc_tri_walk_start_vertex_15	2002Sep14
PASS		
349	r400sc_tri_walk_start_vertex_16	2002Sep14
PASS		
350	r400sc_window_offset_01	2002Sep14
PASS		
351	r400sc_window_offset_fc_01	2002Sep14
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352	r400sc_zbuffer_line_list_01	2002Sep14
PASS		
353	r400sc_zbuffer_point_list_01	2002Sep14
PASS		
354	r400sc_zbuffer_rectangle_list_01	2002Sep14
PASS		
355	r400sc_zbuffer_rectangle_list_02	2002Sep14
PASS		
356	r400sc_zbuffer_rectangle_list_fc_02	2002Sep14

PASS		
357	r400sc_zbuffer_triangle_list_01	2002Sep14
PASS		
358	r400su_4tri_text_offscreen_01	2002Sep14
PASS		
359	r400su_baryc_test_01	2002Sep14
PASS		
360	r400su_baryc_test_02	2002Sep14
PASS		
361	r400su_baryc_test_03	2002Sep14
PASS		
362	r400su_baryc_test_04	2002Sep14
PASS		
363	r400su_clip_baryc_test_01	2002Sep14
PASS		
364	r400su_clip_baryc_test_02	2002Sep14
PASS		
365	r400su_clip_edgeflag_polymode_01	2002Sep14
PASS		
366	r400su_clipline_01	2002Sep14
PASS		
367	r400su_clippoint_01	2002Sep14
PASS		
368	r400su_clip_pointsize_test_01	2002Sep14
PASS		
369	r400su_clip_pointttest_01	2002Sep14
PASS		
370	r400su_clip_pointttest_02	2002Sep14
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371	r400su_clip_pointttest_03	2002Sep14
PASS		
372	r400su_clip_pointttest_04	2002Sep14
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373	r400su_clip_polymode_test_01	2002Sep14
PASS		
374	r400su_clipvertexsortingfunctional_01	2002Sep14
PASS		
375	r400su_cullingfunctional_01	2002Sep14
PASS		
376	r400su_degentri_test_01	2002Sep14
PASS		
377	r400su_degentri_test_02	2002Sep14
PASS		
378	r400su_degentri_test_03	2002Sep14
PASS		
379	r400su_edge_flag_01	2002Sep14
PASS		
380	r400su_edge_flag_02	2002Sep14

PASS		
381	r400su_edgeflags_triangle_01	2002Sep14
PASS		
382	r400su_edgeflags_triangle_02	2002Sep14
PASS		
383	r400su_geom_sort_01	2002Sep14
PASS		
384	r400su_line_end_cap_functional_01	2002Sep14
PASS		
385	r400su_line_end_cap_width_functional_02	2002Sep14
PASS		
386	r400su_line_test_01	2002Sep14
PASS		
387	r400su_line_test_02	2002Sep14
PASS		
388	r400su_longstrip_01	2002Sep14
PASS		
389	r400su_multi_context_01	2002Sep14
PASS		
390	r400su_multi_prim_01	2002Sep14
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391	r400su_multi_prim_02	2002Sep14
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392	r400su_parallel_orientation_all_01	2002Sep14
PASS		
393	r400su_parallel_orientation_all_02	2002Sep14
PASS		
394	r400su_pc_management_01	2002Sep14
PASS		
395	r400su_pc_management_02	2002Sep14
PASS		
396	r400su_pc_management_03	2002Sep14
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397	r400su_pointsizepresent_01	2002Sep14
PASS		
398	r400su_pointsizepresent_02	2002Sep14
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399	r400su_point_sprite_01	2002Sep14
PASS		
400	r400su_point_sprite_02	2002Sep14
PASS		
401	r400su_point_sprite_03	2002Sep14
PASS		
402	r400su_point_sprite_04	2002Sep14
PASS		
403	r400su_point_sprite_05	2002Sep14
PASS		
404	r400su_point_sprite_06	2002Sep14

PASS		
405	r400su_point_sprite_07	2002Sep14
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406	r400su_point_sprite_08	2002Sep14
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407	r400su_point_wl6_hl_functional_01	2002Sep14
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408	r400su_point_wl_hl6_functional_01	2002Sep14
PASS		
409	r400su_polymodeculling_01	2002Sep14
PASS		
410	r400su_polymodefunctional_01	2002Sep14
PASS		
411	r400su_polymode_lines_degen_triangle_01	2002Sep14
PASS		
412	r400su_polymode_lines_degen_triangle_02	2002Sep14
PASS		
413	r400su_polymode_lines_zero_area_triangle_01	2002Sep14
PASS		
414	r400su_polymode_lines_zero_area_triangle_02	2002Sep14
PASS		
415	r400su_polymode_points_degen_triangle_01	2002Sep14
PASS		
416	r400su_polymode_points_degen_triangle_02	2002Sep14
PASS		
417	r400su_polymode_points_zero_area_triangle_01	2002Sep14
PASS		
418	r400su_polymode_points_zero_area_triangle_02	2002Sep14
PASS		
419	r400su_polymode_rectangle_01	2002Sep14
PASS		
420	r400su_polymode_zero_area_triangle_01	2002Sep14
PASS		
421	r400su_polymode_zero_area_triangle_02	2002Sep14
PASS		
422	r400su_polymode_zero_area_triangle_03	2002Sep14
PASS		
423	r400su_polymode_zero_area_triangle_04	2002Sep14
PASS		
424	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep14
PASS		
425	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep14
PASS		
426	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep14
PASS		
427	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep14
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428	r400su_provoking_vtx_line_01	2002Sep14

PASS		
429	r400su_provoking_vtx_point_01	2002Sep14
PASS		
430	r400su_provoking_vtx_polymode_rectangle_01	2002Sep14
PASS		
431	r400su_provoking_vtx_rectangle_01	2002Sep14
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432	r400su_provoking_vtx_triangle_01	2002Sep14
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433	r400su_rand_line_01	2002Sep14
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434	r400su_rand_point_01	2002Sep14
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435	r400su_rand_tri_01	2002Sep14
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436	r400su_rectangle_list_01	2002Sep14
PASS		
437	r400su_sliver_01	2002Sep14
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438	r400su_triarea_test_01	2002Sep14
PASS		
439	r400su_triarea_test_02	2002Sep14
PASS		
440	r400su_triarea_test_03	2002Sep14
PASS		
441	r400su_triarea_test_04	2002Sep14
PASS		
442	r400su_vertexsortingfunctional_01	2002Sep14
PASS		
443	r400vgt_auto_index_line_list_01	2002Sep14
PASS		
444	r400vgt_auto_index_line_loop_01	2002Sep14
PASS		
445	r400vgt_auto_index_line_strip_01	2002Sep14
PASS		
446	r400vgt_auto_index_points_01	2002Sep14
PASS		
447	r400vgt_auto_index_polygon_01	2002Sep14
PASS		
448	r400vgt_auto_index_primitives_01	2002Sep14
PASS		
449	r400vgt_auto_index_quad_list_01	2002Sep14
PASS		
450	r400vgt_auto_index_quad_strip_01	2002Sep14
PASS		
451	r400vgt_auto_index_rectangle_list_01	2002Sep14
PASS		
452	r400vgt_auto_index_tri_fan_01	2002Sep14

PASS		
453	r400vgt_auto_index_tri_list_01	2002Sep14
PASS		
454	r400vgt_auto_index_tri_strip_01	2002Sep14
PASS		
455	r400vgt_auto_index_tri_wflags_01	2002Sep14
PASS		
456	r400vgt_dma_engine_01	2002Sep14
PASS		
457	r400vgt_dma_engine_02	2002Sep14
PASS		
458	r400vgt_dma_engine_03	2002Sep14
PASS		
459	r400vgt_dma_engine_04	2002Sep14
PASS		
460	r400vgt_dma_engine_05	2002Sep14
PASS		
461	r400vgt_dma_engine_06	2002Sep14
PASS		
462	r400vgt_dma_engine_07	2002Sep14
PASS		
463	r400vgt_dma_engine_08	2002Sep14
PASS		
464	r400vgt_dma_engine_09	2002Sep14
PASS		
465	r400vgt_dma_engine_10	2002Sep14
PASS		
466	r400vgt_dma_index_line_list_01	2002Sep14
PASS		
467	r400vgt_dma_index_line_loop_01	2002Sep14
PASS		
468	r400vgt_dma_index_line_strip_01	2002Sep14
PASS		
469	r400vgt_dma_index_points_01	2002Sep14
PASS		
470	r400vgt_dma_index_polygon_01	2002Sep14
PASS		
471	r400vgt_dma_index_primitives_01	2002Sep14
PASS		
472	r400vgt_dma_index_quad_list_01	2002Sep14
PASS		
473	r400vgt_dma_index_quad_strip_01	2002Sep14
PASS		
474	r400vgt_dma_index_rectangle_list_01	2002Sep14
PASS		
475	r400vgt_dma_index_tri_fan_01	2002Sep14
PASS		
476	r400vgt_dma_index_tri_list_01	2002Sep14

PASS		
477	r400vgt_dma_index_tri_strip_01	2002Sep14
PASS		
478	r400vgt_dma_index_tri_wflags_01	2002Sep14
PASS		
479	r400vgt_dma_swap_indxl6_01	2002Sep14
PASS		
480	r400vgt_dma_swap_indx32_01	2002Sep14
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481	r400vgt_edgeflags_polygon_01	2002Sep14
PASS		
482	r400vgt_edgeflags_quad_list_01	2002Sep14
PASS		
483	r400vgt_edgeflags_quad_strip_01	2002Sep14
PASS		
484	r400vgt_edgeflags_triangle_fan_01	2002Sep14
PASS		
485	r400vgt_edgeflags_triangle_list_01	2002Sep14
PASS		
486	r400vgt_edgeflags_triangle_strip_01	2002Sep14
PASS		
487	r400vgt_edgeflags_triangle_wflags_01	2002Sep14
PASS		
488	r400vgt_ext2int_index_line_list_01	2002Sep14
PASS		
489	r400vgt_ext2int_index_line_loop_01	2002Sep14
PASS		
490	r400vgt_ext2int_index_line_strip_01	2002Sep14
PASS		
491	r400vgt_ext2int_index_points_01	2002Sep14
PASS		
492	r400vgt_ext2int_index_polygon_01	2002Sep14
PASS		
493	r400vgt_ext2int_index_quad_list_01	2002Sep14
PASS		
494	r400vgt_ext2int_index_quad_strip_01	2002Sep14
PASS		
495	r400vgt_ext2int_index_triangle_fan_01	2002Sep14
PASS		
496	r400vgt_ext2int_index_triangle_list_01	2002Sep14
PASS		
497	r400vgt_ext2int_index_triangle_strip_01	2002Sep14
PASS		
498	r400vgt_hos_auto_index_line_list_01	2002Sep14
PASS		
499	r400vgt_hos_auto_index_quad_list_01	2002Sep14
PASS		
500	r400vgt_hos_auto_index_triangle_list_01	2002Sep14

PASS		
501	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep14
PASS		
502	r400vgt_hos_LPatch_01	2002Sep14
PASS		
503	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep14
PASS		
504	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep14
PASS		
505	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep14
PASS		
506	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep14
PASS		
507	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep14
PASS		
508	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep14
PASS		
509	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep14
INCOMPLETE		
510	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep14
PASS		
511	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep14
PASS		
512	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep14
PASS		
513	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep14
PASS		
514	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep14
PASS		
515	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep14
PASS		
516	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep14
PASS		
517	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep14
PASS		
518	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep14
INCOMPLETE		
519	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep14
PASS		
520	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep14
PASS		
521	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep14
PASS		
522	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep14
PASS		
523	r400vgt_hos_PNT_edge_detection_01	2002Sep14
PASS		
524	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep14

PASS	
525	r400vgt_hos_RPatch_cp_02 2002Sep14
INCOMPLETE	
526	r400vgt_hos_RPatch_lp_02 2002Sep14
INCOMPLETE	
527	r400vgt_hos_simple_linear_PNT_discrete_01 2002Sep13
INCOMPLETE	
528	r400vgt_hos_TPatch_01 2002Sep13
INCOMPLETE	
529	r400vgt_hos_TPatch_02 2002Sep13
INCOMPLETE	
530	r400vgt_immed_index_line_list_01 2002Sep14
PASS	
531	r400vgt_immed_index_line_loop_01 2002Sep14
PASS	
532	r400vgt_immed_index_line_strip_01 2002Sep14
PASS	
533	r400vgt_immed_index_points_01 2002Sep14
PASS	
534	r400vgt_immed_index_polygon_01 2002Sep14
PASS	
535	r400vgt_immed_index_primitives_01 2002Sep14
PASS	
536	r400vgt_immed_index_quad_list_01 2002Sep14
PASS	
537	r400vgt_immed_index_quad_strip_01 2002Sep14
PASS	
538	r400vgt_immed_index_rectangle_list_01 2002Sep14
PASS	
539	r400vgt_immed_index_tri_fan_01 2002Sep14
PASS	
540	r400vgt_immed_index_tri_list_01 2002Sep14
PASS	
541	r400vgt_immed_index_tri_strip_01 2002Sep14
PASS	
542	r400vgt_immed_index_tri_wflags_01 2002Sep14
PASS	
543	r400vgt_index_dealloc_line_list_01 2002Sep14
PASS	
544	r400vgt_index_dealloc_points_01 2002Sep14
PASS	
545	r400vgt_index_dealloc_triangle_list_01 2002Sep14
PASS	
546	r400vgt_index_min_max_01 2002Sep14
PASS	
547	r400vgt_index_min_max_02 2002Sep14
PASS	
548	r400vgt_index_offset_01 2002Sep14

PASS		
549	r400vgt_index_offset_02	2002Sep14
PASS		
550	r400vgt_index_offset_03	2002Sep14
PASS		
551	r400vgt_index_size_01	2002Sep14
PASS		
552	r400vgt_index_source_switch_01	2002Sep14
PASS		
553	r400vgt_line_list_01	2002Sep14
PASS		
554	r400vgt_line_loop_01	2002Sep14
PASS		
555	r400vgt_line_strip_01	2002Sep14
PASS		
556	r400vgt_multi_context_01	2002Sep14
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557	r400vgt_multi_context_02	2002Sep14
PASS		
558	r400vgt_multi_context_03	2002Sep14
PASS		
559	r400vgt_multi_context_04	2002Sep14
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560	r400vgt_multi_context_05	2002Sep14
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561	r400vgt_multi_context_06	2002Sep14
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562	r400vgt_multi_context_07	2002Sep14
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563	r400vgt_multi_context_08	2002Sep14
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564	r400vgt_multi_context_09	2002Sep14
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565	r400vgt_multi_context_10	2002Sep14
PASS		
566	r400vgt_multi_context_11	2002Sep14
PASS		
567	r400vgt_multi_context_12	2002Sep14
PASS		
568	r400vgt_multi_prim_reset_index_all_01	2002Sep14
PASS		
569	r400vgt_pass_thru_all_prims_01	2002Sep14
PASS		
570	r400vgt_point_list_01	2002Sep14
PASS		
571	r400vgt_polygon_01	2002Sep14
PASS		
572	r400vgt_provoking_vtx_all_01	2002Sep14

PASS		
573	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep14
PASS		
574	r400vgt_provoking_vtx_polygon_01	2002Sep14
PASS		
575	r400vgt_provoking_vtx_quad_list_01	2002Sep14
PASS		
576	r400vgt_provoking_vtx_quad_strip_01	2002Sep14
PASS		
577	r400vgt_provoking_vtx_tri_fan_01	2002Sep14
PASS		
578	r400vgt_provoking_vtx_tri_strip_01	2002Sep14
PASS		
579	r400vgt_quad_list_01	2002Sep14
PASS		
580	r400vgt_quad_strip_01	2002Sep14
PASS		
581	r400vgt_rectangle_list_01	2002Sep14
PASS		
582	r400vgt_reuse_depth_line_list_01	2002Sep14
PASS		
583	r400vgt_reuse_depth_point_list_01	2002Sep14
PASS		
584	r400vgt_reuse_depth_triangle_list_01	2002Sep14
PASS		
585	r400vgt_reuse_index_line_list_01	2002Sep14
PASS		
586	r400vgt_reuse_index_point_list_01	2002Sep14
PASS		
587	r400vgt_reuse_index_triangle_list_01	2002Sep14
PASS		
588	r400vgt_reuse_index_triangle_list_02	2002Sep14
PASS		
589	r400vgt_reuse_index_triangle_list_03	2002Sep14
PASS		
590	r400vgt_suppress_eop_01	2002Sep14
PASS		
591	r400vgt_suppress_eop_02	2002Sep14
PASS		
592	r400vgt_suppress_eop_03	2002Sep14
PASS		
593	r400vgt_suppress_eop_04	2002Sep14
PASS		
594	r400vgt_triangle_fan_01	2002Sep14
PASS		
595	r400vgt_triangle_list_01	2002Sep14
PASS		
596	r400vgt_triangle_strip_01	2002Sep14

PASS
597 r400vgt_triangle_wflags_01 2002Sep14
PASS
598 r400vgt_viz_query_01 2002Sep14
PASS
599 r400vgt_vtx_vector_packing_01 2002Sep14
PASS
600 r400vte_coverage_01 2002Sep14
PASS
601 r400vte_simple_point_01 2002Sep14
PASS
602 r400vte_simple_triangle_01 2002Sep14
PASS
603 r400vte_w0_fmt_01 2002Sep14
PASS
604 r400vte_w0_fmt_03 2002Sep14
PASS
605 r400vte_xy_fmt_01 2002Sep14
PASS
606 r400vte_xyz_scale_01 2002Sep14
PASS
607 r400vte_xyz_scale_02 2002Sep14
PASS
608 r400vte_z_fmt_01 2002Sep14
PASS

=====
Tests_Run_count = 608 Pass_count = 600 Fail_count = 0 Incomplete_count = 8
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug22
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug22
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug22
PASS			
4	primlib_parameterized_simple_triangle		2002Aug22
PASS			
5	primlib_template_simple_triangle		2002Aug22
PASS			
6	primlib_tex_tri		2002Aug22
PASS			
7	r400cl_clip_10_verts_01		2002Aug22
PASS			
8	r400cl_clip_disable_01		2002Aug22
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug22
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug22
PASS			
11	r400cl_cull_only_ena_01		2002Aug22
PASS			
12	r400cl_edgeflags_01		2002Aug22
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug22
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug22
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug22
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug22
PASS			
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18	r400cl_edgeflags_frustum_top_01		2002Aug22
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19	r400cl_edgeflags_gband_01		2002Aug22
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20	r400cl_edgeflags_gband_bottom_01		2002Aug22

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77	r400cl_gband_tcl_01	2002Aug22
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79	r400cl_ucp_combos_01	2002Aug22
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81	r400cl_ucp_combos_03	2002Aug22
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83	r400cl_vertex_reuse_clip_01	2002Aug22
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85	r400cl_vtx_kill_02	2002Aug22
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127 r400sc_jss_4x3_02 2002Aug22
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128 r400sc_jss_4x4_01 2002Aug22
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129 r400sc_jss_4x4_02 2002Aug22
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130 r400sc_jss_4x4_aa_mask_01 2002Aug22
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131 r400sc_jss_4x4_aa_mask_02 2002Aug22
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132 r400sc_jss_4x4_max_dist_01 2002Aug22
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133 r400sc_line_dx10_eq_0_01 2002Aug22
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134 r400sc_line_dx10_ge_0_01 2002Aug22
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135 r400sc_line_dx10_lt_0_01 2002Aug22
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140 r400sc_line_msaa_8_01 2002Aug22

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153	r400sc_msa_6_01	2002Aug22
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154	r400sc_msa_8_01	2002Aug22
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155	r400sc_msa_8_aa_mask_01	2002Aug22
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156	r400sc_msa_8_aa_mask_02	2002Aug22
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173	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug22
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343	r400vgt_hos_auto_index_triangle_list_01	2002Aug22
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345	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug22
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346	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug22
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357	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug22
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358	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug22
PASS		
359	r400vgt_hos_PNT_cp_qn_disc_l4_04_lit_tex_proj_01	2002Aug22
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360	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug22
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361	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug22
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362	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug22
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363	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug22
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364	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug22
INCOMPLETE		
365	r400vgt_hos_PNT_edge_detection_01	2002Aug22
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366	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug22
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367	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug22
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368	r400vgt_immed_index_line_list_01	2002Aug22
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369	r400vgt_immed_index_line_loop_01	2002Aug22
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370	r400vgt_immed_index_line_strip_01	2002Aug22
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371	r400vgt_immed_index_points_01	2002Aug22
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372	r400vgt_immed_index_polygon_01	2002Aug22
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373	r400vgt_immed_index_primitives_01	2002Aug22
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374	r400vgt_immed_index_quad_list_01	2002Aug22
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375	r400vgt_immed_index_quad_strip_01	2002Aug22
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376	r400vgt_immed_index_rectangle_list_01	2002Aug22
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377	r400vgt_immed_index_tri_fan_01	2002Aug22
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378	r400vgt_immed_index_tri_list_01	2002Aug22
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379	r400vgt_immed_index_tri_strip_01	2002Aug22
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380	r400vgt_immed_index_tri_wflags_01	2002Aug22

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381	r400vgt_index_dealloc_line_list_01	2002Aug22
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382	r400vgt_index_dealloc_points_01	2002Aug22
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383	r400vgt_index_dealloc_triangle_list_01	2002Aug22
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384	r400vgt_index_min_max_01	2002Aug22
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385	r400vgt_index_min_max_02	2002Aug22
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386	r400vgt_index_offset_01	2002Aug22
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387	r400vgt_index_offset_02	2002Aug22
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388	r400vgt_index_offset_03	2002Aug22
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389	r400vgt_index_size_01	2002Aug22
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390	r400vgt_index_source_switch_01	2002Aug22
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391	r400vgt_line_list_01	2002Aug22
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392	r400vgt_line_loop_01	2002Aug22
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393	r400vgt_line_strip_01	2002Aug22
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394	r400vgt_pass_thru_all_prims_01	2002Aug22
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395	r400vgt_point_list_01	2002Aug22
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396	r400vgt_polygon_01	2002Aug22
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397	r400vgt_provoking_vtx_all_01	2002Aug22
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398	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug22
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399	r400vgt_quad_list_01	2002Aug22
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400	r400vgt_quad_strip_01	2002Aug22
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402	r400vgt_reuse_depth_line_list_01	2002Aug22
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404	r400vgt_reuse_depth_triangle_list_01	2002Aug22

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406 r400vgt_reuse_index_point_list_01 2002Aug22
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407 r400vgt_reuse_index_triangle_list_01 2002Aug22
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409 r400vgt_reuse_index_triangle_list_03 2002Aug22
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411 r400vgt_triangle_list_01 2002Aug22
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412 r400vgt_triangle_strip_01 2002Aug22
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413 r400vgt_triangle_wflags_01 2002Aug22
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414 r400vgt_vtx_vector_packing_01 2002Aug22
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415 r400vte_coverage_01 2002Aug22
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416 r400vte_simple_point_01 2002Aug22
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417 r400vte_simple_triangle_01 2002Aug22
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418 r400vte_w0_fmt_01 2002Aug22
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419 r400vte_w0_fmt_03 2002Aug22
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420 r400vte_xy_fmt_01 2002Aug22
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421 r400vte_xyz_scale_01 2002Aug22
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422 r400vte_xyz_scale_02 2002Aug22
PASS
423 r400vte_z_fmt_01 2002Aug22
PASS

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Tests_Run_count = 423 Pass_count = 418 Fail_count = 0 Incomplete_count = 5
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Regression Report For VGT - Project = r400 Created on Sun Sep 29 22:14:43 2002
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No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep29
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2	cp_e2_hostdata_blt_pntr_8888		2002Sep29
PASS			
3	cp_e2_one_blit		2002Sep29
PASS			
4	cp_e2_one_line		2002Sep29
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Sep29
PASS			
6	cp_e2paint_565		2002Sep29
PASS			
7	cp_e2paint_multi		2002Sep29
PASS			
8	cp_e2polyscanlines_brush		2002Sep29
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9	cp_e2polyscanlines_brush_mt		2002Sep29
PASS			
10	cp_e2smalltext_jcl		2002Sep29
PASS			
11	cp_simple_triangle		2002Sep29
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12	primlib_1st_tri_june15		2002Sep29
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep29
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep29
PASS			
15	primlib_parameterized_simple_triangle		2002Sep29
PASS			
16	primlib_template_simple_triangle		2002Sep29
PASS			
17	primlib_tex_tri		2002Sep29
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18	r400cl_bary_texture_08_bug		2002Sep21
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19	r400cl_clip_10_verts_01		2002Sep29
PASS			
20	r400cl_clip_disable_01		2002Sep29

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21	r400cl_clip_space_dx_ogl_01	2002Sep29
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22	r400cl_clip_ucp_6bits_01	2002Sep29
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23	r400cl_cull_only_ena_01	2002Sep29
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24	r400cl_edgeflags_01	2002Sep29
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25	r400cl_edgeflags_01_bug	2002Sep29
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26	r400cl_edgeflags_frustum_bottom_01	2002Sep29
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep29
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28	r400cl_edgeflags_frustum_left_01	2002Sep29
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep29
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep29
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep29
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32	r400cl_edgeflags_gband_01	2002Sep29
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33	r400cl_edgeflags_gband_bottom_01	2002Sep29
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34	r400cl_edgeflags_gband_left_01	2002Sep29
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep29
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36	r400cl_edgeflags_gband_top_01	2002Sep29
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37	r400cl_frustum_01	2002Sep29
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66	r400cl_frustum_all_vols_tris	2002Sep29
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68	r400cl_frustum_lines_02	2002Sep29

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92	r400cl_gband_tcl_01	2002Sep29

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95	r400cl_polymode_line_fill_01_bug	2002Sep29
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96	r400cl_simple_triangle_01	2002Sep29
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97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep29
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131	r400cl_vtx_kill_02	2002Sep29
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133	r400rbm_simple_triangle_01	2002Sep29
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134	r400sanity_persp_projection_01	2002Sep29
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135	r400sanity_vfd_texture_sample_01	2002Sep29
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138	r400sc_bres_cntl_02	2002Sep29
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139	r400sc_bres_cntl_03	2002Sep29
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155	r400sc_jss_1x2_02	2002Sep29
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161	r400sc_jss_1x4_02	2002Sep29
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209	r400sc_line_dy10_eq_0_01	2002Sep29
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210	r400sc_line_dy10_ge_0_01	2002Sep29
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219	r400sc_line_list_concentric_circle_02	2002Sep29
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220	r400sc_line_list_concentric_circle_03	2002Sep29
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221	r400sc_line_list_textured_01	2002Sep29
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222	r400sc_line_list_verify_st_01	2002Sep29
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223	r400sc_line_list_verify_st_01_bug	2002Sep29
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224	r400sc_line_msa_8_01	2002Sep29
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225	r400sc_line_msa_8_textured_01	2002Sep29
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226	r400sc_line_stipple_01	2002Sep29
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227	r400sc_line_stipple_02	2002Sep29
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239	r400sc_line_stipple_14	2002Sep29
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240	r400sc_line_stipple_fc_08	2002Sep29
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241	r400sc_line_strip_stipple_01	2002Sep25
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242	r400sc_msa_1_01	2002Sep29
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244	r400sc_msa_1_rectangle_list_01	2002Sep29
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594	r400vgt_hos_RPatch_lp_02	2002Sep29
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595	r400vgt_hos_TPatch_01	2002Sep29
INCOMPLETE		
596	r400vgt_hos_TPatch_02	2002Sep29

INCOMPLETE

597	r400vgt_hos_auto_index_line_list_01	2002Sep29
	PASS	
598	r400vgt_hos_auto_index_quad_list_01	2002Sep29
	PASS	
599	r400vgt_hos_auto_index_triangle_list_01	2002Sep29
	PASS	
600	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep29
	PASS	
601	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep29
	PASS	
602	r400vgt_immed_index_line_list_01	2002Sep29
	PASS	
603	r400vgt_immed_index_line_loop_01	2002Sep29
	PASS	
604	r400vgt_immed_index_line_strip_01	2002Sep29
	PASS	
605	r400vgt_immed_index_points_01	2002Sep29
	PASS	
606	r400vgt_immed_index_polygon_01	2002Sep29
	PASS	
607	r400vgt_immed_index_primitives_01	2002Sep29
	PASS	
608	r400vgt_immed_index_quad_list_01	2002Sep29
	PASS	
609	r400vgt_immed_index_quad_strip_01	2002Sep29
	PASS	
610	r400vgt_immed_index_rectangle_list_01	2002Sep29
	PASS	
611	r400vgt_immed_index_tri_fan_01	2002Sep29
	PASS	
612	r400vgt_immed_index_tri_list_01	2002Sep29
	PASS	
613	r400vgt_immed_index_tri_strip_01	2002Sep29
	PASS	
614	r400vgt_immed_index_tri_wflags_01	2002Sep29
	PASS	
615	r400vgt_index_dealloc_line_list_01	2002Sep29
	PASS	
616	r400vgt_index_dealloc_points_01	2002Sep29
	PASS	
617	r400vgt_index_dealloc_triangle_list_01	2002Sep29
	PASS	
618	r400vgt_index_min_max_01	2002Sep29
	PASS	
619	r400vgt_index_min_max_02	2002Sep29
	PASS	
620	r400vgt_index_offset_01	2002Sep29

PASS		
621	r400vgt_index_offset_02	2002Sep29
PASS		
622	r400vgt_index_offset_03	2002Sep29
PASS		
623	r400vgt_index_size_01	2002Sep29
PASS		
624	r400vgt_index_source_switch_01	2002Sep29
PASS		
625	r400vgt_line_list_01	2002Sep29
PASS		
626	r400vgt_line_list_02	2002Sep29
PASS		
627	r400vgt_line_loop_01	2002Sep29
PASS		
628	r400vgt_line_loop_02	2002Sep29
PASS		
629	r400vgt_line_strip_01	2002Sep29
PASS		
630	r400vgt_line_strip_02	2002Sep29
PASS		
631	r400vgt_multi_context_01	2002Sep29
PASS		
632	r400vgt_multi_context_02	2002Sep29
PASS		
633	r400vgt_multi_context_03	2002Sep29
PASS		
634	r400vgt_multi_context_04	2002Sep29
PASS		
635	r400vgt_multi_context_05	2002Sep29
PASS		
636	r400vgt_multi_context_06	2002Sep29
PASS		
637	r400vgt_multi_context_07	2002Sep29
PASS		
638	r400vgt_multi_context_08	2002Sep29
PASS		
639	r400vgt_multi_context_09	2002Sep29
PASS		
640	r400vgt_multi_context_10	2002Sep29
PASS		
641	r400vgt_multi_context_11	2002Sep29
PASS		
642	r400vgt_multi_context_12	2002Sep29
PASS		
643	r400vgt_multi_prim_reset_index_all_01	2002Sep29
PASS		
644	r400vgt_multi_prim_reset_index_all_02	2002Sep29

PASS		
645	r400vgt_pass_thru_all_prims_01	2002Sep29
PASS		
646	r400vgt_point_list_01	2002Sep29
PASS		
647	r400vgt_point_list_02	2002Sep29
PASS		
648	r400vgt_polygon_01	2002Sep29
PASS		
649	r400vgt_polygon_02	2002Sep29
PASS		
650	r400vgt_provoking_vtx_all_01	2002Sep29
PASS		
651	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep29
PASS		
652	r400vgt_provoking_vtx_polygon_01	2002Sep29
PASS		
653	r400vgt_provoking_vtx_quad_list_01	2002Sep29
PASS		
654	r400vgt_provoking_vtx_quad_strip_01	2002Sep29
PASS		
655	r400vgt_provoking_vtx_tri_fan_01	2002Sep29
PASS		
656	r400vgt_provoking_vtx_tri_strip_01	2002Sep29
PASS		
657	r400vgt_quad_list_01	2002Sep29
PASS		
658	r400vgt_quad_list_02	2002Sep29
PASS		
659	r400vgt_quad_strip_01	2002Sep29
PASS		
660	r400vgt_quad_strip_02	2002Sep29
PASS		
661	r400vgt_rectangle_list_01	2002Sep29
PASS		
662	r400vgt_rectangle_list_02	2002Sep29
PASS		
663	r400vgt_reuse_depth_line_list_01	2002Sep29
PASS		
664	r400vgt_reuse_depth_point_list_01	2002Sep29
PASS		
665	r400vgt_reuse_depth_triangle_list_01	2002Sep29
PASS		
666	r400vgt_reuse_index_line_list_01	2002Sep29
PASS		
667	r400vgt_reuse_index_point_list_01	2002Sep29
PASS		
668	r400vgt_reuse_index_triangle_list_01	2002Sep29

PASS			
669	r400vgt_reuse_index_triangle_list_02		2002Sep29
PASS			
670	r400vgt_reuse_index_triangle_list_03		2002Sep29
PASS			
671	r400vgt_suppress_eop_01	0:0:1	2002Sep29
PASS			
672	r400vgt_suppress_eop_02	0:0:1	2002Sep29
PASS			
673	r400vgt_suppress_eop_03	0:0:1	2002Sep29
PASS			
674	r400vgt_suppress_eop_04	0:0:1	2002Sep29
PASS			
675	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
676	r400vgt_triangle_fan_01	0:0:1	2002Sep29
PASS			
677	r400vgt_triangle_fan_02	0:0:1	2002Sep29
PASS			
678	r400vgt_triangle_list_01	0:0:1	2002Sep29
PASS			
679	r400vgt_triangle_list_02	0:0:1	2002Sep29
PASS			
680	r400vgt_triangle_strip_01	0:0:1	2002Sep29
PASS			
681	r400vgt_triangle_strip_02	0:0:1	2002Sep29
PASS			
682	r400vgt_triangle_wflags_01	0:0:1	2002Sep29
PASS			
683	r400vgt_triangle_wflags_02	0:0:1	2002Sep29
PASS			
684	r400vgt_viz_query_01	0:0:1	2002Sep29
PASS			
685	r400vgt_vtx_vector_packing_01	0:0:1	2002Sep29
PASS			
686	r400vte_coverage_01	0:0:1	2002Sep29
PASS			
687	r400vte_simple_point_01	0:0:1	2002Sep29
PASS			
688	r400vte_simple_triangle_01	0:0:1	2002Sep29
PASS			
689	r400vte_w0_fmt_01	0:0:1	2002Sep29
PASS			
690	r400vte_w0_fmt_03	0:0:1	2002Sep29
PASS			
691	r400vte_xy_fmt_01	0:0:1	2002Sep29
PASS			
692	r400vte_xy_fmt_mk_01_bug	0:0:1	2002Sep29

PASS
693 r400vte_xyz_scale_01 0:0:1 2002Sep29
PASS
694 r400vte_xyz_scale_02 0:0:1 2002Sep29
PASS
695 r400vte_z_fmt_01 0:0:1 2002Sep29
PASS

=====
Tests_Run_count = 695 Pass_count = 693 Fail_count = 0 Incomplete_count = 2
=====

Regression Report For VGT - Project = r400 Created on Tue Sep 24 10:30:41 2002

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep23
PASS			
2	cp_e2blit_brush_mt_ropcc		2002Sep23
PASS			
3	cp_e2_hostdata_blt_pntr_8888		2002Sep23
PASS			
4	cp_e2_one_blit		2002Sep23
PASS			
5	cp_e2_one_line		2002Sep23
PASS			
6	cp_e2paint_565		2002Sep23
PASS			
7	cp_e2paint_multi		2002Sep23
PASS			
8	cp_e2polyscanlines_brush		2002Sep23
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep23
PASS			
10	cp_e2smalltext_jcl		2002Sep23
PASS			
11	cp_simple_triangle		2002Sep23
PASS			
12	primlib_1st_tri_june15		2002Sep24
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep24
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep24
PASS			
15	primlib_parameterized_simple_triangle		2002Sep24
PASS			
16	primlib_template_simple_triangle		2002Sep24
PASS			
17	primlib_tex_tri		2002Sep24
INCOMPLETE			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep24
PASS			
20	r400cl_clip_disable_01		2002Sep24

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep24
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep24
PASS		
23	r400cl_cull_only_ena_01	2002Sep24
PASS		
24	r400cl_edgeflags_01	2002Sep24
PASS		
25	r400cl_edgeflags_01_bug	2002Sep23
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep24
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep24
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep24
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep24
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep24
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep24
PASS		
32	r400cl_edgeflags_gband_01	2002Sep24
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep24
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep24
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep24
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep24
PASS		
37	r400cl_frustum_01	2002Sep24
PASS		
38	r400cl_frustum_02	2002Sep24
PASS		
39	r400cl_frustum_03	2002Sep24
PASS		
40	r400cl_frustum_04	2002Sep24
PASS		
41	r400cl_frustum_05	2002Sep24
PASS		
42	r400cl_frustum_06	2002Sep24
PASS		
43	r400cl_frustum_07	2002Sep24
PASS		
44	r400cl_frustum_08	2002Sep24

PASS		
45	r400cl_frustum_09	2002Sep24
PASS		
46	r400cl_frustum_10	2002Sep24
PASS		
47	r400cl_frustum_11	2002Sep24
PASS		
48	r400cl_frustum_12	2002Sep24
PASS		
49	r400cl_frustum_13	2002Sep24
PASS		
50	r400cl_frustum_14	2002Sep24
PASS		
51	r400cl_frustum_15	2002Sep24
PASS		
52	r400cl_frustum_16	2002Sep24
PASS		
53	r400cl_frustum_17	2002Sep24
PASS		
54	r400cl_frustum_18	2002Sep24
PASS		
55	r400cl_frustum_19	2002Sep24
PASS		
56	r400cl_frustum_20	2002Sep24
PASS		
57	r400cl_frustum_21	2002Sep24
PASS		
58	r400cl_frustum_22	2002Sep24
PASS		
59	r400cl_frustum_23	2002Sep24
PASS		
60	r400cl_frustum_24	2002Sep24
PASS		
61	r400cl_frustum_25	2002Sep24
PASS		
62	r400cl_frustum_26	2002Sep24
PASS		
63	r400cl_frustum_27	2002Sep24
PASS		
64	r400cl_frustum_28	2002Sep24
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep24
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep24
PASS		
67	r400cl_frustum_lines_01	2002Sep24
PASS		
68	r400cl_frustum_lines_02	2002Sep24

PASS		
69	r400cl_frustum_lines_03	2002Sep24
PASS		
70	r400cl_frustum_lines_04	2002Sep24
PASS		
71	r400cl_frustum_lines_05	2002Sep24
PASS		
72	r400cl_frustum_lines_06	2002Sep24
PASS		
73	r400cl_frustum_lines_07	2002Sep24
PASS		
74	r400cl_frustum_lines_08	2002Sep24
PASS		
75	r400cl_frustum_lines_09	2002Sep24
PASS		
76	r400cl_frustum_lines_10	2002Sep24
PASS		
77	r400cl_frustum_lines_11	2002Sep24
PASS		
78	r400cl_frustum_lines_12	2002Sep24
PASS		
79	r400cl_frustum_lines_13	2002Sep24
PASS		
80	r400cl_frustum_lines_14	2002Sep24
PASS		
81	r400cl_frustum_lines_15	2002Sep24
PASS		
82	r400cl_frustum_lines_16	2002Sep24
PASS		
83	r400cl_frustum_lines_17	2002Sep24
PASS		
84	r400cl_frustum_lines_18	2002Sep24
PASS		
85	r400cl_frustum_lines_19	2002Sep24
PASS		
86	r400cl_frustum_lines_20	2002Sep24
PASS		
87	r400cl_frustum_lines_21	2002Sep24
PASS		
88	r400cl_frustum_lines_22	2002Sep24
PASS		
89	r400cl_frustum_lines_23	2002Sep24
PASS		
90	r400cl_frustum_lines_24	2002Sep24
PASS		
91	r400cl_gband_04	2002Sep24
PASS		
92	r400cl_gband_tcl_01	2002Sep24

PASS		
93	r400cl_nan_kill_01_bug	2002Sep23
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Sep24
PASS		
95	r400cl_polymode_line_fill_01_bug	2002Sep23
PASS		
96	r400cl_simple_triangle_01	2002Sep24
PASS		
97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep23
PASS		
98	r400cl_ucp_combos_01	2002Sep24
PASS		
99	r400cl_ucp_combos_02	2002Sep24
PASS		
100	r400cl_ucp_combos_03	2002Sep24
PASS		
101	r400cl_ucp_combos_04	2002Sep24
PASS		
102	r400cl_ucp_combos_05	2002Sep24
PASS		
103	r400cl_ucp_combos_06	2002Sep24
PASS		
104	r400cl_ucp_combos_07	2002Sep24
PASS		
105	r400cl_ucp_combos_08	2002Sep24
PASS		
106	r400cl_ucp_combos_09	2002Sep24
PASS		
107	r400cl_ucp_combos_10	2002Sep24
PASS		
108	r400cl_ucp_combos_11	2002Sep24
PASS		
109	r400cl_ucp_combos_12	2002Sep24
PASS		
110	r400cl_ucp_combos_13	2002Sep24
PASS		
111	r400cl_ucp_combos_14	2002Sep24
PASS		
112	r400cl_ucp_combos_15	2002Sep24
PASS		
113	r400cl_ucp_combos_16	2002Sep24
PASS		
114	r400cl_ucp_combos_17	2002Sep24
PASS		
115	r400cl_ucp_combos_18	2002Sep24
PASS		
116	r400cl_ucp_combos_19	2002Sep24

PASS		
117	r400cl_ucp_combos_20	2002Sep24
PASS		
118	r400cl_ucp_combos_21	2002Sep24
PASS		
119	r400cl_ucp_combos_22	2002Sep24
PASS		
120	r400cl_ucp_combos_23	2002Sep24
PASS		
121	r400cl_ucp_combos_24	2002Sep24
PASS		
122	r400cl_ucp_combos_25	2002Sep24
PASS		
123	r400cl_ucp_combos_26	2002Sep24
PASS		
124	r400cl_ucp_combos_27	2002Sep24
PASS		
125	r400cl_ucp_combos_28	2002Sep24
PASS		
126	r400cl_ucp_combos_29	2002Sep24
PASS		
127	r400cl_ucp_combos_30	2002Sep24
PASS		
128	r400cl_ucp_pointlist_01	2002Sep24
PASS		
129	r400cl_vertex_reuse_clip_01	2002Sep24
PASS		
130	r400cl_vtx_kill_01	2002Sep24
PASS		
131	r400cl_vtx_kill_02	2002Sep24
PASS		
132	r400cl_w_eq_0	2002Sep24
PASS		
133	r400rbm_simple_triangle_01	2002Sep23
PASS		
134	r400sanity_persp_projection_01	2002Sep24
PASS		
135	r400sanity_vfd_texture_sample_01	2002Sep24
PASS		
136	r400sc_baryc_01	2002Sep24
PASS		
137	r400sc_bres_cntl_01	2002Sep7
PASS		
138	r400sc_bres_cntl_02	2002Sep24
PASS		
139	r400sc_bres_cntl_03	2002Sep24
PASS		
140	r400sc_bres_cntl_04	2002Sep24

PASS		
141	r400sc_bres_cntl_w2k_01	2002Sep24
PASS		
142	r400sc_bres_cntl_w9x_01	2002Sep24
PASS		
143	r400sc_clip_rect_01	2002Sep24
PASS		
144	r400sc_clip_rect_02	2002Sep24
PASS		
145	r400sc_clip_rect_03	2002Sep24
PASS		
146	r400sc_clip_rect_04	2002Sep24
PASS		
147	r400sc_clip_rect_fc_01	2002Sep24
PASS		
148	r400sc_diamond_exit_01	2002Sep24
PASS		
149	r400sc_diamond_exit_02	2002Sep24
PASS		
150	r400sc_diamond_exit_03	2002Sep24
PASS		
151	r400sc_diamond_exit_04	2002Sep24
INCOMPLETE		
152	r400sc_diamond_exit_05	2002Sep24
INCOMPLETE		
153	r400sc_jss_1x1_primitives_01	2002Sep24
PASS		
154	r400sc_jss_1x2_01	2002Sep24
PASS		
155	r400sc_jss_1x2_02	2002Sep24
PASS		
156	r400sc_jss_1x2_primitives_01	2002Sep24
PASS		
157	r400sc_jss_1x3_01	2002Sep24
INCOMPLETE		
158	r400sc_jss_1x3_02	2002Sep24
FAIL		
159	r400sc_jss_1x3_primitives_01	2002Sep24
PASS		
160	r400sc_jss_1x4_01	2002Sep24
INCOMPLETE		
161	r400sc_jss_1x4_02	2002Sep24
PASS		
162	r400sc_jss_1x4_primitives_01	2002Sep24
PASS		
163	r400sc_jss_2x1_01	2002Sep24
INCOMPLETE		
164	r400sc_jss_2x1_02	2002Sep24

PASS		
165	r400sc_jss_2x1_primtypes_01	2002Sep24
PASS		
166	r400sc_jss_2x2_01	2002Sep24
INCOMPLETE		
167	r400sc_jss_2x2_02	2002Sep24
PASS		
168	r400sc_jss_2x2_primtypes_01	2002Sep24
PASS		
169	r400sc_jss_2x3_01	2002Sep24
PASS		
170	r400sc_jss_2x3_02	2002Sep24
PASS		
171	r400sc_jss_2x3_primtypes_01	2002Sep24
PASS		
172	r400sc_jss_2x4_01	2002Sep24
PASS		
173	r400sc_jss_2x4_02	2002Sep24
PASS		
174	r400sc_jss_2x4_primtypes_01	2002Sep24
PASS		
175	r400sc_jss_3x1_01	2002Sep24
INCOMPLETE		
176	r400sc_jss_3x1_02	2002Sep24
PASS		
177	r400sc_jss_3x1_primtypes_01	2002Sep24
PASS		
178	r400sc_jss_3x2_01	2002Sep24
PASS		
179	r400sc_jss_3x2_02	2002Sep24
INCOMPLETE		
180	r400sc_jss_3x2_primtypes_01	2002Sep24
PASS		
181	r400sc_jss_3x3_01	2002Sep24
PASS		
182	r400sc_jss_3x3_02	2002Sep24
PASS		
183	r400sc_jss_3x3_primtypes_01	2002Sep24
PASS		
184	r400sc_jss_3x4_01	2002Sep24
PASS		
185	r400sc_jss_3x4_02	2002Sep24
PASS		
186	r400sc_jss_3x4_03	2002Sep24
INCOMPLETE		
187	r400sc_jss_3x4_primtypes_01	2002Sep24
PASS		
188	r400sc_jss_4x1_01	2002Sep24

PASS		
189	r400sc_jss_4x1_02	2002Sep24
PASS		
190	r400sc_jss_4x1_primitives_01	2002Sep24
PASS		
191	r400sc_jss_4x2_01	2002Sep24
PASS		
192	r400sc_jss_4x2_02	2002Sep24
PASS		
193	r400sc_jss_4x2_primitives_01	2002Sep24
PASS		
194	r400sc_jss_4x3_01	2002Sep24
PASS		
195	r400sc_jss_4x3_02	2002Sep24
PASS		
196	r400sc_jss_4x3_primitives_01	2002Sep24
PASS		
197	r400sc_jss_4x4_01	2002Sep24
PASS		
198	r400sc_jss_4x4_02	2002Sep24
INCOMPLETE		
199	r400sc_jss_4x4_03	2002Sep24
INCOMPLETE		
200	r400sc_jss_4x4_aa_mask_01	2002Sep24
PASS		
201	r400sc_jss_4x4_aa_mask_02	2002Sep24
INCOMPLETE		
202	r400sc_jss_4x4_fc_01	2002Sep24
INCOMPLETE		
203	r400sc_jss_4x4_fc_02	2002Sep24
PASS		
204	r400sc_jss_4x4_max_dist_01	2002Sep24
INCOMPLETE		
205	r400sc_jss_4x4_primitives_01	2002Sep24
PASS		
206	r400sc_line_dx10_eq_0_01	2002Sep24
PASS		
207	r400sc_line_dx10_ge_0_01	2002Sep24
PASS		
208	r400sc_line_dx10_lt_0_01	2002Sep24
PASS		
209	r400sc_line_dy10_eq_0_01	2002Sep24
INCOMPLETE		
210	r400sc_line_dy10_ge_0_01	2002Sep24
PASS		
211	r400sc_line_dy10_lt_0_01	2002Sep24
PASS		
212	r400sc_line_expand_width_msa_8_01	2002Sep24

INCOMPLETE	
213 r400sc_line_expand_width_msa_8_02	2002Sep24
INCOMPLETE	
214 r400sc_line_expand_width_msa_8_03	2002Sep24
PASS	
215 r400sc_line_jss_3x4_01	2002Sep24
INCOMPLETE	
216 r400sc_line_list_01	2002Sep24
INCOMPLETE	
217 r400sc_line_list_02	2002Sep24
INCOMPLETE	
218 r400sc_line_list_concentric_circle_01	2002Sep24
PASS	
219 r400sc_line_list_concentric_circle_02	2002Sep24
PASS	
220 r400sc_line_list_concentric_circle_03	2002Sep24
PASS	
221 r400sc_line_list_textured_01	2002Sep24
INCOMPLETE	
222 r400sc_line_list_verify_st_01	2002Sep24
PASS	
223 r400sc_line_list_verify_st_01_bug	2002Sep23
PASS	
224 r400sc_line_msa_8_01	2002Sep24
PASS	
225 r400sc_line_msa_8_textured_01	2002Sep24
PASS	
226 r400sc_line_stipple_01	2002Sep24
PASS	
227 r400sc_line_stipple_02	2002Sep24
PASS	
228 r400sc_line_stipple_03	2002Sep24
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229 r400sc_line_stipple_04	2002Sep24
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230 r400sc_line_stipple_05	2002Sep24
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231 r400sc_line_stipple_06	2002Sep24
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232 r400sc_line_stipple_07	2002Sep24
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233 r400sc_line_stipple_08	2002Sep24
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234 r400sc_line_stipple_09	2002Sep24
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235 r400sc_line_stipple_10	2002Sep24
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236 r400sc_line_stipple_11	2002Sep24

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238	r400sc_line_stipple_13	2002Sep24
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239	r400sc_line_stipple_14	2002Sep24
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240	r400sc_line_stipple_fc_08	2002Sep24
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241	r400sc_line_strip_stipple_01	2002Sep24
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242	r400sc_msa_1_01	2002Sep24
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243	r400sc_msa_1_primitives_01	2002Sep24
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250	r400sc_msa_1_rectangle_list_07	2002Sep24
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251	r400sc_msa_1_rectangle_list_08	2002Sep24
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253	r400sc_msa_1_zbuffer_rectangle_list_02	2002Sep24
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254	r400sc_msa_2_primitives_01	2002Sep24
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255	r400sc_msa_2_rectangle_list_01	2002Sep24
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256	r400sc_msa_2_rectangle_list_02	2002Sep24
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261	r400sc_msa_2_rectangle_list_07	2002Sep24
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262	r400sc_msa_2_rectangle_list_08	2002Sep24
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263	r400sc_msa_2_zbuffer_rectangle_list_01	2002Sep24
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264	r400sc_msa_2_zbuffer_rectangle_list_02	2002Sep24
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266	r400sc_msa_3_rectangle_list_01	2002Sep24
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267	r400sc_msa_3_rectangle_list_02	2002Sep24
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274	r400sc_msa_3_zbuffer_rectangle_list_01	2002Sep24
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275	r400sc_msa_3_zbuffer_rectangle_list_02	2002Sep24
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277	r400sc_msa_4_primitives_01	2002Sep24
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278	r400sc_msa_4_rectangle_list_01	2002Sep24
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279	r400sc_msa_4_rectangle_list_02	2002Sep24
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286 r400sc_msaa_4_zbuffer_rectangle_list_01 2002Sep24
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287 r400sc_msaa_4_zbuffer_rectangle_list_02 2002Sep24
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288 r400sc_msaa_6_01 2002Sep24
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289 r400sc_msaa_6_primitives_01 2002Sep24
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290 r400sc_msaa_6_rectangle_list_01 2002Sep24
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291 r400sc_msaa_6_rectangle_list_02 2002Sep24
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296 r400sc_msaa_6_rectangle_list_07 2002Sep24
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297 r400sc_msaa_6_rectangle_list_08 2002Sep24
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299 r400sc_msaa_6_zbuffer_rectangle_list_02 2002Sep24
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300 r400sc_msaa_8_01 2002Sep24
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301 r400sc_msaa_8_02 2002Sep24
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302 r400sc_msaa_8_03 2002Sep24
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303 r400sc_msaa_8_04 2002Sep24
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304 r400sc_msaa_8_05 2002Sep24
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305 r400sc_msaa_8_aa_mask_01 2002Sep24
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306 r400sc_msaa_8_aa_mask_02 2002Sep24
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307 r400sc_msaa_8_aa_mask_fc_02 2002Sep24
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308 r400sc_msaa_8_hos_PNT_cont_cp_qn_complex_01 2002Sep24

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309	r400sc_msa_8_primtypes_01	2002Sep24
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310	r400sc_msa_8_rectangle_list_01	2002Sep24
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311	r400sc_msa_8_rectangle_list_02	2002Sep24
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316	r400sc_msa_8_rectangle_list_07	2002Sep24
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317	r400sc_msa_8_rectangle_list_08	2002Sep24
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318	r400sc_msa_8_zbuffer_rectangle_list_01	2002Sep24
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319	r400sc_msa_8_zbuffer_rectangle_list_02	2002Sep24
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320	r400sc_msa_X_primtypes_01_bug	2002Sep23
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321	r400sc_msa_X_primtypes_02_bug	2002Sep23
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322	r400sc_null_triangles_01	2002Sep24
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323	r400sc_null_triangles_fc_01	2002Sep24
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324	r400sc_pinwheel_01	2002Sep24
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325	r400sc_pinwheel_02	2002Sep24
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326	r400sc_pinwheel_02_bug	2002Sep23
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327	r400sc_point_jss_3x4_01	2002Sep24
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328	r400sc_point_list_01	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
329	r400sc_point_list_02	2002Sep24
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330	r400sc_point_list_03	2002Sep24
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331	r400sc_point_list_04	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
332	r400sc_point_list_05	2002Sep24

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333	r400sc_point_list_06	2002Sep24
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334	r400sc_point_list_06_bug	2002Sep23
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335	r400sc_point_list_06_bug_02	2002Sep23
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337	r400sc_point_list_08	2002Sep24
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338	r400sc_point_list_09	2002Sep24
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339	r400sc_point_msa_8_01	2002Sep24
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340	r400sc_polygon_stipple_01	2002Sep24
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341	r400sc_polymode_tri_fill_01	2002Sep24
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342	r400sc_polymode_tri_fill_01_bug	2002Sep23
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343	r400sc_poly_offset_01	2002Sep24
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344	r400sc_poly_offset_02	2002Sep24
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345	r400sc_poly_offset_fc_01	2002Sep24
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346	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep24
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347	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep24
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348	r400sc_prsp_byc_intrp_ref_pix_03	2002Sep24
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349	r400sc_prsp_byc_intrp_ref_pix_04	2002Sep24
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350	r400sc_prsp_byc_intrp_ref_pix_05	2002Sep24
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351	r400sc_prsp_byc_intrp_ref_pix_06	2002Sep24
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352	r400sc_prsp_byc_intrp_ref_pix_07	2002Sep24
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353	r400sc_prsp_byc_intrp_ref_pix_08	2002Sep24
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354	r400sc_raster_fill_rule_01	2002Sep24
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355	r400sc_raster_fill_rule_02	2002Sep24
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356	r400sc_raster_fill_rule_03	2002Sep24

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357	r400sc_raster_fill_rule_04	2002Sep24
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358	r400sc_raster_fill_rule_05	2002Sep24
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359	r400sc_raster_fill_rule_fc_01	2002Sep24
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360	r400sc_rectangle_list_01	2002Sep24
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361	r400sc_rectangle_list_02	2002Sep24
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362	r400sc_rectangle_list_03	2002Sep24
PASS		
363	r400sc_rectangle_list_04	2002Sep24
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364	r400sc_rectangle_list_05	2002Sep24
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366	r400sc_rectangle_list_07	2002Sep24
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367	r400sc_rectangle_list_08	2002Sep24
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368	r400sc_scissor_rect_01	2002Sep24
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369	r400sc_scissor_rect_02	2002Sep24
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370	r400sc_scissor_rect_03	2002Sep24
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371	r400sc_scissor_rect_04	2002Sep24
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372	r400sc_scissor_rect_05	2002Sep24
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373	r400sc_scissor_rect_fc_01	2002Sep24
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374	r400sc_set_state_01	2002Sep24
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375	r400sc_sp_sample_cntl_01	2002Sep24
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376	r400sc_sp_sample_cntl_02	2002Sep24
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377	r400sc_sp_sample_cntl_03	2002Sep24
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378	r400sc_sp_sample_cntl_04	2002Sep24
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379	r400sc_sp_sample_cntl_05	2002Sep24
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380	r400sc_sp_sample_cntl_06	2002Sep24

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381	r400sc_sp_sample_cntl_07	2002Sep24
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382	r400sc_sp_sample_cntl_08	2002Sep24
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384	r400sc_tri_8textures_01	2002Sep24
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385	r400sc_tri_8textures_02	2002Sep24
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386	r400sc_triangle_stipple_01	2002Sep24
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387	r400sc_tri_walk_start_vertex_01	2002Sep24
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388	r400sc_tri_walk_start_vertex_02	2002Sep24
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389	r400sc_tri_walk_start_vertex_03	2002Sep24
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390	r400sc_tri_walk_start_vertex_04	2002Sep24
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391	r400sc_tri_walk_start_vertex_05	2002Sep24
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392	r400sc_tri_walk_start_vertex_06	2002Sep24
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393	r400sc_tri_walk_start_vertex_07	2002Sep24
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394	r400sc_tri_walk_start_vertex_08	2002Sep24
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395	r400sc_tri_walk_start_vertex_09	2002Sep24
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396	r400sc_tri_walk_start_vertex_10	2002Sep24
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397	r400sc_tri_walk_start_vertex_11	2002Sep24
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398	r400sc_tri_walk_start_vertex_12	2002Sep24
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399	r400sc_tri_walk_start_vertex_13	2002Sep24
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400	r400sc_tri_walk_start_vertex_14	2002Sep24
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401	r400sc_tri_walk_start_vertex_15	2002Sep24
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402	r400sc_tri_walk_start_vertex_16	2002Sep24
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403	r400sc_window_offset_01	2002Sep24
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404	r400sc_window_offset_fc_01	2002Sep24

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406	r400sc_zbuffer_point_list_01	2002Sep24
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407	r400sc_zbuffer_rectangle_list_01	2002Sep24
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408	r400sc_zbuffer_rectangle_list_02	2002Sep24
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409	r400sc_zbuffer_rectangle_list_fc_02	2002Sep24
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410	r400sc_zbuffer_triangle_list_01	2002Sep24
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412	r400su_baryc_test_01	2002Sep24
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413	r400su_baryc_test_02	2002Sep24
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414	r400su_baryc_test_03	2002Sep24
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415	r400su_baryc_test_04	2002Sep24
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416	r400su_clip_baryc_test_01	2002Sep24
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417	r400su_clip_baryc_test_02	2002Sep24
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418	r400su_clip_edgflag_polymode_01	2002Sep24
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419	r400su_clipline_01	2002Sep24
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420	r400su_clippoint_01	2002Sep24
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421	r400su_clip_pointsize_test_01	2002Sep24
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422	r400su_clip_pointttest_01	2002Sep24
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423	r400su_clip_pointttest_02	2002Sep24
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424	r400su_clip_pointttest_03	2002Sep24
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425	r400su_clip_pointttest_04	2002Sep24
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426	r400su_clip_polymode_test_01	2002Sep24
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427	r400su_clipvertexsortingfunctional_01	2002Sep24
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428	r400su_cullingfunctional_01	2002Sep24

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430	r400su_degentri_test_02	2002Sep24
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434	r400su_edgeflags_triangle_01	2002Sep24
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435	r400su_edgeflags_triangle_02	2002Sep24
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436	r400su_geom_sort_01	2002Sep24
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437	r400su_line_end_cap_functional_01	2002Sep24
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438	r400su_line_end_cap_width_functional_02	2002Sep24
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439	r400su_line_test_01	2002Sep24
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440	r400su_line_test_02	2002Sep24
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442	r400su_multi_context_01	2002Sep24
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443	r400su_multi_prim_01	2002Sep24
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444	r400su_multi_prim_02	2002Sep24
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445	r400su_parallel_orientation_all_01	2002Sep24
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446	r400su_parallel_orientation_all_02	2002Sep24
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447	r400su_pc_management_01	2002Sep24
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448	r400su_pc_management_02	2002Sep24
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451	r400su_pointsizepresent_02	2002Sep24
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452	r400su_point_sprite_01	2002Sep24

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453	r400su_point_sprite_02	2002Sep24
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459	r400su_point_sprite_08	2002Sep24
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461	r400su_point_wl_hl6_functional_01	2002Sep24
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463	r400su_polymodefunctional_01	2002Sep24
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476	r400su_polymode_zero_area_triangle_04	2002Sep24

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485	r400su_provoking_vtx_triangle_01	2002Sep24
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488	r400su_rand_tri_01	2002Sep24
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492	r400su_triarea_test_02	2002Sep24
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493	r400su_triarea_test_03	2002Sep24
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494	r400su_triarea_test_04	2002Sep24
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495	r400su_vertexsortingfunctional_01	2002Sep24
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497	r400vgt_auto_index_line_loop_01	2002Sep24
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498	r400vgt_auto_index_line_strip_01	2002Sep24
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506	r400vgt_auto_index_tri_list_01	2002Sep24
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507	r400vgt_auto_index_tri_strip_01	2002Sep24
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508	r400vgt_auto_index_tri_wflags_01	2002Sep24
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509	r400vgt_dma_engine_01	2002Sep24
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510	r400vgt_dma_engine_02	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
511	r400vgt_dma_engine_03	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
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PASS		
513	r400vgt_dma_engine_05	2002Sep24
PASS		
514	r400vgt_dma_engine_06	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
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FAIL	VgtPaClipS_VgtSide.cmp	
516	r400vgt_dma_engine_08	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
517	r400vgt_dma_engine_09	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
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FAIL	VgtPaClipS_VgtSide.cmp	
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521	r400vgt_dma_index_line_strip_01	2002Sep24
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533	r400vgt_dma_swap_indx32_01	2002Sep24
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535	r400vgt_edgeflags_quad_list_01	2002Sep24
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536	r400vgt_edgeflags_quad_strip_01	2002Sep24
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538	r400vgt_edgeflags_triangle_list_01	2002Sep24
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539	r400vgt_edgeflags_triangle_strip_01	2002Sep24
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540	r400vgt_edgeflags_triangle_wflags_01	2002Sep24
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542	r400vgt_ext2int_index_line_list_01	2002Sep24
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543	r400vgt_ext2int_index_line_loop_01	2002Sep24
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544	r400vgt_ext2int_index_line_strip_01	2002Sep24
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545	r400vgt_ext2int_index_points_01	2002Sep24
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547	r400vgt_ext2int_index_quad_list_01	2002Sep24
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550	r400vgt_ext2int_index_triangle_fan_01	2002Sep24
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552	r400vgt_ext2int_index_triangle_strip_01	2002Sep24
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553	r400vgt_ext2int_index_triangle_wflags_01	2002Sep24
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554	r400vgt_hos_auto_index_line_list_01	2002Sep24
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555	r400vgt_hos_auto_index_quad_list_01	2002Sep24
PASS		
556	r400vgt_hos_auto_index_triangle_list_01	2002Sep24
PASS		
557	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep24
PASS		
558	r400vgt_hos_LPatch_01	2002Sep24
PASS		
559	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep24
PASS		
560	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep24
PASS		
561	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep24
PASS		
562	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep24
PASS		
563	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep24
PASS		
564	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep24
PASS		
565	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep24
PASS		
566	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep24
PASS		
567	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep24
PASS		
568	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep24
PASS		
569	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep24
PASS		
570	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep24
PASS		
571	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep24
PASS		
572	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep24

PASS		
573	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep24
PASS		
574	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep24
PASS		
575	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep24
PASS		
576	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep24
PASS		
577	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep24
PASS		
578	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep24
INCOMPLETE		
579	r400vgt_hos_PNT_edge_detection_01	2002Sep24
PASS		
580	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep24
INCOMPLETE		
581	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep24
INCOMPLETE		
582	r400vgt_hos_RPatch_cp_02	2002Sep24
PASS		
583	r400vgt_hos_RPatch_lp_02	2002Sep24
PASS		
584	r400vgt_immed_index_line_list_01	2002Sep24
INCOMPLETE		
585	r400vgt_immed_index_line_loop_01	2002Sep24
PASS		
586	r400vgt_immed_index_line_strip_01	2002Sep24
PASS		
587	r400vgt_immed_index_points_01	2002Sep24
PASS		
588	r400vgt_immed_index_polygon_01	2002Sep24
PASS		
589	r400vgt_immed_index_primitives_01	2002Sep24
PASS		
590	r400vgt_immed_index_quad_list_01	2002Sep24
PASS		
591	r400vgt_immed_index_quad_strip_01	2002Sep24
INCOMPLETE		
592	r400vgt_immed_index_rectangle_list_01	2002Sep24
INCOMPLETE		
593	r400vgt_immed_index_tri_fan_01	2002Sep24
PASS		
594	r400vgt_immed_index_tri_list_01	2002Sep24
PASS		
595	r400vgt_immed_index_tri_strip_01	2002Sep24
PASS		
596	r400vgt_immed_index_tri_wflags_01	2002Sep24

PASS		
597	r400vgt_index_dealloc_line_list_01	2002Sep24
INCOMPLETE		
598	r400vgt_index_dealloc_points_01	2002Sep24
INCOMPLETE		
599	r400vgt_index_dealloc_triangle_list_01	2002Sep24
INCOMPLETE		
600	r400vgt_index_min_max_01	2002Sep24
INCOMPLETE		
601	r400vgt_index_min_max_02	2002Sep24
PASS		
602	r400vgt_index_offset_01	2002Sep24
PASS		
603	r400vgt_index_offset_02	2002Sep24
INCOMPLETE		
604	r400vgt_index_offset_03	2002Sep24
PASS		
605	r400vgt_index_size_01	2002Sep24
PASS		
606	r400vgt_index_source_switch_01	2002Sep24
PASS		
607	r400vgt_line_list_01	2002Sep24
PASS		
608	r400vgt_line_list_02	2002Sep24
PASS		
609	r400vgt_line_loop_01	2002Sep24
PASS		
610	r400vgt_line_loop_02	2002Sep24
PASS		
611	r400vgt_line_strip_01	2002Sep24
PASS		
612	r400vgt_line_strip_02	2002Sep24
PASS		
613	r400vgt_multi_context_01	2002Sep24
PASS		
614	r400vgt_multi_context_02	2002Sep24
PASS		
615	r400vgt_multi_context_03	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
616	r400vgt_multi_context_04	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
617	r400vgt_multi_context_05	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
618	r400vgt_multi_context_06	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
619	r400vgt_multi_context_07	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
620	r400vgt_multi_context_08	2002Sep24

PASS		
621	r400vgt_multi_context_09	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
622	r400vgt_multi_context_10	2002Sep24
PASS		
623	r400vgt_multi_context_11	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
624	r400vgt_multi_context_12	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
625	r400vgt_multi_prim_reset_index_all_01	2002Sep24
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
626	r400vgt_pass_thru_all_prims_01	2002Sep24
PASS		
627	r400vgt_point_list_01	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
628	r400vgt_point_list_02	2002Sep24
PASS		
629	r400vgt_polygon_01	2002Sep24
PASS		
630	r400vgt_polygon_02	2002Sep24
PASS		
631	r400vgt_provoking_vtx_all_01	2002Sep24
PASS		
632	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep24
PASS		
633	r400vgt_provoking_vtx_polygon_01	2002Sep24
PASS		
634	r400vgt_provoking_vtx_quad_list_01	2002Sep24
PASS		
635	r400vgt_provoking_vtx_quad_strip_01	2002Sep24
PASS		
636	r400vgt_provoking_vtx_tri_fan_01	2002Sep24
PASS		
637	r400vgt_provoking_vtx_tri_strip_01	2002Sep24
PASS		
638	r400vgt_quad_list_01	2002Sep24
PASS		
639	r400vgt_quad_list_02	2002Sep24
PASS		
640	r400vgt_quad_strip_01	2002Sep24
PASS		
641	r400vgt_quad_strip_02	2002Sep24
PASS		
642	r400vgt_rectangle_list_01	2002Sep24
PASS		
643	r400vgt_rectangle_list_02	2002Sep24
PASS		
644	r400vgt_reuse_depth_line_list_01	2002Sep24

PASS		
645	r400vgt_reuse_depth_point_list_01	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
646	r400vgt_reuse_depth_triangle_list_01	2002Sep24
PASS		
647	r400vgt_reuse_index_line_list_01	2002Sep24
PASS		
648	r400vgt_reuse_index_point_list_01	2002Sep24
FAIL	VgtPaClipS_VgtSide.cmp	
649	r400vgt_reuse_index_triangle_list_01	2002Sep24
PASS		
650	r400vgt_reuse_index_triangle_list_02	2002Sep24
PASS		
651	r400vgt_reuse_index_triangle_list_03	2002Sep24
PASS		
652	r400vgt_suppress_eop_01	2002Sep24
PASS		
653	r400vgt_suppress_eop_02	2002Sep24
PASS		
654	r400vgt_suppress_eop_03	2002Sep24
PASS		
655	r400vgt_suppress_eop_04	2002Sep24
PASS		
656	r400vgt_triangle_fan_01	2002Sep24
PASS		
657	r400vgt_triangle_fan_02	2002Sep24
PASS		
658	r400vgt_triangle_list_01	2002Sep24
PASS		
659	r400vgt_triangle_list_02	2002Sep24
PASS		
660	r400vgt_triangle_strip_01	2002Sep24
PASS		
661	r400vgt_triangle_strip_02	2002Sep24
PASS		
662	r400vgt_triangle_wflags_01	2002Sep24
PASS		
663	r400vgt_triangle_wflags_02	2002Sep24
PASS		
664	r400vgt_viz_query_01	2002Sep24
PASS		
665	r400vgt_vtx_vector_packing_01	2002Sep24
PASS		
666	r400vte_coverage_01	2002Sep24
PASS		
667	r400vte_simple_point_01	2002Sep24
PASS		
668	r400vte_simple_triangle_01	2002Sep24

PASS
669 r400vte_w0_fmt_01 2002Sep24
PASS
670 r400vte_w0_fmt_03 2002Sep24
PASS
671 r400vte_xy_fmt_01 2002Sep24
PASS
672 r400vte_xy_fmt_mk_01_bug 2002Sep23
PASS
673 r400vte_xyz_scale_01 2002Sep24
PASS
674 r400vte_xyz_scale_02 2002Sep24
PASS
675 r400vte_z_fmt_01 2002Sep24
PASS

=====
Tests_Run_count = 675 Pass_count = 614 Fail_count = 24 Incomplete_count = 37
=====

=====
Regression Report For VGT - Project = r400 Created on Sat Oct 5 21:34:29 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

=====
1 cp_dma_2desc 2002Oct5
PASS
2 cp_dma_interrupt 2002Oct5
PASS
3 cp_dma_m2m_01 2002Oct5
PASS
4 cp_dma_m2m_02 2002Oct5
PASS
5 cp_dma_m2m_03 2002Oct5
PASS
6 cp_dma_m2m_04 2002Oct5
PASS
7 cp_dma_m2r_01 2002Oct5
PASS
8 cp_dma_m2r_02 2002Oct5
PASS
9 cp_dma_m2r_03 2002Oct5
PASS
10 cp_dma_m2r_04 2002Oct5
PASS
11 cp_dma_m2r_r2m 2002Oct5
PASS
12 cp_dma_pio_simple 2002Oct5
PASS
13 cp_dma_pio_stress 2002Oct5
PASS
14 cp_dma_piobm_stress 2002Oct5
PASS
15 cp_dma_r2m_01 2002Oct5
PASS
16 cp_dma_r2m_02 2002Oct5
PASS
17 cp_dma_r2m_03 2002Oct5
PASS
18 cp_dma_r2m_04 2002Oct5
PASS
19 cp_dma_r2r_01 2002Oct5
PASS
20 cp_dma_r2r_02 2002Oct5

PASS		
21	cp_dma_r2r_03	20020ct5
PASS		
22	cp_dma_r2r_r2m	20020ct5
PASS		
23	cp_dma_r2r_r2m_m2m	20020ct5
PASS		
24	cp_dma_r2r_r2m_m2m_r2m	20020ct5
PASS		
25	cp_dma_simple	20020ct5
PASS		
26	cp_e2_hostdata_blt_pntr_8888	20020ct5
PASS		
27	cp_e2_one_blit	20020ct5
PASS		
28	cp_e2_one_line	20020ct5
PASS		
29	cp_e2blit_brush_mt_ropcc	20020ct5
PASS		
30	cp_e2paint_565	20020ct5
PASS		
31	cp_e2paint_multi	20020ct5
PASS		
32	cp_e2polyscanlines_brush	20020ct5
PASS		
33	cp_e2polyscanlines_brush_mt	20020ct5
PASS		
34	cp_e2smalltext_jcl	20020ct5
PASS		
35	cp_im_load_indirect	20020ct5
PASS		
36	cp_push_aper_indirect1	20020ct5
PASS		
37	cp_push_aper_primary	20020ct5
PASS		
38	cp_queue_avail_01	20020ct5
PASS		
39	cp_queue_avail_02	20020ct5
PASS		
40	cp_queue_avail_03	20020ct5
PASS		
41	cp_queue_avail_04	20020ct5
PASS		
42	cp_queue_avail_05	20020ct5
PASS		
43	cp_queue_avail_06	20020ct5
PASS		
44	cp_queue_avail_07	20020ct5

PASS		
45	cp_queue_avail_08	2002Oct5
PASS		
46	cp_queue_avail_09	2002Oct5
PASS		
47	cp_queue_avail_10	2002Oct5
PASS		
48	cp_queue_avail_11	2002Oct5
PASS		
49	cp_queue_avail_12	2002Oct5
PASS		
50	cp_simple_triangle	2002Oct5
PASS		
51	milestone_event_flush_tri1	2002Oct3
PASS		
52	milestone_event_flush_tri2	2002Oct3
PASS		
53	milestone_event_flush_tri3	2002Oct3
PASS		
54	milestone_event_flush_tri4	2002Oct3
PASS		
55	milestone_event_flush_tri5	2002Oct3
PASS		
56	milestone_event_flush_tri6	2002Oct3
PASS		
57	milestone_event_flush_tri7	2002Oct3
PASS		
58	milestone_event_flush_tri8	2002Oct3
PASS		
59	milestone_event_flush_tri9	2002Oct3
PASS		
60	milestone_event_randomized	2002Oct3
PASS		
61	milestone_event_standard	2002Oct3
PASS		
62	milestone_event_switch_flush_tri1	2002Oct3
PASS		
63	milestone_event_switch_flush_tri2	2002Oct3
PASS		
64	milestone_event_switch_flush_tri3	2002Oct3
PASS		
65	milestone_event_switch_flush_tri4	2002Oct3
PASS		
66	milestone_event_switch_flush_tri5	2002Oct3
PASS		
67	milestone_event_switch_flush_tri6	2002Oct3
PASS		
68	milestone_event_switch_flush_tri7	2002Oct3

PASS		
69	milestone_event_switch_flush_tri8	2002Oct3
PASS		
70	milestone_event_switch_flush_tri9	2002Oct3
PASS		
71	milestone_event_switch_tri2	2002Oct3
PASS		
72	milestone_event_switch_tri3	2002Oct3
PASS		
73	milestone_event_switch_tri4	2002Oct3
PASS		
74	milestone_event_switch_tri5	2002Oct3
PASS		
75	milestone_event_switch_tri6	2002Oct3
PASS		
76	milestone_event_switch_tri7	2002Oct3
PASS		
77	milestone_event_switch_tri8	2002Oct3
PASS		
78	milestone_event_switch_tri9	2002Oct3
PASS		
79	milestone_event_tri2	2002Oct3
PASS		
80	milestone_event_tri3	2002Oct3
PASS		
81	milestone_event_tri4	2002Oct3
PASS		
82	milestone_event_tri5	2002Oct3
PASS		
83	milestone_event_tri6	2002Oct3
PASS		
84	milestone_event_tri7	2002Oct3
PASS		
85	milestone_event_tri8	2002Oct3
PASS		
86	milestone_event_tri9	2002Oct3
PASS		
87	milestone_tri_random_rect	2002Oct3
PASS		
88	milestone_tri_random_tri	2002Oct3
PASS		
89	milestone_tri_randomized	2002Oct3
PASS		
90	milestone_tri_standard	2002Oct3
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91	milestone_tri_tril28_pix4	2002Oct3
PASS		
92	milestone_tri_tril_pix1	2002Oct3

PASS		
93	milestone_tri_tril_pix2	2002Oct3
PASS		
94	milestone_tri_tril_pix3	2002Oct3
PASS		
95	milestone_tri_tril_pix4	2002Oct3
PASS		
96	milestone_tri_tri24x24	2002Oct3
PASS		
97	milestone_tri_tri28x28	2002Oct3
PASS		
98	milestone_tri_tri2_pix1	2002Oct3
PASS		
99	milestone_tri_tri2_pix16	2002Oct3
PASS		
100	milestone_tri_tri2_pix4	2002Oct3
PASS		
101	milestone_tri_tri32_pix1	2002Oct3
PASS		
102	milestone_tri_tri32_pix4	2002Oct3
PASS		
103	milestone_tri_tri32x32	2002Oct3
PASS		
104	milestone_tri_tri36x36	2002Oct3
PASS		
105	milestone_tri_tri40x40	2002Oct3
PASS		
106	milestone_tri_tri44x44	2002Oct3
PASS		
107	milestone_tri_tri48x48	2002Oct3
PASS		
108	milestone_tri_tri52x52	2002Oct3
PASS		
109	milestone_tri_tri56x56	2002Oct3
PASS		
110	milestone_tri_tri60x60	2002Oct3
PASS		
111	milestone_tri_tri64x64	2002Oct3
PASS		
112	milestone_tri_tri8_pix4	2002Oct3
PASS		
113	primlib_1st_tri_june15	2002Oct5
PASS		
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct5
PASS		
115	primlib_gouraud_triangles_2_draw_passes	2002Oct5
PASS		
116	primlib_parameterized_simple_triangle	2002Oct5

PASS		
117	primlib_template_simple_triangle	2002Oct5
PASS		
118	primlib_tex_tri	2002Oct5
PASS		
119	r400cl_bary_texture_08_bug	2002Sep21
PASS		
120	r400cl_clip_10_verts_01	2002Oct5
PASS		
121	r400cl_clip_disable_01	2002Oct5
PASS		
122	r400cl_clip_space_dx_ogl_01	2002Oct5
PASS		
123	r400cl_clip_ucp_6bits_01	2002Oct5
PASS		
124	r400cl_cull_only_ena_01	2002Oct5
PASS		
125	r400cl_edgeflags_01	2002Oct5
PASS		
126	r400cl_edgeflags_01_bug	2002Oct5
PASS		
127	r400cl_edgeflags_frustum_bottom_01	2002Oct5
PASS		
128	r400cl_edgeflags_frustum_far_01	2002Oct5
PASS		
129	r400cl_edgeflags_frustum_left_01	2002Oct5
PASS		
130	r400cl_edgeflags_frustum_near_01	2002Oct5
PASS		
131	r400cl_edgeflags_frustum_right_01	2002Oct5
PASS		
132	r400cl_edgeflags_frustum_top_01	2002Oct5
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133	r400cl_edgeflags_gband_01	2002Oct5
PASS		
134	r400cl_edgeflags_gband_bottom_01	2002Oct5
PASS		
135	r400cl_edgeflags_gband_left_01	2002Oct5
PASS		
136	r400cl_edgeflags_gband_right_01	2002Oct5
PASS		
137	r400cl_edgeflags_gband_top_01	2002Oct5
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138	r400cl_frustum_01	2002Oct5
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139	r400cl_frustum_02	2002Oct5
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140	r400cl_frustum_03	2002Oct5

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141	r400cl_frustum_04	20020ct5
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142	r400cl_frustum_05	20020ct5
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143	r400cl_frustum_06	20020ct5
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144	r400cl_frustum_07	20020ct5
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145	r400cl_frustum_08	20020ct5
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146	r400cl_frustum_09	20020ct5
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147	r400cl_frustum_10	20020ct5
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148	r400cl_frustum_11	20020ct5
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151	r400cl_frustum_14	20020ct5
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152	r400cl_frustum_15	20020ct5
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153	r400cl_frustum_16	20020ct5
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154	r400cl_frustum_17	20020ct5
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155	r400cl_frustum_18	20020ct5
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156	r400cl_frustum_19	20020ct5
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157	r400cl_frustum_20	20020ct5
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158	r400cl_frustum_21	20020ct5
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159	r400cl_frustum_22	20020ct5
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160	r400cl_frustum_23	20020ct5
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161	r400cl_frustum_24	20020ct5
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162	r400cl_frustum_25	20020ct5
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163	r400cl_frustum_26	20020ct5
PASS		
164	r400cl_frustum_27	20020ct5

PASS		
165	r400cl_frustum_28	2002Oct5
PASS		
166	r400cl_frustum_all_vols_lines	2002Oct5
PASS		
167	r400cl_frustum_all_vols_tris	2002Oct5
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168	r400cl_frustum_lines_01	2002Oct5
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169	r400cl_frustum_lines_02	2002Oct5
PASS		
170	r400cl_frustum_lines_03	2002Oct5
PASS		
171	r400cl_frustum_lines_04	2002Oct5
PASS		
172	r400cl_frustum_lines_05	2002Oct5
PASS		
173	r400cl_frustum_lines_06	2002Oct5
PASS		
174	r400cl_frustum_lines_07	2002Oct5
PASS		
175	r400cl_frustum_lines_08	2002Oct5
PASS		
176	r400cl_frustum_lines_09	2002Oct5
PASS		
177	r400cl_frustum_lines_10	2002Oct5
PASS		
178	r400cl_frustum_lines_11	2002Oct5
PASS		
179	r400cl_frustum_lines_12	2002Oct5
PASS		
180	r400cl_frustum_lines_13	2002Oct5
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181	r400cl_frustum_lines_14	2002Oct5
PASS		
182	r400cl_frustum_lines_15	2002Oct5
PASS		
183	r400cl_frustum_lines_16	2002Oct5
PASS		
184	r400cl_frustum_lines_17	2002Oct5
PASS		
185	r400cl_frustum_lines_18	2002Oct5
PASS		
186	r400cl_frustum_lines_19	2002Oct5
PASS		
187	r400cl_frustum_lines_20	2002Oct5
PASS		
188	r400cl_frustum_lines_21	2002Oct5

PASS		
189	r400cl_frustum_lines_22	2002Oct5
PASS		
190	r400cl_frustum_lines_23	2002Oct5
PASS		
191	r400cl_frustum_lines_24	2002Oct5
PASS		
192	r400cl_gband_04	2002Oct5
PASS		
193	r400cl_gband_tcl_01	2002Oct5
PASS		
194	r400cl_nan_kill_01_bug	2002Oct5
PASS		
195	r400cl_point_ucp_combos_01	2002Oct5
INCOMPLETE		
196	r400cl_pointlist_vertex_state_ucp_01	2002Oct5
PASS		
197	r400cl_polymode_line_fill_01_bug	2002Oct5
PASS		
198	r400cl_simple_triangle_01	2002Oct5
PASS		
199	r400cl_tri_polymode_line_stipple_ucp_combos_01	2002Oct5
PASS		
200	r400cl_tri_polymode_line_ucp_combos_01	2002Oct5
PASS		
201	r400cl_triangle_polymode_line_stippled_01	2002Oct5
PASS		
202	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct5
PASS		
203	r400cl_ucp_combos_01	2002Oct5
PASS		
204	r400cl_ucp_combos_02	2002Oct5
PASS		
205	r400cl_ucp_combos_03	2002Oct5
PASS		
206	r400cl_ucp_combos_04	2002Oct5
PASS		
207	r400cl_ucp_combos_05	2002Oct5
PASS		
208	r400cl_ucp_combos_06	2002Oct5
PASS		
209	r400cl_ucp_combos_07	2002Oct5
PASS		
210	r400cl_ucp_combos_08	2002Oct5
PASS		
211	r400cl_ucp_combos_09	2002Oct5
PASS		
212	r400cl_ucp_combos_10	2002Oct5

PASS		
213	r400cl_ucp_combos_11	2002Oct5
PASS		
214	r400cl_ucp_combos_12	2002Oct5
PASS		
215	r400cl_ucp_combos_13	2002Oct5
PASS		
216	r400cl_ucp_combos_14	2002Oct5
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234	r400cl_vertex_reuse_clip_01	2002Oct5
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235	r400cl_vtx_kill_01	2002Oct5
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236	r400cl_vtx_kill_02	2002Oct5

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238	r400rbm_simple_triangle_01	2002Oct5
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239	r400sanity_persp_projection_01	2002Oct5
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240	r400sanity_vfd_texture_sample_01	2002Oct5
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247	r400sc_bres_cntl_w9x_01	2002Oct5
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324	r400sc_line_list_concentric_circle_02	2002Oct5
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325	r400sc_line_list_concentric_circle_03	2002Oct5
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355	r400sc_line_strip_stipple_01	2002Oct5
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435	r400sc_msa_X_primitives_02_bug	2002Oct5
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546	r400su_clip_baryc_test_01	2002Oct5
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547	r400su_clip_baryc_test_02	2002Oct5
PASS		
548	r400su_clip_edgeflag_polymode_01	2002Oct5

PASS		
549	r400su_clip_pointsize_test_01	2002Oct5
PASS		
550	r400su_clip_pointtest_01	2002Oct5
PASS		
551	r400su_clip_pointtest_02	2002Oct5
PASS		
552	r400su_clip_pointtest_03	2002Oct5
PASS		
553	r400su_clip_pointtest_04	2002Oct5
PASS		
554	r400su_clip_polymode_test_01	2002Oct5
PASS		
555	r400su_clipline_01	2002Oct5
PASS		
556	r400su_clippoint_01	2002Oct5
PASS		
557	r400su_clipvertexsortingfunctional_01	2002Oct5
PASS		
558	r400su_cullingfunctional_01	2002Oct5
PASS		
559	r400su_degentri_test_01	2002Oct5
PASS		
560	r400su_degentri_test_02	2002Oct5
PASS		
561	r400su_degentri_test_03	2002Oct5
PASS		
562	r400su_edge_flag_01	2002Oct5
PASS		
563	r400su_edge_flag_02	2002Oct5
PASS		
564	r400su_edgeflags_triangle_01	2002Oct5
PASS		
565	r400su_edgeflags_triangle_02	2002Oct5
PASS		
566	r400su_geom_sort_01	2002Oct5
PASS		
567	r400su_line_end_cap_functional_01	2002Oct5
PASS		
568	r400su_line_end_cap_width_functional_02	2002Oct5
PASS		
569	r400su_line_orientation_01	2002Oct5
PASS		
570	r400su_line_orientation_02	2002Oct5
PASS		
571	r400su_line_orientation_dx01_01	2002Oct5
PASS		
572	r400su_line_orientation_dx01_02	2002Oct5

PASS		
573	r400su_line_orientation_dy01_01	2002Oct5
PASS		
574	r400su_line_orientation_dy01_02	2002Oct5
PASS		
575	r400su_line_test_01	2002Oct5
PASS		
576	r400su_line_test_02	2002Oct5
PASS		
577	r400su_longstrip_01	2002Oct5
PASS		
578	r400su_multi_context_01	2002Oct5
PASS		
579	r400su_multi_prim_01	2002Oct5
PASS		
580	r400su_multi_prim_02	2002Oct5
PASS		
581	r400su_parallel_orientation_all_01	2002Oct5
PASS		
582	r400su_parallel_orientation_all_02	2002Oct5
PASS		
583	r400su_pc_management_01	2002Oct5
PASS		
584	r400su_pc_management_02	2002Oct5
PASS		
585	r400su_pc_management_03	2002Oct5
PASS		
586	r400su_point_sprite_01	2002Oct5
PASS		
587	r400su_point_sprite_02	2002Oct5
PASS		
588	r400su_point_sprite_03	2002Oct5
PASS		
589	r400su_point_sprite_04	2002Oct5
PASS		
590	r400su_point_sprite_05	2002Oct5
PASS		
591	r400su_point_sprite_06	2002Oct5
PASS		
592	r400su_point_sprite_07	2002Oct5
PASS		
593	r400su_point_sprite_08	2002Oct5
PASS		
594	r400su_point_wl6_h1_functional_01	2002Oct5
PASS		
595	r400su_point_wl_h16_functional_01	2002Oct5
PASS		
596	r400su_pointsizepresent_01	2002Oct5

PASS		
597	r400su_pointsizepresent_02	2002Oct5
PASS		
598	r400su_polymode_lines_degen_triangle_01	2002Oct5
PASS		
599	r400su_polymode_lines_degen_triangle_02	2002Oct5
PASS		
600	r400su_polymode_lines_zero_area_triangle_01	2002Oct5
PASS		
601	r400su_polymode_lines_zero_area_triangle_02	2002Oct5
PASS		
602	r400su_polymode_points_degen_triangle_01	2002Oct5
PASS		
603	r400su_polymode_points_degen_triangle_02	2002Oct5
PASS		
604	r400su_polymode_points_zero_area_triangle_01	2002Oct5
PASS		
605	r400su_polymode_points_zero_area_triangle_02	2002Oct5
PASS		
606	r400su_polymode_rectangle_01	2002Oct5
PASS		
607	r400su_polymode_zero_area_triangle_01	2002Oct5
PASS		
608	r400su_polymode_zero_area_triangle_02	2002Oct5
PASS		
609	r400su_polymode_zero_area_triangle_03	2002Oct5
PASS		
610	r400su_polymode_zero_area_triangle_04	2002Oct5
PASS		
611	r400su_polymodeculling_01	2002Oct5
PASS		
612	r400su_polymodefunctional_01	2002Oct5
PASS		
613	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct5
PASS		
614	r400su_provoking_vtx_edgeflags_triangle_02	2002Oct5
PASS		
615	r400su_provoking_vtx_edgeflags_triangle_03	2002Oct5
PASS		
616	r400su_provoking_vtx_edgeflags_triangle_04	2002Oct5
PASS		
617	r400su_provoking_vtx_line_01	2002Oct5
PASS		
618	r400su_provoking_vtx_point_01	2002Oct5
PASS		
619	r400su_provoking_vtx_polymode_rectangle_01	2002Oct5
PASS		
620	r400su_provoking_vtx_rectangle_01	2002Oct5

PASS		
621	r400su_provoking_vtx_triangle_01	2002Oct5
PASS		
622	r400su_rand_line_01	2002Oct5
PASS		
623	r400su_rand_point_01	2002Oct5
PASS		
624	r400su_rand_tri_01	2002Oct5
PASS		
625	r400su_rectangle_list_01	2002Oct5
PASS		
626	r400su_sliver_01	2002Oct5
PASS		
627	r400su_triarea_test_01	2002Oct5
PASS		
628	r400su_triarea_test_02	2002Oct5
PASS		
629	r400su_triarea_test_03	2002Oct5
PASS		
630	r400su_triarea_test_04	2002Oct5
PASS		
631	r400su_vertexsortingfunctional_01	2002Oct5
PASS		
632	r400su_w_grad_test_01	2002Oct5
PASS		
633	r400su_w_grad_test_02	2002Oct5
PASS		
634	r400su_w_grad_test_03	2002Oct5
PASS		
635	r400su_z_grad_test_01	2002Oct5
PASS		
636	r400su_z_grad_test_02	2002Oct5
PASS		
637	r400su_z_grad_test_03	2002Oct5
PASS		
638	r400vgt_auto_index_line_list_01	2002Oct5
PASS		
639	r400vgt_auto_index_line_loop_01	2002Oct5
PASS		
640	r400vgt_auto_index_line_strip_01	2002Oct5
PASS		
641	r400vgt_auto_index_points_01	2002Oct5
PASS		
642	r400vgt_auto_index_polygon_01	2002Oct5
PASS		
643	r400vgt_auto_index_primitives_01	2002Oct5
PASS		
644	r400vgt_auto_index_quad_list_01	2002Oct5

PASS		
645	r400vgt_auto_index_quad_strip_01	2002Oct5
PASS		
646	r400vgt_auto_index_rectangle_list_01	2002Oct5
PASS		
647	r400vgt_auto_index_tri_fan_01	2002Oct5
PASS		
648	r400vgt_auto_index_tri_list_01	2002Oct5
PASS		
649	r400vgt_auto_index_tri_strip_01	2002Oct5
PASS		
650	r400vgt_auto_index_tri_wflags_01	2002Oct5
PASS		
651	r400vgt_dma_engine_01	2002Oct5
PASS		
652	r400vgt_dma_engine_02	2002Oct5
PASS		
653	r400vgt_dma_engine_03	2002Oct5
PASS		
654	r400vgt_dma_engine_04	2002Oct5
PASS		
655	r400vgt_dma_engine_05	2002Oct5
PASS		
656	r400vgt_dma_engine_06	2002Oct5
PASS		
657	r400vgt_dma_engine_07	2002Oct5
PASS		
658	r400vgt_dma_engine_08	2002Oct5
PASS		
659	r400vgt_dma_engine_09	2002Oct5
PASS		
660	r400vgt_dma_engine_10	2002Oct5
PASS		
661	r400vgt_dma_index_line_list_01	2002Oct5
PASS		
662	r400vgt_dma_index_line_loop_01	2002Oct5
PASS		
663	r400vgt_dma_index_line_strip_01	2002Oct5
PASS		
664	r400vgt_dma_index_points_01	2002Oct5
PASS		
665	r400vgt_dma_index_polygon_01	2002Oct5
PASS		
666	r400vgt_dma_index_primtypes_01	2002Oct5
PASS		
667	r400vgt_dma_index_quad_list_01	2002Oct5
PASS		
668	r400vgt_dma_index_quad_strip_01	2002Oct5

PASS		
669	r400vgt_dma_index_rectangle_list_01	2002Oct5
PASS		
670	r400vgt_dma_index_tri_fan_01	2002Oct5
PASS		
671	r400vgt_dma_index_tri_list_01	2002Oct5
PASS		
672	r400vgt_dma_index_tri_strip_01	2002Oct5
PASS		
673	r400vgt_dma_index_tri_wflags_01	2002Oct5
PASS		
674	r400vgt_dma_swap_indx16_01	2002Oct5
PASS		
675	r400vgt_dma_swap_indx32_01	2002Oct5
PASS		
676	r400vgt_edgeflags_polygon_01	2002Oct5
PASS		
677	r400vgt_edgeflags_quad_list_01	2002Oct5
PASS		
678	r400vgt_edgeflags_quad_strip_01	2002Oct5
PASS		
679	r400vgt_edgeflags_triangle_fan_01	2002Oct5
PASS		
680	r400vgt_edgeflags_triangle_list_01	2002Oct5
PASS		
681	r400vgt_edgeflags_triangle_strip_01	2002Oct5
PASS		
682	r400vgt_edgeflags_triangle_wflags_01	2002Oct5
PASS		
683	r400vgt_event_handling_01	2002Sep26
PASS		
684	r400vgt_ext2int_index_line_list_01	2002Oct5
PASS		
685	r400vgt_ext2int_index_line_loop_01	2002Oct5
PASS		
686	r400vgt_ext2int_index_line_strip_01	2002Oct5
PASS		
687	r400vgt_ext2int_index_points_01	2002Oct5
PASS		
688	r400vgt_ext2int_index_polygon_01	2002Oct5
PASS		
689	r400vgt_ext2int_index_quad_list_01	2002Oct5
PASS		
690	r400vgt_ext2int_index_quad_strip_01	2002Oct5
PASS		
691	r400vgt_ext2int_index_rectangle_list_01	2002Oct5
PASS		
692	r400vgt_ext2int_index_triangle_fan_01	2002Oct5

PASS		
693	r400vgt_ext2int_index_triangle_list_01	2002Oct5
PASS		
694	r400vgt_ext2int_index_triangle_strip_01	2002Oct5
PASS		
695	r400vgt_ext2int_index_triangle_wflags_01	2002Oct5
PASS		
696	r400vgt_hos_LPatch_01	2002Oct5
PASS		
697	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Oct5
PASS		
698	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct5
PASS		
699	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct5
PASS		
700	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Oct5
PASS		
701	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Oct5
PASS		
702	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct5
PASS		
703	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct5
PASS		
704	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct5
PASS		
705	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct5
PASS		
706	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct5
PASS		
707	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct5
PASS		
708	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct5
PASS		
709	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct5
PASS		
710	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct5
PASS		
711	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct5
PASS		
712	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Oct5
PASS		
713	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct5
PASS		
714	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Oct5
PASS		
715	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Oct5
PASS		
716	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Oct5

PASS		
717	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct5
PASS		
718	r400vgt_hos_PNT_edge_detection_01	2002Oct5
PASS		
719	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct5
PASS		
720	r400vgt_hos_RPatch_cp_02	2002Oct5
PASS		
721	r400vgt_hos_RPatch_lp_02	2002Oct5
PASS		
722	r400vgt_hos_TPatch_01	2002Oct5
PASS		
723	r400vgt_hos_TPatch_02	2002Oct5
PASS		
724	r400vgt_hos_auto_index_line_list_01	2002Oct5
PASS		
725	r400vgt_hos_auto_index_quad_list_01	2002Oct5
PASS		
726	r400vgt_hos_auto_index_triangle_list_01	2002Oct5
PASS		
727	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Oct5
PASS		
728	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct5
PASS		
729	r400vgt_immed_index_line_list_01	2002Oct5
PASS		
730	r400vgt_immed_index_line_loop_01	2002Oct5
PASS		
731	r400vgt_immed_index_line_strip_01	2002Oct5
PASS		
732	r400vgt_immed_index_points_01	2002Oct5
PASS		
733	r400vgt_immed_index_polygon_01	2002Oct5
PASS		
734	r400vgt_immed_index_primitives_01	2002Oct5
PASS		
735	r400vgt_immed_index_quad_list_01	2002Oct5
PASS		
736	r400vgt_immed_index_quad_strip_01	2002Oct5
PASS		
737	r400vgt_immed_index_rectangle_list_01	2002Oct5
PASS		
738	r400vgt_immed_index_tri_fan_01	2002Oct5
PASS		
739	r400vgt_immed_index_tri_list_01	2002Oct5
PASS		
740	r400vgt_immed_index_tri_strip_01	2002Oct5

PASS		
741	r400vgt_immed_index_tri_wflags_01	2002Oct5
PASS		
742	r400vgt_index_dealloc_line_list_01	2002Oct5
PASS		
743	r400vgt_index_dealloc_points_01	2002Oct5
PASS		
744	r400vgt_index_dealloc_triangle_list_01	2002Oct5
PASS		
745	r400vgt_index_min_max_01	2002Oct5
PASS		
746	r400vgt_index_min_max_02	2002Oct5
PASS		
747	r400vgt_index_offset_01	2002Oct5
PASS		
748	r400vgt_index_offset_02	2002Oct5
PASS		
749	r400vgt_index_offset_03	2002Oct5
PASS		
750	r400vgt_index_offset_04	2002Oct5
PASS		
751	r400vgt_index_offset_05	2002Oct5
PASS		
752	r400vgt_index_offset_06	2002Oct5
PASS		
753	r400vgt_index_offset_07	2002Oct5
PASS		
754	r400vgt_index_offset_08	2002Oct5
PASS		
755	r400vgt_index_size_01	2002Oct5
PASS		
756	r400vgt_index_source_switch_01	2002Oct5
PASS		
757	r400vgt_line_list_01	2002Oct5
PASS		
758	r400vgt_line_list_02	2002Oct5
PASS		
759	r400vgt_line_loop_01	2002Oct5
PASS		
760	r400vgt_line_loop_02	2002Oct5
PASS		
761	r400vgt_line_strip_01	2002Oct5
PASS		
762	r400vgt_line_strip_02	2002Oct5
PASS		
763	r400vgt_multi_context_01	2002Oct5
PASS		
764	r400vgt_multi_context_02	2002Oct5

PASS		
765	r400vgt_multi_context_03	2002Oct5
PASS		
766	r400vgt_multi_context_04	2002Oct5
PASS		
767	r400vgt_multi_context_05	2002Oct5
PASS		
768	r400vgt_multi_context_06	2002Oct5
PASS		
769	r400vgt_multi_context_07	2002Oct5
PASS		
770	r400vgt_multi_context_08	2002Oct5
PASS		
771	r400vgt_multi_context_09	2002Oct5
PASS		
772	r400vgt_multi_context_10	2002Oct5
PASS		
773	r400vgt_multi_context_11	2002Oct5
PASS		
774	r400vgt_multi_context_12	2002Oct5
PASS		
775	r400vgt_multi_prim_reset_index_all_01	2002Oct5
PASS		
776	r400vgt_multi_prim_reset_index_all_02	2002Oct5
PASS		
777	r400vgt_pass_thru_all_prims_01	2002Oct5
PASS		
778	r400vgt_point_list_01	2002Oct5
PASS		
779	r400vgt_point_list_02	2002Oct5
PASS		
780	r400vgt_polygon_01	2002Oct5
PASS		
781	r400vgt_polygon_02	2002Oct5
PASS		
782	r400vgt_provoking_vtx_all_01	2002Oct5
PASS		
783	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct5
PASS		
784	r400vgt_provoking_vtx_polygon_01	2002Oct5
PASS		
785	r400vgt_provoking_vtx_quad_list_01	2002Oct5
PASS		
786	r400vgt_provoking_vtx_quad_strip_01	2002Oct5
PASS		
787	r400vgt_provoking_vtx_tri_fan_01	2002Oct5
PASS		
788	r400vgt_provoking_vtx_tri_strip_01	2002Oct5

PASS			
789	r400vgt_quad_list_01		2002Oct5
PASS			
790	r400vgt_quad_list_02		2002Oct5
PASS			
791	r400vgt_quad_strip_01		2002Oct5
PASS			
792	r400vgt_quad_strip_02		2002Oct5
PASS			
793	r400vgt_rectangle_list_01		2002Oct5
PASS			
794	r400vgt_rectangle_list_02		2002Oct5
PASS			
795	r400vgt_reuse_depth_line_list_01		2002Oct5
PASS			
796	r400vgt_reuse_depth_point_list_01		2002Oct5
PASS			
797	r400vgt_reuse_depth_triangle_list_01		2002Oct5
PASS			
798	r400vgt_reuse_index_line_list_01		2002Oct5
PASS			
799	r400vgt_reuse_index_point_list_01		2002Oct5
PASS			
800	r400vgt_reuse_index_triangle_list_01		2002Oct5
PASS			
801	r400vgt_reuse_index_triangle_list_02		2002Oct5
PASS			
802	r400vgt_reuse_index_triangle_list_03		2002Oct5
PASS			
803	r400vgt_suppress_eop_01	0:0:1	2002Oct5
PASS			
804	r400vgt_suppress_eop_02	0:0:1	2002Oct5
PASS			
805	r400vgt_suppress_eop_03	0:0:1	2002Oct5
PASS			
806	r400vgt_suppress_eop_04	0:0:1	2002Oct5
PASS			
807	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
808	r400vgt_triangle_fan_01	0:0:1	2002Oct5
PASS			
809	r400vgt_triangle_fan_02	0:0:1	2002Oct5
PASS			
810	r400vgt_triangle_list_01	0:0:1	2002Oct5
PASS			
811	r400vgt_triangle_list_02	0:0:1	2002Oct5
PASS			
812	r400vgt_triangle_strip_01	0:0:1	2002Oct5

```

PASS
813 r400vgt_triangle_strip_02          0:0:1  2002Oct5
PASS
814 r400vgt_triangle_wflags_01        0:0:1  2002Oct5
PASS
815 r400vgt_triangle_wflags_02        0:0:1  2002Oct5
PASS
816 r400vgt_viz_query_01              0:0:1  2002Oct5
PASS
817 r400vgt_vtx_vector_packing_01     0:0:1  2002Oct5
PASS
818 r400vte_coverage_01               0:0:1  2002Oct5
PASS
819 r400vte_simple_point_01           0:0:1  2002Oct5
PASS
820 r400vte_simple_triangle_01        0:0:1  2002Oct5
PASS
821 r400vte_w0_fmt_01                  0:0:1  2002Oct5
PASS
822 r400vte_w0_fmt_03                  0:0:1  2002Oct5
PASS
823 r400vte_xy_fmt_01                  0:0:1  2002Oct5
PASS
824 r400vte_xy_fmt_mk_01_bug           0:0:1  2002Oct5
PASS
825 r400vte_xyz_scale_01               0:0:1  2002Oct5
PASS
826 r400vte_xyz_scale_02               0:0:1  2002Oct5
PASS
827 r400vte_z_fmt_01                   0:0:1  2002Oct5
PASS

```

```

=====
Tests_Run_count = 827  Pass_count = 826  Fail_count = 0  Incomplete_count = 1
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Aug 29  3:19:37 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141          100
              SC           157          157          156           99
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY    SU           81           81           81          100
              SANITY       2           2           2           100
+++++
              TOTAL        504          504          503           99
+++++

```

```

=====
No   Test_name                               RunTime  Baseline
Test_Status  Probable_Cause_of_Failures                LastPass
=====
=====
1    primlib_lst_tri_june15                    2002Aug20
INCOMPLETE
2    primlib_gouraud_tri_1_xyz_vb_1_rgb_vb     2002Aug20
INCOMPLETE
3    primlib_gouraud_triangles_2_draw_passes   2002Aug20
INCOMPLETE
4    primlib_parameterized_simple_triangle     2002Aug20
INCOMPLETE
5    primlib_template_simple_triangle          2002Aug20
INCOMPLETE
6    primlib_tex_tri                            2002Aug20
INCOMPLETE
7    r400cl_clip_10_verts_01                   2002Aug21
PASS
8    r400cl_clip_disable_01                    2002Aug21
PASS
9    r400cl_clip_space_dx_ogl_01              2002Aug21
PASS
10   r400cl_clip_ucp_6bits_01                  2002Aug21
PASS
11   r400cl_cull_only_ena_01                   2002Aug21
PASS
12   r400cl_edgeflags_01                       2002Aug21
PASS
13   r400cl_edgeflags_frustum_bottom_01       2002Aug21
PASS
14   r400cl_edgeflags_frustum_far_01          2002Aug21
PASS
15   r400cl_edgeflags_frustum_left_01         2002Aug21
PASS
16   r400cl_edgeflags_frustum_near_01         2002Aug21
PASS
17   r400cl_edgeflags_frustum_right_01        2002Aug21
PASS
18   r400cl_edgeflags_frustum_top_01          2002Aug21
PASS
19   r400cl_edgeflags_gband_01                2002Aug21
PASS
20   r400cl_edgeflags_gband_bottom_01         2002Aug21

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PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug21
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug21
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug21
PASS		
24	r400cl_frustum_01	2002Aug21
PASS		
25	r400cl_frustum_02	2002Aug21
PASS		
26	r400cl_frustum_03	2002Aug21
PASS		
27	r400cl_frustum_04	2002Aug21
PASS		
28	r400cl_frustum_05	2002Aug21
PASS		
29	r400cl_frustum_06	2002Aug21
PASS		
30	r400cl_frustum_07	2002Aug21
PASS		
31	r400cl_frustum_08	2002Aug21
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361 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Aug20
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362 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Aug20
INCOMPLETE
363 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Aug20
INCOMPLETE
364 r400vgt_hos_PNT_disc_cp_qn_precision_02 2002Aug20
INCOMPLETE
365 r400vgt_hos_PNT_edge_detection_01 2002Aug20
INCOMPLETE
366 r400vgt_hos_PNT_lp_cont_no_projection_01 2002Aug20
INCOMPLETE
367 r400vgt_hos_simple_linear_PNT_discrete_01 2002Aug20
INCOMPLETE
368 r400vgt_immed_index_line_list_01 2002Aug20
INCOMPLETE
369 r400vgt_immed_index_line_loop_01 2002Aug20
INCOMPLETE
370 r400vgt_immed_index_line_strip_01 2002Aug20
INCOMPLETE
371 r400vgt_immed_index_points_01 2002Aug20
INCOMPLETE
372 r400vgt_immed_index_polygon_01 2002Aug20
INCOMPLETE
373 r400vgt_immed_index_primitives_01 2002Aug20
INCOMPLETE
374 r400vgt_immed_index_quad_list_01 2002Aug20
INCOMPLETE
375 r400vgt_immed_index_quad_strip_01 2002Aug20
INCOMPLETE
376 r400vgt_immed_index_rectangle_list_01 2002Aug20
INCOMPLETE
377 r400vgt_immed_index_tri_fan_01 2002Aug20
INCOMPLETE
378 r400vgt_immed_index_tri_list_01 2002Aug20
INCOMPLETE
379 r400vgt_immed_index_tri_strip_01 2002Aug20
INCOMPLETE
380 r400vgt_immed_index_tri_wflags_01 2002Aug20

INCOMPLETE
381 r400vgt_index_dealloc_line_list_01 2002Aug20
INCOMPLETE
382 r400vgt_index_dealloc_points_01 2002Aug20
INCOMPLETE
383 r400vgt_index_dealloc_triangle_list_01 2002Aug20
INCOMPLETE
384 r400vgt_index_min_max_01 2002Aug20
INCOMPLETE
385 r400vgt_index_min_max_02 2002Aug20
INCOMPLETE
386 r400vgt_index_offset_01 2002Aug20
INCOMPLETE
387 r400vgt_index_offset_02 2002Aug20
INCOMPLETE
388 r400vgt_index_offset_03 2002Aug20
INCOMPLETE
389 r400vgt_index_size_01 2002Aug20
INCOMPLETE
390 r400vgt_index_source_switch_01 2002Aug20
INCOMPLETE
391 r400vgt_line_list_01 2002Aug20
INCOMPLETE
392 r400vgt_line_loop_01 2002Aug20
INCOMPLETE
393 r400vgt_line_strip_01 2002Aug20
INCOMPLETE
394 r400vgt_pass_thru_all_prims_01 2002Aug20
INCOMPLETE
395 r400vgt_point_list_01 2002Aug20
INCOMPLETE
396 r400vgt_polygon_01 2002Aug20
INCOMPLETE
397 r400vgt_provoking_vtx_all_01 2002Aug20
INCOMPLETE
398 r400vgt_provoking_vtx_edgeflags_all_01 2002Aug20
INCOMPLETE
399 r400vgt_quad_list_01 2002Aug20
INCOMPLETE
400 r400vgt_quad_strip_01 2002Aug20
INCOMPLETE
401 r400vgt_rectangle_list_01 2002Aug20
INCOMPLETE
402 r400vgt_reuse_depth_line_list_01 2002Aug20
INCOMPLETE
403 r400vgt_reuse_depth_point_list_01 2002Aug20
INCOMPLETE
404 r400vgt_reuse_depth_triangle_list_01 2002Aug20

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INCOMPLETE
405  r400vgt_reuse_index_line_list_01          2002Aug20
INCOMPLETE
406  r400vgt_reuse_index_point_list_01        2002Aug20
INCOMPLETE
407  r400vgt_reuse_index_triangle_list_01     2002Aug20
INCOMPLETE
408  r400vgt_reuse_index_triangle_list_02     2002Aug20
INCOMPLETE
409  r400vgt_reuse_index_triangle_list_03     2002Aug20
INCOMPLETE
410  r400vgt_triangle_fan_01                 2002Aug20
INCOMPLETE
411  r400vgt_triangle_list_01                2002Aug20
INCOMPLETE
412  r400vgt_triangle_strip_01               2002Aug20
INCOMPLETE
413  r400vgt_triangle_wflags_01              2002Aug20
INCOMPLETE
414  r400vgt_vtx_vector_packing_01           2002Aug20
INCOMPLETE
415  r400vte_coverage_01                     2002Aug20
INCOMPLETE
416  r400vte_simple_point_01                 2002Aug20
INCOMPLETE
417  r400vte_simple_triangle_01              2002Aug20
INCOMPLETE
418  r400vte_w0_fmt_01                       2002Aug20
INCOMPLETE
419  r400vte_w0_fmt_03                       2002Aug20
INCOMPLETE
420  r400vte_xy_fmt_01                       2002Aug20
INCOMPLETE
421  r400vte_xyz_scale_01                    2002Aug20
INCOMPLETE
422  r400vte_xyz_scale_02                    2002Aug20
INCOMPLETE
423  r400vte_z_fmt_01                        2002Aug20
INCOMPLETE

```

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=====
Tests_Run_count = 423  Pass_count = 272  Fail_count = 0  Incomplete_count = 151
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```

Regression Report For VGT - Project = r400 Created on Wed Oct 16 22:06:01 2002

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc	0:0:3	2002Oct16
PASS			
2	cp_dma_interrupt	0:0:2	2002Oct16
PASS			
3	cp_dma_m2m_01	0:0:1	2002Oct16
PASS			
4	cp_dma_m2m_02	0:0:1	2002Oct16
PASS			
5	cp_dma_m2m_03	0:0:1	2002Oct16
PASS			
6	cp_dma_m2m_04	0:0:1	2002Oct16
PASS			
7	cp_dma_m2r_01	0:0:1	2002Oct16
PASS			
8	cp_dma_m2r_02	0:0:2	2002Oct16
PASS			
9	cp_dma_m2r_03	0:0:1	2002Oct16
PASS			
10	cp_dma_m2r_04	0:0:1	2002Oct16
PASS			
11	cp_dma_m2r_r2m	0:0:1	2002Oct16
PASS			
12	cp_dma_pio_simple	0:0:1	2002Oct16
PASS			
13	cp_dma_pio_stress	0:0:1	2002Oct16
PASS			
14	cp_dma_piobm_stress	0:0:1	2002Oct16
PASS			
15	cp_dma_r2m_01	0:0:1	2002Oct16
PASS			
16	cp_dma_r2m_02	0:0:2	2002Oct16
PASS			
17	cp_dma_r2m_03	0:0:2	2002Oct16
PASS			
18	cp_dma_r2m_04	0:0:2	2002Oct16
PASS			
19	cp_dma_r2r_01	0:0:1	2002Oct16
PASS			
20	cp_dma_r2r_02	0:0:1	2002Oct16

PASS			
21	cp_dma_r2r_03	0:0:2	2002Oct16
PASS			
22	cp_dma_r2r_r2m	0:0:1	2002Oct16
PASS			
23	cp_dma_r2r_r2m_m2m	0:0:2	2002Oct16
PASS			
24	cp_dma_r2r_r2m_m2m_r2m	0:0:1	2002Oct16
PASS			
25	cp_dma_simple	0:0:1	2002Oct16
PASS			
26	cp_e2_hostdata_blt_pntr_8888	0:0:1	2002Oct16
PASS			
27	cp_e2_one_blit	0:0:0	2002Oct16
PASS			
28	cp_e2_one_line	0:0:2	2002Oct16
PASS			
29	cp_e2blit_brush_mt_ropcc	0:0:1	2002Oct16
PASS			
30	cp_e2paint_565	0:0:1	2002Oct16
PASS			
31	cp_e2paint_multi	0:0:2	2002Oct16
PASS			
32	cp_e2polyscanlines_brush	0:0:1	2002Oct16
PASS			
33	cp_e2polyscanlines_brush_mt	0:0:1	2002Oct16
PASS			
34	cp_e2smalltext_jcl	0:0:3	2002Oct16
PASS			
35	cp_im_load_indirect	0:0:3	2002Oct16
PASS			
36	cp_push_aper_indirect1	0:0:3	2002Oct16
PASS			
37	cp_push_aper_primary	0:0:3	2002Oct16
PASS			
38	cp_queue_avail_01	0:0:3	2002Oct16
PASS			
39	cp_queue_avail_02	0:0:3	2002Oct16
PASS			
40	cp_queue_avail_03	0:0:3	2002Oct16
PASS			
41	cp_queue_avail_04	0:0:3	2002Oct16
PASS			
42	cp_queue_avail_05	0:0:3	2002Oct16
PASS			
43	cp_queue_avail_06	0:0:3	2002Oct16
PASS			
44	cp_queue_avail_07	0:0:3	2002Oct16

PASS			
45	cp_queue_avail_08	0:0:3	2002Oct16
PASS			
46	cp_queue_avail_09	0:0:3	2002Oct16
PASS			
47	cp_queue_avail_10	0:0:3	2002Oct16
PASS			
48	cp_queue_avail_11	0:0:3	2002Oct16
PASS			
49	cp_queue_avail_12	0:0:3	2002Oct16
PASS			
50	cp_simple_triangle	0:0:3	2002Oct16
PASS			
51	milestone_event_flush_tri1	0:0:3	2002Oct16
PASS			
52	milestone_event_flush_tri2	0:0:3	2002Oct16
PASS			
53	milestone_event_flush_tri3	0:0:3	2002Oct16
PASS			
54	milestone_event_flush_tri4	0:0:3	2002Oct16
PASS			
55	milestone_event_flush_tri5	0:0:3	2002Oct16
PASS			
56	milestone_event_flush_tri6	0:0:3	2002Oct16
PASS			
57	milestone_event_flush_tri7	0:0:3	2002Oct16
PASS			
58	milestone_event_flush_tri8	0:0:3	2002Oct16
PASS			
59	milestone_event_flush_tri9	0:0:3	2002Oct16
PASS			
60	milestone_event_randomized	0:0:3	2002Oct16
PASS			
61	milestone_event_standard	0:0:3	2002Oct16
PASS			
62	milestone_event_switch_flush_tri1	0:0:3	2002Oct16
PASS			
63	milestone_event_switch_flush_tri2	0:0:3	2002Oct16
PASS			
64	milestone_event_switch_flush_tri3	0:0:3	2002Oct16
PASS			
65	milestone_event_switch_flush_tri4	0:0:3	2002Oct16
PASS			
66	milestone_event_switch_flush_tri5	0:0:3	2002Oct16
PASS			
67	milestone_event_switch_flush_tri6	0:0:3	2002Oct16
PASS			
68	milestone_event_switch_flush_tri7	0:0:3	2002Oct16

PASS			
69	milestone_event_switch_flush_tri8	0:0:3	2002Oct16
PASS			
70	milestone_event_switch_flush_tri9	0:0:3	2002Oct16
PASS			
71	milestone_event_switch_tri2	0:0:3	2002Oct16
PASS			
72	milestone_event_switch_tri3	0:0:3	2002Oct16
PASS			
73	milestone_event_switch_tri4	0:0:3	2002Oct16
PASS			
74	milestone_event_switch_tri5	0:0:3	2002Oct16
PASS			
75	milestone_event_switch_tri6	0:0:3	2002Oct16
PASS			
76	milestone_event_switch_tri7	0:0:3	2002Oct16
PASS			
77	milestone_event_switch_tri8	0:0:3	2002Oct16
PASS			
78	milestone_event_switch_tri9	0:0:3	2002Oct16
PASS			
79	milestone_event_tri2	0:0:3	2002Oct16
PASS			
80	milestone_event_tri3	0:0:3	2002Oct16
PASS			
81	milestone_event_tri4	0:0:3	2002Oct16
PASS			
82	milestone_event_tri5	0:0:3	2002Oct16
PASS			
83	milestone_event_tri6	0:0:3	2002Oct16
PASS			
84	milestone_event_tri7	0:0:3	2002Oct16
PASS			
85	milestone_event_tri8	0:0:3	2002Oct16
PASS			
86	milestone_event_tri9	0:0:3	2002Oct16
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87	milestone_tri_random_rect	0:0:3	2002Oct16
PASS			
88	milestone_tri_random_tri	0:0:3	2002Oct16
PASS			
89	milestone_tri_randomized	0:0:3	2002Oct16
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90	milestone_tri_standard	0:0:3	2002Oct16
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91	milestone_tri_tril28_pix4	0:0:3	2002Oct16
PASS			
92	milestone_tri_tril_pix1	0:0:3	2002Oct16

PASS			
93	milestone_tri_tri1_pix2	0:0:3	2002Oct16
PASS			
94	milestone_tri_tri1_pix3	0:0:3	2002Oct16
PASS			
95	milestone_tri_tri1_pix4	0:0:3	2002Oct16
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96	milestone_tri_tri24x24	0:0:3	2002Oct16
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97	milestone_tri_tri28x28	0:0:3	2002Oct16
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98	milestone_tri_tri2_pix1	0:0:3	2002Oct16
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99	milestone_tri_tri2_pix16	0:0:3	2002Oct16
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100	milestone_tri_tri2_pix4	0:0:3	2002Oct16
PASS			
101	milestone_tri_tri32_pix1	0:0:3	2002Oct16
PASS			
102	milestone_tri_tri32_pix4	0:0:3	2002Oct16
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103	milestone_tri_tri32x32	0:0:3	2002Oct16
PASS			
104	milestone_tri_tri36x36	0:0:3	2002Oct16
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105	milestone_tri_tri40x40	0:0:3	2002Oct16
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106	milestone_tri_tri44x44	0:0:3	2002Oct16
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107	milestone_tri_tri48x48	0:0:3	2002Oct16
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108	milestone_tri_tri52x52	0:0:3	2002Oct16
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109	milestone_tri_tri56x56	0:0:3	2002Oct16
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110	milestone_tri_tri60x60	0:0:3	2002Oct16
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111	milestone_tri_tri64x64	0:0:3	2002Oct16
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112	milestone_tri_tri8_pix4	0:0:3	2002Oct16
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113	primlib_1st_tri_june15	0:0:3	2002Oct16
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114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	0:0:3	2002Oct16
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115	primlib_gouraud_triangles_2_draw_passes	0:0:3	2002Oct16
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116	primlib_parameterized_simple_triangle	0:0:3	2002Oct16

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117	primlib_template_simple_triangle	0:0:3	2002Oct16
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118	primlib_tex_tri	0:0:3	2002Oct16
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119	r400cl_bary_texture_08_bug	0:0:3	2002Sep21
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120	r400cl_clip_10_verts_01	0:0:3	2002Oct16
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121	r400cl_clip_disable_01	0:0:3	2002Oct16
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122	r400cl_clip_space_dx_ogl_01	0:0:3	2002Oct16
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123	r400cl_clip_ucp_6bits_01	0:0:3	2002Oct16
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124	r400cl_cull_only_ena_01	0:0:3	2002Oct16
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125	r400cl_edgeflags_01	0:0:3	2002Oct16
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126	r400cl_edgeflags_01_bug	0:0:3	2002Oct9
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127	r400cl_edgeflags_frustum_bottom_01	0:0:3	2002Oct16
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128	r400cl_edgeflags_frustum_far_01	0:0:3	2002Oct16
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129	r400cl_edgeflags_frustum_left_01	0:0:3	2002Oct16
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130	r400cl_edgeflags_frustum_near_01	0:0:3	2002Oct16
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131	r400cl_edgeflags_frustum_right_01	0:0:3	2002Oct16
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132	r400cl_edgeflags_frustum_top_01	0:0:3	2002Oct16
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133	r400cl_edgeflags_gband_01	0:0:3	2002Oct16
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134	r400cl_edgeflags_gband_bottom_01	0:0:3	2002Oct16
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135	r400cl_edgeflags_gband_left_01	0:0:3	2002Oct16
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136	r400cl_edgeflags_gband_right_01	0:0:3	2002Oct16
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137	r400cl_edgeflags_gband_top_01	0:0:3	2002Oct16
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138	r400cl_frustum_01	0:0:3	2002Oct16
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139	r400cl_frustum_02	0:0:3	2002Oct16
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140	r400cl_frustum_03	0:0:3	2002Oct16

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143	r400cl_frustum_06	0:0:3	2002Oct16
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144	r400cl_frustum_07	0:0:3	2002Oct16
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158	r400cl_frustum_21	0:0:3	2002Oct16
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159	r400cl_frustum_22	0:0:3	2002Oct16
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160	r400cl_frustum_23	0:0:3	2002Oct16
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163	r400cl_frustum_26	0:0:3	2002Oct16
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176	r400cl_frustum_39	0:0:3	2002Oct16
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177	r400cl_frustum_40	0:0:3	2002Oct16
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178	r400cl_frustum_all_vols_lines	0:0:3	2002Oct16
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179	r400cl_frustum_all_vols_tris	0:0:3	2002Oct16
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180	r400cl_frustum_lines_01	0:0:3	2002Oct16
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181	r400cl_frustum_lines_02	0:0:3	2002Oct16
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182	r400cl_frustum_lines_03	0:0:3	2002Oct16
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183	r400cl_frustum_lines_04	0:0:3	2002Oct16
PASS			
184	r400cl_frustum_lines_05	0:0:3	2002Oct16
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185	r400cl_frustum_lines_06	0:0:3	2002Oct16
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186	r400cl_frustum_lines_07	0:0:3	2002Oct16
PASS			
187	r400cl_frustum_lines_08	0:0:3	2002Oct16
PASS			
188	r400cl_frustum_lines_09	0:0:3	2002Oct16

PASS			
189	r400cl_frustum_lines_10	0:0:3	2002Oct16
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190	r400cl_frustum_lines_101	0:0:3	2002Oct16
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191	r400cl_frustum_lines_102	0:0:3	2002Oct16
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192	r400cl_frustum_lines_103	0:0:3	2002Oct16
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193	r400cl_frustum_lines_104	0:0:3	2002Oct16
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194	r400cl_frustum_lines_105	0:0:3	2002Oct16
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195	r400cl_frustum_lines_106	0:0:3	2002Oct16
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196	r400cl_frustum_lines_107	0:0:3	2002Oct16
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197	r400cl_frustum_lines_108	0:0:3	2002Oct16
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198	r400cl_frustum_lines_11	0:0:3	2002Oct16
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199	r400cl_frustum_lines_12	0:0:3	2002Oct16
PASS			
200	r400cl_frustum_lines_13	0:0:3	2002Oct16
PASS			
201	r400cl_frustum_lines_14	0:0:3	2002Oct16
PASS			
202	r400cl_frustum_lines_15	0:0:3	2002Oct16
PASS			
203	r400cl_frustum_lines_16	0:0:3	2002Oct16
PASS			
204	r400cl_frustum_lines_17	0:0:3	2002Oct16
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205	r400cl_frustum_lines_18	0:0:3	2002Oct16
PASS			
206	r400cl_frustum_lines_19	0:0:3	2002Oct16
PASS			
207	r400cl_frustum_lines_20	0:0:3	2002Oct16
PASS			
208	r400cl_frustum_lines_21	0:0:3	2002Oct16
PASS			
209	r400cl_frustum_lines_22	0:0:3	2002Oct16
PASS			
210	r400cl_frustum_lines_23	0:0:3	2002Oct16
PASS			
211	r400cl_frustum_lines_24	0:0:3	2002Oct16
PASS			
212	r400cl_frustum_lines_25	0:0:3	2002Oct16

PASS			
213	r400cl_frustum_lines_26	0:0:3	2002Oct16
PASS			
214	r400cl_frustum_lines_27	0:0:3	2002Oct16
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215	r400cl_frustum_lines_28	0:0:3	2002Oct16
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216	r400cl_frustum_lines_29	0:0:3	2002Oct16
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217	r400cl_frustum_lines_30	0:0:3	2002Oct16
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218	r400cl_frustum_lines_31	0:0:3	2002Oct16
PASS			
219	r400cl_frustum_lines_32	0:0:3	2002Oct16
PASS			
220	r400cl_frustum_lines_33	0:0:3	2002Oct16
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221	r400cl_frustum_lines_34	0:0:3	2002Oct16
PASS			
222	r400cl_frustum_lines_35	0:0:3	2002Oct16
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223	r400cl_frustum_lines_36	0:0:3	2002Oct16
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224	r400cl_frustum_lines_37	0:0:3	2002Oct16
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225	r400cl_frustum_lines_38	0:0:3	2002Oct16
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226	r400cl_frustum_lines_39	0:0:3	2002Oct16
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227	r400cl_frustum_lines_40	0:0:3	2002Oct16
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228	r400cl_frustum_lines_41	0:0:3	2002Oct16
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229	r400cl_frustum_lines_42	0:0:3	2002Oct16
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230	r400cl_frustum_lines_43	0:0:3	2002Oct16
PASS			
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498	r400sc_msa_8_rectangle_list_04	0:0:3	2002Oct16
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499	r400sc_msa_8_rectangle_list_05	0:0:3	2002Oct16
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500	r400sc_msa_8_rectangle_list_06	0:0:3	2002Oct16

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501	r400sc_msaa_8_rectangle_list_07	0:0:3	2002Oct16
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502	r400sc_msaa_8_rectangle_list_08	0:0:3	2002Oct16
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503	r400sc_msaa_8_zbuffer_rectangle_list_01	0:0:3	2002Oct16
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504	r400sc_msaa_8_zbuffer_rectangle_list_02	0:0:3	2002Oct16
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505	r400sc_msaa_X_primitives_01_bug	0:0:3	2002Oct9
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506	r400sc_msaa_X_primitives_02_bug	0:0:3	2002Oct9
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507	r400sc_multi_chip_prim_discard_00_07_standard	0:0:3	2002Oct16
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508	r400sc_multi_chip_prim_discard_00_07_test00	0:0:3	2002Oct16
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509	r400sc_multi_chip_prim_discard_00_07_test01	0:0:3	2002Oct16
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510	r400sc_multi_chip_prim_discard_00_07_test02	0:0:3	2002Oct16
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511	r400sc_multi_chip_prim_discard_00_07_test03	0:0:3	2002Oct16
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514	r400sc_multi_chip_prim_discard_00_07_test06	0:0:3	2002Oct16
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519	r400sc_multi_chip_tile_discard_00_07_test03	0:0:3	2002Oct16
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521	r400sc_multi_chip_tile_discard_00_07_test05	0:0:3	2002Oct16
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522	r400sc_multi_chip_tile_discard_00_07_test06	0:0:3	2002Oct16
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524	r400sc_null_triangles_fc_01	0:0:3	2002Oct16

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525	r400sc_packed_color_01	0:0:2	2002Oct16
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567	r400sc_polymode_tri_fill_01_bug	0:0:2	2002Oct9
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568	r400sc_prsp_byc_intrp_ref_pix_01	0:0:2	2002Oct16
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606	r400sc_tri_8textures_01	0:0:2	2002Oct16
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636	r400sc_zbuffer_triangle_list_01	0:0:2	2002Oct16
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651	r400su_clipline_01	0:0:2	2002Oct16
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652	r400su_clippoint_01	0:0:2	2002Oct16
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653	r400su_clipvertexsortingfunctional_01	0:0:2	2002Oct16
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654	r400su_cullingfunctional_01	0:0:2	2002Oct16
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664	r400su_line_clip_end_cap_01	0:0:2	2002Oct16
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665	r400su_line_clip_orientation_01	0:0:2	2002Oct16
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666	r400su_line_clip_orientation_02	0:0:2	2002Oct16
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766	r400vgt_dma_engine_06	0:0:2	2002Oct16
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773	r400vgt_dma_index_line_strip_01	0:0:2	2002Oct16
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774	r400vgt_dma_index_points_01	0:0:2	2002Oct16
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775	r400vgt_dma_index_polygon_01	0:0:2	2002Oct16
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783	r400vgt_dma_index_tri_wflags_01	0:0:2	2002Oct16
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784	r400vgt_dma_swap_idx16_01	0:0:2	2002Oct16
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785	r400vgt_dma_swap_idx32_01	0:0:2	2002Oct16
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786	r400vgt_edgeflags_polygon_01	0:0:2	2002Oct16
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787	r400vgt_edgeflags_quad_list_01	0:0:2	2002Oct16
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788	r400vgt_edgeflags_quad_strip_01	0:0:2	2002Oct16

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789	r400vgt_edgeflags_triangle_fan_01	0:0:2	2002Oct16
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791	r400vgt_edgeflags_triangle_strip_01	0:0:2	2002Oct16
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792	r400vgt_edgeflags_triangle_wflags_01	0:0:2	2002Oct16
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795	r400vgt_event_handling_03	0:0:2	2002Oct16
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796	r400vgt_event_handling_04	0:0:2	2002Oct16
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798	r400vgt_ext2int_index_line_loop_01	0:0:2	2002Oct16
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808	r400vgt_ext2int_index_triangle_wflags_01	0:0:2	2002Oct16
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811	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	0:0:2	2002Oct16
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812	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	0:0:2	2002Oct16

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813	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	0:0:2	2002Oct16
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815	r400vgt_hos_PNQ_lp_cont_no_projection_01	0:0:2	2002Oct16
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816	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	0:0:2	2002Oct16
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817	r400vgt_hos_PNT_cont_cp_qn_complex_01	0:0:2	2002Oct16
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818	r400vgt_hos_PNT_cont_cp_qn_precision_01	0:0:2	2002Oct16
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819	r400vgt_hos_PNT_cont_cp_qn_precision_02	0:0:2	2002Oct16
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820	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	0:0:2	2002Oct16
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822	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	0:0:2	2002Oct16
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824	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	0:0:2	2002Oct16
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826	r400vgt_hos_PNT_disc_cp_qn_complex_01	0:0:2	2002Oct16
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830	r400vgt_hos_PNT_disc_cp_qn_precision_02	0:0:2	2002Oct16
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831	r400vgt_hos_PNT_edge_detection_01	0:0:2	2002Oct16
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832	r400vgt_hos_PNT_lp_cont_no_projection_01	0:0:2	2002Oct16
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833	r400vgt_hos_RPatch_cp_02	0:0:2	2002Oct16
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834	r400vgt_hos_RPatch_lp_02	0:0:2	2002Oct16
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838	r400vgt_hos_auto_index_quad_list_01	0:0:2	2002Oct16
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839	r400vgt_hos_auto_index_triangle_list_01	0:0:2	2002Oct16
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840	r400vgt_hos_cubic_pos_pnt_discrete_01	0:0:2	2002Oct16
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841	r400vgt_hos_simple_linear_PNT_discrete_01	0:0:2	2002Oct16
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848	r400vgt_immed_index_quad_list_01	0:0:2	2002Oct16
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854	r400vgt_immed_index_tri_wflags_01	0:0:2	2002Oct16
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855	r400vgt_index_dealloc_line_list_01	0:0:2	2002Oct16
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857	r400vgt_index_dealloc_triangle_list_01	0:0:2	2002Oct16
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858	r400vgt_index_min_max_01	0:0:2	2002Oct16
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859	r400vgt_index_min_max_02	0:0:2	2002Oct16
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860	r400vgt_index_min_max_03	0:0:2	2002Oct16

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861	r400vgt_index_min_max_04	0:0:2	2002Oct16
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862	r400vgt_index_offset_01	0:0:2	2002Oct16
PASS			
863	r400vgt_index_offset_02	0:0:2	2002Oct16
PASS			
864	r400vgt_index_offset_03	0:0:2	2002Oct16
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865	r400vgt_index_offset_04	0:0:2	2002Oct16
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866	r400vgt_index_offset_05	0:0:2	2002Oct16
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868	r400vgt_index_offset_07	0:0:2	2002Oct16
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870	r400vgt_index_size_01	0:0:2	2002Oct16
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871	r400vgt_index_size_02	0:0:2	2002Oct16
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PASS			
876	r400vgt_line_loop_02	0:0:2	2002Oct16
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877	r400vgt_line_strip_01	0:0:2	2002Oct16
PASS			
878	r400vgt_line_strip_02	0:0:2	2002Oct16
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PASS			
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PASS			
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PASS			
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PASS			
889	r400vgt_multi_context_11	0:0:2	2002Oct16
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891	r400vgt_multi_prim_reset_index_all_01	0:0:2	2002Oct16
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892	r400vgt_multi_prim_reset_index_all_02	0:0:2	2002Oct16
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893	r400vgt_pass_thru_all_prims_01	0:0:2	2002Oct16
PASS			
894	r400vgt_point_list_01	0:0:2	2002Oct16
PASS			
895	r400vgt_point_list_02	0:0:2	2002Oct16
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896	r400vgt_polygon_01	0:0:2	2002Oct16
PASS			
897	r400vgt_polygon_02	0:0:2	2002Oct16
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898	r400vgt_provoking_vtx_all_01	0:0:2	2002Oct16
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899	r400vgt_provoking_vtx_edgeflags_all_01	0:0:2	2002Oct16
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901	r400vgt_provoking_vtx_quad_list_01	0:0:2	2002Oct16
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PASS			
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907	r400vgt_quad_strip_01	0:0:2	2002Oct16
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910	r400vgt_rectangle_list_02	0:0:2	2002Oct16
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911	r400vgt_reuse_depth_line_list_01	0:0:2	2002Oct16
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PASS			
913	r400vgt_reuse_depth_triangle_list_01	0:0:2	2002Oct16
PASS			
914	r400vgt_reuse_index_line_list_01	0:0:2	2002Oct16
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PASS			
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PASS			
917	r400vgt_reuse_index_triangle_list_02	0:0:2	2002Oct16
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918	r400vgt_reuse_index_triangle_list_03	0:0:2	2002Oct16
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919	r400vgt_suppress_eop_01	0:0:1	2002Oct16
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920	r400vgt_suppress_eop_02	0:0:1	2002Oct16
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921	r400vgt_suppress_eop_03	0:0:1	2002Oct16
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923	r400vgt_suppress_eop_05	0:0:1	2002Oct16
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PASS			
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929	r400vgt_triangle_strip_02	0:0:1	2002Oct16
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930	r400vgt_triangle_wflags_01	0:0:1	2002Oct16
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931	r400vgt_triangle_wflags_02	0:0:1	2002Oct16
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932	r400vgt_viz_query_01	0:0:1	2002Oct16

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PASS
933 r400vgt_vtx_vector_packing_01          0:0:1    2002Oct16
PASS
934 r400vte_coverage_01                    0:0:1    2002Oct16
PASS
935 r400vte_simple_point_01                0:0:1    2002Oct16
PASS
936 r400vte_simple_triangle_01             0:0:1    2002Oct16
PASS
937 r400vte_w0_fmt_01                       0:0:1    2002Oct16
PASS
938 r400vte_w0_fmt_03                       0:0:1    2002Oct16
PASS
939 r400vte_xy_fmt_01                       0:0:1    2002Oct16
PASS
940 r400vte_xy_fmt_mk_01_bug                0:0:1    2002Oct9
PASS
941 r400vte_xyz_scale_01                    0:0:1    2002Oct16
PASS
942 r400vte_xyz_scale_02                    0:0:1    2002Oct16
PASS
943 r400vte_z_fmt_01                        0:0:1    2002Oct16
PASS

```

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Tests_Run_count = 943 Pass_count = 934 Fail_count = 0 Incomplete_count = 9
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No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
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PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep5
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep5
PASS			
4	primlib_parameterized_simple_triangle		2002Sep5
PASS			
5	primlib_template_simple_triangle		2002Sep5
PASS			
6	primlib_tex_tri		2002Sep5
PASS			
7	r400cl_clip_10_verts_01		2002Sep5
PASS			
8	r400cl_clip_disable_01		2002Sep5
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep5
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep5
PASS			
11	r400cl_cull_only_ena_01		2002Sep5
PASS			
12	r400cl_edgeflags_01		2002Sep5
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep5
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep5
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep5
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep5
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep5
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep5
PASS			
19	r400cl_edgeflags_gband_01		2002Sep5
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep5

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep5
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep5
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep5
PASS		
24	r400cl_frustum_01	2002Sep5
PASS		
25	r400cl_frustum_02	2002Sep5
PASS		
26	r400cl_frustum_03	2002Sep5
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27	r400cl_frustum_04	2002Sep5
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43	r400cl_frustum_20	2002Sep5
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44	r400cl_frustum_21	2002Sep5

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77	r400cl_gband_tcl_01	2002Sep5
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78	r400cl_pointlist_vertex_state_ucp_01	2002Sep5
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79	r400cl_simple_triangle_01	2002Sep5
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80	r400cl_ucp_combos_01	2002Sep5
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81	r400cl_ucp_combos_02	2002Sep5
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438	r400vgt_hos_PNT_edge_detection_01	2002Sep5
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439	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep5
PASS		
440	r400vgt_hos_RPatch_cp_02	2002Sep5
PASS		
441	r400vgt_hos_RPatch_lp_02	2002Sep5
PASS		
442	r400vgt_hos_auto_index_line_list_01	2002Sep5
PASS		
443	r400vgt_hos_auto_index_quad_list_01	2002Sep5
PASS		
444	r400vgt_hos_auto_index_triangle_list_01	2002Sep5
PASS		
445	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep5
PASS		
446	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep5
PASS		
447	r400vgt_immed_index_line_list_01	2002Sep5
PASS		
448	r400vgt_immed_index_line_loop_01	2002Sep5
PASS		
449	r400vgt_immed_index_line_strip_01	2002Sep5
PASS		
450	r400vgt_immed_index_points_01	2002Sep5
PASS		
451	r400vgt_immed_index_polygon_01	2002Sep5
PASS		
452	r400vgt_immed_index_primitives_01	2002Sep5

PASS		
453	r400vgt_immed_index_quad_list_01	2002Sep5
PASS		
454	r400vgt_immed_index_quad_strip_01	2002Sep5
PASS		
455	r400vgt_immed_index_rectangle_list_01	2002Sep5
PASS		
456	r400vgt_immed_index_tri_fan_01	2002Sep5
PASS		
457	r400vgt_immed_index_tri_list_01	2002Sep5
PASS		
458	r400vgt_immed_index_tri_strip_01	2002Sep5
PASS		
459	r400vgt_immed_index_tri_wflags_01	2002Sep5
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460	r400vgt_index_dealloc_line_list_01	2002Sep5
PASS		
461	r400vgt_index_dealloc_points_01	2002Sep5
PASS		
462	r400vgt_index_dealloc_triangle_list_01	2002Sep5
PASS		
463	r400vgt_index_min_max_01	2002Sep5
PASS		
464	r400vgt_index_min_max_02	2002Sep5
PASS		
465	r400vgt_index_offset_01	2002Sep5
PASS		
466	r400vgt_index_offset_02	2002Sep5
PASS		
467	r400vgt_index_offset_03	2002Sep5
PASS		
468	r400vgt_index_size_01	2002Sep5
PASS		
469	r400vgt_index_source_switch_01	2002Sep5
PASS		
470	r400vgt_line_list_01	2002Sep5
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471	r400vgt_line_loop_01	2002Sep5
PASS		
472	r400vgt_line_strip_01	2002Sep5
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473	r400vgt_multi_context_01	2002Sep5
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474	r400vgt_multi_context_02	2002Sep5
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475	r400vgt_multi_context_03	2002Sep5
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476	r400vgt_multi_context_04	2002Sep5

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477	r400vgt_multi_context_05	2002Sep5
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478	r400vgt_multi_context_06	2002Sep5
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479	r400vgt_multi_context_07	2002Sep5
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480	r400vgt_multi_context_08	2002Sep5
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481	r400vgt_multi_context_09	2002Sep5
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483	r400vgt_multi_context_11	2002Sep5
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484	r400vgt_multi_context_12	2002Sep5
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485	r400vgt_pass_thru_all_prims_01	2002Sep5
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486	r400vgt_point_list_01	2002Sep5
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487	r400vgt_polygon_01	2002Sep5
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488	r400vgt_provoking_vtx_all_01	2002Sep5
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489	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep5
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490	r400vgt_quad_list_01	2002Sep5
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491	r400vgt_quad_strip_01	2002Sep5
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492	r400vgt_rectangle_list_01	2002Sep5
PASS		
493	r400vgt_reuse_depth_line_list_01	2002Sep5
PASS		
494	r400vgt_reuse_depth_point_list_01	2002Sep5
PASS		
495	r400vgt_reuse_depth_triangle_list_01	2002Sep5
PASS		
496	r400vgt_reuse_index_line_list_01	2002Sep5
PASS		
497	r400vgt_reuse_index_point_list_01	2002Sep5
PASS		
498	r400vgt_reuse_index_triangle_list_01	2002Sep5
PASS		
499	r400vgt_reuse_index_triangle_list_02	2002Sep5
PASS		
500	r400vgt_reuse_index_triangle_list_03	2002Sep5

PASS
501 r400vgt_suppress_eop_01 2002Sep5
PASS
502 r400vgt_suppress_eop_02 2002Sep5
PASS
503 r400vgt_suppress_eop_03 2002Sep5
PASS
504 r400vgt_suppress_eop_04 2002Sep5
PASS
505 r400vgt_triangle_fan_01 2002Sep5
PASS
506 r400vgt_triangle_list_01 2002Sep5
PASS
507 r400vgt_triangle_strip_01 2002Sep5
PASS
508 r400vgt_triangle_wflags_01 2002Sep5
PASS
509 r400vgt_vtx_vector_packing_01 2002Sep5
PASS
510 r400vte_coverage_01 2002Sep5
PASS
511 r400vte_simple_point_01 2002Sep5
PASS
512 r400vte_simple_triangle_01 2002Sep5
PASS
513 r400vte_w0_fmt_01 2002Sep5
PASS
514 r400vte_w0_fmt_03 2002Sep5
PASS
515 r400vte_xy_fmt_01 2002Sep5
PASS
516 r400vte_xyz_scale_01 2002Sep5
PASS
517 r400vte_xyz_scale_02 2002Sep5
PASS
518 r400vte_z_fmt_01 2002Sep5
PASS

=====
Tests_Run_count = 518 Pass_count = 516 Fail_count = 1 Incomplete_count = 1
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep 12  8:01:35 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          151          151          149          98
              SC           239          239          236          98
              CL           110          110          110          100
              VTE           9           9           9           100
SPECIALTY    SU           85          85          85          100
              SANITY       2           2           2           100
+++++
              TOTAL        602          602          597          99
+++++

```

=====
Regression Report For VGT - Project = r400 Created on Fri Sep 27 21:24:17 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep27
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Sep27
PASS			
3	cp_e2_one_blit		2002Sep27
PASS			
4	cp_e2_one_line		2002Sep27
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Sep27
PASS			
6	cp_e2paint_565		2002Sep27
PASS			
7	cp_e2paint_multi		2002Sep27
PASS			
8	cp_e2polyscanlines_brush		2002Sep27
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep27
PASS			
10	cp_e2smalltext_jcl		2002Sep27
PASS			
11	cp_simple_triangle		2002Sep27
PASS			
12	primlib_1st_tri_june15		2002Sep27
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep27
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep27
PASS			
15	primlib_parameterized_simple_triangle		2002Sep27
PASS			
16	primlib_template_simple_triangle		2002Sep27
PASS			
17	primlib_tex_tri		2002Sep27
PASS			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep27
PASS			
20	r400cl_clip_disable_01		2002Sep27

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep27
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep27
PASS		
23	r400cl_cull_only_ena_01	2002Sep27
PASS		
24	r400cl_edgeflags_01	2002Sep27
PASS		
25	r400cl_edgeflags_01_bug	2002Sep27
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep27
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep27
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep27
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep27
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep27
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep27
PASS		
32	r400cl_edgeflags_gband_01	2002Sep27
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep27
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep27
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep27
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep27
PASS		
37	r400cl_frustum_01	2002Sep27
PASS		
38	r400cl_frustum_02	2002Sep27
PASS		
39	r400cl_frustum_03	2002Sep27
PASS		
40	r400cl_frustum_04	2002Sep27
PASS		
41	r400cl_frustum_05	2002Sep27
PASS		
42	r400cl_frustum_06	2002Sep27
PASS		
43	r400cl_frustum_07	2002Sep27
PASS		
44	r400cl_frustum_08	2002Sep27

PASS		
45	r400cl_frustum_09	2002Sep27
PASS		
46	r400cl_frustum_10	2002Sep27
PASS		
47	r400cl_frustum_11	2002Sep27
PASS		
48	r400cl_frustum_12	2002Sep27
PASS		
49	r400cl_frustum_13	2002Sep27
PASS		
50	r400cl_frustum_14	2002Sep27
PASS		
51	r400cl_frustum_15	2002Sep27
PASS		
52	r400cl_frustum_16	2002Sep27
PASS		
53	r400cl_frustum_17	2002Sep27
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54	r400cl_frustum_18	2002Sep27
PASS		
55	r400cl_frustum_19	2002Sep27
PASS		
56	r400cl_frustum_20	2002Sep27
PASS		
57	r400cl_frustum_21	2002Sep27
PASS		
58	r400cl_frustum_22	2002Sep27
PASS		
59	r400cl_frustum_23	2002Sep27
PASS		
60	r400cl_frustum_24	2002Sep27
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61	r400cl_frustum_25	2002Sep27
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62	r400cl_frustum_26	2002Sep27
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63	r400cl_frustum_27	2002Sep27
PASS		
64	r400cl_frustum_28	2002Sep27
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep27
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep27
PASS		
67	r400cl_frustum_lines_01	2002Sep27
PASS		
68	r400cl_frustum_lines_02	2002Sep27

PASS		
69	r400cl_frustum_lines_03	2002Sep27
PASS		
70	r400cl_frustum_lines_04	2002Sep27
PASS		
71	r400cl_frustum_lines_05	2002Sep27
PASS		
72	r400cl_frustum_lines_06	2002Sep27
PASS		
73	r400cl_frustum_lines_07	2002Sep27
PASS		
74	r400cl_frustum_lines_08	2002Sep27
PASS		
75	r400cl_frustum_lines_09	2002Sep27
PASS		
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PASS		
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79	r400cl_frustum_lines_13	2002Sep27
PASS		
80	r400cl_frustum_lines_14	2002Sep27
PASS		
81	r400cl_frustum_lines_15	2002Sep27
PASS		
82	r400cl_frustum_lines_16	2002Sep27
PASS		
83	r400cl_frustum_lines_17	2002Sep27
PASS		
84	r400cl_frustum_lines_18	2002Sep27
PASS		
85	r400cl_frustum_lines_19	2002Sep27
PASS		
86	r400cl_frustum_lines_20	2002Sep27
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87	r400cl_frustum_lines_21	2002Sep27
PASS		
88	r400cl_frustum_lines_22	2002Sep27
PASS		
89	r400cl_frustum_lines_23	2002Sep27
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90	r400cl_frustum_lines_24	2002Sep27
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91	r400cl_gband_04	2002Sep27
PASS		
92	r400cl_gband_tcl_01	2002Sep27

PASS		
93	r400cl_nan_kill_01_bug	2002Sep27
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Sep27
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95	r400cl_polymode_line_fill_01_bug	2002Sep27
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96	r400cl_simple_triangle_01	2002Sep27
PASS		
97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep27
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98	r400cl_ucp_combos_01	2002Sep27
PASS		
99	r400cl_ucp_combos_02	2002Sep27
PASS		
100	r400cl_ucp_combos_03	2002Sep27
PASS		
101	r400cl_ucp_combos_04	2002Sep27
PASS		
102	r400cl_ucp_combos_05	2002Sep27
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104	r400cl_ucp_combos_07	2002Sep27
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113	r400cl_ucp_combos_16	2002Sep27
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118	r400cl_ucp_combos_21	2002Sep27
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121	r400cl_ucp_combos_24	2002Sep27
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122	r400cl_ucp_combos_25	2002Sep27
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123	r400cl_ucp_combos_26	2002Sep27
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125	r400cl_ucp_combos_28	2002Sep27
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126	r400cl_ucp_combos_29	2002Sep27
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127	r400cl_ucp_combos_30	2002Sep27
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128	r400cl_ucp_pointlist_01	2002Sep27
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129	r400cl_vertex_reuse_clip_01	2002Sep27
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130	r400cl_vtx_kill_01	2002Sep27
PASS		
131	r400cl_vtx_kill_02	2002Sep27
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132	r400cl_w_eq_0	2002Sep27
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133	r400rbm_simple_triangle_01	2002Sep27
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134	r400sanity_persp_projection_01	2002Sep27
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135	r400sanity_vfd_texture_sample_01	2002Sep27
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136	r400sc_baryc_01	2002Sep27
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137	r400sc_bres_cntl_01	2002Sep7
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138	r400sc_bres_cntl_02	2002Sep27
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139	r400sc_bres_cntl_03	2002Sep27
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140	r400sc_bres_cntl_04	2002Sep27

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141	r400sc_bres_cntl_w2k_01	2002Sep27
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142	r400sc_bres_cntl_w9x_01	2002Sep27
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143	r400sc_clip_rect_01	2002Sep27
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144	r400sc_clip_rect_02	2002Sep27
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145	r400sc_clip_rect_03	2002Sep27
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146	r400sc_clip_rect_04	2002Sep27
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147	r400sc_clip_rect_fc_01	2002Sep27
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148	r400sc_diamond_exit_01	2002Sep27
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149	r400sc_diamond_exit_02	2002Sep27
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150	r400sc_diamond_exit_03	2002Sep27
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151	r400sc_diamond_exit_04	2002Sep27
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152	r400sc_diamond_exit_05	2002Sep27
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153	r400sc_jss_1x1_primitives_01	2002Sep27
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154	r400sc_jss_1x2_01	2002Sep27
PASS		
155	r400sc_jss_1x2_02	2002Sep27
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156	r400sc_jss_1x2_primitives_01	2002Sep27
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157	r400sc_jss_1x3_01	2002Sep27
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158	r400sc_jss_1x3_02	2002Sep27
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159	r400sc_jss_1x3_primitives_01	2002Sep27
PASS		
160	r400sc_jss_1x4_01	2002Sep27
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161	r400sc_jss_1x4_02	2002Sep27
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162	r400sc_jss_1x4_primitives_01	2002Sep27
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164	r400sc_jss_2x1_02	2002Sep27

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165	r400sc_jss_2x1_primtypes_01	2002Sep27
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166	r400sc_jss_2x2_01	2002Sep27
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167	r400sc_jss_2x2_02	2002Sep27
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168	r400sc_jss_2x2_primtypes_01	2002Sep27
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169	r400sc_jss_2x3_01	2002Sep27
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170	r400sc_jss_2x3_02	2002Sep27
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171	r400sc_jss_2x3_primtypes_01	2002Sep27
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172	r400sc_jss_2x4_01	2002Sep27
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173	r400sc_jss_2x4_02	2002Sep27
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174	r400sc_jss_2x4_primtypes_01	2002Sep27
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175	r400sc_jss_3x1_01	2002Sep27
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176	r400sc_jss_3x1_02	2002Sep27
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177	r400sc_jss_3x1_primtypes_01	2002Sep27
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178	r400sc_jss_3x2_01	2002Sep27
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179	r400sc_jss_3x2_02	2002Sep27
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180	r400sc_jss_3x2_primtypes_01	2002Sep27
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181	r400sc_jss_3x3_01	2002Sep27
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182	r400sc_jss_3x3_02	2002Sep27
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183	r400sc_jss_3x3_primtypes_01	2002Sep27
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184	r400sc_jss_3x4_01	2002Sep27
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185	r400sc_jss_3x4_02	2002Sep27
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186	r400sc_jss_3x4_03	2002Sep27
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187	r400sc_jss_3x4_primtypes_01	2002Sep27
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188	r400sc_jss_4x1_01	2002Sep27

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189	r400sc_jss_4x1_02	2002Sep27
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191	r400sc_jss_4x2_01	2002Sep27
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192	r400sc_jss_4x2_02	2002Sep27
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193	r400sc_jss_4x2_primitives_01	2002Sep27
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194	r400sc_jss_4x3_01	2002Sep27
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195	r400sc_jss_4x3_02	2002Sep27
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197	r400sc_jss_4x4_01	2002Sep27
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198	r400sc_jss_4x4_02	2002Sep27
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199	r400sc_jss_4x4_03	2002Sep27
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200	r400sc_jss_4x4_aa_mask_01	2002Sep27
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201	r400sc_jss_4x4_aa_mask_02	2002Sep27
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202	r400sc_jss_4x4_fc_01	2002Sep27
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203	r400sc_jss_4x4_fc_02	2002Sep27
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204	r400sc_jss_4x4_max_dist_01	2002Sep27
PASS		
205	r400sc_jss_4x4_primitives_01	2002Sep27
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206	r400sc_line_dx10_eq_0_01	2002Sep27
PASS		
207	r400sc_line_dx10_ge_0_01	2002Sep27
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208	r400sc_line_dx10_lt_0_01	2002Sep27
PASS		
209	r400sc_line_dy10_eq_0_01	2002Sep27
PASS		
210	r400sc_line_dy10_ge_0_01	2002Sep27
PASS		
211	r400sc_line_dy10_lt_0_01	2002Sep27
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212	r400sc_line_expand_width_msa_8_01	2002Sep27

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213	r400sc_line_expand_width_msa_8_02	2002Sep27
PASS		
214	r400sc_line_expand_width_msa_8_03	2002Sep27
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215	r400sc_line_jss_3x4_01	2002Sep27
PASS		
216	r400sc_line_list_01	2002Sep27
PASS		
217	r400sc_line_list_02	2002Sep27
PASS		
218	r400sc_line_list_concentric_circle_01	2002Sep27
PASS		
219	r400sc_line_list_concentric_circle_02	2002Sep27
PASS		
220	r400sc_line_list_concentric_circle_03	2002Sep27
PASS		
221	r400sc_line_list_textured_01	2002Sep27
PASS		
222	r400sc_line_list_verify_st_01	2002Sep27
PASS		
223	r400sc_line_list_verify_st_01_bug	2002Sep27
PASS		
224	r400sc_line_msa_8_01	2002Sep27
PASS		
225	r400sc_line_msa_8_textured_01	2002Sep27
PASS		
226	r400sc_line_stipple_01	2002Sep27
PASS		
227	r400sc_line_stipple_02	2002Sep27
PASS		
228	r400sc_line_stipple_03	2002Sep27
PASS		
229	r400sc_line_stipple_04	2002Sep27
PASS		
230	r400sc_line_stipple_05	2002Sep27
PASS		
231	r400sc_line_stipple_06	2002Sep27
PASS		
232	r400sc_line_stipple_07	2002Sep27
PASS		
233	r400sc_line_stipple_08	2002Sep27
PASS		
234	r400sc_line_stipple_09	2002Sep27
PASS		
235	r400sc_line_stipple_10	2002Sep27
PASS		
236	r400sc_line_stipple_11	2002Sep27

PASS		
237	r400sc_line_stipple_12	2002Sep27
PASS		
238	r400sc_line_stipple_13	2002Sep27
PASS		
239	r400sc_line_stipple_14	2002Sep27
PASS		
240	r400sc_line_stipple_fc_08	2002Sep27
PASS		
241	r400sc_line_strip_stipple_01	2002Sep25
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242	r400sc_msa_1_01	2002Sep27
PASS		
243	r400sc_msa_1_primitives_01	2002Sep27
PASS		
244	r400sc_msa_1_rectangle_list_01	2002Sep27
PASS		
245	r400sc_msa_1_rectangle_list_02	2002Sep27
PASS		
246	r400sc_msa_1_rectangle_list_03	2002Sep27
PASS		
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248	r400sc_msa_1_rectangle_list_05	2002Sep27
PASS		
249	r400sc_msa_1_rectangle_list_06	2002Sep27
PASS		
250	r400sc_msa_1_rectangle_list_07	2002Sep27
PASS		
251	r400sc_msa_1_rectangle_list_08	2002Sep27
PASS		
252	r400sc_msa_1_zbuffer_rectangle_list_01	2002Sep27
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576	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep27
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577	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep27
PASS		
578	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep27
PASS		
579	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep27
PASS		
580	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep27
PASS		
581	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep27
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582	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep27
PASS		
583	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep27
PASS		
584	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep27
PASS		
585	r400vgt_hos_PNT_edge_detection_01	2002Sep27
PASS		
586	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep27
PASS		
587	r400vgt_hos_RPatch_cp_02	2002Sep27
PASS		
588	r400vgt_hos_RPatch_lp_02	2002Sep27
PASS		
589	r400vgt_hos_TPatch_01	2002Sep27
INCOMPLETE		
590	r400vgt_hos_TPatch_02	2002Sep27
INCOMPLETE		
591	r400vgt_hos_auto_index_line_list_01	2002Sep27
PASS		
592	r400vgt_hos_auto_index_quad_list_01	2002Sep27
PASS		
593	r400vgt_hos_auto_index_triangle_list_01	2002Sep27
PASS		
594	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep27
PASS		
595	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep27
PASS		
596	r400vgt_immed_index_line_list_01	2002Sep27

PASS		
597	r400vgt_immed_index_line_loop_01	2002Sep27
PASS		
598	r400vgt_immed_index_line_strip_01	2002Sep27
PASS		
599	r400vgt_immed_index_points_01	2002Sep27
PASS		
600	r400vgt_immed_index_polygon_01	2002Sep27
PASS		
601	r400vgt_immed_index_primitives_01	2002Sep27
PASS		
602	r400vgt_immed_index_quad_list_01	2002Sep27
PASS		
603	r400vgt_immed_index_quad_strip_01	2002Sep27
PASS		
604	r400vgt_immed_index_rectangle_list_01	2002Sep27
PASS		
605	r400vgt_immed_index_tri_fan_01	2002Sep27
PASS		
606	r400vgt_immed_index_tri_list_01	2002Sep27
PASS		
607	r400vgt_immed_index_tri_strip_01	2002Sep27
PASS		
608	r400vgt_immed_index_tri_wflags_01	2002Sep27
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609	r400vgt_index_dealloc_line_list_01	2002Sep27
PASS		
610	r400vgt_index_dealloc_points_01	2002Sep27
PASS		
611	r400vgt_index_dealloc_triangle_list_01	2002Sep27
PASS		
612	r400vgt_index_min_max_01	2002Sep27
PASS		
613	r400vgt_index_min_max_02	2002Sep27
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614	r400vgt_index_offset_01	2002Sep27
PASS		
615	r400vgt_index_offset_02	2002Sep27
PASS		
616	r400vgt_index_offset_03	2002Sep27
PASS		
617	r400vgt_index_size_01	2002Sep27
PASS		
618	r400vgt_index_source_switch_01	2002Sep27
PASS		
619	r400vgt_line_list_01	2002Sep27
PASS		
620	r400vgt_line_list_02	2002Sep27

PASS		
621	r400vgt_line_loop_01	2002Sep27
PASS		
622	r400vgt_line_loop_02	2002Sep27
PASS		
623	r400vgt_line_strip_01	2002Sep27
PASS		
624	r400vgt_line_strip_02	2002Sep27
PASS		
625	r400vgt_multi_context_01	2002Sep27
PASS		
626	r400vgt_multi_context_02	2002Sep27
PASS		
627	r400vgt_multi_context_03	2002Sep27
PASS		
628	r400vgt_multi_context_04	2002Sep27
PASS		
629	r400vgt_multi_context_05	2002Sep27
PASS		
630	r400vgt_multi_context_06	2002Sep27
PASS		
631	r400vgt_multi_context_07	2002Sep27
PASS		
632	r400vgt_multi_context_08	2002Sep27
PASS		
633	r400vgt_multi_context_09	2002Sep27
PASS		
634	r400vgt_multi_context_10	2002Sep27
PASS		
635	r400vgt_multi_context_11	2002Sep27
PASS		
636	r400vgt_multi_context_12	2002Sep27
PASS		
637	r400vgt_multi_prim_reset_index_all_01	2002Sep27
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
638	r400vgt_multi_prim_reset_index_all_02	2002Sep27
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
639	r400vgt_pass_thru_all_prims_01	2002Sep27
PASS		
640	r400vgt_point_list_01	2002Sep27
PASS		
641	r400vgt_point_list_02	2002Sep27
PASS		
642	r400vgt_polygon_01	2002Sep27
PASS		
643	r400vgt_polygon_02	2002Sep27
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644	r400vgt_provoking_vtx_all_01	2002Sep27

PASS		
645	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep27
PASS		
646	r400vgt_provoking_vtx_polygon_01	2002Sep27
PASS		
647	r400vgt_provoking_vtx_quad_list_01	2002Sep27
PASS		
648	r400vgt_provoking_vtx_quad_strip_01	2002Sep27
PASS		
649	r400vgt_provoking_vtx_tri_fan_01	2002Sep27
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650	r400vgt_provoking_vtx_tri_strip_01	2002Sep27
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PASS		
652	r400vgt_quad_list_02	2002Sep27
PASS		
653	r400vgt_quad_strip_01	2002Sep27
PASS		
654	r400vgt_quad_strip_02	2002Sep27
PASS		
655	r400vgt_rectangle_list_01	2002Sep27
PASS		
656	r400vgt_rectangle_list_02	2002Sep27
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657	r400vgt_reuse_depth_line_list_01	2002Sep27
PASS		
658	r400vgt_reuse_depth_point_list_01	2002Sep27
PASS		
659	r400vgt_reuse_depth_triangle_list_01	2002Sep27
PASS		
660	r400vgt_reuse_index_line_list_01	2002Sep27
PASS		
661	r400vgt_reuse_index_point_list_01	2002Sep27
PASS		
662	r400vgt_reuse_index_triangle_list_01	2002Sep27
PASS		
663	r400vgt_reuse_index_triangle_list_02	2002Sep27
PASS		
664	r400vgt_reuse_index_triangle_list_03	2002Sep27
PASS		
665	r400vgt_suppress_eop_01	0:0:1 2002Sep27
PASS		
666	r400vgt_suppress_eop_02	0:0:1 2002Sep27
PASS		
667	r400vgt_suppress_eop_03	0:0:1 2002Sep27
PASS		
668	r400vgt_suppress_eop_04	0:0:1 2002Sep27


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PASS
669 r400vgt_suppress_eop_05          0:0:1    2002Sep26
PASS
670 r400vgt_triangle_fan_01         0:0:1    2002Sep27
PASS
671 r400vgt_triangle_fan_02         0:0:1    2002Sep27
PASS
672 r400vgt_triangle_list_01        0:0:1    2002Sep27
PASS
673 r400vgt_triangle_list_02        0:0:1    2002Sep27
PASS
674 r400vgt_triangle_strip_01       0:0:1    2002Sep27
PASS
675 r400vgt_triangle_strip_02       0:0:1    2002Sep27
PASS
676 r400vgt_triangle_wflags_01      0:0:1    2002Sep27
PASS
677 r400vgt_triangle_wflags_02      0:0:1    2002Sep27
PASS
678 r400vgt_viz_query_01            0:0:1    2002Sep27
PASS
679 r400vgt_vtx_vector_packing_01   0:0:1    2002Sep27
PASS
680 r400vte_coverage_01             0:0:1    2002Sep27
PASS
681 r400vte_simple_point_01         0:0:1    2002Sep27
PASS
682 r400vte_simple_triangle_01      0:0:1    2002Sep27
PASS
683 r400vte_w0_fmt_01               0:0:1    2002Sep27
PASS
684 r400vte_w0_fmt_03               0:0:1    2002Sep27
PASS
685 r400vte_xy_fmt_01               0:0:1    2002Sep27
PASS
686 r400vte_xy_fmt_mk_01_bug        0:0:1    2002Sep27
PASS
687 r400vte_xyz_scale_01            0:0:1    2002Sep27
PASS
688 r400vte_xyz_scale_02            0:0:1    2002Sep27
PASS
689 r400vte_z_fmt_01                0:0:1    2002Sep27
PASS

```

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=====
Tests_Run_count = 689 Pass_count = 685 Fail_count = 2 Incomplete_count = 2
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep30
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Sep30
PASS			
3	cp_e2_one_blit		2002Sep30
PASS			
4	cp_e2_one_line		2002Sep30
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Sep30
PASS			
6	cp_e2paint_565		2002Sep30
PASS			
7	cp_e2paint_multi		2002Sep30
PASS			
8	cp_e2polyscanlines_brush		2002Sep30
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep30
PASS			
10	cp_e2smalltext_jcl		2002Sep30
PASS			
11	cp_simple_triangle		2002Sep30
PASS			
12	primlib_1st_tri_june15		2002Sep30
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep30
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep30
PASS			
15	primlib_parameterized_simple_triangle		2002Sep30
PASS			
16	primlib_template_simple_triangle		2002Sep30
PASS			
17	primlib_tex_tri		2002Sep30
PASS			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep30
PASS			
20	r400cl_clip_disable_01		2002Sep30

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep30
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep30
PASS		
23	r400cl_cull_only_ena_01	2002Sep30
PASS		
24	r400cl_edgeflags_01	2002Sep30
PASS		
25	r400cl_edgeflags_01_bug	2002Sep30
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep30
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep30
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep30
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep30
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep30
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep30
PASS		
32	r400cl_edgeflags_gband_01	2002Sep30
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep30
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep30
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep30
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep30
PASS		
37	r400cl_frustum_01	2002Sep30
PASS		
38	r400cl_frustum_02	2002Sep30
PASS		
39	r400cl_frustum_03	2002Sep30
PASS		
40	r400cl_frustum_04	2002Sep30
PASS		
41	r400cl_frustum_05	2002Sep30
PASS		
42	r400cl_frustum_06	2002Sep30
PASS		
43	r400cl_frustum_07	2002Sep30
PASS		
44	r400cl_frustum_08	2002Sep30

PASS		
45	r400cl_frustum_09	2002Sep30
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57	r400cl_frustum_21	2002Sep30
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63	r400cl_frustum_27	2002Sep30
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64	r400cl_frustum_28	2002Sep30
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65	r400cl_frustum_all_vols_lines	2002Sep30
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66	r400cl_frustum_all_vols_tris	2002Sep30
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67	r400cl_frustum_lines_01	2002Sep30
PASS		
68	r400cl_frustum_lines_02	2002Sep30

PASS		
69	r400cl_frustum_lines_03	2002Sep30
PASS		
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PASS		
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83	r400cl_frustum_lines_17	2002Sep30
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87	r400cl_frustum_lines_21	2002Sep30
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88	r400cl_frustum_lines_22	2002Sep30
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90	r400cl_frustum_lines_24	2002Sep30
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91	r400cl_gband_04	2002Sep30
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92	r400cl_gband_tcl_01	2002Sep30

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93	r400cl_nan_kill_01_bug	2002Sep30
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94	r400cl_pointlist_vertex_state_ucp_01	2002Sep30
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95	r400cl_polymode_line_fill_01_bug	2002Sep30
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96	r400cl_simple_triangle_01	2002Sep30
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97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep30
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98	r400cl_ucp_combos_01	2002Sep30
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99	r400cl_ucp_combos_02	2002Sep30
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100	r400cl_ucp_combos_03	2002Sep30
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129	r400cl_vertex_reuse_clip_01	2002Sep30
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130	r400cl_vtx_kill_01	2002Sep30
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131	r400cl_vtx_kill_02	2002Sep30
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133	r400rbm_simple_triangle_01	2002Sep30
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134	r400sanity_persp_projection_01	2002Sep30
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135	r400sanity_vfd_texture_sample_01	2002Sep30
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136	r400sc_baryc_01	2002Sep30
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137	r400sc_bres_cntl_01	2002Sep7
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138	r400sc_bres_cntl_02	2002Sep30
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139	r400sc_bres_cntl_03	2002Sep30
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140	r400sc_bres_cntl_04	2002Sep30

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142	r400sc_bres_cntl_w9x_01	2002Sep30
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147	r400sc_clip_rect_fc_01	2002Sep30
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155	r400sc_jss_1x2_02	2002Sep30
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156	r400sc_jss_1x2_primtypes_01	2002Sep30
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158	r400sc_jss_1x3_02	2002Sep30
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161	r400sc_jss_1x4_02	2002Sep30
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162	r400sc_jss_1x4_primtypes_01	2002Sep30
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174	r400sc_jss_2x4_primtypes_01	2002Sep30
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176	r400sc_jss_3x1_02	2002Sep30
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177	r400sc_jss_3x1_primtypes_01	2002Sep30
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179	r400sc_jss_3x2_02	2002Sep30
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182	r400sc_jss_3x3_02	2002Sep30
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183	r400sc_jss_3x3_primtypes_01	2002Sep30
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186	r400sc_jss_3x4_03	2002Sep30
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187	r400sc_jss_3x4_primtypes_01	2002Sep30
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189	r400sc_jss_4x1_02	2002Sep30
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199	r400sc_jss_4x4_03	2002Sep30
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203	r400sc_jss_4x4_fc_02	2002Sep30
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205	r400sc_jss_4x4_primitives_01	2002Sep30
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208	r400sc_line_dx10_lt_0_01	2002Sep30
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209	r400sc_line_dy10_eq_0_01	2002Sep30
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211	r400sc_line_dy10_lt_0_01	2002Sep30
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212	r400sc_line_expand_width_msa_8_01	2002Sep30

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213	r400sc_line_expand_width_msa_8_02	2002Sep30
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221	r400sc_line_list_textured_01	2002Sep30
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222	r400sc_line_list_verify_st_01	2002Sep30
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223	r400sc_line_list_verify_st_01_bug	2002Sep30
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224	r400sc_line_msa_8_01	2002Sep30
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546	r400vgt_dma_index_tri_wflags_01	2002Sep30
PASS		
547	r400vgt_dma_swap_idx16_01	2002Sep30
PASS		
548	r400vgt_dma_swap_idx32_01	2002Sep30

PASS		
549	r400vgt_edgeflags_polygon_01	2002Sep30
PASS		
550	r400vgt_edgeflags_quad_list_01	2002Sep30
PASS		
551	r400vgt_edgeflags_quad_strip_01	2002Sep30
PASS		
552	r400vgt_edgeflags_triangle_fan_01	2002Sep30
PASS		
553	r400vgt_edgeflags_triangle_list_01	2002Sep30
PASS		
554	r400vgt_edgeflags_triangle_strip_01	2002Sep30
PASS		
555	r400vgt_edgeflags_triangle_wflags_01	2002Sep30
PASS		
556	r400vgt_event_handling_01	2002Sep26
PASS		
557	r400vgt_ext2int_index_line_list_01	2002Sep30
PASS		
558	r400vgt_ext2int_index_line_loop_01	2002Sep30
PASS		
559	r400vgt_ext2int_index_line_strip_01	2002Sep30
PASS		
560	r400vgt_ext2int_index_points_01	2002Sep30
PASS		
561	r400vgt_ext2int_index_polygon_01	2002Sep30
PASS		
562	r400vgt_ext2int_index_quad_list_01	2002Sep30
PASS		
563	r400vgt_ext2int_index_quad_strip_01	2002Sep30
PASS		
564	r400vgt_ext2int_index_rectangle_list_01	2002Sep30
PASS		
565	r400vgt_ext2int_index_triangle_fan_01	2002Sep30
PASS		
566	r400vgt_ext2int_index_triangle_list_01	2002Sep30
PASS		
567	r400vgt_ext2int_index_triangle_strip_01	2002Sep30
PASS		
568	r400vgt_ext2int_index_triangle_wflags_01	2002Sep30
PASS		
569	r400vgt_hos_LPatch_01	2002Sep30
PASS		
570	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep30
PASS		
571	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep30
PASS		
572	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep30

PASS		
573	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep30
PASS		
574	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep30
PASS		
575	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep30
PASS		
576	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep30
PASS		
577	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep30
PASS		
578	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep30
PASS		
579	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep30
PASS		
580	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep30
PASS		
581	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep30
PASS		
582	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep30
PASS		
583	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep30
PASS		
584	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep30
PASS		
585	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep30
PASS		
586	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep30
PASS		
587	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep30
PASS		
588	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep30
PASS		
589	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep30
PASS		
590	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep30
PASS		
591	r400vgt_hos_PNT_edge_detection_01	2002Sep30
PASS		
592	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep30
PASS		
593	r400vgt_hos_RPatch_cp_02	2002Sep30
PASS		
594	r400vgt_hos_RPatch_lp_02	2002Sep30
PASS		
595	r400vgt_hos_TPatch_01	2002Sep30
INCOMPLETE		
596	r400vgt_hos_TPatch_02	2002Sep30

INCOMPLETE

597	r400vgt_hos_auto_index_line_list_01	2002Sep30
	PASS	
598	r400vgt_hos_auto_index_quad_list_01	2002Sep30
	PASS	
599	r400vgt_hos_auto_index_triangle_list_01	2002Sep30
	PASS	
600	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep30
	PASS	
601	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep30
	PASS	
602	r400vgt_immed_index_line_list_01	2002Sep30
	PASS	
603	r400vgt_immed_index_line_loop_01	2002Sep30
	PASS	
604	r400vgt_immed_index_line_strip_01	2002Sep30
	PASS	
605	r400vgt_immed_index_points_01	2002Sep30
	PASS	
606	r400vgt_immed_index_polygon_01	2002Sep30
	PASS	
607	r400vgt_immed_index_primitives_01	2002Sep30
	PASS	
608	r400vgt_immed_index_quad_list_01	2002Sep30
	PASS	
609	r400vgt_immed_index_quad_strip_01	2002Sep30
	PASS	
610	r400vgt_immed_index_rectangle_list_01	2002Sep30
	PASS	
611	r400vgt_immed_index_tri_fan_01	2002Sep30
	PASS	
612	r400vgt_immed_index_tri_list_01	2002Sep30
	PASS	
613	r400vgt_immed_index_tri_strip_01	2002Sep30
	PASS	
614	r400vgt_immed_index_tri_wflags_01	2002Sep30
	PASS	
615	r400vgt_index_dealloc_line_list_01	2002Sep30
	PASS	
616	r400vgt_index_dealloc_points_01	2002Sep30
	PASS	
617	r400vgt_index_dealloc_triangle_list_01	2002Sep30
	PASS	
618	r400vgt_index_min_max_01	2002Sep30
	PASS	
619	r400vgt_index_min_max_02	2002Sep30
	PASS	
620	r400vgt_index_offset_01	2002Sep30

PASS		
621	r400vgt_index_offset_02	2002Sep30
PASS		
622	r400vgt_index_offset_03	2002Sep30
PASS		
623	r400vgt_index_size_01	2002Sep30
PASS		
624	r400vgt_index_source_switch_01	2002Sep30
PASS		
625	r400vgt_line_list_01	2002Sep30
PASS		
626	r400vgt_line_list_02	2002Sep30
PASS		
627	r400vgt_line_loop_01	2002Sep30
PASS		
628	r400vgt_line_loop_02	2002Sep30
PASS		
629	r400vgt_line_strip_01	2002Sep30
PASS		
630	r400vgt_line_strip_02	2002Sep30
PASS		
631	r400vgt_multi_context_01	2002Sep30
PASS		
632	r400vgt_multi_context_02	2002Sep30
PASS		
633	r400vgt_multi_context_03	2002Sep30
PASS		
634	r400vgt_multi_context_04	2002Sep30
PASS		
635	r400vgt_multi_context_05	2002Sep30
PASS		
636	r400vgt_multi_context_06	2002Sep30
PASS		
637	r400vgt_multi_context_07	2002Sep30
PASS		
638	r400vgt_multi_context_08	2002Sep30
PASS		
639	r400vgt_multi_context_09	2002Sep30
PASS		
640	r400vgt_multi_context_10	2002Sep30
PASS		
641	r400vgt_multi_context_11	2002Sep30
PASS		
642	r400vgt_multi_context_12	2002Sep30
PASS		
643	r400vgt_multi_prim_reset_index_all_01	2002Sep30
PASS		
644	r400vgt_multi_prim_reset_index_all_02	2002Sep30

PASS		
645	r400vgt_pass_thru_all_prims_01	2002Sep30
PASS		
646	r400vgt_point_list_01	2002Sep30
PASS		
647	r400vgt_point_list_02	2002Sep30
PASS		
648	r400vgt_polygon_01	2002Sep30
PASS		
649	r400vgt_polygon_02	2002Sep30
PASS		
650	r400vgt_provoking_vtx_all_01	2002Sep30
PASS		
651	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep30
PASS		
652	r400vgt_provoking_vtx_polygon_01	2002Sep30
PASS		
653	r400vgt_provoking_vtx_quad_list_01	2002Sep30
PASS		
654	r400vgt_provoking_vtx_quad_strip_01	2002Sep30
PASS		
655	r400vgt_provoking_vtx_tri_fan_01	2002Sep30
PASS		
656	r400vgt_provoking_vtx_tri_strip_01	2002Sep30
PASS		
657	r400vgt_quad_list_01	2002Sep30
PASS		
658	r400vgt_quad_list_02	2002Sep30
PASS		
659	r400vgt_quad_strip_01	2002Sep30
PASS		
660	r400vgt_quad_strip_02	2002Sep30
PASS		
661	r400vgt_rectangle_list_01	2002Sep30
PASS		
662	r400vgt_rectangle_list_02	2002Sep30
PASS		
663	r400vgt_reuse_depth_line_list_01	2002Sep30
PASS		
664	r400vgt_reuse_depth_point_list_01	2002Sep30
PASS		
665	r400vgt_reuse_depth_triangle_list_01	2002Sep30
PASS		
666	r400vgt_reuse_index_line_list_01	2002Sep30
PASS		
667	r400vgt_reuse_index_point_list_01	2002Sep30
PASS		
668	r400vgt_reuse_index_triangle_list_01	2002Sep30

PASS			
669	r400vgt_reuse_index_triangle_list_02		2002Sep30
PASS			
670	r400vgt_reuse_index_triangle_list_03		2002Sep30
PASS			
671	r400vgt_suppress_eop_01	0:0:1	2002Sep30
PASS			
672	r400vgt_suppress_eop_02	0:0:1	2002Sep30
PASS			
673	r400vgt_suppress_eop_03	0:0:1	2002Sep30
PASS			
674	r400vgt_suppress_eop_04	0:0:1	2002Sep30
PASS			
675	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
676	r400vgt_triangle_fan_01	0:0:1	2002Sep30
PASS			
677	r400vgt_triangle_fan_02	0:0:1	2002Sep30
PASS			
678	r400vgt_triangle_list_01	0:0:1	2002Sep30
PASS			
679	r400vgt_triangle_list_02	0:0:1	2002Sep30
PASS			
680	r400vgt_triangle_strip_01	0:0:1	2002Sep30
PASS			
681	r400vgt_triangle_strip_02	0:0:1	2002Sep30
PASS			
682	r400vgt_triangle_wflags_01	0:0:1	2002Sep30
PASS			
683	r400vgt_triangle_wflags_02	0:0:1	2002Sep30
PASS			
684	r400vgt_viz_query_01	0:0:1	2002Sep30
PASS			
685	r400vgt_vtx_vector_packing_01	0:0:1	2002Sep30
PASS			
686	r400vte_coverage_01	0:0:1	2002Sep30
PASS			
687	r400vte_simple_point_01	0:0:1	2002Sep30
PASS			
688	r400vte_simple_triangle_01	0:0:1	2002Sep30
PASS			
689	r400vte_w0_fmt_01	0:0:1	2002Sep30
PASS			
690	r400vte_w0_fmt_03	0:0:1	2002Sep30
PASS			
691	r400vte_xy_fmt_01	0:0:1	2002Sep30
PASS			
692	r400vte_xy_fmt_mk_01_bug	0:0:1	2002Sep30

PASS
693 r400vte_xyz_scale_01 0:0:1 2002Sep30
PASS
694 r400vte_xyz_scale_02 0:0:1 2002Sep30
PASS
695 r400vte_z_fmt_01 0:0:1 2002Sep30
PASS

=====
Tests_Run_count = 695 Pass_count = 693 Fail_count = 0 Incomplete_count = 2
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep  9  9:03:49 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          151          151          151      100
              SC           235          235          233       99
              CL           110          110          110      100
              VTE           9           9           9       100
SPECIALTY    SU           82           82           82      100
              SANITY       2           2           2       100
+++++
              TOTAL        595          595          593       99
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep3
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep3
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep3
PASS			
4	primlib_parameterized_simple_triangle		2002Sep3
PASS			
5	primlib_template_simple_triangle		2002Sep3
PASS			
6	primlib_tex_tri		2002Sep3
PASS			
7	r400cl_clip_10_verts_01		2002Sep3
PASS			
8	r400cl_clip_disable_01		2002Sep3
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep3
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep3
PASS			
11	r400cl_cull_only_ena_01		2002Sep3
PASS			
12	r400cl_edgeflags_01		2002Sep3
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep3
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep3
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep3
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep3
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep3
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep3
PASS			
19	r400cl_edgeflags_gband_01		2002Sep3
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep3

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep3
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep3
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep3
PASS		
24	r400cl_frustum_01	2002Sep3
PASS		
25	r400cl_frustum_02	2002Sep3
PASS		
26	r400cl_frustum_03	2002Sep3
PASS		
27	r400cl_frustum_04	2002Sep3
PASS		
28	r400cl_frustum_05	2002Sep3
PASS		
29	r400cl_frustum_06	2002Sep3
PASS		
30	r400cl_frustum_07	2002Sep3
PASS		
31	r400cl_frustum_08	2002Sep3
PASS		
32	r400cl_frustum_09	2002Sep3
PASS		
33	r400cl_frustum_10	2002Sep3
PASS		
34	r400cl_frustum_11	2002Sep3
PASS		
35	r400cl_frustum_12	2002Sep3
PASS		
36	r400cl_frustum_13	2002Sep3
PASS		
37	r400cl_frustum_14	2002Sep3
PASS		
38	r400cl_frustum_15	2002Sep3
PASS		
39	r400cl_frustum_16	2002Sep3
PASS		
40	r400cl_frustum_17	2002Sep3
PASS		
41	r400cl_frustum_18	2002Sep3
PASS		
42	r400cl_frustum_19	2002Sep3
PASS		
43	r400cl_frustum_20	2002Sep3
PASS		
44	r400cl_frustum_21	2002Sep3

PASS		
45	r400cl_frustum_22	2002Sep3
PASS		
46	r400cl_frustum_23	2002Sep3
PASS		
47	r400cl_frustum_24	2002Sep3
PASS		
48	r400cl_frustum_25	2002Sep3
PASS		
49	r400cl_frustum_26	2002Sep3
PASS		
50	r400cl_frustum_27	2002Sep3
PASS		
51	r400cl_frustum_28	2002Sep3
PASS		
52	r400cl_frustum_lines_01	2002Sep3
PASS		
53	r400cl_frustum_lines_02	2002Sep3
PASS		
54	r400cl_frustum_lines_03	2002Sep3
PASS		
55	r400cl_frustum_lines_04	2002Sep3
PASS		
56	r400cl_frustum_lines_05	2002Sep3
PASS		
57	r400cl_frustum_lines_06	2002Sep3
PASS		
58	r400cl_frustum_lines_07	2002Sep3
PASS		
59	r400cl_frustum_lines_08	2002Sep3
PASS		
60	r400cl_frustum_lines_09	2002Sep3
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61	r400cl_frustum_lines_10	2002Sep3
PASS		
62	r400cl_frustum_lines_11	2002Sep3
PASS		
63	r400cl_frustum_lines_12	2002Sep3
PASS		
64	r400cl_frustum_lines_13	2002Sep3
PASS		
65	r400cl_frustum_lines_14	2002Sep3
PASS		
66	r400cl_frustum_lines_15	2002Sep3
PASS		
67	r400cl_frustum_lines_16	2002Sep3
PASS		
68	r400cl_frustum_lines_17	2002Sep3

PASS		
69	r400cl_frustum_lines_18	2002Sep3
PASS		
70	r400cl_frustum_lines_19	2002Sep3
PASS		
71	r400cl_frustum_lines_20	2002Sep3
PASS		
72	r400cl_frustum_lines_21	2002Sep3
PASS		
73	r400cl_frustum_lines_22	2002Sep3
PASS		
74	r400cl_frustum_lines_23	2002Sep3
PASS		
75	r400cl_frustum_lines_24	2002Sep3
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76	r400cl_gband_04	2002Sep3
PASS		
77	r400cl_gband_tcl_01	2002Sep3
PASS		
78	r400cl_pointlist_vertex_state_ucp_01	2002Sep3
PASS		
79	r400cl_simple_triangle_01	2002Sep3
PASS		
80	r400cl_ucp_combos_01	2002Sep3
PASS		
81	r400cl_ucp_combos_02	2002Sep3
PASS		
82	r400cl_ucp_combos_03	2002Sep3
PASS		
83	r400cl_ucp_combos_04	2002Sep3
PASS		
84	r400cl_ucp_combos_05	2002Sep3
PASS		
85	r400cl_ucp_combos_06	2002Sep3
PASS		
86	r400cl_ucp_combos_07	2002Sep3
PASS		
87	r400cl_ucp_combos_08	2002Sep3
PASS		
88	r400cl_ucp_combos_09	2002Sep3
PASS		
89	r400cl_ucp_combos_10	2002Sep3
PASS		
90	r400cl_ucp_combos_11	2002Sep3
PASS		
91	r400cl_ucp_combos_12	2002Sep3
PASS		
92	r400cl_ucp_combos_13	2002Sep3

PASS		
93	r400cl_ucp_combos_14	2002Sep3
PASS		
94	r400cl_ucp_combos_15	2002Sep3
PASS		
95	r400cl_ucp_combos_16	2002Sep3
PASS		
96	r400cl_ucp_combos_17	2002Sep3
PASS		
97	r400cl_ucp_combos_18	2002Sep3
PASS		
98	r400cl_ucp_combos_19	2002Sep3
PASS		
99	r400cl_ucp_combos_20	2002Sep3
PASS		
100	r400cl_ucp_combos_21	2002Sep3
PASS		
101	r400cl_ucp_combos_22	2002Sep3
PASS		
102	r400cl_ucp_combos_23	2002Sep3
PASS		
103	r400cl_ucp_combos_24	2002Sep3
PASS		
104	r400cl_ucp_combos_25	2002Sep3
PASS		
105	r400cl_ucp_combos_26	2002Sep3
PASS		
106	r400cl_ucp_combos_27	2002Sep3
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107	r400cl_ucp_combos_28	2002Sep3
PASS		
108	r400cl_ucp_combos_29	2002Sep3
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109	r400cl_ucp_combos_30	2002Sep3
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110	r400cl_ucp_pointlist_01	2002Sep3
PASS		
111	r400cl_vertex_reuse_clip_01	2002Sep3
PASS		
112	r400cl_vtx_kill_01	2002Sep3
PASS		
113	r400cl_vtx_kill_02	2002Sep3
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114	r400cl_w_eq_0	2002Sep3
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115	r400sanity_persp_projection_01	2002Sep3
PASS		
116	r400sanity_vfd_texture_sample_01	2002Sep3

PASS		
117	r400sc_bres_cntl_01	2002Sep3
PASS		
118	r400sc_bres_cntl_02	2002Sep3
PASS		
119	r400sc_bres_cntl_03	2002Sep3
PASS		
120	r400sc_bres_cntl_04	2002Sep3
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121	r400sc_clip_rect_01	2002Sep3
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122	r400sc_clip_rect_02	2002Sep3
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123	r400sc_clip_rect_03	2002Sep3
PASS		
124	r400sc_clip_rect_04	2002Sep3
PASS		
125	r400sc_clip_rect_fc_01	2002Sep3
PASS		
126	r400sc_diamond_exit_01	2002Sep3
PASS		
127	r400sc_diamond_exit_02	2002Sep3
PASS		
128	r400sc_diamond_exit_03	2002Sep3
PASS		
129	r400sc_diamond_exit_04	2002Sep3
PASS		
130	r400sc_diamond_exit_05	2002Sep3
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131	r400sc_jss_1x2_01	2002Sep3
PASS		
132	r400sc_jss_1x2_02	2002Sep3
PASS		
133	r400sc_jss_1x3_01	2002Sep3
PASS		
134	r400sc_jss_1x3_02	2002Sep3
PASS		
135	r400sc_jss_1x4_01	2002Sep3
PASS		
136	r400sc_jss_1x4_02	2002Sep3
PASS		
137	r400sc_jss_2x1_01	2002Sep3
PASS		
138	r400sc_jss_2x1_02	2002Sep3
PASS		
139	r400sc_jss_2x2_01	2002Sep3
PASS		
140	r400sc_jss_2x2_02	2002Sep3

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141	r400sc_jss_2x3_01	2002Sep3
PASS		
142	r400sc_jss_2x3_02	2002Sep3
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143	r400sc_jss_2x4_01	2002Sep3
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144	r400sc_jss_2x4_02	2002Sep3
PASS		
145	r400sc_jss_3x1_01	2002Sep3
PASS		
146	r400sc_jss_3x1_02	2002Sep3
PASS		
147	r400sc_jss_3x2_01	2002Sep3
PASS		
148	r400sc_jss_3x2_02	2002Sep3
PASS		
149	r400sc_jss_3x3_01	2002Sep3
PASS		
150	r400sc_jss_3x3_02	2002Sep3
PASS		
151	r400sc_jss_3x4_01	2002Sep3
PASS		
152	r400sc_jss_3x4_02	2002Sep3
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153	r400sc_jss_3x4_03	2002Sep3
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154	r400sc_jss_4x1_01	2002Sep3
PASS		
155	r400sc_jss_4x1_02	2002Sep3
PASS		
156	r400sc_jss_4x2_01	2002Sep3
PASS		
157	r400sc_jss_4x2_02	2002Sep3
PASS		
158	r400sc_jss_4x3_01	2002Sep3
PASS		
159	r400sc_jss_4x3_02	2002Sep3
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160	r400sc_jss_4x4_01	2002Sep3
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161	r400sc_jss_4x4_02	2002Sep3
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162	r400sc_jss_4x4_03	2002Sep3
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163	r400sc_jss_4x4_aa_mask_01	2002Sep3
PASS		
164	r400sc_jss_4x4_aa_mask_02	2002Sep3

PASS		
165	r400sc_jss_4x4_fc_01	2002Sep3
PASS		
166	r400sc_jss_4x4_fc_02	2002Sep3
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167	r400sc_jss_4x4_max_dist_01	2002Sep3
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168	r400sc_line_dx10_eq_0_01	2002Sep3
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169	r400sc_line_dx10_ge_0_01	2002Sep3
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170	r400sc_line_dx10_lt_0_01	2002Sep3
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171	r400sc_line_dy10_eq_0_01	2002Sep3
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172	r400sc_line_dy10_ge_0_01	2002Sep3
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173	r400sc_line_dy10_lt_0_01	2002Sep3
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174	r400sc_line_expand_width_msa_8_01	2002Sep3
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175	r400sc_line_expand_width_msa_8_02	2002Sep3
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176	r400sc_line_expand_width_msa_8_03	2002Sep3
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177	r400sc_line_jss_3x4_01	2002Sep3
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178	r400sc_line_list_01	2002Sep3
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179	r400sc_line_list_02	2002Sep3
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180	r400sc_line_list_concentric_circle_01	2002Sep3
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181	r400sc_line_list_concentric_circle_02	2002Sep3
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182	r400sc_line_list_concentric_circle_03	2002Sep3
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183	r400sc_line_list_textured_01	2002Aug31
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184	r400sc_line_list_verify_st_01	2002Sep3
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185	r400sc_line_msa_8_01	2002Sep3
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186	r400sc_line_stipple_01	2002Sep3
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187	r400sc_line_stipple_02	2002Sep3
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192	r400sc_line_stipple_07	2002Sep3
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193	r400sc_line_stipple_08	2002Sep3
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194	r400sc_line_stipple_09	2002Sep3
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195	r400sc_line_stipple_fc_08	2002Sep3
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196	r400sc_line_strip_stipple_01	2002Sep3
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197	r400sc_msa_1_01	2002Sep3
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198	r400sc_msa_4_01	2002Sep3
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199	r400sc_msa_6_01	2002Sep3
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200	r400sc_msa_8_01	2002Sep3
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201	r400sc_msa_8_aa_mask_01	2002Sep3
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202	r400sc_msa_8_aa_mask_02	2002Sep3
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203	r400sc_msa_8_aa_mask_fc_02	2002Sep3
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204	r400sc_null_triangles_01	2002Sep3
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205	r400sc_null_triangles_fc_01	2002Sep3
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206	r400sc_point_jss_3x4_01	2002Sep3
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207	r400sc_point_list_01	2002Sep3
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208	r400sc_point_list_02	2002Sep3
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209	r400sc_point_list_03	2002Sep3
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210	r400sc_point_list_04	2002Sep3
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212	r400sc_point_list_06	2002Aug28

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214	r400sc_point_list_08	2002Sep3
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215	r400sc_point_list_09	2002Sep3
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217	r400sc_polygon_stipple_01	2002Sep3
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218	r400sc_poly_offset_01	2002Sep3
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219	r400sc_poly_offset_02	2002Sep3
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220	r400sc_poly_offset_fc_01	2002Sep3
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221	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep3
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222	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep3
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223	r400sc_prsp_byc_intrp_ref_pix_03	2002Sep3
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227	r400sc_prsp_byc_intrp_ref_pix_07	2002Sep3
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228	r400sc_prsp_byc_intrp_ref_pix_08	2002Sep3
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229	r400sc_raster_fill_rule_01	2002Sep3
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230	r400sc_raster_fill_rule_02	2002Sep3
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231	r400sc_raster_fill_rule_03	2002Sep3
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234	r400sc_raster_fill_rule_fc_01	2002Sep3
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242	r400sc_rectangle_list_08	2002Sep3
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243	r400sc_scissor_rect_01	2002Sep3
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244	r400sc_scissor_rect_02	2002Sep3
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245	r400sc_scissor_rect_03	2002Sep3
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246	r400sc_scissor_rect_04	2002Sep3
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248	r400sc_scissor_rect_fc_01	2002Sep3
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249	r400sc_set_state_01	2002Sep3
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250	r400sc_tri_16_par_64_dwords_01	2002Sep3
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251	r400sc_tri_8textures_01	2002Sep3
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252	r400sc_tri_8textures_02	2002Sep3
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253	r400sc_triangle_stipple_01	2002Sep3
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254	r400sc_tri_walk_start_vertex_01	2002Sep3
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255	r400sc_tri_walk_start_vertex_02	2002Sep3
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256	r400sc_tri_walk_start_vertex_03	2002Sep3
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257	r400sc_tri_walk_start_vertex_04	2002Sep3
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259	r400sc_tri_walk_start_vertex_06	2002Sep3
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260	r400sc_tri_walk_start_vertex_07	2002Sep3

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261	r400sc_tri_walk_start_vertex_08	2002Sep3
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262	r400sc_tri_walk_start_vertex_09	2002Sep3
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264	r400sc_tri_walk_start_vertex_11	2002Sep3
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265	r400sc_tri_walk_start_vertex_12	2002Sep3
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266	r400sc_tri_walk_start_vertex_13	2002Sep3
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267	r400sc_tri_walk_start_vertex_14	2002Sep3
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269	r400sc_tri_walk_start_vertex_16	2002Sep3
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270	r400sc_window_offset_01	2002Sep3
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271	r400sc_window_offset_fc_01	2002Sep3
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273	r400sc_zbuffer_point_list_01	2002Sep3
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274	r400sc_zbuffer_rectangle_list_01	2002Sep3
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275	r400sc_zbuffer_rectangle_list_02	2002Sep3
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276	r400sc_zbuffer_rectangle_list_fc_02	2002Sep3
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277	r400sc_zbuffer_triangle_list_01	2002Sep3
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278	r400su_4tri_text_offscreen_01	2002Sep3
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279	r400su_baryc_test_01	2002Sep3
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280	r400su_baryc_test_02	2002Sep3
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281	r400su_baryc_test_03	2002Sep3
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283	r400su_clip_edgflag_polymode_01	2002Sep3
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287	r400su_clip_pointttest_01	2002Sep3
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288	r400su_clip_pointttest_02	2002Sep3
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289	r400su_clip_pointttest_03	2002Sep3
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291	r400su_clip_polymode_test_01	2002Sep3
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292	r400su_clipvertexsortingfunctional_01	2002Sep3
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293	r400su_cullingfunctional_01	2002Sep3
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294	r400su_degentri_test_01	2002Sep3
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295	r400su_degentri_test_02	2002Sep3
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296	r400su_degentri_test_03	2002Sep3
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298	r400su_edge_flag_02	2002Sep3
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300	r400su_edgeflags_triangle_02	2002Sep3
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301	r400su_geom_sort_01	2002Sep3
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303	r400su_line_end_cap_width_functional_02	2002Sep3
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305	r400su_line_test_02	2002Sep3
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307	r400su_multi_prim_01	2002Sep3
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308	r400su_parallel_orientation_all_01	2002Sep3

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309	r400su_parallel_orientation_all_02	2002Sep3
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310	r400su_pc_management_01	2002Sep3
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311	r400su_pc_management_02	2002Sep3
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314	r400su_pointsizepresent_02	2002Sep3
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324	r400su_point_w1_h16_functional_01	2002Sep3
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325	r400su_polymodeculling_01	2002Sep3
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326	r400su_polymodefunctional_01	2002Sep3
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328	r400su_polymode_lines_degen_triangle_02	2002Sep3
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330	r400su_polymode_lines_zero_area_triangle_02	2002Sep3
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340 r400su_provoking_vtx_edgeflags_triangle_01 2002Sep3
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346 r400su_provoking_vtx_polymode_rectangle_01 2002Sep3
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352 r400su_rectangle_list_01 2002Sep3
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396	r400vgt_dma_swap_idx32_01	2002Sep3
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419	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep3
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438	r400vgt_hos_PNT_edge_detection_01	2002Sep3
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485	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep3
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487	r400vgt_quad_strip_01	2002Sep3
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491	r400vgt_reuse_depth_triangle_list_01	2002Sep3
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495	r400vgt_reuse_index_triangle_list_02	2002Sep3
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496	r400vgt_reuse_index_triangle_list_03	2002Sep3
PASS		
497	r400vgt_suppress_eop_01	2002Sep3
PASS		
498	r400vgt_suppress_eop_02	2002Sep3
PASS		
499	r400vgt_suppress_eop_03	2002Sep3
PASS		
500	r400vgt_suppress_eop_04	2002Sep3

PASS
501 r400vgt_triangle_fan_01 2002Sep3
PASS
502 r400vgt_triangle_list_01 2002Sep3
PASS
503 r400vgt_triangle_strip_01 2002Sep3
PASS
504 r400vgt_triangle_wflags_01 2002Sep3
PASS
505 r400vgt_vtx_vector_packing_01 2002Sep3
PASS
506 r400vte_coverage_01 2002Sep3
PASS
507 r400vte_simple_point_01 2002Sep3
PASS
508 r400vte_simple_triangle_01 2002Sep3
PASS
509 r400vte_w0_fmt_01 2002Sep3
PASS
510 r400vte_w0_fmt_03 2002Sep3
PASS
511 r400vte_xy_fmt_01 2002Sep3
PASS
512 r400vte_xyz_scale_01 2002Sep3
PASS
513 r400vte_xyz_scale_02 2002Sep3
PASS
514 r400vte_z_fmt_01 2002Sep3
PASS

=====
Tests_Run_count = 514 Pass_count = 513 Fail_count = 0 Incomplete_count = 1
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug30
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug30
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug30
PASS			
4	primlib_parameterized_simple_triangle		2002Aug30
PASS			
5	primlib_template_simple_triangle		2002Aug30
PASS			
6	primlib_tex_tri		2002Aug30
PASS			
7	r400cl_clip_10_verts_01		2002Aug30
PASS			
8	r400cl_clip_disable_01		2002Aug30
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug30
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug30
PASS			
11	r400cl_cull_only_ena_01		2002Aug30
PASS			
12	r400cl_edgeflags_01		2002Aug30
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug30
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug30
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug30
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug30
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug30
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug30
PASS			
19	r400cl_edgeflags_gband_01		2002Aug30
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Aug30

PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug30
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug30
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug30
PASS		
24	r400cl_frustum_01	2002Aug30
PASS		
25	r400cl_frustum_02	2002Aug30
PASS		
26	r400cl_frustum_03	2002Aug30
PASS		
27	r400cl_frustum_04	2002Aug30
PASS		
28	r400cl_frustum_05	2002Aug30
PASS		
29	r400cl_frustum_06	2002Aug30
PASS		
30	r400cl_frustum_07	2002Aug30
PASS		
31	r400cl_frustum_08	2002Aug30
PASS		
32	r400cl_frustum_09	2002Aug30
PASS		
33	r400cl_frustum_10	2002Aug30
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34	r400cl_frustum_11	2002Aug30
PASS		
35	r400cl_frustum_12	2002Aug30
PASS		
36	r400cl_frustum_13	2002Aug30
PASS		
37	r400cl_frustum_14	2002Aug30
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38	r400cl_frustum_15	2002Aug30
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39	r400cl_frustum_16	2002Aug30
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40	r400cl_frustum_17	2002Aug30
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41	r400cl_frustum_18	2002Aug30
PASS		
42	r400cl_frustum_19	2002Aug30
PASS		
43	r400cl_frustum_20	2002Aug30
PASS		
44	r400cl_frustum_21	2002Aug30

PASS		
45	r400cl_frustum_22	2002Aug30
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46	r400cl_frustum_23	2002Aug30
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47	r400cl_frustum_24	2002Aug30
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50	r400cl_frustum_27	2002Aug30
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51	r400cl_frustum_28	2002Aug30
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52	r400cl_frustum_lines_01	2002Aug30
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53	r400cl_frustum_lines_02	2002Aug30
PASS		
54	r400cl_frustum_lines_03	2002Aug30
PASS		
55	r400cl_frustum_lines_04	2002Aug30
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56	r400cl_frustum_lines_05	2002Aug30
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67	r400cl_frustum_lines_16	2002Aug30
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68	r400cl_frustum_lines_17	2002Aug30

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71	r400cl_frustum_lines_20	2002Aug30
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73	r400cl_frustum_lines_22	2002Aug30
PASS		
74	r400cl_frustum_lines_23	2002Aug30
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75	r400cl_frustum_lines_24	2002Aug30
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76	r400cl_gband_04	2002Aug30
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77	r400cl_gband_tcl_01	2002Aug30
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78	r400cl_pointlist_vertex_state_ucp_01	2002Aug30
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79	r400cl_simple_triangle_01	2002Aug30
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80	r400cl_ucp_combos_01	2002Aug30
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81	r400cl_ucp_combos_02	2002Aug30
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82	r400cl_ucp_combos_03	2002Aug30
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83	r400cl_ucp_combos_04	2002Aug30
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84	r400cl_ucp_combos_05	2002Aug30
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88	r400cl_ucp_combos_09	2002Aug30
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91	r400cl_ucp_combos_12	2002Aug30
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103	r400cl_ucp_combos_24	2002Aug30
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104	r400cl_ucp_combos_25	2002Aug30
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106	r400cl_ucp_combos_27	2002Aug30
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107	r400cl_ucp_combos_28	2002Aug30
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108	r400cl_ucp_combos_29	2002Aug30
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109	r400cl_ucp_combos_30	2002Aug30
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110	r400cl_ucp_pointlist_01	2002Aug30
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111	r400cl_vertex_reuse_clip_01	2002Aug30
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112	r400cl_vtx_kill_01	2002Aug30
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113	r400cl_vtx_kill_02	2002Aug30
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114	r400cl_w_eq_0	2002Aug30
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115	r400sanity_persp_projection_01	2002Aug30
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116	r400sanity_vfd_texture_sample_01	2002Aug30

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117	r400sc_bres_cntl_01	2002Aug30
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118	r400sc_bres_cntl_02	2002Aug30
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119	r400sc_bres_cntl_03	2002Aug30
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120	r400sc_bres_cntl_04	2002Aug30
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122	r400sc_clip_rect_02	2002Aug30
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123	r400sc_clip_rect_03	2002Aug30
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124	r400sc_clip_rect_04	2002Aug30
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125	r400sc_clip_rect_fc_01	2002Aug30
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126	r400sc_diamond_exit_01	2002Aug30
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127	r400sc_diamond_exit_02	2002Aug30
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128	r400sc_diamond_exit_03	2002Aug30
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129	r400sc_diamond_exit_04	2002Aug30
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130	r400sc_diamond_exit_05	2002Aug30
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131	r400sc_jss_1x2_01	2002Aug30
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132	r400sc_jss_1x2_02	2002Aug30
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133	r400sc_jss_1x3_01	2002Aug30
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134	r400sc_jss_1x3_02	2002Aug30
PASS		
135	r400sc_jss_1x4_01	2002Aug30
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136	r400sc_jss_1x4_02	2002Aug30
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137	r400sc_jss_2x1_01	2002Aug30
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138	r400sc_jss_2x1_02	2002Aug30
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139	r400sc_jss_2x2_01	2002Aug30
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140	r400sc_jss_2x2_02	2002Aug30

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142	r400sc_jss_2x3_02	2002Aug30
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143	r400sc_jss_2x4_01	2002Aug30
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144	r400sc_jss_2x4_02	2002Aug30
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145	r400sc_jss_3x1_01	2002Aug30
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146	r400sc_jss_3x1_02	2002Aug30
PASS		
147	r400sc_jss_3x2_01	2002Aug30
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148	r400sc_jss_3x2_02	2002Aug30
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149	r400sc_jss_3x3_01	2002Aug30
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150	r400sc_jss_3x3_02	2002Aug30
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151	r400sc_jss_3x4_01	2002Aug30
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152	r400sc_jss_3x4_02	2002Aug30
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153	r400sc_jss_3x4_03	2002Aug30
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154	r400sc_jss_4x1_01	2002Aug30
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155	r400sc_jss_4x1_02	2002Aug30
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156	r400sc_jss_4x2_01	2002Aug30
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157	r400sc_jss_4x2_02	2002Aug30
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158	r400sc_jss_4x3_01	2002Aug30
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159	r400sc_jss_4x3_02	2002Aug30
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160	r400sc_jss_4x4_01	2002Aug30
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161	r400sc_jss_4x4_02	2002Aug30
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162	r400sc_jss_4x4_03	2002Aug30
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163	r400sc_jss_4x4_aa_mask_01	2002Aug30
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164	r400sc_jss_4x4_aa_mask_02	2002Aug30

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165 r400sc_jss_4x4_fc_01 2002Aug30
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166 r400sc_jss_4x4_max_dist_01 2002Aug30
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167 r400sc_line_dx10_eq_0_01 2002Aug30
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168 r400sc_line_dx10_ge_0_01 2002Aug30
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169 r400sc_line_dx10_lt_0_01 2002Aug30
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170 r400sc_line_dy10_eq_0_01 2002Aug30
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171 r400sc_line_dy10_ge_0_01 2002Aug30
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172 r400sc_line_dy10_lt_0_01 2002Aug30
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173 r400sc_line_expand_width_msa_8_01 2002Aug30
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174 r400sc_line_expand_width_msa_8_02 2002Aug30
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175 r400sc_line_expand_width_msa_8_03 2002Aug30
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176 r400sc_line_jss_3x4_01 2002Aug30
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177 r400sc_line_list_01 2002Aug30
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178 r400sc_line_list_02 2002Aug30
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179 r400sc_line_list_concentric_circle_01 2002Aug30
PASS
180 r400sc_line_list_concentric_circle_02 2002Aug30
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181 r400sc_line_list_concentric_circle_03 2002Aug30
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182 r400sc_line_list_textured_01 2002Aug30
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183 r400sc_line_list_verify_st_01 2002Aug30
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184 r400sc_line_msa_8_01 2002Aug30
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185 r400sc_line_stipple_01 2002Aug30
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186 r400sc_line_stipple_02 2002Aug30
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187 r400sc_line_stipple_03 2002Aug30
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188 r400sc_line_stipple_04 2002Aug30

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191	r400sc_line_stipple_07	2002Aug30
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193	r400sc_line_stipple_09	2002Aug30
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194	r400sc_line_stipple_fc_08	2002Aug30
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195	r400sc_line_strip_stipple_01	2002Aug30
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196	r400sc_msaa_1_01	2002Aug30
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197	r400sc_msaa_4_01	2002Aug30
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198	r400sc_msaa_6_01	2002Aug30
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199	r400sc_msaa_8_01	2002Aug30
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200	r400sc_msaa_8_aa_mask_01	2002Aug30
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201	r400sc_msaa_8_aa_mask_02	2002Aug30
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202	r400sc_msaa_8_aa_mask_fc_02	2002Aug30
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203	r400sc_null_triangles_01	2002Aug30
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204	r400sc_null_triangles_fc_01	2002Aug30
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205	r400sc_point_jss_3x4_01	2002Aug30
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206	r400sc_point_list_01	2002Aug30
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207	r400sc_point_list_02	2002Aug30
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208	r400sc_point_list_03	2002Aug30
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209	r400sc_point_list_04	2002Aug30
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210	r400sc_point_list_05	2002Aug30
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211	r400sc_point_list_06	2002Aug28
INCOMPLETE		
212	r400sc_point_list_07	2002Aug30

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213	r400sc_point_list_08	2002Aug30
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214	r400sc_point_list_09	2002Aug30
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215	r400sc_point_msa_8_01	2002Aug30
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217	r400sc_poly_offset_02	2002Aug30
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218	r400sc_poly_offset_fc_01	2002Aug30
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219	r400sc_polygon_stipple_01	2002Aug30
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221	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug30
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222	r400sc_prsp_byc_intrp_ref_pix_03	2002Aug30
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225	r400sc_prsp_byc_intrp_ref_pix_06	2002Aug30
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226	r400sc_prsp_byc_intrp_ref_pix_07	2002Aug30
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227	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug30
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229	r400sc_raster_fill_rule_02	2002Aug30
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230	r400sc_raster_fill_rule_03	2002Aug30
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231	r400sc_raster_fill_rule_04	2002Aug30
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232	r400sc_raster_fill_rule_05	2002Aug30
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233	r400sc_raster_fill_rule_fc_01	2002Aug30
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234	r400sc_rectangle_list_01	2002Aug30
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235	r400sc_rectangle_list_02	2002Aug30
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240	r400sc_rectangle_list_07	2002Aug30
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241	r400sc_rectangle_list_08	2002Aug30
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243	r400sc_scissor_rect_02	2002Aug30
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244	r400sc_scissor_rect_03	2002Aug30
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246	r400sc_scissor_rect_05	2002Aug30
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247	r400sc_scissor_rect_fc_01	2002Aug30
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248	r400sc_set_state_01	2002Aug30
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249	r400sc_tri_walk_start_vertex_01	2002Aug30
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250	r400sc_tri_walk_start_vertex_02	2002Aug30
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251	r400sc_tri_walk_start_vertex_03	2002Aug30
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253	r400sc_tri_walk_start_vertex_05	2002Aug30
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254	r400sc_tri_walk_start_vertex_06	2002Aug30
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255	r400sc_tri_walk_start_vertex_07	2002Aug30
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256	r400sc_tri_walk_start_vertex_08	2002Aug30
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257	r400sc_tri_walk_start_vertex_09	2002Aug30
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258	r400sc_tri_walk_start_vertex_10	2002Aug30
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259	r400sc_tri_walk_start_vertex_11	2002Aug30
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260	r400sc_tri_walk_start_vertex_12	2002Aug30

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261	r400sc_tri_walk_start_vertex_13	2002Aug30
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262	r400sc_tri_walk_start_vertex_14	2002Aug30
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263	r400sc_tri_walk_start_vertex_15	2002Aug30
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264	r400sc_tri_walk_start_vertex_16	2002Aug30
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265	r400sc_triangle_stipple_01	2002Aug30
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266	r400sc_window_offset_01	2002Aug30
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267	r400sc_window_offset_fc_01	2002Aug30
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268	r400sc_zbuffer_line_list_01	2002Aug30
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269	r400sc_zbuffer_point_list_01	2002Aug30
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270	r400sc_zbuffer_rectangle_list_01	2002Aug30
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271	r400sc_zbuffer_rectangle_list_02	2002Aug30
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272	r400sc_zbuffer_rectangle_list_fc_02	2002Aug30
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273	r400sc_zbuffer_triangle_list_01	2002Aug30
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274	r400su_4tri_text_offscreen_01	2002Aug30
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275	r400su_baryc_test_01	2002Aug30
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276	r400su_baryc_test_02	2002Aug30
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277	r400su_baryc_test_03	2002Aug30
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278	r400su_baryc_test_04	2002Aug30
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279	r400su_clip_edgeflag_polymode_01	2002Aug30
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280	r400su_clip_pointsize_test_01	2002Aug30
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281	r400su_clip_pointttest_01	2002Aug30
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282	r400su_clip_pointttest_02	2002Aug30
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283	r400su_clip_pointttest_03	2002Aug30
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284	r400su_clip_pointttest_04	2002Aug30

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285	r400su_clip_polymode_test_01	2002Aug30
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287	r400su_clippoint_01	2002Aug30
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288	r400su_clipvertexsortingfunctional_01	2002Aug30
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289	r400su_cullingfunctional_01	2002Aug30
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290	r400su_degentri_test_01	2002Aug30
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291	r400su_degentri_test_02	2002Aug30
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292	r400su_degentri_test_03	2002Aug30
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293	r400su_edge_flag_01	2002Aug30
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294	r400su_edge_flag_02	2002Aug30
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295	r400su_edgeflags_triangle_01	2002Aug30
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296	r400su_edgeflags_triangle_02	2002Aug30
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297	r400su_geom_sort_01	2002Aug30
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298	r400su_line_end_cap_functional_01	2002Aug30
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299	r400su_line_end_cap_width_functional_02	2002Aug30
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300	r400su_line_test_01	2002Aug30
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301	r400su_line_test_02	2002Aug30
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302	r400su_longstrip_01	2002Aug30
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303	r400su_multi_prim_01	2002Aug30
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304	r400su_parallel_orientation_all_01	2002Aug30
PASS		
305	r400su_parallel_orientation_all_02	2002Aug30
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306	r400su_pc_management_01	2002Aug30
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307	r400su_pc_management_02	2002Aug30
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308	r400su_pc_management_03	2002Aug30

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309	r400su_point_sprite_01	2002Aug30
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310	r400su_point_sprite_02	2002Aug30
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311	r400su_point_sprite_03	2002Aug30
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312	r400su_point_sprite_04	2002Aug30
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313	r400su_point_sprite_05	2002Aug30
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314	r400su_point_sprite_06	2002Aug30
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315	r400su_point_sprite_07	2002Aug30
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316	r400su_point_sprite_08	2002Aug30
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317	r400su_point_w16_h1_functional_01	2002Aug30
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318	r400su_point_w1_h16_functional_01	2002Aug30
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319	r400su_pointsizepresent_01	2002Aug30
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320	r400su_pointsizepresent_02	2002Aug30
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321	r400su_polymode_lines_degen_triangle_01	2002Aug30
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322	r400su_polymode_lines_degen_triangle_02	2002Aug30
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323	r400su_polymode_lines_zero_area_triangle_01	2002Aug30
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324	r400su_polymode_lines_zero_area_triangle_02	2002Aug30
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325	r400su_polymode_points_degen_triangle_01	2002Aug30
PASS		
326	r400su_polymode_points_degen_triangle_02	2002Aug30
PASS		
327	r400su_polymode_points_zero_area_triangle_01	2002Aug30
PASS		
328	r400su_polymode_points_zero_area_triangle_02	2002Aug30
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329	r400su_polymode_rectangle_01	2002Aug30
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330	r400su_polymode_zero_area_triangle_01	2002Aug30
PASS		
331	r400su_polymode_zero_area_triangle_02	2002Aug30
PASS		
332	r400su_polymode_zero_area_triangle_03	2002Aug30

PASS		
333	r400su_polymode_zero_area_triangle_04	2002Aug30
PASS		
334	r400su_polymodeculling_01	2002Aug30
PASS		
335	r400su_polymodefunctional_01	2002Aug30
PASS		
336	r400su_provoking_vtx_edgeflags_triangle_01	2002Aug30
PASS		
337	r400su_provoking_vtx_edgeflags_triangle_02	2002Aug30
PASS		
338	r400su_provoking_vtx_edgeflags_triangle_03	2002Aug30
PASS		
339	r400su_provoking_vtx_edgeflags_triangle_04	2002Aug30
PASS		
340	r400su_provoking_vtx_line_01	2002Aug30
PASS		
341	r400su_provoking_vtx_point_01	2002Aug30
PASS		
342	r400su_provoking_vtx_polymode_rectangle_01	2002Aug30
PASS		
343	r400su_provoking_vtx_rectangle_01	2002Aug30
PASS		
344	r400su_provoking_vtx_triangle_01	2002Aug30
PASS		
345	r400su_rand_line_01	2002Aug30
PASS		
346	r400su_rand_point_01	2002Aug30
PASS		
347	r400su_rand_tri_01	2002Aug30
PASS		
348	r400su_rectangle_list_01	2002Aug30
PASS		
349	r400su_sliver_01	2002Aug30
PASS		
350	r400su_triarea_test_01	2002Aug30
PASS		
351	r400su_triarea_test_02	2002Aug30
PASS		
352	r400su_triarea_test_03	2002Aug30
PASS		
353	r400su_triarea_test_04	2002Aug30
PASS		
354	r400su_vertexsortingfunctional_01	2002Aug30
PASS		
355	r400vgt_auto_index_line_list_01	2002Aug30
PASS		
356	r400vgt_auto_index_line_loop_01	2002Aug30

PASS		
357	r400vgt_auto_index_line_strip_01	2002Aug30
PASS		
358	r400vgt_auto_index_points_01	2002Aug30
PASS		
359	r400vgt_auto_index_polygon_01	2002Aug30
PASS		
360	r400vgt_auto_index_primitives_01	2002Aug30
PASS		
361	r400vgt_auto_index_quad_list_01	2002Aug30
PASS		
362	r400vgt_auto_index_quad_strip_01	2002Aug30
PASS		
363	r400vgt_auto_index_rectangle_list_01	2002Aug30
PASS		
364	r400vgt_auto_index_tri_fan_01	2002Aug30
PASS		
365	r400vgt_auto_index_tri_list_01	2002Aug30
PASS		
366	r400vgt_auto_index_tri_strip_01	2002Aug30
PASS		
367	r400vgt_auto_index_tri_wflags_01	2002Aug30
PASS		
368	r400vgt_dma_engine_01	2002Aug30
PASS		
369	r400vgt_dma_engine_02	2002Aug30
PASS		
370	r400vgt_dma_engine_03	2002Aug30
PASS		
371	r400vgt_dma_engine_04	2002Aug30
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372	r400vgt_dma_engine_05	2002Aug30
PASS		
373	r400vgt_dma_engine_06	2002Aug30
PASS		
374	r400vgt_dma_engine_07	2002Aug30
PASS		
375	r400vgt_dma_engine_08	2002Aug30
PASS		
376	r400vgt_dma_engine_09	2002Aug30
PASS		
377	r400vgt_dma_engine_10	2002Aug30
PASS		
378	r400vgt_dma_index_line_list_01	2002Aug30
PASS		
379	r400vgt_dma_index_line_loop_01	2002Aug30
PASS		
380	r400vgt_dma_index_line_strip_01	2002Aug30

PASS		
381	r400vgt_dma_index_points_01	2002Aug30
PASS		
382	r400vgt_dma_index_polygon_01	2002Aug30
PASS		
383	r400vgt_dma_index_primitives_01	2002Aug30
PASS		
384	r400vgt_dma_index_quad_list_01	2002Aug30
PASS		
385	r400vgt_dma_index_quad_strip_01	2002Aug30
PASS		
386	r400vgt_dma_index_rectangle_list_01	2002Aug30
PASS		
387	r400vgt_dma_index_tri_fan_01	2002Aug30
PASS		
388	r400vgt_dma_index_tri_list_01	2002Aug30
PASS		
389	r400vgt_dma_index_tri_strip_01	2002Aug30
PASS		
390	r400vgt_dma_index_tri_wflags_01	2002Aug30
PASS		
391	r400vgt_dma_swap_indx16_01	2002Aug30
PASS		
392	r400vgt_dma_swap_indx32_01	2002Aug30
PASS		
393	r400vgt_edgeflags_polygon_01	2002Aug30
PASS		
394	r400vgt_edgeflags_quad_list_01	2002Aug30
PASS		
395	r400vgt_edgeflags_quad_strip_01	2002Aug30
PASS		
396	r400vgt_edgeflags_triangle_fan_01	2002Aug30
PASS		
397	r400vgt_edgeflags_triangle_list_01	2002Aug30
PASS		
398	r400vgt_edgeflags_triangle_strip_01	2002Aug30
PASS		
399	r400vgt_edgeflags_triangle_wflags_01	2002Aug30
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400	r400vgt_ext2int_index_line_list_01	2002Aug30
PASS		
401	r400vgt_ext2int_index_line_loop_01	2002Aug30
PASS		
402	r400vgt_ext2int_index_line_strip_01	2002Aug30
PASS		
403	r400vgt_ext2int_index_points_01	2002Aug30
PASS		
404	r400vgt_ext2int_index_polygon_01	2002Aug30

PASS
405 r400vgt_ext2int_index_quad_list_01 2002Aug30
PASS
406 r400vgt_ext2int_index_quad_strip_01 2002Aug30
PASS
407 r400vgt_ext2int_index_triangle_fan_01 2002Aug30
PASS
408 r400vgt_ext2int_index_triangle_list_01 2002Aug30
PASS
409 r400vgt_ext2int_index_triangle_strip_01 2002Aug30
PASS
410 r400vgt_hos_PNL_cp_ln_cont_no_projection_01 2002Aug30
PASS
411 r400vgt_hos_PNL_lp_ln_cont_no_projection_01 2002Aug30
PASS
412 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01 2002Aug30
PASS
413 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02 2002Aug30
PASS
414 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01 2002Aug30
PASS
415 r400vgt_hos_PNQ_lp_cont_no_projection_01 2002Aug30
PASS
416 r400vgt_hos_PNT_cont_cp_qn_complex_01 2002Aug30
PASS
417 r400vgt_hos_PNT_cont_cp_qn_precision_01 2002Aug30
PASS
418 r400vgt_hos_PNT_cont_cp_qn_precision_02 2002Aug30
PASS
419 r400vgt_hos_PNT_cp_qn_cont_light_texture_01 2002Aug30
PASS
420 r400vgt_hos_PNT_cp_qn_cont_light_texture_02 2002Aug30
PASS
421 r400vgt_hos_PNT_cp_qn_cont_light_texture_03 2002Aug30
PASS
422 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01 2002Aug30
PASS
423 r400vgt_hos_PNT_cp_qn_cont_no_projection_01 2002Aug30
PASS
424 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01 2002Aug30
PASS
425 r400vgt_hos_PNT_disc_cp_qn_complex_01 2002Aug30
PASS
426 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Aug30
PASS
427 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Aug30
PASS
428 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Aug30

PASS		
429	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug30
PASS		
430	r400vgt_hos_PNT_edge_detection_01	2002Aug30
PASS		
431	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug30
PASS		
432	r400vgt_hos_auto_index_line_list_01	2002Aug30
PASS		
433	r400vgt_hos_auto_index_quad_list_01	2002Aug30
PASS		
434	r400vgt_hos_auto_index_triangle_list_01	2002Aug30
PASS		
435	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug30
PASS		
436	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug30
PASS		
437	r400vgt_immed_index_line_list_01	2002Aug30
PASS		
438	r400vgt_immed_index_line_loop_01	2002Aug30
PASS		
439	r400vgt_immed_index_line_strip_01	2002Aug30
PASS		
440	r400vgt_immed_index_points_01	2002Aug30
PASS		
441	r400vgt_immed_index_polygon_01	2002Aug30
PASS		
442	r400vgt_immed_index_primitives_01	2002Aug30
PASS		
443	r400vgt_immed_index_quad_list_01	2002Aug30
PASS		
444	r400vgt_immed_index_quad_strip_01	2002Aug30
PASS		
445	r400vgt_immed_index_rectangle_list_01	2002Aug30
PASS		
446	r400vgt_immed_index_tri_fan_01	2002Aug30
PASS		
447	r400vgt_immed_index_tri_list_01	2002Aug30
PASS		
448	r400vgt_immed_index_tri_strip_01	2002Aug30
PASS		
449	r400vgt_immed_index_tri_wflags_01	2002Aug30
PASS		
450	r400vgt_index_dealloc_line_list_01	2002Aug30
PASS		
451	r400vgt_index_dealloc_points_01	2002Aug30
PASS		
452	r400vgt_index_dealloc_triangle_list_01	2002Aug30

PASS		
453	r400vgt_index_min_max_01	2002Aug30
PASS		
454	r400vgt_index_min_max_02	2002Aug30
PASS		
455	r400vgt_index_offset_01	2002Aug30
PASS		
456	r400vgt_index_offset_02	2002Aug30
PASS		
457	r400vgt_index_offset_03	2002Aug30
PASS		
458	r400vgt_index_size_01	2002Aug30
PASS		
459	r400vgt_index_source_switch_01	2002Aug30
PASS		
460	r400vgt_line_list_01	2002Aug30
PASS		
461	r400vgt_line_loop_01	2002Aug30
PASS		
462	r400vgt_line_strip_01	2002Aug30
PASS		
463	r400vgt_multi_context_01	2002Aug30
PASS		
464	r400vgt_multi_context_02	2002Aug30
PASS		
465	r400vgt_multi_context_03	2002Aug30
PASS		
466	r400vgt_multi_context_04	2002Aug30
PASS		
467	r400vgt_multi_context_05	2002Aug30
PASS		
468	r400vgt_multi_context_06	2002Aug30
PASS		
469	r400vgt_multi_context_07	2002Aug30
PASS		
470	r400vgt_multi_context_08	2002Aug30
PASS		
471	r400vgt_multi_context_09	2002Aug30
PASS		
472	r400vgt_multi_context_10	2002Aug30
PASS		
473	r400vgt_multi_context_11	2002Aug30
PASS		
474	r400vgt_multi_context_12	2002Aug30
PASS		
475	r400vgt_pass_thru_all_prims_01	2002Aug30
PASS		
476	r400vgt_point_list_01	2002Aug30

PASS		
477	r400vgt_polygon_01	2002Aug30
PASS		
478	r400vgt_provoking_vtx_all_01	2002Aug30
PASS		
479	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug30
PASS		
480	r400vgt_quad_list_01	2002Aug30
PASS		
481	r400vgt_quad_strip_01	2002Aug30
PASS		
482	r400vgt_rectangle_list_01	2002Aug30
PASS		
483	r400vgt_reuse_depth_line_list_01	2002Aug30
PASS		
484	r400vgt_reuse_depth_point_list_01	2002Aug30
PASS		
485	r400vgt_reuse_depth_triangle_list_01	2002Aug30
PASS		
486	r400vgt_reuse_index_line_list_01	2002Aug30
PASS		
487	r400vgt_reuse_index_point_list_01	2002Aug30
PASS		
488	r400vgt_reuse_index_triangle_list_01	2002Aug30
PASS		
489	r400vgt_reuse_index_triangle_list_02	2002Aug30
PASS		
490	r400vgt_reuse_index_triangle_list_03	2002Aug30
PASS		
491	r400vgt_triangle_fan_01	2002Aug30
PASS		
492	r400vgt_triangle_list_01	2002Aug30
PASS		
493	r400vgt_triangle_strip_01	2002Aug30
PASS		
494	r400vgt_triangle_wflags_01	2002Aug30
PASS		
495	r400vgt_vtx_vector_packing_01	2002Aug30
PASS		
496	r400vte_coverage_01	2002Aug30
PASS		
497	r400vte_simple_point_01	2002Aug30
PASS		
498	r400vte_simple_triangle_01	2002Aug30
PASS		
499	r400vte_w0_fmt_01	2002Aug30
PASS		
500	r400vte_w0_fmt_03	2002Aug30

PASS
501 r400vte_xy_fmt_01 2002Aug30
PASS
502 r400vte_xyz_scale_01 2002Aug30
PASS
503 r400vte_xyz_scale_02 2002Aug30
PASS
504 r400vte_z_fmt_01 2002Aug30
PASS

=====
Tests_Run_count = 504 Pass_count = 503 Fail_count = 0 Incomplete_count = 1
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Oct  4 21:18:36 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             180         180         179           99
              SC              299         299         298           99
              CL              115         115         115           100
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              PARAMETER        62          62          62           100
              SU               97          97          97           100
              SANITY            2           2           2           100
+++++
              TOTAL            783         783         781           99
+++++

```

=====
Regression Report For VGT - Project = r400 Created on Sat Sep 21 15:00:27 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep21
PASS			
2	cp_e2blit_brush_mt_ropcc		2002Sep21
PASS			
3	cp_e2_hostdata_blt_pntr_8888		2002Sep21
PASS			
4	cp_e2_one_blit		2002Sep21
PASS			
5	cp_e2_one_line		2002Sep21
PASS			
6	cp_e2paint_565		2002Sep21
PASS			
7	cp_e2paint_multi		2002Sep21
PASS			
8	cp_e2polyscanlines_brush		2002Sep21
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep21
PASS			
10	cp_e2smalltext_jcl		2002Sep21
PASS			
11	cp_simple_triangle		2002Sep21
PASS			
12	primlib_1st_tri_june15		2002Sep21
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep21
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep21
PASS			
15	primlib_parameterized_simple_triangle		2002Sep21
PASS			
16	primlib_template_simple_triangle		2002Sep21
PASS			
17	primlib_tex_tri		2002Sep21
INCOMPLETE			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep21
PASS			
20	r400cl_clip_disable_01		2002Sep21

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep21
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep21
PASS		
23	r400cl_cull_only_ena_01	2002Sep21
PASS		
24	r400cl_edgeflags_01	2002Sep21
PASS		
25	r400cl_edgeflags_01_bug	2002Sep21
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep21
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep21
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep21
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep21
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep21
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep21
PASS		
32	r400cl_edgeflags_gband_01	2002Sep21
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep21
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep21
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep21
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep21
PASS		
37	r400cl_frustum_01	2002Sep21
PASS		
38	r400cl_frustum_02	2002Sep21
PASS		
39	r400cl_frustum_03	2002Sep21
PASS		
40	r400cl_frustum_04	2002Sep21
PASS		
41	r400cl_frustum_05	2002Sep21
PASS		
42	r400cl_frustum_06	2002Sep21
PASS		
43	r400cl_frustum_07	2002Sep21
PASS		
44	r400cl_frustum_08	2002Sep21

PASS		
45	r400cl_frustum_09	2002Sep21
PASS		
46	r400cl_frustum_10	2002Sep21
PASS		
47	r400cl_frustum_11	2002Sep21
PASS		
48	r400cl_frustum_12	2002Sep21
PASS		
49	r400cl_frustum_13	2002Sep21
PASS		
50	r400cl_frustum_14	2002Sep21
PASS		
51	r400cl_frustum_15	2002Sep21
PASS		
52	r400cl_frustum_16	2002Sep21
PASS		
53	r400cl_frustum_17	2002Sep21
PASS		
54	r400cl_frustum_18	2002Sep21
PASS		
55	r400cl_frustum_19	2002Sep21
PASS		
56	r400cl_frustum_20	2002Sep21
PASS		
57	r400cl_frustum_21	2002Sep21
PASS		
58	r400cl_frustum_22	2002Sep21
PASS		
59	r400cl_frustum_23	2002Sep21
PASS		
60	r400cl_frustum_24	2002Sep21
PASS		
61	r400cl_frustum_25	2002Sep21
PASS		
62	r400cl_frustum_26	2002Sep21
PASS		
63	r400cl_frustum_27	2002Sep21
PASS		
64	r400cl_frustum_28	2002Sep21
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep21
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep21
PASS		
67	r400cl_frustum_lines_01	2002Sep21
PASS		
68	r400cl_frustum_lines_02	2002Sep21

PASS		
69	r400cl_frustum_lines_03	2002Sep21
PASS		
70	r400cl_frustum_lines_04	2002Sep21
PASS		
71	r400cl_frustum_lines_05	2002Sep21
PASS		
72	r400cl_frustum_lines_06	2002Sep21
PASS		
73	r400cl_frustum_lines_07	2002Sep21
PASS		
74	r400cl_frustum_lines_08	2002Sep21
PASS		
75	r400cl_frustum_lines_09	2002Sep21
PASS		
76	r400cl_frustum_lines_10	2002Sep21
PASS		
77	r400cl_frustum_lines_11	2002Sep21
PASS		
78	r400cl_frustum_lines_12	2002Sep21
PASS		
79	r400cl_frustum_lines_13	2002Sep21
PASS		
80	r400cl_frustum_lines_14	2002Sep21
PASS		
81	r400cl_frustum_lines_15	2002Sep21
PASS		
82	r400cl_frustum_lines_16	2002Sep21
PASS		
83	r400cl_frustum_lines_17	2002Sep21
PASS		
84	r400cl_frustum_lines_18	2002Sep21
PASS		
85	r400cl_frustum_lines_19	2002Sep21
PASS		
86	r400cl_frustum_lines_20	2002Sep21
PASS		
87	r400cl_frustum_lines_21	2002Sep21
PASS		
88	r400cl_frustum_lines_22	2002Sep21
PASS		
89	r400cl_frustum_lines_23	2002Sep21
PASS		
90	r400cl_frustum_lines_24	2002Sep21
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91	r400cl_gband_04	2002Sep21
PASS		
92	r400cl_gband_tcl_01	2002Sep21

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93	r400cl_nan_kill_01_bug	2002Sep21
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Sep21
PASS		
95	r400cl_polymode_line_fill_01_bug	2002Sep21
PASS		
96	r400cl_simple_triangle_01	2002Sep21
PASS		
97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep21
PASS		
98	r400cl_ucp_combos_01	2002Sep21
PASS		
99	r400cl_ucp_combos_02	2002Sep21
PASS		
100	r400cl_ucp_combos_03	2002Sep21
PASS		
101	r400cl_ucp_combos_04	2002Sep21
PASS		
102	r400cl_ucp_combos_05	2002Sep21
PASS		
103	r400cl_ucp_combos_06	2002Sep21
PASS		
104	r400cl_ucp_combos_07	2002Sep21
PASS		
105	r400cl_ucp_combos_08	2002Sep21
PASS		
106	r400cl_ucp_combos_09	2002Sep21
PASS		
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PASS		
108	r400cl_ucp_combos_11	2002Sep21
PASS		
109	r400cl_ucp_combos_12	2002Sep21
PASS		
110	r400cl_ucp_combos_13	2002Sep21
PASS		
111	r400cl_ucp_combos_14	2002Sep21
PASS		
112	r400cl_ucp_combos_15	2002Sep21
PASS		
113	r400cl_ucp_combos_16	2002Sep21
PASS		
114	r400cl_ucp_combos_17	2002Sep21
PASS		
115	r400cl_ucp_combos_18	2002Sep21
PASS		
116	r400cl_ucp_combos_19	2002Sep21

PASS		
117	r400cl_ucp_combos_20	2002Sep21
PASS		
118	r400cl_ucp_combos_21	2002Sep21
PASS		
119	r400cl_ucp_combos_22	2002Sep21
PASS		
120	r400cl_ucp_combos_23	2002Sep21
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121	r400cl_ucp_combos_24	2002Sep21
PASS		
122	r400cl_ucp_combos_25	2002Sep21
PASS		
123	r400cl_ucp_combos_26	2002Sep21
PASS		
124	r400cl_ucp_combos_27	2002Sep21
PASS		
125	r400cl_ucp_combos_28	2002Sep21
PASS		
126	r400cl_ucp_combos_29	2002Sep21
PASS		
127	r400cl_ucp_combos_30	2002Sep21
PASS		
128	r400cl_ucp_pointlist_01	2002Sep21
PASS		
129	r400cl_vertex_reuse_clip_01	2002Sep21
PASS		
130	r400cl_vtx_kill_01	2002Sep21
PASS		
131	r400cl_vtx_kill_02	2002Sep21
PASS		
132	r400cl_w_eq_0	2002Sep21
PASS		
133	r400rbm_simple_triangle_01	2002Sep21
PASS		
134	r400sanity_persp_projection_01	2002Sep21
PASS		
135	r400sanity_vfd_texture_sample_01	2002Sep21
PASS		
136	r400sc_bres_cntl_01	2002Sep7
PASS		
137	r400sc_bres_cntl_02	2002Sep21
PASS		
138	r400sc_bres_cntl_03	2002Sep21
PASS		
139	r400sc_bres_cntl_04	2002Sep21
PASS		
140	r400sc_bres_cntl_w2k_01	2002Sep21

PASS		
141	r400sc_bres_cntl_w9x_01	2002Sep21
PASS		
142	r400sc_clip_rect_01	2002Sep21
PASS		
143	r400sc_clip_rect_02	2002Sep21
PASS		
144	r400sc_clip_rect_03	2002Sep21
PASS		
145	r400sc_clip_rect_04	2002Sep21
PASS		
146	r400sc_clip_rect_fc_01	2002Sep21
PASS		
147	r400sc_diamond_exit_01	2002Sep21
PASS		
148	r400sc_diamond_exit_02	2002Sep21
PASS		
149	r400sc_diamond_exit_03	2002Sep21
PASS		
150	r400sc_diamond_exit_04	2002Sep21
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151	r400sc_diamond_exit_05	2002Sep21
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153	r400sc_jss_1x2_01	2002Sep21
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154	r400sc_jss_1x2_02	2002Sep21
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156	r400sc_jss_1x3_01	2002Sep21
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157	r400sc_jss_1x3_02	2002Sep21
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205	r400sc_line_dx10_eq_0_01	2002Sep21
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206	r400sc_line_dx10_ge_0_01	2002Sep21
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207	r400sc_line_dx10_lt_0_01	2002Sep21
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219	r400sc_line_list_concentric_circle_03	2002Sep21
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222	r400sc_line_list_verify_st_01_bug	2002Sep21
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317	r400sc_msa_8_zbuffer_rectangle_list_02	2002Sep21
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319	r400sc_msa_X_primitives_02_bug	2002Sep21
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321	r400sc_null_triangles_fc_01	2002Sep21
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323	r400sc_pinwheel_02	2002Sep21
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324	r400sc_pinwheel_02_bug	2002Sep21
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340	r400sc_polymode_tri_fill_01_bug	2002Sep21
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342	r400sc_poly_offset_02	2002Sep21
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344	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep21
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380	r400sc_sp_sample_cntl_08	2002Sep21

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383	r400sc_tri_8textures_02	2002Sep21
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415	r400su_clip_baryc_test_02	2002Sep21
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418	r400su_clippoint_01	2002Sep21
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465	r400su_polymode_lines_zero_area_triangle_02	2002Sep21
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477	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep21
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478	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep21
PASS		
479	r400su_provoking_vtx_line_01	2002Sep21
PASS		
480	r400su_provoking_vtx_point_01	2002Sep21
PASS		
481	r400su_provoking_vtx_polymode_rectangle_01	2002Sep21
PASS		
482	r400su_provoking_vtx_rectangle_01	2002Sep21
PASS		
483	r400su_provoking_vtx_triangle_01	2002Sep21
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484	r400su_rand_line_01	2002Sep21
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485	r400su_rand_point_01	2002Sep21
PASS		
486	r400su_rand_tri_01	2002Sep21
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487	r400su_rectangle_list_01	2002Sep21
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488	r400su_sliver_01	2002Sep21
PASS		
489	r400su_triarea_test_01	2002Sep21
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490	r400su_triarea_test_02	2002Sep21
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491	r400su_triarea_test_03	2002Sep21
PASS		
492	r400su_triarea_test_04	2002Sep21
PASS		
493	r400su_vertexsortingfunctional_01	2002Sep21
PASS		
494	r400vgt_auto_index_line_list_01	2002Sep21
PASS		
495	r400vgt_auto_index_line_loop_01	2002Sep21
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496	r400vgt_auto_index_line_strip_01	2002Sep21
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497	r400vgt_auto_index_points_01	2002Sep21
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498	r400vgt_auto_index_polygon_01	2002Sep21
PASS		
499	r400vgt_auto_index_primitives_01	2002Sep21
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500	r400vgt_auto_index_quad_list_01	2002Sep21

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501	r400vgt_auto_index_quad_strip_01	2002Sep21
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502	r400vgt_auto_index_rectangle_list_01	2002Sep21
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503	r400vgt_auto_index_tri_fan_01	2002Sep21
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504	r400vgt_auto_index_tri_list_01	2002Sep21
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505	r400vgt_auto_index_tri_strip_01	2002Sep21
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506	r400vgt_auto_index_tri_wflags_01	2002Sep21
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507	r400vgt_dma_engine_01	2002Sep21
PASS		
508	r400vgt_dma_engine_02	2002Sep21
PASS		
509	r400vgt_dma_engine_03	2002Sep21
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516	r400vgt_dma_engine_10	2002Sep21
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519	r400vgt_dma_index_line_strip_01	2002Sep21
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520	r400vgt_dma_index_points_01	2002Sep21
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521	r400vgt_dma_index_polygon_01	2002Sep21
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522	r400vgt_dma_index_primtypes_01	2002Sep21
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523	r400vgt_dma_index_quad_list_01	2002Sep21
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524	r400vgt_dma_index_quad_strip_01	2002Sep21

PASS		
525	r400vgt_dma_index_rectangle_list_01	2002Sep21
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526	r400vgt_dma_index_tri_fan_01	2002Sep21
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527	r400vgt_dma_index_tri_list_01	2002Sep21
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528	r400vgt_dma_index_tri_strip_01	2002Sep21
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529	r400vgt_dma_index_tri_wflags_01	2002Sep21
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530	r400vgt_dma_swap_indx16_01	2002Sep21
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531	r400vgt_dma_swap_indx32_01	2002Sep21
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532	r400vgt_edgeflags_polygon_01	2002Sep21
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533	r400vgt_edgeflags_quad_list_01	2002Sep21
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534	r400vgt_edgeflags_quad_strip_01	2002Sep21
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535	r400vgt_edgeflags_triangle_fan_01	2002Sep21
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536	r400vgt_edgeflags_triangle_list_01	2002Sep21
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537	r400vgt_edgeflags_triangle_strip_01	2002Sep21
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538	r400vgt_edgeflags_triangle_wflags_01	2002Sep21
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539	r400vgt_event_handling_01	2002Sep21
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540	r400vgt_ext2int_index_line_list_01	2002Sep21
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541	r400vgt_ext2int_index_line_loop_01	2002Sep21
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542	r400vgt_ext2int_index_line_strip_01	2002Sep21
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543	r400vgt_ext2int_index_points_01	2002Sep21
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544	r400vgt_ext2int_index_polygon_01	2002Sep21
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545	r400vgt_ext2int_index_quad_list_01	2002Sep21
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546	r400vgt_ext2int_index_quad_strip_01	2002Sep21
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548	r400vgt_ext2int_index_triangle_list_01	2002Sep21

PASS		
549	r400vgt_ext2int_index_triangle_strip_01	2002Sep21
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550	r400vgt_hos_auto_index_line_list_01	2002Sep21
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551	r400vgt_hos_auto_index_quad_list_01	2002Sep21
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552	r400vgt_hos_auto_index_triangle_list_01	2002Sep21
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553	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep21
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554	r400vgt_hos_LPatch_01	2002Sep21
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555	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep21
PASS		
556	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep21
PASS		
557	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep21
PASS		
558	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep21
PASS		
559	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep21
PASS		
560	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep21
PASS		
561	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep21
PASS		
562	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
563	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep21
PASS		
564	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep21
PASS		
565	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
566	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep21
PASS		
567	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep21
PASS		
568	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep21
PASS		
569	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep21
PASS		
570	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep21
PASS		
571	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep21
PASS		
572	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep21

PASS		
573	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep21
PASS		
574	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep21
PASS		
575	r400vgt_hos_PNT_edge_detection_01	2002Sep21
PASS		
576	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep21
PASS		
577	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep21
PASS		
578	r400vgt_hos_RPatch_cp_02	2002Sep21
PASS		
579	r400vgt_hos_RPatch_lp_02	2002Sep21
PASS		
580	r400vgt_immed_index_line_list_01	2002Sep21
PASS		
581	r400vgt_immed_index_line_loop_01	2002Sep21
PASS		
582	r400vgt_immed_index_line_strip_01	2002Sep21
PASS		
583	r400vgt_immed_index_points_01	2002Sep21
FAIL	VgtPaClips_VgtSide.cmp	
584	r400vgt_immed_index_polygon_01	2002Sep21
PASS		
585	r400vgt_immed_index_primitives_01	2002Sep21
PASS		
586	r400vgt_immed_index_quad_list_01	2002Sep21
PASS		
587	r400vgt_immed_index_quad_strip_01	2002Sep21
PASS		
588	r400vgt_immed_index_rectangle_list_01	2002Sep21
PASS		
589	r400vgt_immed_index_tri_fan_01	2002Sep21
PASS		
590	r400vgt_immed_index_tri_list_01	2002Sep21
PASS		
591	r400vgt_immed_index_tri_strip_01	2002Sep21
PASS		
592	r400vgt_immed_index_tri_wflags_01	2002Sep21
PASS		
593	r400vgt_index_dealloc_line_list_01	2002Sep21
PASS		
594	r400vgt_index_dealloc_points_01	2002Sep21
FAIL	VgtPaClips_VgtSide.cmp	
595	r400vgt_index_dealloc_triangle_list_01	2002Sep21
PASS		
596	r400vgt_index_min_max_01	2002Sep21

FAIL	VgtPaClipS_VgtSide.cmp	
597	r400vgt_index_min_max_02	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
598	r400vgt_index_offset_01	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
599	r400vgt_index_offset_02	2002Sep21
PASS		
600	r400vgt_index_offset_03	2002Sep21
PASS		
601	r400vgt_index_size_01	2002Sep21
PASS		
602	r400vgt_index_source_switch_01	2002Sep21
PASS		
603	r400vgt_line_list_01	2002Sep21
PASS		
604	r400vgt_line_list_02	2002Sep21
PASS		
605	r400vgt_line_loop_01	2002Sep21
PASS		
606	r400vgt_line_loop_02	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
607	r400vgt_line_strip_01	2002Sep21
PASS		
608	r400vgt_line_strip_02	2002Sep21
PASS		
609	r400vgt_multi_context_01	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
610	r400vgt_multi_context_02	2002Sep21
PASS		
611	r400vgt_multi_context_03	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
612	r400vgt_multi_context_04	2002Sep21
PASS		
613	r400vgt_multi_context_05	2002Sep21
PASS		
614	r400vgt_multi_context_06	2002Sep21
PASS		
615	r400vgt_multi_context_07	2002Sep21
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616	r400vgt_multi_context_08	2002Sep21
PASS		
617	r400vgt_multi_context_09	2002Sep21
PASS		
618	r400vgt_multi_context_10	2002Sep21
PASS		
619	r400vgt_multi_context_11	2002Sep21
PASS		
620	r400vgt_multi_context_12	2002Sep21

PASS		
621	r400vgt_multi_prim_reset_index_all_01	2002Sep21
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
622	r400vgt_pass_thru_all_prims_01	2002Sep21
PASS		
623	r400vgt_point_list_01	2002Sep21
PASS		
624	r400vgt_point_list_02	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
625	r400vgt_polygon_01	2002Sep21
PASS		
626	r400vgt_polygon_02	2002Sep21
PASS		
627	r400vgt_provoking_vtx_all_01	2002Sep21
PASS		
628	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep21
PASS		
629	r400vgt_provoking_vtx_polygon_01	2002Sep21
PASS		
630	r400vgt_provoking_vtx_quad_list_01	2002Sep21
PASS		
631	r400vgt_provoking_vtx_quad_strip_01	2002Sep21
PASS		
632	r400vgt_provoking_vtx_tri_fan_01	2002Sep21
PASS		
633	r400vgt_provoking_vtx_tri_strip_01	2002Sep21
PASS		
634	r400vgt_quad_list_01	2002Sep21
PASS		
635	r400vgt_quad_list_02	2002Sep21
PASS		
636	r400vgt_quad_strip_01	2002Sep21
PASS		
637	r400vgt_quad_strip_02	2002Sep21
PASS		
638	r400vgt_rectangle_list_01	2002Sep21
PASS		
639	r400vgt_rectangle_list_02	2002Sep21
PASS		
640	r400vgt_reuse_depth_line_list_01	2002Sep21
PASS		
641	r400vgt_reuse_depth_point_list_01	2002Sep21
PASS		
642	r400vgt_reuse_depth_triangle_list_01	2002Sep21
PASS		
643	r400vgt_reuse_index_line_list_01	2002Sep21
PASS		
644	r400vgt_reuse_index_point_list_01	2002Sep21

PASS		
645	r400vgt_reuse_index_triangle_list_01	2002Sep21
PASS		
646	r400vgt_reuse_index_triangle_list_02	2002Sep21
PASS		
647	r400vgt_reuse_index_triangle_list_03	2002Sep21
PASS		
648	r400vgt_suppress_eop_01	2002Sep21
PASS		
649	r400vgt_suppress_eop_02	2002Sep21
PASS		
650	r400vgt_suppress_eop_03	2002Sep21
PASS		
651	r400vgt_suppress_eop_04	2002Sep21
PASS		
652	r400vgt_triangle_fan_01	2002Sep21
PASS		
653	r400vgt_triangle_fan_02	2002Sep21
PASS		
654	r400vgt_triangle_list_01	2002Sep21
PASS		
655	r400vgt_triangle_list_02	2002Sep21
PASS		
656	r400vgt_triangle_strip_01	2002Sep21
PASS		
657	r400vgt_triangle_strip_02	2002Sep21
PASS		
658	r400vgt_triangle_wflags_01	2002Sep21
PASS		
659	r400vgt_triangle_wflags_02	2002Sep21
PASS		
660	r400vgt_viz_query_01	2002Sep21
PASS		
661	r400vgt_vtx_vector_packing_01	2002Sep21
PASS		
662	r400vte_coverage_01	2002Sep21
FAIL	VgtPaClipS_VgtSide.cmp	
663	r400vte_simple_point_01	2002Sep21
PASS		
664	r400vte_simple_triangle_01	2002Sep21
PASS		
665	r400vte_w0_fmt_01	2002Sep21
PASS		
666	r400vte_w0_fmt_03	2002Sep21
PASS		
667	r400vte_xy_fmt_01	2002Sep21
PASS		
668	r400vte_xy_fmt_mk_01_bug	2002Sep21

```
PASS
669 r400vte_xyz_scale_01 2002Sep21
PASS
670 r400vte_xyz_scale_02 2002Sep21
**FAIL**      VgtPaClipS_VgtSide.cmp
671 r400vte_z_fmt_01 2002Sep21
PASS
```

```
=====
Tests_Run_count = 671 Pass_count = 653 Fail_count = 17 Incomplete_count = 1
=====
```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep11
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep11
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep11
PASS			
4	primlib_parameterized_simple_triangle		2002Sep11
PASS			
5	primlib_template_simple_triangle		2002Sep11
PASS			
6	primlib_tex_tri		2002Sep11
PASS			
7	r400cl_clip_10_verts_01		2002Sep11
PASS			
8	r400cl_clip_disable_01		2002Sep11
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep11
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep11
PASS			
11	r400cl_cull_only_ena_01		2002Sep11
PASS			
12	r400cl_edgeflags_01		2002Sep11
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep11
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep11
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep11
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep11
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep11
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep11
PASS			
19	r400cl_edgeflags_gband_01		2002Sep11
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep11

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep11
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep11
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep11
PASS		
24	r400cl_frustum_01	2002Sep11
PASS		
25	r400cl_frustum_02	2002Sep11
PASS		
26	r400cl_frustum_03	2002Sep11
PASS		
27	r400cl_frustum_04	2002Sep11
PASS		
28	r400cl_frustum_05	2002Sep11
PASS		
29	r400cl_frustum_06	2002Sep11
PASS		
30	r400cl_frustum_07	2002Sep11
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31	r400cl_frustum_08	2002Sep11
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32	r400cl_frustum_09	2002Sep11
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33	r400cl_frustum_10	2002Sep11
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34	r400cl_frustum_11	2002Sep11
PASS		
35	r400cl_frustum_12	2002Sep11
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36	r400cl_frustum_13	2002Sep11
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37	r400cl_frustum_14	2002Sep11
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38	r400cl_frustum_15	2002Sep11
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39	r400cl_frustum_16	2002Sep11
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40	r400cl_frustum_17	2002Sep11
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41	r400cl_frustum_18	2002Sep11
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42	r400cl_frustum_19	2002Sep11
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43	r400cl_frustum_20	2002Sep11
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44	r400cl_frustum_21	2002Sep11

PASS		
45	r400cl_frustum_22	2002Sep11
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46	r400cl_frustum_23	2002Sep11
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50	r400cl_frustum_27	2002Sep11
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51	r400cl_frustum_28	2002Sep11
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52	r400cl_frustum_all_vols_lines	2002Sep11
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53	r400cl_frustum_all_vols_tris	2002Sep11
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54	r400cl_frustum_lines_01	2002Sep11
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55	r400cl_frustum_lines_02	2002Sep11
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56	r400cl_frustum_lines_03	2002Sep11
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57	r400cl_frustum_lines_04	2002Sep11
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66	r400cl_frustum_lines_13	2002Sep11
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68	r400cl_frustum_lines_15	2002Sep11

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69	r400cl_frustum_lines_16	2002Sep11
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74	r400cl_frustum_lines_21	2002Sep11
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75	r400cl_frustum_lines_22	2002Sep11
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76	r400cl_frustum_lines_23	2002Sep11
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77	r400cl_frustum_lines_24	2002Sep11
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78	r400cl_gband_04	2002Sep11
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79	r400cl_gband_tcl_01	2002Sep11
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80	r400cl_pointlist_vertex_state_ucp_01	2002Sep11
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81	r400cl_simple_triangle_01	2002Sep11
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82	r400cl_ucp_combos_01	2002Sep11
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83	r400cl_ucp_combos_02	2002Sep11
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84	r400cl_ucp_combos_03	2002Sep11
PASS		
85	r400cl_ucp_combos_04	2002Sep11
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86	r400cl_ucp_combos_05	2002Sep11
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87	r400cl_ucp_combos_06	2002Sep11
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88	r400cl_ucp_combos_07	2002Sep11
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89	r400cl_ucp_combos_08	2002Sep11
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90	r400cl_ucp_combos_09	2002Sep11
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PASS		
93	r400cl_ucp_combos_12	2002Sep11
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94	r400cl_ucp_combos_13	2002Sep11
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96	r400cl_ucp_combos_15	2002Sep11
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97	r400cl_ucp_combos_16	2002Sep11
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98	r400cl_ucp_combos_17	2002Sep11
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PASS		
103	r400cl_ucp_combos_22	2002Sep11
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105	r400cl_ucp_combos_24	2002Sep11
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107	r400cl_ucp_combos_26	2002Sep11
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108	r400cl_ucp_combos_27	2002Sep11
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109	r400cl_ucp_combos_28	2002Sep11
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110	r400cl_ucp_combos_29	2002Sep11
PASS		
111	r400cl_ucp_combos_30	2002Sep11
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112	r400cl_ucp_pointlist_01	2002Sep11
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113	r400cl_vertex_reuse_clip_01	2002Sep11
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114	r400cl_vtx_kill_01	2002Sep11
PASS		
115	r400cl_vtx_kill_02	2002Sep11
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116	r400cl_w_eq_0	2002Sep11

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117	r400sanity_persp_projection_01	2002Sep11
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118	r400sanity_vfd_texture_sample_01	2002Sep11
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119	r400sc_bres_cntl_01	2002Sep7
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120	r400sc_bres_cntl_02	2002Sep11
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121	r400sc_bres_cntl_03	2002Sep11
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122	r400sc_bres_cntl_04	2002Sep11
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123	r400sc_bres_cntl_w2k_01	2002Sep11
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124	r400sc_bres_cntl_w9x_01	2002Sep11
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125	r400sc_clip_rect_01	2002Sep11
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126	r400sc_clip_rect_02	2002Sep11
PASS		
127	r400sc_clip_rect_03	2002Sep11
PASS		
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185	r400sc_line_list_concentric_circle_02	2002Sep11
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186	r400sc_line_list_concentric_circle_03	2002Sep11
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459	r400vgt_dma_index_line_list_01	2002Sep11
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460	r400vgt_dma_index_line_loop_01	2002Sep11
PASS		
461	r400vgt_dma_index_line_strip_01	2002Sep11
PASS		
462	r400vgt_dma_index_points_01	2002Sep11
PASS		
463	r400vgt_dma_index_polygon_01	2002Sep11
PASS		
464	r400vgt_dma_index_primitives_01	2002Sep11
PASS		
465	r400vgt_dma_index_quad_list_01	2002Sep11
PASS		
466	r400vgt_dma_index_quad_strip_01	2002Sep11
PASS		
467	r400vgt_dma_index_rectangle_list_01	2002Sep11
PASS		
468	r400vgt_dma_index_tri_fan_01	2002Sep11
PASS		
469	r400vgt_dma_index_tri_list_01	2002Sep11
PASS		
470	r400vgt_dma_index_tri_strip_01	2002Sep11
PASS		
471	r400vgt_dma_index_tri_wflags_01	2002Sep11
PASS		
472	r400vgt_dma_swap_indx16_01	2002Sep11
PASS		
473	r400vgt_dma_swap_indx32_01	2002Sep11
PASS		
474	r400vgt_edgeflags_polygon_01	2002Sep11
PASS		
475	r400vgt_edgeflags_quad_list_01	2002Sep11
PASS		
476	r400vgt_edgeflags_quad_strip_01	2002Sep11

PASS		
477	r400vgt_edgeflags_triangle_fan_01	2002Sep11
PASS		
478	r400vgt_edgeflags_triangle_list_01	2002Sep11
PASS		
479	r400vgt_edgeflags_triangle_strip_01	2002Sep11
PASS		
480	r400vgt_edgeflags_triangle_wflags_01	2002Sep11
PASS		
481	r400vgt_ext2int_index_line_list_01	2002Sep11
PASS		
482	r400vgt_ext2int_index_line_loop_01	2002Sep11
PASS		
483	r400vgt_ext2int_index_line_strip_01	2002Sep11
PASS		
484	r400vgt_ext2int_index_points_01	2002Sep11
PASS		
485	r400vgt_ext2int_index_polygon_01	2002Sep11
PASS		
486	r400vgt_ext2int_index_quad_list_01	2002Sep11
PASS		
487	r400vgt_ext2int_index_quad_strip_01	2002Sep11
PASS		
488	r400vgt_ext2int_index_triangle_fan_01	2002Sep11
PASS		
489	r400vgt_ext2int_index_triangle_list_01	2002Sep11
PASS		
490	r400vgt_ext2int_index_triangle_strip_01	2002Sep11
PASS		
491	r400vgt_hos_auto_index_line_list_01	2002Sep11
PASS		
492	r400vgt_hos_auto_index_quad_list_01	2002Sep11
PASS		
493	r400vgt_hos_auto_index_triangle_list_01	2002Sep11
PASS		
494	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep11
PASS		
495	r400vgt_hos_LPatch_01	2002Sep11
PASS		
496	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep11
PASS		
497	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep11
PASS		
498	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep11
PASS		
499	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep11
PASS		
500	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep11

PASS		
501	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep11
PASS		
502	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep11
PASS		
503	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep11
PASS		
504	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep11
PASS		
505	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep11
PASS		
506	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep11
PASS		
507	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep11
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508	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep11
PASS		
509	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep11
PASS		
510	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep11
PASS		
511	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep11
PASS		
512	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep11
PASS		
513	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep11
PASS		
514	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep11
PASS		
515	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep11
PASS		
516	r400vgt_hos_PNT_edge_detection_01	2002Sep11
PASS		
517	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep11
PASS		
518	r400vgt_hos_RPatch_cp_02	2002Sep11
PASS		
519	r400vgt_hos_RPatch_lp_02	2002Sep11
PASS		
520	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep11
PASS		
521	r400vgt_hos_TPatch_01	2002Sep11
INCOMPLETE		
522	r400vgt_hos_TPatch_02	2002Sep11
INCOMPLETE		
523	r400vgt_immed_index_line_list_01	2002Sep11
PASS		
524	r400vgt_immed_index_line_loop_01	2002Sep11

PASS		
525	r400vgt_immed_index_line_strip_01	2002Sep11
PASS		
526	r400vgt_immed_index_points_01	2002Sep11
PASS		
527	r400vgt_immed_index_polygon_01	2002Sep11
PASS		
528	r400vgt_immed_index_primitives_01	2002Sep11
PASS		
529	r400vgt_immed_index_quad_list_01	2002Sep11
PASS		
530	r400vgt_immed_index_quad_strip_01	2002Sep11
PASS		
531	r400vgt_immed_index_rectangle_list_01	2002Sep11
PASS		
532	r400vgt_immed_index_tri_fan_01	2002Sep11
PASS		
533	r400vgt_immed_index_tri_list_01	2002Sep11
PASS		
534	r400vgt_immed_index_tri_strip_01	2002Sep11
PASS		
535	r400vgt_immed_index_tri_wflags_01	2002Sep11
PASS		
536	r400vgt_index_dealloc_line_list_01	2002Sep11
PASS		
537	r400vgt_index_dealloc_points_01	2002Sep11
PASS		
538	r400vgt_index_dealloc_triangle_list_01	2002Sep11
PASS		
539	r400vgt_index_min_max_01	2002Sep11
PASS		
540	r400vgt_index_min_max_02	2002Sep11
PASS		
541	r400vgt_index_offset_01	2002Sep11
PASS		
542	r400vgt_index_offset_02	2002Sep11
PASS		
543	r400vgt_index_offset_03	2002Sep11
PASS		
544	r400vgt_index_size_01	2002Sep11
PASS		
545	r400vgt_index_source_switch_01	2002Sep11
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546	r400vgt_line_list_01	2002Sep11
PASS		
547	r400vgt_line_loop_01	2002Sep11
PASS		
548	r400vgt_line_strip_01	2002Sep11

PASS		
549	r400vgt_multi_context_01	2002Sep11
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550	r400vgt_multi_context_02	2002Sep11
PASS		
551	r400vgt_multi_context_03	2002Sep11
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552	r400vgt_multi_context_04	2002Sep11
PASS		
553	r400vgt_multi_context_05	2002Sep11
PASS		
554	r400vgt_multi_context_06	2002Sep11
PASS		
555	r400vgt_multi_context_07	2002Sep11
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556	r400vgt_multi_context_08	2002Sep11
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557	r400vgt_multi_context_09	2002Sep11
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558	r400vgt_multi_context_10	2002Sep11
PASS		
559	r400vgt_multi_context_11	2002Sep11
PASS		
560	r400vgt_multi_context_12	2002Sep11
PASS		
561	r400vgt_pass_thru_all_prims_01	2002Sep11
PASS		
562	r400vgt_point_list_01	2002Sep11
PASS		
563	r400vgt_polygon_01	2002Sep11
PASS		
564	r400vgt_provoking_vtx_all_01	2002Sep11
PASS		
565	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep11
PASS		
566	r400vgt_quad_list_01	2002Sep11
PASS		
567	r400vgt_quad_strip_01	2002Sep11
PASS		
568	r400vgt_rectangle_list_01	2002Sep11
PASS		
569	r400vgt_reuse_depth_line_list_01	2002Sep11
PASS		
570	r400vgt_reuse_depth_point_list_01	2002Sep11
PASS		
571	r400vgt_reuse_depth_triangle_list_01	2002Sep11
PASS		
572	r400vgt_reuse_index_line_list_01	2002Sep11

PASS
573 r400vgt_reuse_index_point_list_01 2002Sep11
PASS
574 r400vgt_reuse_index_triangle_list_01 2002Sep11
PASS
575 r400vgt_reuse_index_triangle_list_02 2002Sep11
PASS
576 r400vgt_reuse_index_triangle_list_03 2002Sep11
PASS
577 r400vgt_suppress_eop_01 2002Sep11
PASS
578 r400vgt_suppress_eop_02 2002Sep11
PASS
579 r400vgt_suppress_eop_03 2002Sep11
PASS
580 r400vgt_suppress_eop_04 2002Sep11
PASS
581 r400vgt_triangle_fan_01 2002Sep11
PASS
582 r400vgt_triangle_list_01 2002Sep11
PASS
583 r400vgt_triangle_strip_01 2002Sep11
PASS
584 r400vgt_triangle_wflags_01 2002Sep11
PASS
585 r400vgt_viz_query_01 2002Sep11
PASS
586 r400vgt_vtx_vector_packing_01 2002Sep11
PASS
587 r400vte_coverage_01 2002Sep11
PASS
588 r400vte_simple_point_01 2002Sep11
PASS
589 r400vte_simple_triangle_01 2002Sep11
PASS
590 r400vte_w0_fmt_01 2002Sep11
PASS
591 r400vte_w0_fmt_03 2002Sep11
PASS
592 r400vte_xy_fmt_01 2002Sep11
PASS
593 r400vte_xyz_scale_01 2002Sep11
PASS
594 r400vte_xyz_scale_02 2002Sep11
PASS
595 r400vte_z_fmt_01 2002Sep11
PASS
=====

Tests_Run_count = 595 Pass_count = 592 Fail_count = 0 Incomplete_count = 3

=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep  4  3:18:28 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          147          147          145          98
              SC           164          164          164          100
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY    SU           81           81           81          100
              SANITY       2           2           2           100
+++++
              TOTAL        517          517          515          99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep2
	PASS		
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep2
	PASS		
3	primlib_gouraud_triangles_2_draw_passes		2002Sep2
	PASS		
4	primlib_parameterized_simple_triangle		2002Sep2
	PASS		
5	primlib_template_simple_triangle		2002Sep2
	PASS		
6	primlib_tex_tri		2002Sep2
	PASS		
7	r400cl_clip_10_verts_01		2002Sep2
	PASS		
8	r400cl_clip_disable_01		2002Sep2
	PASS		
9	r400cl_clip_space_dx_ogl_01		2002Sep2
	PASS		
10	r400cl_clip_ucp_6bits_01		2002Sep2
	PASS		
11	r400cl_cull_only_ena_01		2002Sep2
	PASS		
12	r400cl_edgeflags_01		2002Sep2
	PASS		
13	r400cl_edgeflags_frustum_bottom_01		2002Sep2
	PASS		
14	r400cl_edgeflags_frustum_far_01		2002Sep2
	PASS		
15	r400cl_edgeflags_frustum_left_01		2002Sep2
	PASS		
16	r400cl_edgeflags_frustum_near_01		2002Sep2
	PASS		
17	r400cl_edgeflags_frustum_right_01		2002Sep2
	PASS		
18	r400cl_edgeflags_frustum_top_01		2002Sep2
	PASS		
19	r400cl_edgeflags_gband_01		2002Sep2
	PASS		
20	r400cl_edgeflags_gband_bottom_01		2002Sep2

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep2
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep2
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep2
PASS		
24	r400cl_frustum_01	2002Sep2
PASS		
25	r400cl_frustum_02	2002Sep2
PASS		
26	r400cl_frustum_03	2002Sep2
PASS		
27	r400cl_frustum_04	2002Sep2
PASS		
28	r400cl_frustum_05	2002Sep2
PASS		
29	r400cl_frustum_06	2002Sep2
PASS		
30	r400cl_frustum_07	2002Sep2
PASS		
31	r400cl_frustum_08	2002Sep2
PASS		
32	r400cl_frustum_09	2002Sep2
PASS		
33	r400cl_frustum_10	2002Sep2
PASS		
34	r400cl_frustum_11	2002Sep2
PASS		
35	r400cl_frustum_12	2002Sep2
PASS		
36	r400cl_frustum_13	2002Sep2
PASS		
37	r400cl_frustum_14	2002Sep2
PASS		
38	r400cl_frustum_15	2002Sep2
PASS		
39	r400cl_frustum_16	2002Sep2
PASS		
40	r400cl_frustum_17	2002Sep2
PASS		
41	r400cl_frustum_18	2002Sep2
PASS		
42	r400cl_frustum_19	2002Sep2
PASS		
43	r400cl_frustum_20	2002Sep2
PASS		
44	r400cl_frustum_21	2002Sep2

PASS		
45	r400cl_frustum_22	2002Sep2
PASS		
46	r400cl_frustum_23	2002Sep2
PASS		
47	r400cl_frustum_24	2002Sep2
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48	r400cl_frustum_25	2002Sep2
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49	r400cl_frustum_26	2002Sep2
PASS		
50	r400cl_frustum_27	2002Sep2
PASS		
51	r400cl_frustum_28	2002Sep2
PASS		
52	r400cl_frustum_lines_01	2002Sep2
PASS		
53	r400cl_frustum_lines_02	2002Sep2
PASS		
54	r400cl_frustum_lines_03	2002Sep2
PASS		
55	r400cl_frustum_lines_04	2002Sep2
PASS		
56	r400cl_frustum_lines_05	2002Sep2
PASS		
57	r400cl_frustum_lines_06	2002Sep2
PASS		
58	r400cl_frustum_lines_07	2002Sep2
PASS		
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PASS		
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PASS		
61	r400cl_frustum_lines_10	2002Sep2
PASS		
62	r400cl_frustum_lines_11	2002Sep2
PASS		
63	r400cl_frustum_lines_12	2002Sep2
PASS		
64	r400cl_frustum_lines_13	2002Sep2
PASS		
65	r400cl_frustum_lines_14	2002Sep2
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66	r400cl_frustum_lines_15	2002Sep2
PASS		
67	r400cl_frustum_lines_16	2002Sep2
PASS		
68	r400cl_frustum_lines_17	2002Sep2

PASS		
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70	r400cl_frustum_lines_19	2002Sep2
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72	r400cl_frustum_lines_21	2002Sep2
PASS		
73	r400cl_frustum_lines_22	2002Sep2
PASS		
74	r400cl_frustum_lines_23	2002Sep2
PASS		
75	r400cl_frustum_lines_24	2002Sep2
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76	r400cl_gband_04	2002Sep2
PASS		
77	r400cl_gband_tcl_01	2002Sep2
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78	r400cl_pointlist_vertex_state_ucp_01	2002Sep2
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79	r400cl_simple_triangle_01	2002Sep2
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80	r400cl_ucp_combos_01	2002Sep2
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81	r400cl_ucp_combos_02	2002Sep2
PASS		
82	r400cl_ucp_combos_03	2002Sep2
PASS		
83	r400cl_ucp_combos_04	2002Sep2
PASS		
84	r400cl_ucp_combos_05	2002Sep2
PASS		
85	r400cl_ucp_combos_06	2002Sep2
PASS		
86	r400cl_ucp_combos_07	2002Sep2
PASS		
87	r400cl_ucp_combos_08	2002Sep2
PASS		
88	r400cl_ucp_combos_09	2002Sep2
PASS		
89	r400cl_ucp_combos_10	2002Sep2
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90	r400cl_ucp_combos_11	2002Sep2
PASS		
91	r400cl_ucp_combos_12	2002Sep2
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92	r400cl_ucp_combos_13	2002Sep2

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94	r400cl_ucp_combos_15	2002Sep2
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95	r400cl_ucp_combos_16	2002Sep2
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96	r400cl_ucp_combos_17	2002Sep2
PASS		
97	r400cl_ucp_combos_18	2002Sep2
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98	r400cl_ucp_combos_19	2002Sep2
PASS		
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PASS		
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PASS		
101	r400cl_ucp_combos_22	2002Sep2
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PASS		
106	r400cl_ucp_combos_27	2002Sep2
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107	r400cl_ucp_combos_28	2002Sep2
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108	r400cl_ucp_combos_29	2002Sep2
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109	r400cl_ucp_combos_30	2002Sep2
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110	r400cl_ucp_pointlist_01	2002Sep2
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113	r400cl_vtx_kill_02	2002Sep2
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114	r400cl_w_eq_0	2002Sep2
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115	r400sanity_persp_projection_01	2002Sep2
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116	r400sanity_vfd_texture_sample_01	2002Sep2

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117	r400sc_bres_cntl_01	2002Sep2
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118	r400sc_bres_cntl_02	2002Sep2
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122	r400sc_clip_rect_02	2002Sep2
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123	r400sc_clip_rect_03	2002Sep2
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124	r400sc_clip_rect_04	2002Sep2
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125	r400sc_clip_rect_fc_01	2002Sep2
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126	r400sc_diamond_exit_01	2002Sep2
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127	r400sc_diamond_exit_02	2002Sep2
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128	r400sc_diamond_exit_03	2002Sep2
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129	r400sc_diamond_exit_04	2002Sep2
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130	r400sc_diamond_exit_05	2002Sep2
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131	r400sc_jss_1x2_01	2002Sep2
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132	r400sc_jss_1x2_02	2002Sep2
PASS		
133	r400sc_jss_1x3_01	2002Sep2
PASS		
134	r400sc_jss_1x3_02	2002Sep2
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135	r400sc_jss_1x4_01	2002Sep2
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136	r400sc_jss_1x4_02	2002Sep2
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137	r400sc_jss_2x1_01	2002Sep2
PASS		
138	r400sc_jss_2x1_02	2002Sep2
PASS		
139	r400sc_jss_2x2_01	2002Sep2
PASS		
140	r400sc_jss_2x2_02	2002Sep2

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142	r400sc_jss_2x3_02	2002Sep2
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143	r400sc_jss_2x4_01	2002Sep2
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144	r400sc_jss_2x4_02	2002Sep2
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146	r400sc_jss_3x1_02	2002Sep2
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147	r400sc_jss_3x2_01	2002Sep2
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148	r400sc_jss_3x2_02	2002Sep2
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149	r400sc_jss_3x3_01	2002Sep2
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152	r400sc_jss_3x4_02	2002Sep2
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153	r400sc_jss_3x4_03	2002Sep2
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154	r400sc_jss_4x1_01	2002Sep2
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155	r400sc_jss_4x1_02	2002Sep2
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156	r400sc_jss_4x2_01	2002Sep2
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157	r400sc_jss_4x2_02	2002Sep2
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159	r400sc_jss_4x3_02	2002Sep2
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160	r400sc_jss_4x4_01	2002Sep2
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161	r400sc_jss_4x4_02	2002Sep2
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162	r400sc_jss_4x4_03	2002Sep2
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163	r400sc_jss_4x4_aa_mask_01	2002Sep2
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164	r400sc_jss_4x4_aa_mask_02	2002Sep2

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168	r400sc_line_dx10_eq_0_01	2002Sep2
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169	r400sc_line_dx10_ge_0_01	2002Sep2
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170	r400sc_line_dx10_lt_0_01	2002Sep2
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171	r400sc_line_dy10_eq_0_01	2002Sep2
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172	r400sc_line_dy10_ge_0_01	2002Sep2
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173	r400sc_line_dy10_lt_0_01	2002Sep2
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174	r400sc_line_expand_width_msa_8_01	2002Sep2
PASS		
175	r400sc_line_expand_width_msa_8_02	2002Sep2
PASS		
176	r400sc_line_expand_width_msa_8_03	2002Sep2
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177	r400sc_line_jss_3x4_01	2002Sep2
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178	r400sc_line_list_01	2002Sep2
PASS		
179	r400sc_line_list_02	2002Sep2
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180	r400sc_line_list_concentric_circle_01	2002Sep2
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181	r400sc_line_list_concentric_circle_02	2002Sep2
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182	r400sc_line_list_concentric_circle_03	2002Sep2
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183	r400sc_line_list_textured_01	2002Aug31
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184	r400sc_line_list_verify_st_01	2002Sep2
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185	r400sc_line_msa_8_01	2002Sep2
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186	r400sc_line_stipple_01	2002Sep2
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187	r400sc_line_stipple_02	2002Sep2
PASS		
188	r400sc_line_stipple_03	2002Sep2

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189	r400sc_line_stipple_04	2002Sep2
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190	r400sc_line_stipple_05	2002Sep2
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191	r400sc_line_stipple_06	2002Sep2
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192	r400sc_line_stipple_07	2002Sep2
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193	r400sc_line_stipple_08	2002Sep2
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194	r400sc_line_stipple_09	2002Sep2
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195	r400sc_line_stipple_fc_08	2002Sep2
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196	r400sc_line_strip_stipple_01	2002Sep2
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197	r400sc_msaa_1_01	2002Sep2
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198	r400sc_msaa_4_01	2002Sep2
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199	r400sc_msaa_6_01	2002Sep2
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201	r400sc_msaa_8_aa_mask_01	2002Sep2
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202	r400sc_msaa_8_aa_mask_02	2002Sep2
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203	r400sc_msaa_8_aa_mask_fc_02	2002Sep2
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204	r400sc_null_triangles_01	2002Sep2
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205	r400sc_null_triangles_fc_01	2002Sep2
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206	r400sc_point_jss_3x4_01	2002Sep2
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207	r400sc_point_list_01	2002Sep2
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210	r400sc_point_list_04	2002Sep2
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211	r400sc_point_list_05	2002Sep2
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212	r400sc_point_list_06	2002Aug28

INCOMPLETE

213	r400sc_point_list_07	2002Sep2
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214	r400sc_point_list_08	2002Sep2
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215	r400sc_point_list_09	2002Sep2
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216	r400sc_point_msa_8_01	2002Sep2
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217	r400sc_polygon_stipple_01	2002Sep2
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218	r400sc_poly_offset_01	2002Sep2
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219	r400sc_poly_offset_02	2002Sep2
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220	r400sc_poly_offset_fc_01	2002Sep2
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221	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep2
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222	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep2
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227	r400sc_prsp_byc_intrp_ref_pix_07	2002Sep2
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228	r400sc_prsp_byc_intrp_ref_pix_08	2002Sep2
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229	r400sc_raster_fill_rule_01	2002Sep2
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230	r400sc_raster_fill_rule_02	2002Sep2
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231	r400sc_raster_fill_rule_03	2002Sep2
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234	r400sc_raster_fill_rule_fc_01	2002Sep2
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244	r400sc_scissor_rect_02	2002Sep2
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247	r400sc_scissor_rect_05	2002Sep2
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250	r400sc_tri_16_par_64_dwords_01	2002Sep2
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251	r400sc_tri_8textures_01	2002Sep2
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252	r400sc_tri_8textures_02	2002Sep2
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269	r400sc_tri_walk_start_vertex_16	2002Sep2
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273	r400sc_zbuffer_point_list_01	2002Sep2
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274	r400sc_zbuffer_rectangle_list_01	2002Sep2
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275	r400sc_zbuffer_rectangle_list_02	2002Sep2
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276	r400sc_zbuffer_rectangle_list_fc_02	2002Sep2
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277	r400sc_zbuffer_triangle_list_01	2002Sep2
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281	r400su_baryc_test_03	2002Sep2
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287	r400su_clip_pointttest_01	2002Sep2
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292	r400su_clipvertexsortingfunctional_01	2002Sep2
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293	r400su_cullingfunctional_01	2002Sep2
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294	r400su_degentri_test_01	2002Sep2
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295	r400su_degentri_test_02	2002Sep2
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296	r400su_degentri_test_03	2002Sep2
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300	r400su_edgeflags_triangle_02	2002Sep2
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324	r400su_point_w1_h16_functional_01	2002Sep2
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326	r400su_polymodefunctional_01	2002Sep2
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504 r400vgt_triangle_wflags_01 2002Sep2
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505 r400vgt_vtx_vector_packing_01 2002Sep2
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506 r400vte_coverage_01 2002Sep2
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508 r400vte_simple_triangle_01 2002Sep2
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++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Sep 27 21:24:17 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             175         175         171           97
              SC              284         284         284           100
              CL              115         115         115           100
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              SU               91          91          85           93
              SANITY            2           2           2           100
+++++
              TOTAL            695         695         685           98
+++++

```

=====
Regression Report For VGT - Project = r400 Created on Mon Oct 7 21:50:04 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

=====
1 cp_dma_2desc 2002Oct7
PASS
2 cp_dma_interrupt 2002Oct7
PASS
3 cp_dma_m2m_01 2002Oct7
PASS
4 cp_dma_m2m_02 2002Oct7
PASS
5 cp_dma_m2m_03 2002Oct7
PASS
6 cp_dma_m2m_04 2002Oct7
PASS
7 cp_dma_m2r_01 2002Oct7
PASS
8 cp_dma_m2r_02 2002Oct7
PASS
9 cp_dma_m2r_03 2002Oct7
PASS
10 cp_dma_m2r_04 2002Oct7
PASS
11 cp_dma_m2r_r2m 2002Oct7
PASS
12 cp_dma_pio_simple 2002Oct7
PASS
13 cp_dma_pio_stress 2002Oct7
PASS
14 cp_dma_piobm_stress 2002Oct7
PASS
15 cp_dma_r2m_01 2002Oct7
PASS
16 cp_dma_r2m_02 2002Oct7
PASS
17 cp_dma_r2m_03 2002Oct7
PASS
18 cp_dma_r2m_04 2002Oct7
PASS
19 cp_dma_r2r_01 2002Oct7
PASS
20 cp_dma_r2r_02 2002Oct7

PASS		
21	cp_dma_r2r_03	20020ct7
PASS		
22	cp_dma_r2r_r2m	20020ct7
PASS		
23	cp_dma_r2r_r2m_m2m	20020ct7
PASS		
24	cp_dma_r2r_r2m_m2m_r2m	20020ct7
PASS		
25	cp_dma_simple	20020ct7
PASS		
26	cp_e2_hostdata_blt_pntr_8888	20020ct7
PASS		
27	cp_e2_one_blit	20020ct7
PASS		
28	cp_e2_one_line	20020ct7
PASS		
29	cp_e2blit_brush_mt_ropcc	20020ct7
PASS		
30	cp_e2paint_565	20020ct7
PASS		
31	cp_e2paint_multi	20020ct7
PASS		
32	cp_e2polyscanlines_brush	20020ct7
PASS		
33	cp_e2polyscanlines_brush_mt	20020ct7
PASS		
34	cp_e2smalltext_jcl	20020ct7
FAIL	VgtPaClipP_VgtSide.cmp	
35	cp_im_load_indirect	20020ct7
PASS		
36	cp_push_aper_indirect1	20020ct7
PASS		
37	cp_push_aper_primary	20020ct7
PASS		
38	cp_queue_avail_01	20020ct7
PASS		
39	cp_queue_avail_02	20020ct7
PASS		
40	cp_queue_avail_03	20020ct7
PASS		
41	cp_queue_avail_04	20020ct7
PASS		
42	cp_queue_avail_05	20020ct7
PASS		
43	cp_queue_avail_06	20020ct7
PASS		
44	cp_queue_avail_07	20020ct7

PASS		
45	cp_queue_avail_08	2002Oct7
PASS		
46	cp_queue_avail_09	2002Oct7
PASS		
47	cp_queue_avail_10	2002Oct7
PASS		
48	cp_queue_avail_11	2002Oct7
PASS		
49	cp_queue_avail_12	2002Oct7
PASS		
50	cp_simple_triangle	2002Oct7
PASS		
51	milestone_event_flush_tri1	2002Oct3
PASS		
52	milestone_event_flush_tri2	2002Oct3
PASS		
53	milestone_event_flush_tri3	2002Oct3
PASS		
54	milestone_event_flush_tri4	2002Oct3
PASS		
55	milestone_event_flush_tri5	2002Oct3
PASS		
56	milestone_event_flush_tri6	2002Oct3
PASS		
57	milestone_event_flush_tri7	2002Oct3
PASS		
58	milestone_event_flush_tri8	2002Oct3
PASS		
59	milestone_event_flush_tri9	2002Oct3
PASS		
60	milestone_event_randomized	2002Oct3
PASS		
61	milestone_event_standard	2002Oct3
PASS		
62	milestone_event_switch_flush_tri1	2002Oct3
PASS		
63	milestone_event_switch_flush_tri2	2002Oct3
PASS		
64	milestone_event_switch_flush_tri3	2002Oct3
PASS		
65	milestone_event_switch_flush_tri4	2002Oct3
PASS		
66	milestone_event_switch_flush_tri5	2002Oct3
PASS		
67	milestone_event_switch_flush_tri6	2002Oct3
PASS		
68	milestone_event_switch_flush_tri7	2002Oct3

PASS		
69	milestone_event_switch_flush_tri8	2002Oct3
PASS		
70	milestone_event_switch_flush_tri9	2002Oct3
PASS		
71	milestone_event_switch_tri2	2002Oct3
PASS		
72	milestone_event_switch_tri3	2002Oct3
PASS		
73	milestone_event_switch_tri4	2002Oct3
PASS		
74	milestone_event_switch_tri5	2002Oct3
PASS		
75	milestone_event_switch_tri6	2002Oct3
PASS		
76	milestone_event_switch_tri7	2002Oct3
PASS		
77	milestone_event_switch_tri8	2002Oct3
PASS		
78	milestone_event_switch_tri9	2002Oct3
PASS		
79	milestone_event_tri2	2002Oct3
PASS		
80	milestone_event_tri3	2002Oct3
PASS		
81	milestone_event_tri4	2002Oct3
PASS		
82	milestone_event_tri5	2002Oct3
PASS		
83	milestone_event_tri6	2002Oct3
PASS		
84	milestone_event_tri7	2002Oct3
PASS		
85	milestone_event_tri8	2002Oct3
PASS		
86	milestone_event_tri9	2002Oct3
PASS		
87	milestone_tri_random_rect	2002Oct3
PASS		
88	milestone_tri_random_tri	2002Oct3
PASS		
89	milestone_tri_randomized	2002Oct3
PASS		
90	milestone_tri_standard	2002Oct3
PASS		
91	milestone_tri_tril28_pix4	2002Oct3
PASS		
92	milestone_tri_tril_pix1	2002Oct3

PASS		
93	milestone_tri_tril_pix2	2002Oct3
PASS		
94	milestone_tri_tril_pix3	2002Oct3
PASS		
95	milestone_tri_tril_pix4	2002Oct3
PASS		
96	milestone_tri_tri24x24	2002Oct3
PASS		
97	milestone_tri_tri28x28	2002Oct3
PASS		
98	milestone_tri_tri2_pix1	2002Oct3
PASS		
99	milestone_tri_tri2_pix16	2002Oct3
PASS		
100	milestone_tri_tri2_pix4	2002Oct3
PASS		
101	milestone_tri_tri32_pix1	2002Oct3
PASS		
102	milestone_tri_tri32_pix4	2002Oct3
PASS		
103	milestone_tri_tri32x32	2002Oct3
PASS		
104	milestone_tri_tri36x36	2002Oct3
PASS		
105	milestone_tri_tri40x40	2002Oct3
PASS		
106	milestone_tri_tri44x44	2002Oct3
PASS		
107	milestone_tri_tri48x48	2002Oct3
PASS		
108	milestone_tri_tri52x52	2002Oct3
PASS		
109	milestone_tri_tri56x56	2002Oct3
PASS		
110	milestone_tri_tri60x60	2002Oct3
PASS		
111	milestone_tri_tri64x64	2002Oct3
PASS		
112	milestone_tri_tri8_pix4	2002Oct3
PASS		
113	primlib_1st_tri_june15	2002Oct7
PASS		
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct7
PASS		
115	primlib_gouraud_triangles_2_draw_passes	2002Oct7
PASS		
116	primlib_parameterized_simple_triangle	2002Oct7

PASS		
117	primlib_template_simple_triangle	2002Oct7
PASS		
118	primlib_tex_tri	2002Oct7
PASS		
119	r400cl_bary_texture_08_bug	2002Sep21
PASS		
120	r400cl_clip_10_verts_01	2002Oct7
PASS		
121	r400cl_clip_disable_01	2002Oct7
PASS		
122	r400cl_clip_space_dx_ogl_01	2002Oct7
PASS		
123	r400cl_clip_ucp_6bits_01	2002Oct7
PASS		
124	r400cl_cull_only_ena_01	2002Oct7
PASS		
125	r400cl_edgeflags_01	2002Oct7
PASS		
126	r400cl_edgeflags_01_bug	2002Oct7
PASS		
127	r400cl_edgeflags_frustum_bottom_01	2002Oct7
PASS		
128	r400cl_edgeflags_frustum_far_01	2002Oct7
PASS		
129	r400cl_edgeflags_frustum_left_01	2002Oct7
PASS		
130	r400cl_edgeflags_frustum_near_01	2002Oct7
PASS		
131	r400cl_edgeflags_frustum_right_01	2002Oct7
PASS		
132	r400cl_edgeflags_frustum_top_01	2002Oct7
PASS		
133	r400cl_edgeflags_gband_01	2002Oct7
PASS		
134	r400cl_edgeflags_gband_bottom_01	2002Oct7
PASS		
135	r400cl_edgeflags_gband_left_01	2002Oct7
PASS		
136	r400cl_edgeflags_gband_right_01	2002Oct7
PASS		
137	r400cl_edgeflags_gband_top_01	2002Oct7
PASS		
138	r400cl_frustum_01	2002Oct7
PASS		
139	r400cl_frustum_02	2002Oct7
PASS		
140	r400cl_frustum_03	2002Oct7

PASS		
141	r400cl_frustum_04	20020ct7
PASS		
142	r400cl_frustum_05	20020ct7
PASS		
143	r400cl_frustum_06	20020ct7
PASS		
144	r400cl_frustum_07	20020ct7
PASS		
145	r400cl_frustum_08	20020ct7
PASS		
146	r400cl_frustum_09	20020ct7
PASS		
147	r400cl_frustum_10	20020ct7
PASS		
148	r400cl_frustum_11	20020ct7
PASS		
149	r400cl_frustum_12	20020ct7
PASS		
150	r400cl_frustum_13	20020ct7
PASS		
151	r400cl_frustum_14	20020ct7
PASS		
152	r400cl_frustum_15	20020ct7
PASS		
153	r400cl_frustum_16	20020ct7
PASS		
154	r400cl_frustum_17	20020ct7
PASS		
155	r400cl_frustum_18	20020ct7
PASS		
156	r400cl_frustum_19	20020ct7
PASS		
157	r400cl_frustum_20	20020ct7
PASS		
158	r400cl_frustum_21	20020ct7
PASS		
159	r400cl_frustum_22	20020ct7
PASS		
160	r400cl_frustum_23	20020ct7
PASS		
161	r400cl_frustum_24	20020ct7
PASS		
162	r400cl_frustum_25	20020ct7
PASS		
163	r400cl_frustum_26	20020ct7
PASS		
164	r400cl_frustum_27	20020ct7

PASS		
165	r400cl_frustum_28	2002Oct7
PASS		
166	r400cl_frustum_all_vols_lines	2002Oct7
PASS		
167	r400cl_frustum_all_vols_tris	2002Oct7
PASS		
168	r400cl_frustum_lines_01	2002Oct7
PASS		
169	r400cl_frustum_lines_02	2002Oct7
PASS		
170	r400cl_frustum_lines_03	2002Oct7
PASS		
171	r400cl_frustum_lines_04	2002Oct7
PASS		
172	r400cl_frustum_lines_05	2002Oct7
PASS		
173	r400cl_frustum_lines_06	2002Oct7
PASS		
174	r400cl_frustum_lines_07	2002Oct7
PASS		
175	r400cl_frustum_lines_08	2002Oct7
PASS		
176	r400cl_frustum_lines_09	2002Oct7
PASS		
177	r400cl_frustum_lines_10	2002Oct7
PASS		
178	r400cl_frustum_lines_11	2002Oct7
PASS		
179	r400cl_frustum_lines_12	2002Oct7
PASS		
180	r400cl_frustum_lines_13	2002Oct7
PASS		
181	r400cl_frustum_lines_14	2002Oct7
PASS		
182	r400cl_frustum_lines_15	2002Oct7
PASS		
183	r400cl_frustum_lines_16	2002Oct7
PASS		
184	r400cl_frustum_lines_17	2002Oct7
PASS		
185	r400cl_frustum_lines_18	2002Oct7
PASS		
186	r400cl_frustum_lines_19	2002Oct7
PASS		
187	r400cl_frustum_lines_20	2002Oct7
PASS		
188	r400cl_frustum_lines_21	2002Oct7

PASS		
189	r400cl_frustum_lines_22	2002Oct7
PASS		
190	r400cl_frustum_lines_23	2002Oct7
PASS		
191	r400cl_frustum_lines_24	2002Oct7
PASS		
192	r400cl_gband_04	2002Oct7
PASS		
193	r400cl_gband_tcl_01	2002Oct7
PASS		
194	r400cl_nan_kill_01_bug	2002Oct7
PASS		
195	r400cl_point_ucp_combos_01	2002Oct7
INCOMPLETE		
196	r400cl_pointlist_vertex_state_ucp_01	2002Oct7
PASS		
197	r400cl_polymode_line_fill_01_bug	2002Oct7
PASS		
198	r400cl_simple_triangle_01	2002Oct7
PASS		
199	r400cl_tri_polymode_line_stipple_ucp_combos_01	2002Oct7
PASS		
200	r400cl_tri_polymode_line_ucp_combos_01	2002Oct7
PASS		
201	r400cl_triangle_polymode_line_stippled_01	2002Oct7
PASS		
202	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct7
PASS		
203	r400cl_ucp_combos_01	2002Oct7
PASS		
204	r400cl_ucp_combos_02	2002Oct7
PASS		
205	r400cl_ucp_combos_03	2002Oct7
PASS		
206	r400cl_ucp_combos_04	2002Oct7
PASS		
207	r400cl_ucp_combos_05	2002Oct7
PASS		
208	r400cl_ucp_combos_06	2002Oct7
PASS		
209	r400cl_ucp_combos_07	2002Oct7
PASS		
210	r400cl_ucp_combos_08	2002Oct7
PASS		
211	r400cl_ucp_combos_09	2002Oct7
PASS		
212	r400cl_ucp_combos_10	2002Oct7

PASS		
213	r400cl_ucp_combos_11	2002Oct7
PASS		
214	r400cl_ucp_combos_12	2002Oct7
PASS		
215	r400cl_ucp_combos_13	2002Oct7
PASS		
216	r400cl_ucp_combos_14	2002Oct7
PASS		
217	r400cl_ucp_combos_15	2002Oct7
PASS		
218	r400cl_ucp_combos_16	2002Oct7
PASS		
219	r400cl_ucp_combos_17	2002Oct7
PASS		
220	r400cl_ucp_combos_18	2002Oct7
PASS		
221	r400cl_ucp_combos_19	2002Oct7
PASS		
222	r400cl_ucp_combos_20	2002Oct7
PASS		
223	r400cl_ucp_combos_21	2002Oct7
PASS		
224	r400cl_ucp_combos_22	2002Oct7
PASS		
225	r400cl_ucp_combos_23	2002Oct7
PASS		
226	r400cl_ucp_combos_24	2002Oct7
PASS		
227	r400cl_ucp_combos_25	2002Oct7
PASS		
228	r400cl_ucp_combos_26	2002Oct7
PASS		
229	r400cl_ucp_combos_27	2002Oct7
PASS		
230	r400cl_ucp_combos_28	2002Oct7
PASS		
231	r400cl_ucp_combos_29	2002Oct7
PASS		
232	r400cl_ucp_combos_30	2002Oct7
PASS		
233	r400cl_ucp_pointlist_01	2002Oct7
PASS		
234	r400cl_vertex_reuse_clip_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
235	r400cl_vtx_kill_01	2002Oct7
PASS		
236	r400cl_vtx_kill_02	2002Oct7

PASS		
237	r400cl_w_eq_0	2002Oct7
PASS		
238	r400rbm_simple_triangle_01	2002Oct7
PASS		
239	r400sanity_persp_projection_01	2002Oct7
PASS		
240	r400sanity_vfd_texture_sample_01	2002Oct7
PASS		
241	r400sc_baryc_01	2002Oct7
PASS		
242	r400sc_bres_cntl_01	2002Sep7
PASS		
243	r400sc_bres_cntl_02	2002Oct7
PASS		
244	r400sc_bres_cntl_03	2002Oct7
PASS		
245	r400sc_bres_cntl_04	2002Oct7
PASS		
246	r400sc_bres_cntl_w2k_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
247	r400sc_bres_cntl_w9x_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
248	r400sc_clip_rect_01	2002Oct7
PASS		
249	r400sc_clip_rect_02	2002Oct7
PASS		
250	r400sc_clip_rect_03	2002Oct7
PASS		
251	r400sc_clip_rect_04	2002Oct7
PASS		
252	r400sc_clip_rect_fc_01	2002Oct7
PASS		
253	r400sc_diamond_exit_01	2002Oct7
PASS		
254	r400sc_diamond_exit_02	2002Oct7
PASS		
255	r400sc_diamond_exit_03	2002Oct7
PASS		
256	r400sc_diamond_exit_04	2002Oct7
PASS		
257	r400sc_diamond_exit_05	2002Oct7
PASS		
258	r400sc_jss_1x1_printypes_01	2002Oct7
PASS		
259	r400sc_jss_1x2_01	2002Oct7
PASS		
260	r400sc_jss_1x2_02	2002Oct7

PASS		
261	r400sc_jss_1x2_primtypes_01	20020ct7
PASS		
262	r400sc_jss_1x3_01	20020ct7
PASS		
263	r400sc_jss_1x3_02	20020ct7
PASS		
264	r400sc_jss_1x3_primtypes_01	20020ct7
PASS		
265	r400sc_jss_1x4_01	20020ct7
PASS		
266	r400sc_jss_1x4_02	20020ct7
PASS		
267	r400sc_jss_1x4_primtypes_01	20020ct7
PASS		
268	r400sc_jss_2x1_01	20020ct7
PASS		
269	r400sc_jss_2x1_02	20020ct7
PASS		
270	r400sc_jss_2x1_primtypes_01	20020ct7
PASS		
271	r400sc_jss_2x2_01	20020ct7
PASS		
272	r400sc_jss_2x2_02	20020ct7
PASS		
273	r400sc_jss_2x2_primtypes_01	20020ct7
PASS		
274	r400sc_jss_2x3_01	20020ct7
PASS		
275	r400sc_jss_2x3_02	20020ct7
PASS		
276	r400sc_jss_2x3_primtypes_01	20020ct7
PASS		
277	r400sc_jss_2x4_01	20020ct7
PASS		
278	r400sc_jss_2x4_02	20020ct7
PASS		
279	r400sc_jss_2x4_primtypes_01	20020ct7
PASS		
280	r400sc_jss_3x1_01	20020ct7
PASS		
281	r400sc_jss_3x1_02	20020ct7
PASS		
282	r400sc_jss_3x1_primtypes_01	20020ct7
PASS		
283	r400sc_jss_3x2_01	20020ct7
PASS		
284	r400sc_jss_3x2_02	20020ct7

PASS		
285	r400sc_jss_3x2_primtypes_01	2002Oct7
PASS		
286	r400sc_jss_3x3_01	2002Oct7
PASS		
287	r400sc_jss_3x3_02	2002Oct7
PASS		
288	r400sc_jss_3x3_primtypes_01	2002Oct7
PASS		
289	r400sc_jss_3x4_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
290	r400sc_jss_3x4_02	2002Oct7
PASS		
291	r400sc_jss_3x4_03	2002Oct7
PASS		
292	r400sc_jss_3x4_primtypes_01	2002Oct7
PASS		
293	r400sc_jss_4x1_01	2002Oct7
PASS		
294	r400sc_jss_4x1_02	2002Oct7
PASS		
295	r400sc_jss_4x1_primtypes_01	2002Oct7
PASS		
296	r400sc_jss_4x2_01	2002Oct7
PASS		
297	r400sc_jss_4x2_02	2002Oct7
PASS		
298	r400sc_jss_4x2_primtypes_01	2002Oct7
PASS		
299	r400sc_jss_4x3_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
300	r400sc_jss_4x3_02	2002Oct7
PASS		
301	r400sc_jss_4x3_primtypes_01	2002Oct7
PASS		
302	r400sc_jss_4x4_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
303	r400sc_jss_4x4_02	2002Oct7
PASS		
304	r400sc_jss_4x4_03	2002Oct7
PASS		
305	r400sc_jss_4x4_aa_mask_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
306	r400sc_jss_4x4_aa_mask_02	2002Oct7
PASS		
307	r400sc_jss_4x4_fc_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
308	r400sc_jss_4x4_fc_02	2002Oct7

PASS		
309	r400sc_jss_4x4_max_dist_01	2002Oct7
PASS		
310	r400sc_jss_4x4_primitives_01	2002Oct7
PASS		
311	r400sc_line_dx10_eq_0_01	2002Oct7
PASS		
312	r400sc_line_dx10_ge_0_01	2002Oct7
PASS		
313	r400sc_line_dx10_lt_0_01	2002Oct7
PASS		
314	r400sc_line_dy10_eq_0_01	2002Oct7
PASS		
315	r400sc_line_dy10_ge_0_01	2002Oct7
PASS		
316	r400sc_line_dy10_lt_0_01	2002Oct7
PASS		
317	r400sc_line_expand_width_msa_8_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
318	r400sc_line_expand_width_msa_8_02	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
319	r400sc_line_expand_width_msa_8_03	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
320	r400sc_line_jss_3x4_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
321	r400sc_line_list_01	2002Oct7
PASS		
322	r400sc_line_list_02	2002Oct7
PASS		
323	r400sc_line_list_concentric_circle_01	2002Oct7
PASS		
324	r400sc_line_list_concentric_circle_02	2002Oct7
PASS		
325	r400sc_line_list_concentric_circle_03	2002Oct7
PASS		
326	r400sc_line_list_textured_01	2002Oct7
PASS		
327	r400sc_line_list_verify_st_01	2002Oct7
PASS		
328	r400sc_line_list_verify_st_01_bug	2002Oct7
PASS		
329	r400sc_line_msa_8_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
330	r400sc_line_msa_8_textured_01	2002Oct7
PASS		
331	r400sc_line_stipple_01	2002Oct7
PASS		
332	r400sc_line_stipple_02	2002Oct7

PASS		
333	r400sc_line_stipple_03	2002Oct7
PASS		
334	r400sc_line_stipple_04	2002Oct7
PASS		
335	r400sc_line_stipple_05	2002Oct7
PASS		
336	r400sc_line_stipple_06	2002Oct7
PASS		
337	r400sc_line_stipple_07	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
338	r400sc_line_stipple_08	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
339	r400sc_line_stipple_09	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
340	r400sc_line_stipple_10	2002Oct7
PASS		
341	r400sc_line_stipple_11	2002Oct7
PASS		
342	r400sc_line_stipple_12	2002Oct7
PASS		
343	r400sc_line_stipple_13	2002Oct7
PASS		
344	r400sc_line_stipple_14	2002Oct7
PASS		
345	r400sc_line_stipple_15	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
346	r400sc_line_stipple_16	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
347	r400sc_line_stipple_17	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
348	r400sc_line_stipple_18	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
349	r400sc_line_stipple_19	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
350	r400sc_line_stipple_20	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
351	r400sc_line_stipple_21	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
352	r400sc_line_stipple_22	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
353	r400sc_line_stipple_23	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
354	r400sc_line_stipple_fc_08	2002Oct7
PASS		
355	r400sc_line_strip_stipple_01	2002Oct7
PASS		
356	r400sc_msa_1_01	2002Oct7

PASS		
357	r400sc_msaa_1_primtypes_01	2002Oct7
PASS		
358	r400sc_msaa_1_rectangle_list_01	2002Oct7
PASS		
359	r400sc_msaa_1_rectangle_list_02	2002Oct7
PASS		
360	r400sc_msaa_1_rectangle_list_03	2002Oct7
PASS		
361	r400sc_msaa_1_rectangle_list_04	2002Oct7
PASS		
362	r400sc_msaa_1_rectangle_list_05	2002Oct7
PASS		
363	r400sc_msaa_1_rectangle_list_06	2002Oct7
PASS		
364	r400sc_msaa_1_rectangle_list_07	2002Oct7
PASS		
365	r400sc_msaa_1_rectangle_list_08	2002Oct7
PASS		
366	r400sc_msaa_1_zbuffer_rectangle_list_01	2002Oct7
PASS		
367	r400sc_msaa_1_zbuffer_rectangle_list_02	2002Oct7
PASS		
368	r400sc_msaa_2_primtypes_01	2002Oct7
PASS		
369	r400sc_msaa_2_rectangle_list_01	2002Oct7
PASS		
370	r400sc_msaa_2_rectangle_list_02	2002Oct7
PASS		
371	r400sc_msaa_2_rectangle_list_03	2002Oct7
PASS		
372	r400sc_msaa_2_rectangle_list_04	2002Oct7
PASS		
373	r400sc_msaa_2_rectangle_list_05	2002Oct7
PASS		
374	r400sc_msaa_2_rectangle_list_06	2002Oct7
PASS		
375	r400sc_msaa_2_rectangle_list_07	2002Oct7
PASS		
376	r400sc_msaa_2_rectangle_list_08	2002Oct7
PASS		
377	r400sc_msaa_2_zbuffer_rectangle_list_01	2002Oct7
PASS		
378	r400sc_msaa_2_zbuffer_rectangle_list_02	2002Oct7
PASS		
379	r400sc_msaa_3_primtypes_01	2002Oct7
PASS		
380	r400sc_msaa_3_rectangle_list_01	2002Oct7

PASS
381 r400sc_msa_3_rectangle_list_02 2002Oct7
PASS
382 r400sc_msa_3_rectangle_list_03 2002Oct7
PASS
383 r400sc_msa_3_rectangle_list_04 2002Oct7
PASS
384 r400sc_msa_3_rectangle_list_05 2002Oct7
PASS
385 r400sc_msa_3_rectangle_list_06 2002Oct7
PASS
386 r400sc_msa_3_rectangle_list_07 2002Oct7
PASS
387 r400sc_msa_3_rectangle_list_08 2002Oct7
PASS
388 r400sc_msa_3_zbuffer_rectangle_list_01 2002Oct7
PASS
389 r400sc_msa_3_zbuffer_rectangle_list_02 2002Oct7
PASS
390 r400sc_msa_4_01 2002Oct7
PASS
391 r400sc_msa_4_primitives_01 2002Oct7
PASS
392 r400sc_msa_4_rectangle_list_01 2002Oct7
PASS
393 r400sc_msa_4_rectangle_list_02 2002Oct7
PASS
394 r400sc_msa_4_rectangle_list_03 2002Oct7
PASS
395 r400sc_msa_4_rectangle_list_04 2002Oct7
PASS
396 r400sc_msa_4_rectangle_list_05 2002Oct7
PASS
397 r400sc_msa_4_rectangle_list_06 2002Oct7
PASS
398 r400sc_msa_4_rectangle_list_07 2002Oct7
PASS
399 r400sc_msa_4_rectangle_list_08 2002Oct7
PASS
400 r400sc_msa_4_zbuffer_rectangle_list_01 2002Oct7
PASS
401 r400sc_msa_4_zbuffer_rectangle_list_02 2002Oct7
PASS
402 r400sc_msa_6_01 2002Oct7
PASS
403 r400sc_msa_6_primitives_01 2002Oct7
PASS
404 r400sc_msa_6_rectangle_list_01 2002Oct7

PASS		
405	r400sc_msa_6_rectangle_list_02	2002Oct7
PASS		
406	r400sc_msa_6_rectangle_list_03	2002Oct7
PASS		
407	r400sc_msa_6_rectangle_list_04	2002Oct7
PASS		
408	r400sc_msa_6_rectangle_list_05	2002Oct7
PASS		
409	r400sc_msa_6_rectangle_list_06	2002Oct7
PASS		
410	r400sc_msa_6_rectangle_list_07	2002Oct7
PASS		
411	r400sc_msa_6_rectangle_list_08	2002Oct7
PASS		
412	r400sc_msa_6_zbuffer_rectangle_list_01	2002Oct7
PASS		
413	r400sc_msa_6_zbuffer_rectangle_list_02	2002Oct7
PASS		
414	r400sc_msa_8_01	2002Oct7
PASS		
415	r400sc_msa_8_02	2002Oct7
PASS		
416	r400sc_msa_8_03	2002Oct7
PASS		
417	r400sc_msa_8_04	2002Oct7
PASS		
418	r400sc_msa_8_05	2002Oct7
PASS		
419	r400sc_msa_8_aa_mask_01	2002Oct7
PASS		
420	r400sc_msa_8_aa_mask_02	2002Oct7
PASS		
421	r400sc_msa_8_aa_mask_fc_02	2002Oct7
PASS		
422	r400sc_msa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
423	r400sc_msa_8_primitives_01	2002Oct7
PASS		
424	r400sc_msa_8_rectangle_list_01	2002Oct7
PASS		
425	r400sc_msa_8_rectangle_list_02	2002Oct7
PASS		
426	r400sc_msa_8_rectangle_list_03	2002Oct7
PASS		
427	r400sc_msa_8_rectangle_list_04	2002Oct7
PASS		
428	r400sc_msa_8_rectangle_list_05	2002Oct7

PASS		
429	r400sc_msa_8_rectangle_list_06	2002Oct7
PASS		
430	r400sc_msa_8_rectangle_list_07	2002Oct7
PASS		
431	r400sc_msa_8_rectangle_list_08	2002Oct7
PASS		
432	r400sc_msa_8_zbuffer_rectangle_list_01	2002Oct7
PASS		
433	r400sc_msa_8_zbuffer_rectangle_list_02	2002Oct7
PASS		
434	r400sc_msa_X_primitives_01_bug	2002Oct7
PASS		
435	r400sc_msa_X_primitives_02_bug	2002Oct7
PASS		
436	r400sc_multi_chip_prim_discard_00_07	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
437	r400sc_multi_chip_tile_discard_00_07	2002Oct7
PASS		
438	r400sc_null_triangles_01	2002Oct7
PASS		
439	r400sc_null_triangles_fc_01	2002Oct7
PASS		
440	r400sc_perf_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
441	r400sc_pinwheel_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
442	r400sc_pinwheel_02	2002Oct7
PASS		
443	r400sc_pinwheel_02_bug	2002Oct7
PASS		
444	r400sc_point_jss_3x4_01	2002Oct7
PASS		
445	r400sc_point_list_01	2002Oct7
PASS		
446	r400sc_point_list_02	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
447	r400sc_point_list_03	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
448	r400sc_point_list_04	2002Oct7
PASS		
449	r400sc_point_list_05	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
450	r400sc_point_list_06	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
451	r400sc_point_list_06_bug	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
452	r400sc_point_list_06_bug_02	2002Oct7

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**FAIL**          VgtPaClipP_VgtSide.cmp
453  r400sc_point_list_07                2002Oct7
PASS
454  r400sc_point_list_08                2002Oct7
PASS
455  r400sc_point_list_09                2002Oct7
PASS
456  r400sc_point_msa_8_01              2002Oct7
PASS
457  r400sc_poly_offset_01              2002Oct7
PASS
458  r400sc_poly_offset_02              2002Oct7
PASS
459  r400sc_poly_offset_03              2002Oct7
PASS
460  r400sc_poly_offset_04              2002Oct7
PASS
461  r400sc_poly_offset_05              2002Oct7
PASS
462  r400sc_poly_offset_06              2002Oct7
PASS
463  r400sc_poly_offset_07              2002Oct7
PASS
464  r400sc_poly_offset_08              2002Oct7
PASS
465  r400sc_poly_offset_09              2002Oct7
PASS
466  r400sc_poly_offset_10              2002Oct7
PASS
467  r400sc_poly_offset_10_bug           2002Oct7
PASS
468  r400sc_poly_offset_fc_01           2002Oct7
PASS
469  r400sc_polygon_stipple_01          2002Oct7
PASS
470  r400sc_polymode_tri_fill_01        2002Oct7
PASS
471  r400sc_polymode_tri_fill_01_bug     2002Oct7
PASS
472  r400sc_prsp_byc_intrp_ref_pix_01    2002Oct7
PASS
473  r400sc_prsp_byc_intrp_ref_pix_02    2002Oct7
PASS
474  r400sc_prsp_byc_intrp_ref_pix_03    2002Oct7
PASS
475  r400sc_prsp_byc_intrp_ref_pix_04    2002Oct7
PASS
476  r400sc_prsp_byc_intrp_ref_pix_05    2002Oct7

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PASS		
477	r400sc_prsp_byc_intrp_ref_pix_06	2002Oct7
PASS		
478	r400sc_prsp_byc_intrp_ref_pix_07	2002Oct7
PASS		
479	r400sc_prsp_byc_intrp_ref_pix_08	2002Oct7
PASS		
480	r400sc_raster_fill_rule_01	2002Oct7
PASS		
481	r400sc_raster_fill_rule_02	2002Oct7
PASS		
482	r400sc_raster_fill_rule_03	2002Oct7
PASS		
483	r400sc_raster_fill_rule_04	2002Oct7
PASS		
484	r400sc_raster_fill_rule_05	2002Oct7
PASS		
485	r400sc_raster_fill_rule_fc_01	2002Oct7
PASS		
486	r400sc_rectangle_list_01	2002Oct7
PASS		
487	r400sc_rectangle_list_02	2002Oct7
PASS		
488	r400sc_rectangle_list_03	2002Oct7
PASS		
489	r400sc_rectangle_list_04	2002Oct7
PASS		
490	r400sc_rectangle_list_05	2002Oct7
PASS		
491	r400sc_rectangle_list_06	2002Oct7
PASS		
492	r400sc_rectangle_list_07	2002Oct7
PASS		
493	r400sc_rectangle_list_08	2002Oct7
PASS		
494	r400sc_scissor_rect_01	2002Oct7
PASS		
495	r400sc_scissor_rect_02	2002Oct7
PASS		
496	r400sc_scissor_rect_03	2002Oct7
PASS		
497	r400sc_scissor_rect_04	2002Oct7
PASS		
498	r400sc_scissor_rect_05	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
499	r400sc_scissor_rect_fc_01	2002Oct7
PASS		
500	r400sc_set_state_01	2002Oct7

PASS		
501	r400sc_sp_sample_cntl_01	20020ct7
PASS		
502	r400sc_sp_sample_cntl_02	20020ct7
PASS		
503	r400sc_sp_sample_cntl_03	20020ct7
PASS		
504	r400sc_sp_sample_cntl_04	20020ct7
PASS		
505	r400sc_sp_sample_cntl_05	20020ct7
PASS		
506	r400sc_sp_sample_cntl_06	20020ct7
PASS		
507	r400sc_sp_sample_cntl_07	20020ct7
PASS		
508	r400sc_sp_sample_cntl_08	20020ct7
PASS		
509	r400sc_tri_16_par_64_dwords_01	20020ct7
PASS		
510	r400sc_tri_8textures_01	20020ct7
PASS		
511	r400sc_tri_8textures_02	20020ct7
PASS		
512	r400sc_tri_walk_start_vertex_01	20020ct7
PASS		
513	r400sc_tri_walk_start_vertex_02	20020ct7
PASS		
514	r400sc_tri_walk_start_vertex_03	20020ct7
PASS		
515	r400sc_tri_walk_start_vertex_04	20020ct7
PASS		
516	r400sc_tri_walk_start_vertex_05	20020ct7
PASS		
517	r400sc_tri_walk_start_vertex_06	20020ct7
PASS		
518	r400sc_tri_walk_start_vertex_07	20020ct7
PASS		
519	r400sc_tri_walk_start_vertex_08	20020ct7
PASS		
520	r400sc_tri_walk_start_vertex_09	20020ct7
PASS		
521	r400sc_tri_walk_start_vertex_10	20020ct7
PASS		
522	r400sc_tri_walk_start_vertex_11	20020ct7
PASS		
523	r400sc_tri_walk_start_vertex_12	20020ct7
PASS		
524	r400sc_tri_walk_start_vertex_13	20020ct7

PASS		
525	r400sc_tri_walk_start_vertex_14	2002Oct7
PASS		
526	r400sc_tri_walk_start_vertex_15	2002Oct7
PASS		
527	r400sc_tri_walk_start_vertex_16	2002Oct7
PASS		
528	r400sc_triangle_stipple_01	2002Oct7
PASS		
529	r400sc_window_offset_01	2002Oct7
PASS		
530	r400sc_window_offset_02	2002Oct7
PASS		
531	r400sc_window_offset_03	2002Oct7
PASS		
532	r400sc_window_offset_04	2002Oct7
PASS		
533	r400sc_window_offset_05	2002Oct7
PASS		
534	r400sc_window_offset_fc_01	2002Oct7
PASS		
535	r400sc_zbuffer_line_list_01	2002Oct7
PASS		
536	r400sc_zbuffer_point_list_01	2002Oct7
PASS		
537	r400sc_zbuffer_rectangle_list_01	2002Oct7
PASS		
538	r400sc_zbuffer_rectangle_list_02	2002Oct7
PASS		
539	r400sc_zbuffer_rectangle_list_fc_02	2002Oct7
PASS		
540	r400sc_zbuffer_triangle_list_01	2002Oct7
PASS		
541	r400su_4tri_text_offscreen_01	2002Oct7
PASS		
542	r400su_baryc_test_01	2002Oct7
PASS		
543	r400su_baryc_test_02	2002Oct7
PASS		
544	r400su_baryc_test_03	2002Oct7
PASS		
545	r400su_baryc_test_04	2002Oct7
PASS		
546	r400su_clip_baryc_test_01	2002Oct7
PASS		
547	r400su_clip_baryc_test_02	2002Oct7
PASS		
548	r400su_clip_edgeflag_polymode_01	2002Oct7

PASS		
549	r400su_clip_pointsize_test_01	2002Oct7
PASS		
550	r400su_clip_pointtest_01	2002Oct7
PASS		
551	r400su_clip_pointtest_02	2002Oct7
PASS		
552	r400su_clip_pointtest_03	2002Oct7
PASS		
553	r400su_clip_pointtest_04	2002Oct7
PASS		
554	r400su_clip_polymode_test_01	2002Oct7
PASS		
555	r400su_clipline_01	2002Oct7
PASS		
556	r400su_clippoint_01	2002Oct7
PASS		
557	r400su_clipvertexsortingfunctional_01	2002Oct7
PASS		
558	r400su_cullingfunctional_01	2002Oct7
PASS		
559	r400su_degentri_test_01	2002Oct7
PASS		
560	r400su_degentri_test_02	2002Oct7
PASS		
561	r400su_degentri_test_03	2002Oct7
PASS		
562	r400su_edge_flag_01	2002Oct7
PASS		
563	r400su_edge_flag_02	2002Oct7
PASS		
564	r400su_edgeflags_triangle_01	2002Oct7
PASS		
565	r400su_edgeflags_triangle_02	2002Oct7
PASS		
566	r400su_geom_sort_01	2002Oct7
PASS		
567	r400su_line_end_cap_functional_01	2002Oct7
PASS		
568	r400su_line_end_cap_width_functional_02	2002Oct7
PASS		
569	r400su_line_orientation_01	2002Oct7
PASS		
570	r400su_line_orientation_02	2002Oct7
PASS		
571	r400su_line_orientation_dx01_01	2002Oct7
PASS		
572	r400su_line_orientation_dx01_02	2002Oct7

PASS		
573	r400su_line_orientation_dy01_01	2002Oct7
PASS		
574	r400su_line_orientation_dy01_02	2002Oct7
PASS		
575	r400su_line_test_01	2002Oct7
PASS		
576	r400su_line_test_02	2002Oct7
PASS		
577	r400su_longstrip_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
578	r400su_multi_context_01	2002Oct7
PASS		
579	r400su_multi_prim_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
580	r400su_multi_prim_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
581	r400su_parallel_orientation_all_01	2002Oct7
PASS		
582	r400su_parallel_orientation_all_02	2002Oct7
PASS		
583	r400su_pc_management_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
584	r400su_pc_management_02	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
585	r400su_pc_management_03	2002Oct7
PASS		
586	r400su_point_sprite_01	2002Oct7
PASS		
587	r400su_point_sprite_02	2002Oct7
PASS		
588	r400su_point_sprite_03	2002Oct7
PASS		
589	r400su_point_sprite_04	2002Oct7
PASS		
590	r400su_point_sprite_05	2002Oct7
PASS		
591	r400su_point_sprite_06	2002Oct7
PASS		
592	r400su_point_sprite_07	2002Oct7
PASS		
593	r400su_point_sprite_08	2002Oct7
PASS		
594	r400su_point_wl6_h1_functional_01	2002Oct7
PASS		
595	r400su_point_wl_h16_functional_01	2002Oct7
PASS		
596	r400su_pointsizepresent_01	2002Oct7

PASS		
597	r400su_pointsizepresent_02	2002Oct7
PASS		
598	r400su_polymode_lines_degen_triangle_01	2002Oct7
PASS		
599	r400su_polymode_lines_degen_triangle_02	2002Oct7
PASS		
600	r400su_polymode_lines_zero_area_triangle_01	2002Oct7
PASS		
601	r400su_polymode_lines_zero_area_triangle_02	2002Oct7
PASS		
602	r400su_polymode_points_degen_triangle_01	2002Oct7
PASS		
603	r400su_polymode_points_degen_triangle_02	2002Oct7
PASS		
604	r400su_polymode_points_zero_area_triangle_01	2002Oct7
PASS		
605	r400su_polymode_points_zero_area_triangle_02	2002Oct7
PASS		
606	r400su_polymode_rectangle_01	2002Oct7
PASS		
607	r400su_polymode_zero_area_triangle_01	2002Oct7
PASS		
608	r400su_polymode_zero_area_triangle_02	2002Oct7
PASS		
609	r400su_polymode_zero_area_triangle_03	2002Oct7
PASS		
610	r400su_polymode_zero_area_triangle_04	2002Oct7
PASS		
611	r400su_polymodeculling_01	2002Oct7
PASS		
612	r400su_polymodefunctional_01	2002Oct7
PASS		
613	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct7
PASS		
614	r400su_provoking_vtx_edgeflags_triangle_02	2002Oct7
PASS		
615	r400su_provoking_vtx_edgeflags_triangle_03	2002Oct7
PASS		
616	r400su_provoking_vtx_edgeflags_triangle_04	2002Oct7
PASS		
617	r400su_provoking_vtx_line_01	2002Oct7
PASS		
618	r400su_provoking_vtx_point_01	2002Oct7
PASS		
619	r400su_provoking_vtx_polymode_rectangle_01	2002Oct7
PASS		
620	r400su_provoking_vtx_rectangle_01	2002Oct7

PASS		
621	r400su_provoking_vtx_triangle_01	20020ct7
PASS		
622	r400su_rand_line_01	20020ct7
PASS		
623	r400su_rand_point_01	20020ct7
PASS		
624	r400su_rand_tri_01	20020ct7
PASS		
625	r400su_rectangle_list_01	20020ct7
PASS		
626	r400su_sliver_01	20020ct7
PASS		
627	r400su_triarea_test_01	20020ct7
PASS		
628	r400su_triarea_test_02	20020ct7
PASS		
629	r400su_triarea_test_03	20020ct7
PASS		
630	r400su_triarea_test_04	20020ct7
PASS		
631	r400su_vertexsortingfunctional_01	20020ct7
PASS		
632	r400su_w_grad_test_01	20020ct7
PASS		
633	r400su_w_grad_test_02	20020ct7
PASS		
634	r400su_w_grad_test_03	20020ct7
PASS		
635	r400su_z_grad_test_01	20020ct7
PASS		
636	r400su_z_grad_test_02	20020ct7
PASS		
637	r400su_z_grad_test_03	20020ct7
PASS		
638	r400vgt_auto_index_line_list_01	20020ct7
PASS		
639	r400vgt_auto_index_line_loop_01	20020ct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
640	r400vgt_auto_index_line_strip_01	20020ct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
641	r400vgt_auto_index_points_01	20020ct7
PASS		
642	r400vgt_auto_index_polygon_01	20020ct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
643	r400vgt_auto_index_primitives_01	20020ct7
PASS		
644	r400vgt_auto_index_quad_list_01	20020ct7

PASS		
645	r400vgt_auto_index_quad_strip_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
646	r400vgt_auto_index_rectangle_list_01	2002Oct7
PASS		
647	r400vgt_auto_index_tri_fan_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
648	r400vgt_auto_index_tri_list_01	2002Oct7
PASS		
649	r400vgt_auto_index_tri_strip_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
650	r400vgt_auto_index_tri_wflags_01	2002Oct7
PASS		
651	r400vgt_dma_engine_01	2002Oct7
PASS		
652	r400vgt_dma_engine_02	2002Oct7
PASS		
653	r400vgt_dma_engine_03	2002Oct7
PASS		
654	r400vgt_dma_engine_04	2002Oct7
PASS		
655	r400vgt_dma_engine_05	2002Oct7
PASS		
656	r400vgt_dma_engine_06	2002Oct7
PASS		
657	r400vgt_dma_engine_07	2002Oct7
PASS		
658	r400vgt_dma_engine_08	2002Oct7
PASS		
659	r400vgt_dma_engine_09	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
660	r400vgt_dma_engine_10	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
661	r400vgt_dma_index_line_list_01	2002Oct7
PASS		
662	r400vgt_dma_index_line_loop_01	2002Oct7
PASS		
663	r400vgt_dma_index_line_strip_01	2002Oct7
PASS		
664	r400vgt_dma_index_points_01	2002Oct7
PASS		
665	r400vgt_dma_index_polygon_01	2002Oct7
PASS		
666	r400vgt_dma_index_primitives_01	2002Oct7
PASS		
667	r400vgt_dma_index_quad_list_01	2002Oct7
PASS		
668	r400vgt_dma_index_quad_strip_01	2002Oct7

PASS		
669	r400vgt_dma_index_rectangle_list_01	2002Oct7
PASS		
670	r400vgt_dma_index_tri_fan_01	2002Oct7
PASS		
671	r400vgt_dma_index_tri_list_01	2002Oct7
PASS		
672	r400vgt_dma_index_tri_strip_01	2002Oct7
PASS		
673	r400vgt_dma_index_tri_wflags_01	2002Oct7
PASS		
674	r400vgt_dma_swap_indx16_01	2002Oct7
PASS		
675	r400vgt_dma_swap_indx32_01	2002Oct7
PASS		
676	r400vgt_edgeflags_polygon_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
677	r400vgt_edgeflags_quad_list_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
678	r400vgt_edgeflags_quad_strip_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
679	r400vgt_edgeflags_triangle_fan_01	2002Oct7
PASS		
680	r400vgt_edgeflags_triangle_list_01	2002Oct7
PASS		
681	r400vgt_edgeflags_triangle_strip_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
682	r400vgt_edgeflags_triangle_wflags_01	2002Oct7
PASS		
683	r400vgt_event_handling_01	2002Sep26
PASS		
684	r400vgt_ext2int_index_line_list_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
685	r400vgt_ext2int_index_line_loop_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
686	r400vgt_ext2int_index_line_strip_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
687	r400vgt_ext2int_index_points_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
688	r400vgt_ext2int_index_polygon_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
689	r400vgt_ext2int_index_quad_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
690	r400vgt_ext2int_index_quad_strip_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
691	r400vgt_ext2int_index_rectangle_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
692	r400vgt_ext2int_index_triangle_fan_01	2002Oct7

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**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
693  r400vgt_ext2int_index_triangle_list_01          2002Oct7
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
694  r400vgt_ext2int_index_triangle_strip_01         2002Oct7
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
695  r400vgt_ext2int_index_triangle_wflags_01        2002Oct7
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
696  r400vgt_hos_LPatch_01                            2002Oct7
PASS
697  r400vgt_hos_PNL_cp_ln_cont_no_projection_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
698  r400vgt_hos_PNL_lp_ln_cont_no_projection_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
699  r400vgt_hos_PNQ_cp_qn_cont_light_texture_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
700  r400vgt_hos_PNQ_cp_qn_cont_light_texture_02    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
701  r400vgt_hos_PNQ_cp_qn_cont_no_projection_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
702  r400vgt_hos_PNQ_lp_cont_no_projection_01        2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
703  r400vgt_hos_PNTQL_cp_qn_cont_stress_01         2002Oct7
PASS
704  r400vgt_hos_PNT_cont_cp_qn_complex_01          2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
705  r400vgt_hos_PNT_cont_cp_qn_precision_01         2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
706  r400vgt_hos_PNT_cont_cp_qn_precision_02         2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
707  r400vgt_hos_PNT_cp_qn_cont_light_texture_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
708  r400vgt_hos_PNT_cp_qn_cont_light_texture_02    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
709  r400vgt_hos_PNT_cp_qn_cont_light_texture_03    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
710  r400vgt_hos_PNT_cp_qn_cont_moving_normals_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
711  r400vgt_hos_PNT_cp_qn_cont_no_projection_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
712  r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01 2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
713  r400vgt_hos_PNT_disc_cp_qn_complex_01           2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
714  r400vgt_hos_PNT_disc_cp_qn_light_texture_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
715  r400vgt_hos_PNT_disc_cp_qn_no_projection_01    2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
716  r400vgt_hos_PNT_disc_cp_qn_precision_01         2002Oct7

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**FAIL**      VgtPaClipP_VgtSide.cmp
717  r400vgt_hos_PNT_disc_cp_qn_precision_02      2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
718  r400vgt_hos_PNT_edge_detection_01            2002Oct7
PASS
719  r400vgt_hos_PNT_lp_cont_no_projection_01     2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
720  r400vgt_hos_RPatch_cp_02                     2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
721  r400vgt_hos_RPatch_lp_02                     2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
722  r400vgt_hos_TPatch_01                        2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
723  r400vgt_hos_TPatch_02                        2002Oct7
PASS
724  r400vgt_hos_auto_index_line_list_01          2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
725  r400vgt_hos_auto_index_quad_list_01          2002Oct7
PASS
726  r400vgt_hos_auto_index_triangle_list_01     2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
727  r400vgt_hos_cubic_pos_pnt_discrete_01        2002Oct7
PASS
728  r400vgt_hos_simple_linear_PNT_discrete_01   2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
729  r400vgt_immed_index_line_list_01             2002Oct7
PASS
730  r400vgt_immed_index_line_loop_01            2002Oct7
PASS
731  r400vgt_immed_index_line_strip_01           2002Oct7
PASS
732  r400vgt_immed_index_points_01               2002Oct7
PASS
733  r400vgt_immed_index_polygon_01              2002Oct7
PASS
734  r400vgt_immed_index_primitives_01           2002Oct7
PASS
735  r400vgt_immed_index_quad_list_01            2002Oct7
PASS
736  r400vgt_immed_index_quad_strip_01           2002Oct7
PASS
737  r400vgt_immed_index_rectangle_list_01       2002Oct7
PASS
738  r400vgt_immed_index_tri_fan_01              2002Oct7
PASS
739  r400vgt_immed_index_tri_list_01             2002Oct7
PASS
740  r400vgt_immed_index_tri_strip_01            2002Oct7

```

PASS		
741	r400vgt_immed_index_tri_wflags_01	2002Oct7
PASS		
742	r400vgt_index_dealloc_line_list_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
743	r400vgt_index_dealloc_points_01	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
744	r400vgt_index_dealloc_triangle_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
745	r400vgt_index_min_max_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
746	r400vgt_index_min_max_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
747	r400vgt_index_offset_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
748	r400vgt_index_offset_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
749	r400vgt_index_offset_03	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
750	r400vgt_index_offset_04	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
751	r400vgt_index_offset_05	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
752	r400vgt_index_offset_06	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
753	r400vgt_index_offset_07	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
754	r400vgt_index_offset_08	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
755	r400vgt_index_size_01	2002Oct7
PASS		
756	r400vgt_index_source_switch_01	2002Oct7
PASS		
757	r400vgt_line_list_01	2002Oct7
PASS		
758	r400vgt_line_list_02	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
759	r400vgt_line_loop_01	2002Oct7
PASS		
760	r400vgt_line_loop_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
761	r400vgt_line_strip_01	2002Oct7
PASS		
762	r400vgt_line_strip_02	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
763	r400vgt_multi_context_01	2002Oct7
PASS		
764	r400vgt_multi_context_02	2002Oct7

PASS		
765	r400vgt_multi_context_03	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
766	r400vgt_multi_context_04	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
767	r400vgt_multi_context_05	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
768	r400vgt_multi_context_06	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
769	r400vgt_multi_context_07	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
770	r400vgt_multi_context_08	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
771	r400vgt_multi_context_09	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
772	r400vgt_multi_context_10	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
773	r400vgt_multi_context_11	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
774	r400vgt_multi_context_12	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
775	r400vgt_multi_prim_reset_index_all_01	2002Oct7
PASS		
776	r400vgt_multi_prim_reset_index_all_02	2002Oct7
PASS		
777	r400vgt_pass_thru_all_prims_01	2002Oct7
PASS		
778	r400vgt_point_list_01	2002Oct7
PASS		
779	r400vgt_point_list_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
780	r400vgt_polygon_01	2002Oct7
PASS		
781	r400vgt_polygon_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
782	r400vgt_provoking_vtx_all_01	2002Oct7
PASS		
783	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct7
PASS		
784	r400vgt_provoking_vtx_polygon_01	2002Oct7
PASS		
785	r400vgt_provoking_vtx_quad_list_01	2002Oct7
PASS		
786	r400vgt_provoking_vtx_quad_strip_01	2002Oct7
PASS		
787	r400vgt_provoking_vtx_tri_fan_01	2002Oct7
PASS		
788	r400vgt_provoking_vtx_tri_strip_01	2002Oct7

PASS		
789	r400vgt_quad_list_01	2002Oct7
PASS		
790	r400vgt_quad_list_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
791	r400vgt_quad_strip_01	2002Oct7
PASS		
792	r400vgt_quad_strip_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
793	r400vgt_rectangle_list_01	2002Oct7
PASS		
794	r400vgt_rectangle_list_02	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
795	r400vgt_reuse_depth_line_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
796	r400vgt_reuse_depth_point_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
797	r400vgt_reuse_depth_triangle_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
798	r400vgt_reuse_index_line_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
799	r400vgt_reuse_index_point_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
800	r400vgt_reuse_index_triangle_list_01	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
801	r400vgt_reuse_index_triangle_list_02	2002Oct7
PASS		
802	r400vgt_reuse_index_triangle_list_03	2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
803	r400vgt_suppress_eop_01	0:0:1 2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
804	r400vgt_suppress_eop_02	0:0:1 2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
805	r400vgt_suppress_eop_03	0:0:1 2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
806	r400vgt_suppress_eop_04	0:0:1 2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp	
807	r400vgt_suppress_eop_05	0:0:1 2002Sep26
PASS		
808	r400vgt_triangle_fan_01	0:0:1 2002Oct7
PASS		
809	r400vgt_triangle_fan_02	0:0:1 2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
810	r400vgt_triangle_list_01	0:0:1 2002Oct7
PASS		
811	r400vgt_triangle_list_02	0:0:1 2002Oct7
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
812	r400vgt_triangle_strip_01	0:0:1 2002Oct7

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PASS
813 r400vgt_triangle_strip_02          0:0:1  2002Oct7
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
814 r400vgt_triangle_wflags_01        0:0:1  2002Oct7
PASS
815 r400vgt_triangle_wflags_02        0:0:1  2002Oct7
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
816 r400vgt_viz_query_01              0:0:1  2002Oct7
PASS
817 r400vgt_vtx_vector_packing_01     0:0:1  2002Oct7
PASS
818 r400vte_coverage_01               0:0:1  2002Oct7
**FAIL**      VgtPaClipP_VgtSide.cmp
819 r400vte_simple_point_01           0:0:1  2002Oct7
PASS
820 r400vte_simple_triangle_01        0:0:1  2002Oct7
PASS
821 r400vte_w0_fmt_01                 0:0:1  2002Oct7
PASS
822 r400vte_w0_fmt_03                 0:0:1  2002Oct7
PASS
823 r400vte_xy_fmt_01                 0:0:1  2002Oct7
PASS
824 r400vte_xy_fmt_mk_01_bug          0:0:1  2002Oct7
PASS
825 r400vte_xyz_scale_01              0:0:1  2002Oct7
PASS
826 r400vte_xyz_scale_02              0:0:1  2002Oct7
PASS
827 r400vte_z_fmt_01                 0:0:1  2002Oct7
PASS

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=====
Tests_Run_count = 827  Pass_count = 686  Fail_count = 140  Incomplete_count = 1
=====

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Aug 23 15:18:28 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          130          92
              SC           153          153          147          96
              CL           81           81           80          98
              VTE          9           9            9          100
SPECIALTY    SU           79          79           77          97
              SANITY       2           2            1          50
+++++
              TOTAL        471          471          450          95
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug25
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug25
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug25
PASS			
4	primlib_parameterized_simple_triangle		2002Aug25
PASS			
5	primlib_template_simple_triangle		2002Aug25
PASS			
6	primlib_tex_tri		2002Aug25
PASS			
7	r400cl_clip_10_verts_01		2002Aug25
PASS			
8	r400cl_clip_disable_01		2002Aug25
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug25
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug25
PASS			
11	r400cl_cull_only_ena_01		2002Aug25
PASS			
12	r400cl_edgeflags_01		2002Aug25
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug25
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug25
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug25
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug25
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug25
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug25
PASS			
19	r400cl_edgeflags_gband_01		2002Aug25
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Aug25

PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug25
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug25
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug25
PASS		
24	r400cl_frustum_01	2002Aug25
PASS		
25	r400cl_frustum_02	2002Aug25
PASS		
26	r400cl_frustum_03	2002Aug25
PASS		
27	r400cl_frustum_04	2002Aug25
PASS		
28	r400cl_frustum_05	2002Aug25
PASS		
29	r400cl_frustum_06	2002Aug25
PASS		
30	r400cl_frustum_07	2002Aug25
PASS		
31	r400cl_frustum_08	2002Aug25
PASS		
32	r400cl_frustum_09	2002Aug25
PASS		
33	r400cl_frustum_10	2002Aug25
PASS		
34	r400cl_frustum_11	2002Aug25
PASS		
35	r400cl_frustum_12	2002Aug25
PASS		
36	r400cl_frustum_13	2002Aug25
PASS		
37	r400cl_frustum_14	2002Aug25
PASS		
38	r400cl_frustum_15	2002Aug25
PASS		
39	r400cl_frustum_16	2002Aug25
PASS		
40	r400cl_frustum_17	2002Aug25
PASS		
41	r400cl_frustum_18	2002Aug25
PASS		
42	r400cl_frustum_19	2002Aug25
PASS		
43	r400cl_frustum_20	2002Aug25
PASS		
44	r400cl_frustum_21	2002Aug25

PASS		
45	r400cl_frustum_22	2002Aug25
PASS		
46	r400cl_frustum_23	2002Aug25
PASS		
47	r400cl_frustum_24	2002Aug25
PASS		
48	r400cl_frustum_25	2002Aug25
PASS		
49	r400cl_frustum_26	2002Aug25
PASS		
50	r400cl_frustum_27	2002Aug25
PASS		
51	r400cl_frustum_28	2002Aug25
PASS		
52	r400cl_frustum_lines_01	2002Aug25
PASS		
53	r400cl_frustum_lines_02	2002Aug25
PASS		
54	r400cl_frustum_lines_03	2002Aug25
PASS		
55	r400cl_frustum_lines_04	2002Aug25
PASS		
56	r400cl_frustum_lines_05	2002Aug25
PASS		
57	r400cl_frustum_lines_06	2002Aug25
PASS		
58	r400cl_frustum_lines_07	2002Aug25
PASS		
59	r400cl_frustum_lines_08	2002Aug25
PASS		
60	r400cl_frustum_lines_09	2002Aug25
PASS		
61	r400cl_frustum_lines_10	2002Aug25
PASS		
62	r400cl_frustum_lines_11	2002Aug25
PASS		
63	r400cl_frustum_lines_12	2002Aug25
PASS		
64	r400cl_frustum_lines_13	2002Aug25
PASS		
65	r400cl_frustum_lines_14	2002Aug25
PASS		
66	r400cl_frustum_lines_15	2002Aug25
PASS		
67	r400cl_frustum_lines_16	2002Aug25
PASS		
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400 r400vgt_hos_auto_index_quad_list_01 2002Aug25
PASS
401 r400vgt_hos_auto_index_triangle_list_01 2002Aug25
PASS
402 r400vgt_hos_cubic_pos_pnt_discrete_01 2002Aug25
PASS
403 r400vgt_hos_simple_linear_PNT_discrete_01 2002Aug25
PASS
404 r400vgt_immed_index_line_list_01 2002Aug25

PASS		
405	r400vgt_immed_index_line_loop_01	2002Aug25
PASS		
406	r400vgt_immed_index_line_strip_01	2002Aug25
PASS		
407	r400vgt_immed_index_points_01	2002Aug25
PASS		
408	r400vgt_immed_index_polygon_01	2002Aug25
PASS		
409	r400vgt_immed_index_primitives_01	2002Aug25
PASS		
410	r400vgt_immed_index_quad_list_01	2002Aug25
PASS		
411	r400vgt_immed_index_quad_strip_01	2002Aug25
PASS		
412	r400vgt_immed_index_rectangle_list_01	2002Aug25
PASS		
413	r400vgt_immed_index_tri_fan_01	2002Aug25
PASS		
414	r400vgt_immed_index_tri_list_01	2002Aug25
PASS		
415	r400vgt_immed_index_tri_strip_01	2002Aug25
PASS		
416	r400vgt_immed_index_tri_wflags_01	2002Aug25
PASS		
417	r400vgt_index_dealloc_line_list_01	2002Aug25
PASS		
418	r400vgt_index_dealloc_points_01	2002Aug25
PASS		
419	r400vgt_index_dealloc_triangle_list_01	2002Aug25
PASS		
420	r400vgt_index_min_max_01	2002Aug25
PASS		
421	r400vgt_index_min_max_02	2002Aug25
PASS		
422	r400vgt_index_offset_01	2002Aug25
PASS		
423	r400vgt_index_offset_02	2002Aug25
PASS		
424	r400vgt_index_offset_03	2002Aug25
PASS		
425	r400vgt_index_size_01	2002Aug25
PASS		
426	r400vgt_index_source_switch_01	2002Aug25
PASS		
427	r400vgt_line_list_01	2002Aug25
PASS		
428	r400vgt_line_loop_01	2002Aug25

PASS		
429	r400vgt_line_strip_01	2002Aug25
PASS		
430	r400vgt_multi_context_01	2002Aug25
PASS		
431	r400vgt_multi_context_02	2002Aug25
PASS		
432	r400vgt_multi_context_03	2002Aug25
PASS		
433	r400vgt_multi_context_04	2002Aug25
PASS		
434	r400vgt_multi_context_05	2002Aug25
PASS		
435	r400vgt_multi_context_06	2002Aug25
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436	r400vgt_multi_context_07	2002Aug25
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437	r400vgt_multi_context_08	2002Aug25
PASS		
438	r400vgt_multi_context_09	2002Aug25
PASS		
439	r400vgt_multi_context_10	2002Aug25
PASS		
440	r400vgt_multi_context_11	2002Aug25
PASS		
441	r400vgt_multi_context_12	2002Aug25
PASS		
442	r400vgt_pass_thru_all_prims_01	2002Aug25
PASS		
443	r400vgt_point_list_01	2002Aug25
PASS		
444	r400vgt_polygon_01	2002Aug25
PASS		
445	r400vgt_provoking_vtx_all_01	2002Aug25
PASS		
446	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug25
PASS		
447	r400vgt_quad_list_01	2002Aug25
PASS		
448	r400vgt_quad_strip_01	2002Aug25
PASS		
449	r400vgt_rectangle_list_01	2002Aug25
PASS		
450	r400vgt_reuse_depth_line_list_01	2002Aug25
PASS		
451	r400vgt_reuse_depth_point_list_01	2002Aug25
PASS		
452	r400vgt_reuse_depth_triangle_list_01	2002Aug25

PASS
453 r400vgt_reuse_index_line_list_01 2002Aug25
PASS
454 r400vgt_reuse_index_point_list_01 2002Aug25
PASS
455 r400vgt_reuse_index_triangle_list_01 2002Aug25
PASS
456 r400vgt_reuse_index_triangle_list_02 2002Aug25
PASS
457 r400vgt_reuse_index_triangle_list_03 2002Aug25
PASS
458 r400vgt_triangle_fan_01 2002Aug25
PASS
459 r400vgt_triangle_list_01 2002Aug25
PASS
460 r400vgt_triangle_strip_01 2002Aug25
PASS
461 r400vgt_triangle_wflags_01 2002Aug25
PASS
462 r400vgt_vtx_vector_packing_01 2002Aug25
PASS
463 r400vte_coverage_01 2002Aug25
PASS
464 r400vte_simple_point_01 2002Aug25
PASS
465 r400vte_simple_triangle_01 2002Aug25
PASS
466 r400vte_w0_fmt_01 2002Aug25
PASS
467 r400vte_w0_fmt_03 2002Aug25
PASS
468 r400vte_xy_fmt_01 2002Aug25
PASS
469 r400vte_xyz_scale_01 2002Aug25
PASS
470 r400vte_xyz_scale_02 2002Aug25
PASS
471 r400vte_z_fmt_01 2002Aug25
PASS

=====
Tests_Run_count = 471 Pass_count = 469 Fail_count = 0 Incomplete_count = 2
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++
++++                        Date: Mon Sep 30 22:37:05 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
++++                        CP          11          11          11          100
++++                        RBBM        1           1           1           100
++++                        VGT         175         175         173          98
++++                        SC          284         284         284          100
++++                        CL          115         115         115          100
++++                        VTE         10          10          10          100
++++      SPECIALTY    6           6           6           100
++++                        SU          91          91          91          100
++++      SANITY       2           2           2           100
+++++
++++                        TOTAL        695         695         693          99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug28
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug28
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug28
PASS			
4	primlib_parameterized_simple_triangle		2002Aug28
PASS			
5	primlib_template_simple_triangle		2002Aug28
PASS			
6	primlib_tex_tri		2002Aug28
PASS			
7	r400cl_clip_10_verts_01		2002Aug28
PASS			
8	r400cl_clip_disable_01		2002Aug28
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug28
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug28
PASS			
11	r400cl_cull_only_ena_01		2002Aug28
PASS			
12	r400cl_edgeflags_01		2002Aug28
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug28
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug28
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug28
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug28
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug28
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug28
PASS			
19	r400cl_edgeflags_gband_01		2002Aug28
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Aug28

PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug28
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug28
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug28
PASS		
24	r400cl_frustum_01	2002Aug28
PASS		
25	r400cl_frustum_02	2002Aug28
PASS		
26	r400cl_frustum_03	2002Aug28
PASS		
27	r400cl_frustum_04	2002Aug28
PASS		
28	r400cl_frustum_05	2002Aug28
PASS		
29	r400cl_frustum_06	2002Aug28
PASS		
30	r400cl_frustum_07	2002Aug28
PASS		
31	r400cl_frustum_08	2002Aug28
PASS		
32	r400cl_frustum_09	2002Aug28
PASS		
33	r400cl_frustum_10	2002Aug28
PASS		
34	r400cl_frustum_11	2002Aug28
PASS		
35	r400cl_frustum_12	2002Aug28
PASS		
36	r400cl_frustum_13	2002Aug28
PASS		
37	r400cl_frustum_14	2002Aug28
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38	r400cl_frustum_15	2002Aug28
PASS		
39	r400cl_frustum_16	2002Aug28
PASS		
40	r400cl_frustum_17	2002Aug28
PASS		
41	r400cl_frustum_18	2002Aug28
PASS		
42	r400cl_frustum_19	2002Aug28
PASS		
43	r400cl_frustum_20	2002Aug28
PASS		
44	r400cl_frustum_21	2002Aug28

PASS		
45	r400cl_frustum_22	2002Aug28
PASS		
46	r400cl_frustum_23	2002Aug28
PASS		
47	r400cl_frustum_24	2002Aug28
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48	r400cl_frustum_25	2002Aug28
PASS		
49	r400cl_frustum_26	2002Aug28
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50	r400cl_frustum_27	2002Aug28
PASS		
51	r400cl_frustum_28	2002Aug28
PASS		
52	r400cl_frustum_lines_01	2002Aug28
PASS		
53	r400cl_frustum_lines_02	2002Aug28
PASS		
54	r400cl_frustum_lines_03	2002Aug28
PASS		
55	r400cl_frustum_lines_04	2002Aug28
PASS		
56	r400cl_frustum_lines_05	2002Aug28
PASS		
57	r400cl_frustum_lines_06	2002Aug28
PASS		
58	r400cl_frustum_lines_07	2002Aug28
PASS		
59	r400cl_frustum_lines_08	2002Aug28
PASS		
60	r400cl_frustum_lines_09	2002Aug28
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61	r400cl_frustum_lines_10	2002Aug28
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62	r400cl_frustum_lines_11	2002Aug28
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63	r400cl_frustum_lines_12	2002Aug28
PASS		
64	r400cl_frustum_lines_13	2002Aug28
PASS		
65	r400cl_frustum_lines_14	2002Aug28
PASS		
66	r400cl_frustum_lines_15	2002Aug28
PASS		
67	r400cl_frustum_lines_16	2002Aug28
PASS		
68	r400cl_frustum_lines_17	2002Aug28

PASS		
69	r400cl_frustum_lines_18	2002Aug28
PASS		
70	r400cl_frustum_lines_19	2002Aug28
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71	r400cl_frustum_lines_20	2002Aug28
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72	r400cl_frustum_lines_21	2002Aug28
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73	r400cl_frustum_lines_22	2002Aug28
PASS		
74	r400cl_frustum_lines_23	2002Aug28
PASS		
75	r400cl_frustum_lines_24	2002Aug28
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76	r400cl_gband_04	2002Aug28
PASS		
77	r400cl_gband_tcl_01	2002Aug28
PASS		
78	r400cl_pointlist_vertex_state_ucp_01	2002Aug28
PASS		
79	r400cl_simple_triangle_01	2002Aug28
PASS		
80	r400cl_ucp_combos_01	2002Aug28
PASS		
81	r400cl_ucp_combos_02	2002Aug28
PASS		
82	r400cl_ucp_combos_03	2002Aug28
PASS		
83	r400cl_ucp_combos_04	2002Aug28
PASS		
84	r400cl_ucp_pointlist_01	2002Aug28
PASS		
85	r400cl_vertex_reuse_clip_01	2002Aug28
PASS		
86	r400cl_vtx_kill_01	2002Aug28
PASS		
87	r400cl_vtx_kill_02	2002Aug28
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88	r400cl_w_eq_0	2002Aug28
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89	r400sanity_persp_projection_01	2002Aug28
PASS		
90	r400sanity_vfd_texture_sample_01	2002Aug28
INCOMPLETE		
91	r400sc_bres_cntl_01	2002Aug28
PASS		
92	r400sc_bres_cntl_02	2002Aug28

PASS		
93	r400sc_bres_cntl_03	2002Aug28
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94	r400sc_bres_cntl_04	2002Aug28
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95	r400sc_clip_rect_01	2002Aug28
PASS		
96	r400sc_clip_rect_02	2002Aug28
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97	r400sc_clip_rect_03	2002Aug28
PASS		
98	r400sc_clip_rect_04	2002Aug28
PASS		
99	r400sc_clip_rect_fc_01	2002Aug28
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100	r400sc_diamond_exit_01	2002Aug28
PASS		
101	r400sc_diamond_exit_02	2002Aug28
PASS		
102	r400sc_diamond_exit_03	2002Aug28
PASS		
103	r400sc_diamond_exit_04	2002Aug28
PASS		
104	r400sc_diamond_exit_05	2002Aug28
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105	r400sc_jss_1x2_01	2002Aug28
PASS		
106	r400sc_jss_1x2_02	2002Aug28
PASS		
107	r400sc_jss_1x3_01	2002Aug28
PASS		
108	r400sc_jss_1x3_02	2002Aug28
PASS		
109	r400sc_jss_1x4_01	2002Aug28
PASS		
110	r400sc_jss_1x4_02	2002Aug28
PASS		
111	r400sc_jss_2x1_01	2002Aug28
PASS		
112	r400sc_jss_2x1_02	2002Aug28
PASS		
113	r400sc_jss_2x2_01	2002Aug28
PASS		
114	r400sc_jss_2x2_02	2002Aug28
PASS		
115	r400sc_jss_2x3_01	2002Aug28
PASS		
116	r400sc_jss_2x3_02	2002Aug28

PASS
117 r400sc_jss_2x4_01 2002Aug28
PASS
118 r400sc_jss_2x4_02 2002Aug28
PASS
119 r400sc_jss_3x1_01 2002Aug28
PASS
120 r400sc_jss_3x1_02 2002Aug28
PASS
121 r400sc_jss_3x2_01 2002Aug28
PASS
122 r400sc_jss_3x2_02 2002Aug28
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123 r400sc_jss_3x3_01 2002Aug28
PASS
124 r400sc_jss_3x3_02 2002Aug28
PASS
125 r400sc_jss_3x4_01 2002Aug28
PASS
126 r400sc_jss_3x4_02 2002Aug28
PASS
127 r400sc_jss_3x4_03 2002Aug28
PASS
128 r400sc_jss_4x1_01 2002Aug28
PASS
129 r400sc_jss_4x1_02 2002Aug28
PASS
130 r400sc_jss_4x2_01 2002Aug28
PASS
131 r400sc_jss_4x2_02 2002Aug28
PASS
132 r400sc_jss_4x3_01 2002Aug28
PASS
133 r400sc_jss_4x3_02 2002Aug28
PASS
134 r400sc_jss_4x4_01 2002Aug28
PASS
135 r400sc_jss_4x4_02 2002Aug28
PASS
136 r400sc_jss_4x4_aa_mask_01 2002Aug28
PASS
137 r400sc_jss_4x4_aa_mask_02 2002Aug28
PASS
138 r400sc_jss_4x4_fc_01 2002Aug28
PASS
139 r400sc_jss_4x4_max_dist_01 2002Aug28
PASS
140 r400sc_line_dx10_eq_0_01 2002Aug28

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141	r400sc_line_dx10_ge_0_01	2002Aug28
PASS		
142	r400sc_line_dx10_lt_0_01	2002Aug28
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143	r400sc_line_dy10_eq_0_01	2002Aug28
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144	r400sc_line_dy10_ge_0_01	2002Aug28
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145	r400sc_line_dy10_lt_0_01	2002Aug28
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146	r400sc_line_expand_width_msa_8_01	2002Aug28
PASS		
147	r400sc_line_expand_width_msa_8_02	2002Aug28
PASS		
148	r400sc_line_expand_width_msa_8_03	2002Aug28
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149	r400sc_line_jss_3x4_01	2002Aug28
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150	r400sc_line_list_01	2002Aug28
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151	r400sc_line_list_02	2002Aug28
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152	r400sc_line_list_concentric_circle_01	2002Aug28
PASS		
153	r400sc_line_list_concentric_circle_02	2002Aug28
PASS		
154	r400sc_line_list_concentric_circle_03	2002Aug28
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155	r400sc_line_list_textured_01	2002Aug28
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156	r400sc_line_list_verify_st_01	2002Aug28
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157	r400sc_line_msa_8_01	2002Aug28
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158	r400sc_line_stipple_01	2002Aug28
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159	r400sc_line_stipple_02	2002Aug28
PASS		
160	r400sc_line_stipple_03	2002Aug28
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161	r400sc_line_stipple_04	2002Aug28
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162	r400sc_line_stipple_05	2002Aug28
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163	r400sc_line_stipple_06	2002Aug28
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164	r400sc_line_stipple_07	2002Aug28

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165	r400sc_line_stipple_08	2002Aug28
PASS		
166	r400sc_line_stipple_09	2002Aug28
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167	r400sc_line_stipple_fc_08	2002Aug28
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168	r400sc_line_strip_stipple_01	2002Aug28
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169	r400sc_msaa_1_01	2002Aug28
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170	r400sc_msaa_4_01	2002Aug28
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171	r400sc_msaa_6_01	2002Aug28
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172	r400sc_msaa_8_01	2002Aug28
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173	r400sc_msaa_8_aa_mask_01	2002Aug28
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174	r400sc_msaa_8_aa_mask_02	2002Aug28
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175	r400sc_msaa_8_aa_mask_fc_02	2002Aug28
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176	r400sc_null_triangles_01	2002Aug28
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177	r400sc_null_triangles_fc_01	2002Aug28
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178	r400sc_point_jss_3x4_01	2002Aug28
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179	r400sc_point_list_01	2002Aug28
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180	r400sc_point_list_02	2002Aug28
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181	r400sc_point_list_03	2002Aug28
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182	r400sc_point_list_04	2002Aug28
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183	r400sc_point_list_05	2002Aug28
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184	r400sc_point_list_06	2002Aug28
INCOMPLETE		
185	r400sc_point_list_07	2002Aug28
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186	r400sc_point_list_08	2002Aug28
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187	r400sc_point_list_09	2002Aug28
PASS		
188	r400sc_point_msaa_8_01	2002Aug28

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189	r400sc_polygon_stipple_01	2002Aug28
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191	r400sc_poly_offset_02	2002Aug28
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192	r400sc_poly_offset_fc_01	2002Aug28
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193	r400sc_prsp_byc_intrp_ref_pix_01	2002Aug28
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194	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug28
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195	r400sc_prsp_byc_intrp_ref_pix_03	2002Aug28
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197	r400sc_prsp_byc_intrp_ref_pix_05	2002Aug28
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200	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug28
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201	r400sc_raster_fill_rule_01	2002Aug28
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202	r400sc_raster_fill_rule_02	2002Aug28
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203	r400sc_raster_fill_rule_03	2002Aug28
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204	r400sc_raster_fill_rule_04	2002Aug28
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205	r400sc_raster_fill_rule_05	2002Aug28
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206	r400sc_raster_fill_rule_fc_01	2002Aug28
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207	r400sc_rectangle_list_01	2002Aug28
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208	r400sc_rectangle_list_02	2002Aug28
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209	r400sc_rectangle_list_03	2002Aug28
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PASS		
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PASS		
213	r400sc_rectangle_list_07	2002Aug28
PASS		
214	r400sc_rectangle_list_08	2002Aug28
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215	r400sc_scissor_rect_01	2002Aug28
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216	r400sc_scissor_rect_02	2002Aug28
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217	r400sc_scissor_rect_03	2002Aug28
PASS		
218	r400sc_scissor_rect_04	2002Aug28
PASS		
219	r400sc_scissor_rect_05	2002Aug28
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220	r400sc_scissor_rect_fc_01	2002Aug28
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221	r400sc_set_state_01	2002Aug28
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222	r400sc_triangle_stipple_01	2002Aug28
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223	r400sc_tri_walk_start_vertex_01	2002Aug28
PASS		
224	r400sc_tri_walk_start_vertex_02	2002Aug28
PASS		
225	r400sc_tri_walk_start_vertex_03	2002Aug28
PASS		
226	r400sc_tri_walk_start_vertex_04	2002Aug28
PASS		
227	r400sc_tri_walk_start_vertex_05	2002Aug28
PASS		
228	r400sc_tri_walk_start_vertex_06	2002Aug28
PASS		
229	r400sc_tri_walk_start_vertex_07	2002Aug28
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230	r400sc_tri_walk_start_vertex_08	2002Aug28
PASS		
231	r400sc_tri_walk_start_vertex_09	2002Aug28
PASS		
232	r400sc_tri_walk_start_vertex_10	2002Aug28
PASS		
233	r400sc_tri_walk_start_vertex_11	2002Aug28
PASS		
234	r400sc_tri_walk_start_vertex_12	2002Aug28
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235	r400sc_tri_walk_start_vertex_13	2002Aug28
PASS		
236	r400sc_tri_walk_start_vertex_14	2002Aug28

PASS		
237	r400sc_tri_walk_start_vertex_15	2002Aug28
PASS		
238	r400sc_tri_walk_start_vertex_16	2002Aug28
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239	r400sc_window_offset_01	2002Aug28
PASS		
240	r400sc_window_offset_fc_01	2002Aug28
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241	r400sc_zbuffer_line_list_01	2002Aug28
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242	r400sc_zbuffer_point_list_01	2002Aug28
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243	r400sc_zbuffer_rectangle_list_01	2002Aug28
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244	r400sc_zbuffer_rectangle_list_02	2002Aug28
PASS		
245	r400sc_zbuffer_rectangle_list_fc_02	2002Aug28
PASS		
246	r400sc_zbuffer_triangle_list_01	2002Aug28
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247	r400su_4tri_text_offscreen_01	2002Aug28
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248	r400su_baryc_test_01	2002Aug28
PASS		
249	r400su_baryc_test_02	2002Aug28
PASS		
250	r400su_baryc_test_03	2002Aug28
PASS		
251	r400su_baryc_test_04	2002Aug28
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252	r400su_clip_edgeflag_polymode_01	2002Aug28
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253	r400su_clipline_01	2002Aug28
PASS		
254	r400su_clippoint_01	2002Aug28
PASS		
255	r400su_clip_pointsize_test_01	2002Aug28
PASS		
256	r400su_clip_pointttest_01	2002Aug28
PASS		
257	r400su_clip_pointttest_02	2002Aug28
PASS		
258	r400su_clip_pointttest_03	2002Aug28
PASS		
259	r400su_clip_pointttest_04	2002Aug28
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260	r400su_clip_polymode_test_01	2002Aug28

PASS		
261	r400su_clipvertexsortingfunctional_01	2002Aug28
PASS		
262	r400su_cullingfunctional_01	2002Aug28
PASS		
263	r400su_degentri_test_01	2002Aug28
PASS		
264	r400su_degentri_test_02	2002Aug28
PASS		
265	r400su_degentri_test_03	2002Aug28
PASS		
266	r400su_edge_flag_01	2002Aug28
PASS		
267	r400su_edge_flag_02	2002Aug28
PASS		
268	r400su_edgeflags_triangle_01	2002Aug28
PASS		
269	r400su_edgeflags_triangle_02	2002Aug28
PASS		
270	r400su_geom_sort_01	2002Aug28
PASS		
271	r400su_line_end_cap_functional_01	2002Aug28
PASS		
272	r400su_line_end_cap_width_functional_02	2002Aug28
PASS		
273	r400su_line_test_01	2002Aug28
PASS		
274	r400su_line_test_02	2002Aug28
PASS		
275	r400su_longstrip_01	2002Aug28
PASS		
276	r400su_multi_prim_01	2002Aug28
PASS		
277	r400su_parallel_orientation_all_01	2002Aug28
PASS		
278	r400su_parallel_orientation_all_02	2002Aug28
PASS		
279	r400su_pc_management_01	2002Aug28
PASS		
280	r400su_pc_management_02	2002Aug28
PASS		
281	r400su_pc_management_03	2002Aug28
PASS		
282	r400su_pointsizepresent_01	2002Aug28
PASS		
283	r400su_pointsizepresent_02	2002Aug28
PASS		
284	r400su_point_sprite_01	2002Aug28

PASS		
285	r400su_point_sprite_02	2002Aug28
PASS		
286	r400su_point_sprite_03	2002Aug28
PASS		
287	r400su_point_sprite_04	2002Aug28
PASS		
288	r400su_point_sprite_05	2002Aug28
PASS		
289	r400su_point_sprite_06	2002Aug28
PASS		
290	r400su_point_sprite_07	2002Aug28
PASS		
291	r400su_point_sprite_08	2002Aug28
PASS		
292	r400su_point_wl6_hl_functional_01	2002Aug28
PASS		
293	r400su_point_wl_hl6_functional_01	2002Aug28
PASS		
294	r400su_polymodeculling_01	2002Aug28
PASS		
295	r400su_polymodefunctional_01	2002Aug28
PASS		
296	r400su_polymode_lines_degen_triangle_01	2002Aug28
PASS		
297	r400su_polymode_lines_degen_triangle_02	2002Aug28
PASS		
298	r400su_polymode_lines_zero_area_triangle_01	2002Aug28
PASS		
299	r400su_polymode_lines_zero_area_triangle_02	2002Aug28
PASS		
300	r400su_polymode_points_degen_triangle_01	2002Aug28
PASS		
301	r400su_polymode_points_degen_triangle_02	2002Aug28
PASS		
302	r400su_polymode_points_zero_area_triangle_01	2002Aug28
PASS		
303	r400su_polymode_points_zero_area_triangle_02	2002Aug28
PASS		
304	r400su_polymode_rectangle_01	2002Aug28
PASS		
305	r400su_polymode_zero_area_triangle_01	2002Aug28
PASS		
306	r400su_polymode_zero_area_triangle_02	2002Aug28
PASS		
307	r400su_polymode_zero_area_triangle_03	2002Aug28
PASS		
308	r400su_polymode_zero_area_triangle_04	2002Aug28

PASS		
309	r400su_provoking_vtx_edgeflags_triangle_01	2002Aug28
PASS		
310	r400su_provoking_vtx_edgeflags_triangle_02	2002Aug28
PASS		
311	r400su_provoking_vtx_edgeflags_triangle_03	2002Aug28
PASS		
312	r400su_provoking_vtx_edgeflags_triangle_04	2002Aug28
PASS		
313	r400su_provoking_vtx_line_01	2002Aug28
PASS		
314	r400su_provoking_vtx_point_01	2002Aug28
PASS		
315	r400su_provoking_vtx_polymode_rectangle_01	2002Aug28
PASS		
316	r400su_provoking_vtx_rectangle_01	2002Aug28
PASS		
317	r400su_provoking_vtx_triangle_01	2002Aug28
PASS		
318	r400su_rand_line_01	2002Aug28
PASS		
319	r400su_rand_point_01	2002Aug28
PASS		
320	r400su_rand_tri_01	2002Aug28
PASS		
321	r400su_rectangle_list_01	2002Aug28
PASS		
322	r400su_sliver_01	2002Aug28
PASS		
323	r400su_triarea_test_01	2002Aug28
PASS		
324	r400su_triarea_test_02	2002Aug28
PASS		
325	r400su_triarea_test_03	2002Aug28
PASS		
326	r400su_triarea_test_04	2002Aug28
PASS		
327	r400su_vertexsortingfunctional_01	2002Aug28
PASS		
328	r400vgt_auto_index_line_list_01	2002Aug28
PASS		
329	r400vgt_auto_index_line_loop_01	2002Aug28
PASS		
330	r400vgt_auto_index_line_strip_01	2002Aug28
PASS		
331	r400vgt_auto_index_points_01	2002Aug28
PASS		
332	r400vgt_auto_index_polygon_01	2002Aug28

PASS		
333	r400vgt_auto_index_primitives_01	2002Aug28
PASS		
334	r400vgt_auto_index_quad_list_01	2002Aug28
PASS		
335	r400vgt_auto_index_quad_strip_01	2002Aug28
PASS		
336	r400vgt_auto_index_rectangle_list_01	2002Aug28
PASS		
337	r400vgt_auto_index_tri_fan_01	2002Aug28
PASS		
338	r400vgt_auto_index_tri_list_01	2002Aug28
PASS		
339	r400vgt_auto_index_tri_strip_01	2002Aug28
PASS		
340	r400vgt_auto_index_tri_wflags_01	2002Aug28
PASS		
341	r400vgt_dma_engine_01	2002Aug28
PASS		
342	r400vgt_dma_engine_02	2002Aug28
PASS		
343	r400vgt_dma_engine_03	2002Aug28
PASS		
344	r400vgt_dma_engine_04	2002Aug28
PASS		
345	r400vgt_dma_engine_05	2002Aug28
PASS		
346	r400vgt_dma_engine_06	2002Aug28
PASS		
347	r400vgt_dma_engine_07	2002Aug28
PASS		
348	r400vgt_dma_engine_08	2002Aug28
PASS		
349	r400vgt_dma_engine_09	2002Aug28
PASS		
350	r400vgt_dma_engine_10	2002Aug28
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351	r400vgt_dma_index_line_list_01	2002Aug28
PASS		
352	r400vgt_dma_index_line_loop_01	2002Aug28
PASS		
353	r400vgt_dma_index_line_strip_01	2002Aug28
PASS		
354	r400vgt_dma_index_points_01	2002Aug28
PASS		
355	r400vgt_dma_index_polygon_01	2002Aug28
PASS		
356	r400vgt_dma_index_primitives_01	2002Aug28

PASS		
357	r400vgt_dma_index_quad_list_01	2002Aug28
PASS		
358	r400vgt_dma_index_quad_strip_01	2002Aug28
PASS		
359	r400vgt_dma_index_rectangle_list_01	2002Aug28
PASS		
360	r400vgt_dma_index_tri_fan_01	2002Aug28
PASS		
361	r400vgt_dma_index_tri_list_01	2002Aug28
PASS		
362	r400vgt_dma_index_tri_strip_01	2002Aug28
PASS		
363	r400vgt_dma_index_tri_wflags_01	2002Aug28
PASS		
364	r400vgt_dma_swap_indx16_01	2002Aug28
PASS		
365	r400vgt_dma_swap_indx32_01	2002Aug28
PASS		
366	r400vgt_edgeflags_polygon_01	2002Aug28
PASS		
367	r400vgt_edgeflags_quad_list_01	2002Aug28
PASS		
368	r400vgt_edgeflags_quad_strip_01	2002Aug28
PASS		
369	r400vgt_edgeflags_triangle_fan_01	2002Aug28
PASS		
370	r400vgt_edgeflags_triangle_list_01	2002Aug28
PASS		
371	r400vgt_edgeflags_triangle_strip_01	2002Aug28
PASS		
372	r400vgt_edgeflags_triangle_wflags_01	2002Aug28
PASS		
373	r400vgt_ext2int_index_line_list_01	2002Aug28
PASS		
374	r400vgt_ext2int_index_line_loop_01	2002Aug28
PASS		
375	r400vgt_ext2int_index_line_strip_01	2002Aug28
PASS		
376	r400vgt_ext2int_index_points_01	2002Aug28
PASS		
377	r400vgt_ext2int_index_polygon_01	2002Aug28
PASS		
378	r400vgt_ext2int_index_quad_list_01	2002Aug28
PASS		
379	r400vgt_ext2int_index_quad_strip_01	2002Aug28
PASS		
380	r400vgt_ext2int_index_triangle_fan_01	2002Aug28

PASS		
381	r400vgt_ext2int_index_triangle_list_01	2002Aug28
PASS		
382	r400vgt_ext2int_index_triangle_strip_01	2002Aug28
PASS		
383	r400vgt_hos_auto_index_line_list_01	2002Aug28
PASS		
384	r400vgt_hos_auto_index_quad_list_01	2002Aug28
PASS		
385	r400vgt_hos_auto_index_triangle_list_01	2002Aug28
PASS		
386	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug28
PASS		
387	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug28
PASS		
388	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug28
PASS		
389	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug28
PASS		
390	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug28
PASS		
391	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug28
PASS		
392	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug28
PASS		
393	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug28
PASS		
394	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug28
PASS		
395	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug28
PASS		
396	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug28
PASS		
397	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug28
PASS		
398	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug28
PASS		
399	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug28
PASS		
400	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug28
PASS		
401	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug28
PASS		
402	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug28
PASS		
403	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug28
PASS		
404	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug28

PASS
405 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Aug28
PASS
406 r400vgt_hos_PNT_disc_cp_qn_precision_02 2002Aug28
PASS
407 r400vgt_hos_PNT_edge_detection_01 2002Aug28
PASS
408 r400vgt_hos_PNT_lp_cont_no_projection_01 2002Aug28
PASS
409 r400vgt_hos_simple_linear_PNT_discrete_01 2002Aug28
PASS
410 r400vgt_immed_index_line_list_01 2002Aug28
PASS
411 r400vgt_immed_index_line_loop_01 2002Aug28
PASS
412 r400vgt_immed_index_line_strip_01 2002Aug28
PASS
413 r400vgt_immed_index_points_01 2002Aug28
PASS
414 r400vgt_immed_index_polygon_01 2002Aug28
PASS
415 r400vgt_immed_index_primitives_01 2002Aug28
PASS
416 r400vgt_immed_index_quad_list_01 2002Aug28
PASS
417 r400vgt_immed_index_quad_strip_01 2002Aug28
PASS
418 r400vgt_immed_index_rectangle_list_01 2002Aug28
PASS
419 r400vgt_immed_index_tri_fan_01 2002Aug28
PASS
420 r400vgt_immed_index_tri_list_01 2002Aug28
PASS
421 r400vgt_immed_index_tri_strip_01 2002Aug28
PASS
422 r400vgt_immed_index_tri_wflags_01 2002Aug28
PASS
423 r400vgt_index_dealloc_line_list_01 2002Aug28
PASS
424 r400vgt_index_dealloc_points_01 2002Aug28
PASS
425 r400vgt_index_dealloc_triangle_list_01 2002Aug28
PASS
426 r400vgt_index_min_max_01 2002Aug28
PASS
427 r400vgt_index_min_max_02 2002Aug28
PASS
428 r400vgt_index_offset_01 2002Aug28

PASS		
429	r400vgt_index_offset_02	2002Aug28
PASS		
430	r400vgt_index_offset_03	2002Aug28
PASS		
431	r400vgt_index_size_01	2002Aug28
PASS		
432	r400vgt_index_source_switch_01	2002Aug28
PASS		
433	r400vgt_line_list_01	2002Aug28
PASS		
434	r400vgt_line_loop_01	2002Aug28
PASS		
435	r400vgt_line_strip_01	2002Aug28
PASS		
436	r400vgt_multi_context_01	2002Aug28
PASS		
437	r400vgt_multi_context_02	2002Aug28
PASS		
438	r400vgt_multi_context_03	2002Aug28
PASS		
439	r400vgt_multi_context_04	2002Aug28
PASS		
440	r400vgt_multi_context_05	2002Aug28
PASS		
441	r400vgt_multi_context_06	2002Aug28
PASS		
442	r400vgt_multi_context_07	2002Aug28
PASS		
443	r400vgt_multi_context_08	2002Aug28
PASS		
444	r400vgt_multi_context_09	2002Aug28
PASS		
445	r400vgt_multi_context_10	2002Aug28
PASS		
446	r400vgt_multi_context_11	2002Aug28
PASS		
447	r400vgt_multi_context_12	2002Aug28
PASS		
448	r400vgt_pass_thru_all_prims_01	2002Aug28
PASS		
449	r400vgt_point_list_01	2002Aug28
PASS		
450	r400vgt_polygon_01	2002Aug28
PASS		
451	r400vgt_provoking_vtx_all_01	2002Aug28
PASS		
452	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug28

PASS		
453	r400vgt_quad_list_01	2002Aug28
PASS		
454	r400vgt_quad_strip_01	2002Aug28
PASS		
455	r400vgt_rectangle_list_01	2002Aug28
PASS		
456	r400vgt_reuse_depth_line_list_01	2002Aug28
PASS		
457	r400vgt_reuse_depth_point_list_01	2002Aug28
PASS		
458	r400vgt_reuse_depth_triangle_list_01	2002Aug28
PASS		
459	r400vgt_reuse_index_line_list_01	2002Aug28
PASS		
460	r400vgt_reuse_index_point_list_01	2002Aug28
PASS		
461	r400vgt_reuse_index_triangle_list_01	2002Aug28
PASS		
462	r400vgt_reuse_index_triangle_list_02	2002Aug28
PASS		
463	r400vgt_reuse_index_triangle_list_03	2002Aug28
PASS		
464	r400vgt_triangle_fan_01	2002Aug28
PASS		
465	r400vgt_triangle_list_01	2002Aug28
PASS		
466	r400vgt_triangle_strip_01	2002Aug28
PASS		
467	r400vgt_triangle_wflags_01	2002Aug28
PASS		
468	r400vgt_vtx_vector_packing_01	2002Aug28
PASS		
469	r400vte_coverage_01	2002Aug28
PASS		
470	r400vte_simple_point_01	2002Aug28
PASS		
471	r400vte_simple_triangle_01	2002Aug28
PASS		
472	r400vte_w0_fmt_01	2002Aug28
PASS		
473	r400vte_w0_fmt_03	2002Aug28
PASS		
474	r400vte_xy_fmt_01	2002Aug28
PASS		
475	r400vte_xyz_scale_01	2002Aug28
PASS		
476	r400vte_xyz_scale_02	2002Aug28

PASS

477 r400vte_z_fmt_01

2002Aug28

PASS

=====
Tests_Run_count = 477 Pass_count = 475 Fail_count = 0 Incomplete_count = 2
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Aug 30  3:16:54 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141      100
              SC           157          157          156       99
              CL           108          108          108      100
              VTE           9           9           9       100
SPECIALTY    SU            81           81           81      100
              SANITY       2           2           2       100
+++++
              TOTAL        504          504          503       99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Sep  7  4:47:23 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          151          151          147          97
              SC           226          226          165          73
              CL           110          110          108          98
              VTE           9           9           9           100
SPECIALTY    SU           82          82          81          98
              SANITY       2           2           2           100
+++++
              TOTAL        586          586          518          88
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep 11  6:49:17 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          151          151          149          98
              SC           236          236          233          98
              CL           110          110          110          100
              VTE           9           9           9           100
SPECIALTY    SU           83           83           83           100
              SANITY       2           2           2           100
+++++
              TOTAL        597          597          592          99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep17
	PASS		
2	cp_e2blit_brush_mt_ropcc		2002Sep17
	INCOMPLETE		
3	cp_e2_hostdata_blt_pntr_8888		2002Sep17
	INCOMPLETE		
4	cp_e2_one_blit		2002Sep17
	INCOMPLETE		
5	cp_e2_one_line		2002Sep17
	INCOMPLETE		
6	cp_e2paint_565		2002Sep17
	INCOMPLETE		
7	cp_e2paint_multi		2002Sep17
	INCOMPLETE		
8	cp_e2polyscanlines_brush		2002Sep17
	INCOMPLETE		
9	cp_e2polyscanlines_brush_mt		2002Sep17
	INCOMPLETE		
10	cp_e2smalltext_jcl		2002Sep17
	INCOMPLETE		
11	cp_simple_triangle		2002Sep17
	PASS		
12	primlib_1st_tri_june15		2002Sep17
	PASS		
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep17
	PASS		
14	primlib_gouraud_triangles_2_draw_passes		2002Sep17
	PASS		
15	primlib_parameterized_simple_triangle		2002Sep17
	PASS		
16	primlib_template_simple_triangle		2002Sep17
	PASS		
17	primlib_tex_tri		2002Sep17
	PASS		
18	r400cl_clip_10_verts_01		2002Sep17
	PASS		
19	r400cl_clip_disable_01		2002Sep17
	PASS		
20	r400cl_clip_space_dx_ogl_01		2002Sep17

PASS		
21	r400cl_clip_ucp_6bits_01	2002Sep17
PASS		
22	r400cl_cull_only_ena_01	2002Sep17
PASS		
23	r400cl_edgeflags_01	2002Sep17
PASS		
24	r400cl_edgeflags_frustum_bottom_01	2002Sep17
PASS		
25	r400cl_edgeflags_frustum_far_01	2002Sep17
PASS		
26	r400cl_edgeflags_frustum_left_01	2002Sep17
PASS		
27	r400cl_edgeflags_frustum_near_01	2002Sep17
PASS		
28	r400cl_edgeflags_frustum_right_01	2002Sep17
PASS		
29	r400cl_edgeflags_frustum_top_01	2002Sep17
PASS		
30	r400cl_edgeflags_gband_01	2002Sep17
PASS		
31	r400cl_edgeflags_gband_bottom_01	2002Sep17
PASS		
32	r400cl_edgeflags_gband_left_01	2002Sep17
PASS		
33	r400cl_edgeflags_gband_right_01	2002Sep17
PASS		
34	r400cl_edgeflags_gband_top_01	2002Sep17
PASS		
35	r400cl_frustum_01	2002Sep17
PASS		
36	r400cl_frustum_02	2002Sep17
PASS		
37	r400cl_frustum_03	2002Sep17
PASS		
38	r400cl_frustum_04	2002Sep17
PASS		
39	r400cl_frustum_05	2002Sep17
PASS		
40	r400cl_frustum_06	2002Sep17
PASS		
41	r400cl_frustum_07	2002Sep17
PASS		
42	r400cl_frustum_08	2002Sep17
PASS		
43	r400cl_frustum_09	2002Sep17
PASS		
44	r400cl_frustum_10	2002Sep17

PASS		
45	r400cl_frustum_11	2002Sep17
PASS		
46	r400cl_frustum_12	2002Sep17
PASS		
47	r400cl_frustum_13	2002Sep17
PASS		
48	r400cl_frustum_14	2002Sep17
PASS		
49	r400cl_frustum_15	2002Sep17
PASS		
50	r400cl_frustum_16	2002Sep17
PASS		
51	r400cl_frustum_17	2002Sep17
PASS		
52	r400cl_frustum_18	2002Sep17
PASS		
53	r400cl_frustum_19	2002Sep17
PASS		
54	r400cl_frustum_20	2002Sep17
PASS		
55	r400cl_frustum_21	2002Sep17
PASS		
56	r400cl_frustum_22	2002Sep17
PASS		
57	r400cl_frustum_23	2002Sep17
PASS		
58	r400cl_frustum_24	2002Sep17
PASS		
59	r400cl_frustum_25	2002Sep17
PASS		
60	r400cl_frustum_26	2002Sep17
PASS		
61	r400cl_frustum_27	2002Sep17
PASS		
62	r400cl_frustum_28	2002Sep17
PASS		
63	r400cl_frustum_all_vols_lines	2002Sep17
PASS		
64	r400cl_frustum_all_vols_tris	2002Sep17
PASS		
65	r400cl_frustum_lines_01	2002Sep17
PASS		
66	r400cl_frustum_lines_02	2002Sep17
PASS		
67	r400cl_frustum_lines_03	2002Sep17
PASS		
68	r400cl_frustum_lines_04	2002Sep17

PASS		
69	r400cl_frustum_lines_05	2002Sep17
PASS		
70	r400cl_frustum_lines_06	2002Sep17
PASS		
71	r400cl_frustum_lines_07	2002Sep17
PASS		
72	r400cl_frustum_lines_08	2002Sep17
PASS		
73	r400cl_frustum_lines_09	2002Sep17
PASS		
74	r400cl_frustum_lines_10	2002Sep17
PASS		
75	r400cl_frustum_lines_11	2002Sep17
PASS		
76	r400cl_frustum_lines_12	2002Sep17
PASS		
77	r400cl_frustum_lines_13	2002Sep17
PASS		
78	r400cl_frustum_lines_14	2002Sep17
PASS		
79	r400cl_frustum_lines_15	2002Sep17
PASS		
80	r400cl_frustum_lines_16	2002Sep17
PASS		
81	r400cl_frustum_lines_17	2002Sep17
PASS		
82	r400cl_frustum_lines_18	2002Sep17
PASS		
83	r400cl_frustum_lines_19	2002Sep17
PASS		
84	r400cl_frustum_lines_20	2002Sep17
PASS		
85	r400cl_frustum_lines_21	2002Sep17
PASS		
86	r400cl_frustum_lines_22	2002Sep17
PASS		
87	r400cl_frustum_lines_23	2002Sep17
PASS		
88	r400cl_frustum_lines_24	2002Sep17
PASS		
89	r400cl_gband_04	2002Sep17
PASS		
90	r400cl_gband_tcl_01	2002Sep17
PASS		
91	r400cl_pointlist_vertex_state_ucp_01	2002Sep17
PASS		
92	r400cl_simple_triangle_01	2002Sep17

PASS		
93	r400cl_ucp_combos_01	2002Sep17
PASS		
94	r400cl_ucp_combos_02	2002Sep17
PASS		
95	r400cl_ucp_combos_03	2002Sep17
PASS		
96	r400cl_ucp_combos_04	2002Sep17
PASS		
97	r400cl_ucp_combos_05	2002Sep17
PASS		
98	r400cl_ucp_combos_06	2002Sep17
PASS		
99	r400cl_ucp_combos_07	2002Sep17
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434	r400su_polymode_rectangle_01	2002Sep17
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435	r400su_polymode_zero_area_triangle_01	2002Sep17
PASS		
436	r400su_polymode_zero_area_triangle_02	2002Sep17
PASS		
437	r400su_polymode_zero_area_triangle_03	2002Sep17
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438	r400su_polymode_zero_area_triangle_04	2002Sep17
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439	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep17
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440	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep17
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441	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep17
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442	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep17
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443	r400su_provoking_vtx_line_01	2002Sep17
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444	r400su_provoking_vtx_point_01	2002Sep17
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445	r400su_provoking_vtx_polymode_rectangle_01	2002Sep17
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446	r400su_provoking_vtx_rectangle_01	2002Sep17
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447	r400su_provoking_vtx_triangle_01	2002Sep17
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448	r400su_rand_line_01	2002Sep17
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449	r400su_rand_point_01	2002Sep17
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450	r400su_rand_tri_01	2002Sep17
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451	r400su_rectangle_list_01	2002Sep17
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452	r400su_sliver_01	2002Sep17

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453	r400su_triarea_test_01	2002Sep17
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454	r400su_triarea_test_02	2002Sep17
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455	r400su_triarea_test_03	2002Sep17
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456	r400su_triarea_test_04	2002Sep17
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457	r400su_vertexsortingfunctional_01	2002Sep17
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458	r400vgt_auto_index_line_list_01	2002Sep17
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459	r400vgt_auto_index_line_loop_01	2002Sep17
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460	r400vgt_auto_index_line_strip_01	2002Sep17
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461	r400vgt_auto_index_points_01	2002Sep17
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462	r400vgt_auto_index_polygon_01	2002Sep17
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463	r400vgt_auto_index_primitives_01	2002Sep17
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464	r400vgt_auto_index_quad_list_01	2002Sep17
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465	r400vgt_auto_index_quad_strip_01	2002Sep17
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466	r400vgt_auto_index_rectangle_list_01	2002Sep17
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467	r400vgt_auto_index_tri_fan_01	2002Sep17
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468	r400vgt_auto_index_tri_list_01	2002Sep17
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469	r400vgt_auto_index_tri_strip_01	2002Sep17
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470	r400vgt_auto_index_tri_wflags_01	2002Sep17
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471	r400vgt_dma_engine_01	2002Sep17
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472	r400vgt_dma_engine_02	2002Sep17
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473	r400vgt_dma_engine_03	2002Sep17
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474	r400vgt_dma_engine_04	2002Sep17
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479	r400vgt_dma_engine_09	2002Sep17
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480	r400vgt_dma_engine_10	2002Sep17
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481	r400vgt_dma_index_line_list_01	2002Sep17
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483	r400vgt_dma_index_line_strip_01	2002Sep17
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484	r400vgt_dma_index_points_01	2002Sep17
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486	r400vgt_dma_index_primitives_01	2002Sep17
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487	r400vgt_dma_index_quad_list_01	2002Sep17
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492	r400vgt_dma_index_tri_strip_01	2002Sep17
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493	r400vgt_dma_index_tri_wflags_01	2002Sep17
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494	r400vgt_dma_swap_idx16_01	2002Sep17
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495	r400vgt_dma_swap_idx32_01	2002Sep17
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496	r400vgt_edgeflags_polygon_01	2002Sep17
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497	r400vgt_edgeflags_quad_list_01	2002Sep17
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498	r400vgt_edgeflags_quad_strip_01	2002Sep17
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500	r400vgt_edgeflags_triangle_list_01	2002Sep17

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501	r400vgt_edgeflags_triangle_strip_01	2002Sep17
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502	r400vgt_edgeflags_triangle_wflags_01	2002Sep17
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503	r400vgt_ext2int_index_line_list_01	2002Sep17
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504	r400vgt_ext2int_index_line_loop_01	2002Sep17
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505	r400vgt_ext2int_index_line_strip_01	2002Sep17
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506	r400vgt_ext2int_index_points_01	2002Sep17
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507	r400vgt_ext2int_index_polygon_01	2002Sep17
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508	r400vgt_ext2int_index_quad_list_01	2002Sep17
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509	r400vgt_ext2int_index_quad_strip_01	2002Sep17
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510	r400vgt_ext2int_index_triangle_fan_01	2002Sep17
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511	r400vgt_ext2int_index_triangle_list_01	2002Sep17
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512	r400vgt_ext2int_index_triangle_strip_01	2002Sep17
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513	r400vgt_hos_auto_index_line_list_01	2002Sep17
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514	r400vgt_hos_auto_index_quad_list_01	2002Sep17
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515	r400vgt_hos_auto_index_triangle_list_01	2002Sep17
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516	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep17
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517	r400vgt_hos_LPatch_01	2002Sep17
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518	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep17
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519	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep17
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520	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep17
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521	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep17
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522	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep17
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523	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep17
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524	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep17

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525	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep17
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526	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep17
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527	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep17
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528	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep17
PASS		
529	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep17
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530	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep17
PASS		
531	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep17
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532	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep17
PASS		
533	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep17
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534	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep17
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535	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep17
PASS		
536	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep17
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537	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep17
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538	r400vgt_hos_PNT_edge_detection_01	2002Sep17
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539	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep17
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540	r400vgt_hos_RPatch_cp_02	2002Sep17
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541	r400vgt_hos_RPatch_lp_02	2002Sep17
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542	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep13
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543	r400vgt_hos_TPatch_01	2002Sep13
INCOMPLETE		
544	r400vgt_hos_TPatch_02	2002Sep13
INCOMPLETE		
545	r400vgt_immed_index_line_list_01	2002Sep17
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546	r400vgt_immed_index_line_loop_01	2002Sep17
PASS		
547	r400vgt_immed_index_line_strip_01	2002Sep17
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548	r400vgt_immed_index_points_01	2002Sep17

PASS		
549	r400vgt_immed_index_polygon_01	2002Sep17
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550	r400vgt_immed_index_primitives_01	2002Sep17
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551	r400vgt_immed_index_quad_list_01	2002Sep17
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552	r400vgt_immed_index_quad_strip_01	2002Sep17
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553	r400vgt_immed_index_rectangle_list_01	2002Sep17
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554	r400vgt_immed_index_tri_fan_01	2002Sep17
PASS		
555	r400vgt_immed_index_tri_list_01	2002Sep17
PASS		
556	r400vgt_immed_index_tri_strip_01	2002Sep17
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557	r400vgt_immed_index_tri_wflags_01	2002Sep17
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558	r400vgt_index_dealloc_line_list_01	2002Sep17
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559	r400vgt_index_dealloc_points_01	2002Sep17
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560	r400vgt_index_dealloc_triangle_list_01	2002Sep17
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561	r400vgt_index_min_max_01	2002Sep17
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562	r400vgt_index_min_max_02	2002Sep17
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563	r400vgt_index_offset_01	2002Sep17
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564	r400vgt_index_offset_02	2002Sep17
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565	r400vgt_index_offset_03	2002Sep17
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566	r400vgt_index_size_01	2002Sep17
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567	r400vgt_index_source_switch_01	2002Sep17
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568	r400vgt_line_list_01	2002Sep17
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569	r400vgt_line_loop_01	2002Sep17
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570	r400vgt_line_strip_01	2002Sep17
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571	r400vgt_multi_context_01	2002Sep17
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572	r400vgt_multi_context_02	2002Sep17

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573	r400vgt_multi_context_03	2002Sep17
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578	r400vgt_multi_context_08	2002Sep17
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579	r400vgt_multi_context_09	2002Sep17
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580	r400vgt_multi_context_10	2002Sep17
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581	r400vgt_multi_context_11	2002Sep17
PASS		
582	r400vgt_multi_context_12	2002Sep17
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583	r400vgt_multi_prim_reset_index_all_01	2002Sep17
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584	r400vgt_pass_thru_all_prims_01	2002Sep17
PASS		
585	r400vgt_point_list_01	2002Sep17
PASS		
586	r400vgt_polygon_01	2002Sep17
PASS		
587	r400vgt_provoking_vtx_all_01	2002Sep17
PASS		
588	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep17
PASS		
589	r400vgt_provoking_vtx_polygon_01	2002Sep17
PASS		
590	r400vgt_provoking_vtx_quad_list_01	2002Sep17
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591	r400vgt_provoking_vtx_quad_strip_01	2002Sep17
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593	r400vgt_provoking_vtx_tri_strip_01	2002Sep17
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594	r400vgt_quad_list_01	2002Sep17
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595	r400vgt_quad_strip_01	2002Sep17
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596	r400vgt_rectangle_list_01	2002Sep17

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597	r400vgt_reuse_depth_line_list_01	2002Sep17
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598	r400vgt_reuse_depth_point_list_01	2002Sep17
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599	r400vgt_reuse_depth_triangle_list_01	2002Sep17
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601	r400vgt_reuse_index_point_list_01	2002Sep17
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602	r400vgt_reuse_index_triangle_list_01	2002Sep17
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603	r400vgt_reuse_index_triangle_list_02	2002Sep17
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604	r400vgt_reuse_index_triangle_list_03	2002Sep17
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605	r400vgt_suppress_eop_01	2002Sep17
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606	r400vgt_suppress_eop_02	2002Sep17
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607	r400vgt_suppress_eop_03	2002Sep17
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608	r400vgt_suppress_eop_04	2002Sep17
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609	r400vgt_triangle_fan_01	2002Sep17
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610	r400vgt_triangle_list_01	2002Sep17
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611	r400vgt_triangle_strip_01	2002Sep17
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612	r400vgt_triangle_wflags_01	2002Sep17
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613	r400vgt_viz_query_01	2002Sep17
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614	r400vgt_vtx_vector_packing_01	2002Sep17
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615	r400vte_coverage_01	2002Sep17
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616	r400vte_simple_point_01	2002Sep17
PASS		
617	r400vte_simple_triangle_01	2002Sep17
PASS		
618	r400vte_w0_fmt_01	2002Sep17
PASS		
619	r400vte_w0_fmt_03	2002Sep17
PASS		
620	r400vte_xy_fmt_01	2002Sep17

PASS
621 r400vte_xyz_scale_01 2002Sep17
PASS
622 r400vte_xyz_scale_02 2002Sep17
PASS
623 r400vte_z_fmt_01 2002Sep17
PASS

=====
Tests_Run_count = 623 Pass_count = 610 Fail_count = 0 Incomplete_count = 13
=====

=====
Regression Report For VGT - Project = r400 Created on Fri Aug 23 15:18:28 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug23
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug22
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug22
PASS			
4	primlib_parameterized_simple_triangle		2002Aug22
PASS			
5	primlib_template_simple_triangle		2002Aug22
PASS			
6	primlib_tex_tri		2002Aug22
PASS			
7	r400cl_clip_10_verts_01		2002Aug22
PASS			
8	r400cl_clip_disable_01		2002Aug22
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug22
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug22
PASS			
11	r400cl_cull_only_ena_01		2002Aug22
PASS			
12	r400cl_edgeflags_01		2002Aug22
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug22
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug22
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug22
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug22
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug22
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug22
PASS			
19	r400cl_edgeflags_gband_01		2002Aug22
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Aug22

PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug22
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug22
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug22
PASS		
24	r400cl_frustum_01	2002Aug22
PASS		
25	r400cl_frustum_02	2002Aug22
PASS		
26	r400cl_frustum_03	2002Aug22
PASS		
27	r400cl_frustum_04	2002Aug22
PASS		
28	r400cl_frustum_05	2002Aug22
PASS		
29	r400cl_frustum_06	2002Aug22
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30	r400cl_frustum_07	2002Aug22
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31	r400cl_frustum_08	2002Aug22
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34	r400cl_frustum_11	2002Aug22
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35	r400cl_frustum_12	2002Aug22
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36	r400cl_frustum_13	2002Aug22
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37	r400cl_frustum_14	2002Aug22
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42	r400cl_frustum_19	2002Aug22
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43	r400cl_frustum_20	2002Aug22
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44	r400cl_frustum_21	2002Aug22

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45	r400cl_frustum_22	2002Aug22
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50	r400cl_frustum_27	2002Aug22
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51	r400cl_frustum_28	2002Aug22
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52	r400cl_frustum_lines_01	2002Aug22
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53	r400cl_frustum_lines_02	2002Aug22
PASS		
54	r400cl_frustum_lines_03	2002Aug22
PASS		
55	r400cl_frustum_lines_04	2002Aug22
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56	r400cl_frustum_lines_05	2002Aug22
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66	r400cl_frustum_lines_15	2002Aug22
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67	r400cl_frustum_lines_16	2002Aug22
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68	r400cl_frustum_lines_17	2002Aug22

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74	r400cl_frustum_lines_23	2002Aug22
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75	r400cl_frustum_lines_24	2002Aug22
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76	r400cl_gband_04	2002Aug22
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77	r400cl_gband_tcl_01	2002Aug22
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78	r400cl_simple_triangle_01	2002Aug22
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79	r400cl_ucp_combos_01	2002Aug22
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80	r400cl_ucp_combos_02	2002Aug22
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81	r400cl_ucp_combos_03	2002Aug22
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82	r400cl_ucp_combos_04	2002Aug22
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83	r400cl_ucp_pointlist_01	2002Aug22
INCOMPLETE		
84	r400cl_vertex_reuse_clip_01	2002Aug22
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85	r400cl_vtx_kill_01	2002Aug22
PASS		
86	r400cl_vtx_kill_02	2002Aug22
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87	r400cl_w_eq_0	2002Aug22
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88	r400sanity_persp_projection_01	2002Aug22
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89	r400sanity_vfd_texture_sample_01	2002Aug22
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90	r400sc_bres_cntl_01	2002Aug22
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91	r400sc_bres_cntl_02	2002Aug22
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92	r400sc_bres_cntl_03	2002Aug22

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93	r400sc_bres_cntl_04	2002Aug22
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95	r400sc_clip_rect_02	2002Aug22
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96	r400sc_clip_rect_03	2002Aug22
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97	r400sc_clip_rect_04	2002Aug22
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98	r400sc_clip_rect_fc_01	2002Aug22
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99	r400sc_diamond_exit_01	2002Aug22
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100	r400sc_diamond_exit_02	2002Aug22
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101	r400sc_diamond_exit_03	2002Aug22
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102	r400sc_diamond_exit_04	2002Aug22
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103	r400sc_diamond_exit_05	2002Aug22
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105	r400sc_jss_1x2_02	2002Aug22
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106	r400sc_jss_1x3_01	2002Aug22
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107	r400sc_jss_1x3_02	2002Aug22
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108	r400sc_jss_1x4_01	2002Aug22
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109	r400sc_jss_1x4_02	2002Aug22
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110	r400sc_jss_2x1_01	2002Aug22
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111	r400sc_jss_2x1_02	2002Aug22
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112	r400sc_jss_2x2_01	2002Aug22
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113	r400sc_jss_2x2_02	2002Aug22
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115	r400sc_jss_2x3_02	2002Aug22
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118 r400sc_jss_3x1_01 2002Aug22
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119 r400sc_jss_3x1_02 2002Aug22
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120 r400sc_jss_3x2_01 2002Aug22
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121 r400sc_jss_3x2_02 2002Aug22
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122 r400sc_jss_3x3_01 2002Aug22
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123 r400sc_jss_3x3_02 2002Aug22
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124 r400sc_jss_3x4_01 2002Aug22
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125 r400sc_jss_3x4_02 2002Aug22
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126 r400sc_jss_3x4_03 2002Aug22
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127 r400sc_jss_4x1_01 2002Aug22
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128 r400sc_jss_4x1_02 2002Aug22
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129 r400sc_jss_4x2_01 2002Aug22
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130 r400sc_jss_4x2_02 2002Aug22
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131 r400sc_jss_4x3_01 2002Aug22
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132 r400sc_jss_4x3_02 2002Aug22
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133 r400sc_jss_4x4_01 2002Aug22
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134 r400sc_jss_4x4_02 2002Aug22
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135 r400sc_jss_4x4_aa_mask_01 2002Aug22
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136 r400sc_jss_4x4_aa_mask_02 2002Aug22
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137 r400sc_jss_4x4_fc_01 2002Aug22
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138 r400sc_jss_4x4_max_dist_01 2002Aug22
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139 r400sc_line_dx10_eq_0_01 2002Aug22
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140 r400sc_line_dx10_ge_0_01 2002Aug22

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141	r400sc_line_dx10_lt_0_01	2002Aug22
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142	r400sc_line_dy10_eq_0_01	2002Aug22
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143	r400sc_line_dy10_ge_0_01	2002Aug22
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144	r400sc_line_dy10_lt_0_01	2002Aug22
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145	r400sc_line_expand_width_msa_8_01	2002Aug22
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146	r400sc_line_expand_width_msa_8_02	2002Aug22
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147	r400sc_line_expand_width_msa_8_03	2002Aug22
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148	r400sc_line_jss_3x4_01	2002Aug22
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149	r400sc_line_list_concentric_circle_01	2002Aug22
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150	r400sc_line_list_concentric_circle_02	2002Aug22
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151	r400sc_line_list_concentric_circle_03	2002Aug22
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152	r400sc_line_msa_8_01	2002Aug22
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153	r400sc_line_stipple_01	2002Aug22
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154	r400sc_line_stipple_02	2002Aug22
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155	r400sc_line_stipple_03	2002Aug22
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156	r400sc_line_stipple_04	2002Aug22
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157	r400sc_line_stipple_05	2002Aug22
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158	r400sc_line_stipple_06	2002Aug22
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159	r400sc_line_stipple_07	2002Aug22
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160	r400sc_line_stipple_08	2002Aug22
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161	r400sc_line_stipple_09	2002Aug22
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162	r400sc_line_stipple_fc_08	2002Aug22
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163	r400sc_line_strip_stipple_01	2002Aug22
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164	r400sc_msa_1_01	2002Aug22

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165	r400sc_msaa_4_01	2002Aug22
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166	r400sc_msaa_6_01	2002Aug22
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167	r400sc_msaa_8_01	2002Aug22
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168	r400sc_msaa_8_aa_mask_01	2002Aug22
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169	r400sc_msaa_8_aa_mask_02	2002Aug22
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170	r400sc_msaa_8_aa_mask_fc_02	2002Aug22
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171	r400sc_null_triangles_01	2002Aug22
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172	r400sc_null_triangles_fc_01	2002Aug22
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173	r400sc_point_jss_3x4_01	2002Aug22
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174	r400sc_point_list_01	2002Aug22
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175	r400sc_point_list_02	2002Aug22
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176	r400sc_point_list_03	2002Aug22
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177	r400sc_point_list_04	2002Aug22
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179	r400sc_point_list_06	2002Aug22
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180	r400sc_point_list_07	2002Aug22
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181	r400sc_point_list_08	2002Aug22
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182	r400sc_point_list_09	2002Aug22
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183	r400sc_point_msaa_8_01	2002Aug22
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184	r400sc_poly_offset_01	2002Aug22
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185	r400sc_poly_offset_02	2002Aug22
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186	r400sc_poly_offset_fc_01	2002Aug22
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187	r400sc_polygon_stipple_01	2002Aug22
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188	r400sc_prsp_byc_intrp_ref_pix_01	2002Aug22

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189	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug22
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190	r400sc_prsp_byc_intrp_ref_pix_03	2002Aug22
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191	r400sc_prsp_byc_intrp_ref_pix_04	2002Aug22
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192	r400sc_prsp_byc_intrp_ref_pix_05	2002Aug22
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193	r400sc_prsp_byc_intrp_ref_pix_06	2002Aug22
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194	r400sc_prsp_byc_intrp_ref_pix_07	2002Aug22
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195	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug22
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196	r400sc_raster_fill_rule_01	2002Aug22
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197	r400sc_raster_fill_rule_02	2002Aug22
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198	r400sc_raster_fill_rule_03	2002Aug22
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199	r400sc_raster_fill_rule_04	2002Aug22
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200	r400sc_raster_fill_rule_05	2002Aug22
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201	r400sc_raster_fill_rule_fc_01	2002Aug22
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202	r400sc_rectangle_list_01	2002Aug22
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203	r400sc_rectangle_list_02	2002Aug22
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208	r400sc_rectangle_list_07	2002Aug22
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209	r400sc_rectangle_list_08	2002Aug22
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210	r400sc_scissor_rect_01	2002Aug22
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211	r400sc_scissor_rect_02	2002Aug22
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212	r400sc_scissor_rect_03	2002Aug22

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213	r400sc_scissor_rect_04	2002Aug22
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214	r400sc_scissor_rect_05	2002Aug22
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215	r400sc_scissor_rect_fc_01	2002Aug22
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216	r400sc_set_state_01	2002Aug22
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217	r400sc_tri_walk_start_vertex_01	2002Aug22
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218	r400sc_tri_walk_start_vertex_02	2002Aug22
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219	r400sc_tri_walk_start_vertex_03	2002Aug22
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222	r400sc_tri_walk_start_vertex_06	2002Aug22
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223	r400sc_tri_walk_start_vertex_07	2002Aug22
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224	r400sc_tri_walk_start_vertex_08	2002Aug22
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225	r400sc_tri_walk_start_vertex_09	2002Aug22
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227	r400sc_tri_walk_start_vertex_11	2002Aug22
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228	r400sc_tri_walk_start_vertex_12	2002Aug22
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229	r400sc_tri_walk_start_vertex_13	2002Aug22
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230	r400sc_tri_walk_start_vertex_14	2002Aug22
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231	r400sc_tri_walk_start_vertex_15	2002Aug22
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232	r400sc_tri_walk_start_vertex_16	2002Aug22
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233	r400sc_triangle_stipple_01	2002Aug22
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234	r400sc_window_offset_01	2002Aug22
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235	r400sc_window_offset_fc_01	2002Aug22
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236	r400sc_zbuffer_line_list_01	2002Aug22

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237	r400sc_zbuffer_point_list_01	2002Aug22
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238	r400sc_zbuffer_rectangle_list_01	2002Aug22
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239	r400sc_zbuffer_rectangle_list_02	2002Aug22
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240	r400sc_zbuffer_rectangle_list_fc_02	2002Aug22
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241	r400sc_zbuffer_triangle_list_01	2002Aug22
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242	r400su_4tri_text_offscreen_01	2002Aug22
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243	r400su_baryc_test_01	2002Aug22
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244	r400su_baryc_test_02	2002Aug22
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245	r400su_baryc_test_03	2002Aug22
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246	r400su_baryc_test_04	2002Aug22
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247	r400su_clip_edgflag_polymode_01	2002Aug22
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248	r400su_clip_pointsize_test_01	2002Aug22
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249	r400su_clip_pointttest_01	2002Aug22
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250	r400su_clip_pointttest_02	2002Aug22
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251	r400su_clip_pointttest_03	2002Aug22
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252	r400su_clip_pointttest_04	2002Aug22
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253	r400su_clip_polymode_test_01	2002Aug22
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254	r400su_clipline_01	2002Aug22
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255	r400su_clippoint_01	2002Aug22
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256	r400su_clipvertextsortingfunctional_01	2002Aug22
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257	r400su_cullingfunctional_01	2002Aug22
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258	r400su_degentri_test_01	2002Aug22
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259	r400su_degentri_test_02	2002Aug22
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260	r400su_degentri_test_03	2002Aug22

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261	r400su_edge_flag_01	2002Aug22
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262	r400su_edge_flag_02	2002Aug22
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263	r400su_edgeflags_triangle_01	2002Aug22
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264	r400su_edgeflags_triangle_02	2002Aug22
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265	r400su_geom_sort_01	2002Aug22
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266	r400su_line_end_cap_functional_01	2002Aug22
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267	r400su_line_end_cap_width_functional_02	2002Aug22
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268	r400su_line_test_01	2002Aug22
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269	r400su_line_test_02	2002Aug22
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270	r400su_longstrip_01	2002Aug22
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271	r400su_multi_prim_01	2002Aug22
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273	r400su_parallel_orientation_all_02	2002Aug22
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274	r400su_pc_management_01	2002Aug22
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275	r400su_pc_management_02	2002Aug22
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276	r400su_pc_management_03	2002Aug22
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277	r400su_point_sprite_01	2002Aug22
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278	r400su_point_sprite_02	2002Aug22
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279	r400su_point_sprite_03	2002Aug22
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280	r400su_point_sprite_04	2002Aug22
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281	r400su_point_sprite_05	2002Aug22
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282	r400su_point_sprite_06	2002Aug22
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283	r400su_point_wl6_h1_functional_01	2002Aug22
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284	r400su_point_wl_h16_functional_01	2002Aug22

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285	r400su_pointsizepresent_01	2002Aug22
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286	r400su_pointsizepresent_02	2002Aug22
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287	r400su_polymode_lines_degen_triangle_01	2002Aug22
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288	r400su_polymode_lines_degen_triangle_02	2002Aug22
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289	r400su_polymode_lines_zero_area_triangle_01	2002Aug22
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290	r400su_polymode_lines_zero_area_triangle_02	2002Aug22
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291	r400su_polymode_points_degen_triangle_01	2002Aug22
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292	r400su_polymode_points_degen_triangle_02	2002Aug22
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293	r400su_polymode_points_zero_area_triangle_01	2002Aug22
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294	r400su_polymode_points_zero_area_triangle_02	2002Aug22
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295	r400su_polymode_rectangle_01	2002Aug22
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296	r400su_polymode_zero_area_triangle_01	2002Aug22
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297	r400su_polymode_zero_area_triangle_02	2002Aug22
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298	r400su_polymode_zero_area_triangle_03	2002Aug22
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299	r400su_polymode_zero_area_triangle_04	2002Aug22
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300	r400su_polymodeculling_01	2002Aug22
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301	r400su_polymodefunctional_01	2002Aug22
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306	r400su_provoking_vtx_line_01	2002Aug22
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307	r400su_provoking_vtx_point_01	2002Aug22
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308	r400su_provoking_vtx_polymode_rectangle_01	2002Aug22

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309	r400su_provoking_vtx_rectangle_01	2002Aug22
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310	r400su_provoking_vtx_triangle_01	2002Aug22
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311	r400su_rand_line_01	2002Aug22
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312	r400su_rand_point_01	2002Aug22
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313	r400su_rand_tri_01	2002Aug22
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314	r400su_rectangle_list_01	2002Aug22
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315	r400su_sliver_01	2002Aug22
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316	r400su_triarea_test_01	2002Aug22
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317	r400su_triarea_test_02	2002Aug22
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318	r400su_triarea_test_03	2002Aug22
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319	r400su_triarea_test_04	2002Aug22
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320	r400su_vertexsortingfunctional_01	2002Aug22
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321	r400vgt_auto_index_line_list_01	2002Aug22
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322	r400vgt_auto_index_line_loop_01	2002Aug22
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323	r400vgt_auto_index_line_strip_01	2002Aug22
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324	r400vgt_auto_index_points_01	2002Aug22
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327	r400vgt_auto_index_quad_list_01	2002Aug22
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331	r400vgt_auto_index_tri_list_01	2002Aug22
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332	r400vgt_auto_index_tri_strip_01	2002Aug22

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333 r400vgt_auto_index_tri_wflags_01 2002Aug22
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334 r400vgt_dma_engine_01 2002Aug22
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335 r400vgt_dma_engine_02 2002Aug22
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336 r400vgt_dma_engine_03 2002Aug22
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337 r400vgt_dma_engine_04 2002Aug22
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338 r400vgt_dma_engine_05 2002Aug22
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339 r400vgt_dma_engine_06 2002Aug22
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340 r400vgt_dma_engine_07 2002Aug22
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341 r400vgt_dma_engine_08 2002Aug22
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342 r400vgt_dma_engine_09 2002Aug22
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343 r400vgt_dma_engine_10 2002Aug22
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344 r400vgt_dma_index_line_list_01 2002Aug22
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345 r400vgt_dma_index_line_loop_01 2002Aug22
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346 r400vgt_dma_index_line_strip_01 2002Aug22
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347 r400vgt_dma_index_points_01 2002Aug22
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348 r400vgt_dma_index_polygon_01 2002Aug22
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349 r400vgt_dma_index_primitives_01 2002Aug22
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351 r400vgt_dma_index_quad_strip_01 2002Aug22
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352 r400vgt_dma_index_rectangle_list_01 2002Aug22
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354 r400vgt_dma_index_tri_list_01 2002Aug22
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355 r400vgt_dma_index_tri_strip_01 2002Aug22
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356 r400vgt_dma_index_tri_wflags_01 2002Aug22

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358	r400vgt_dma_swap_idx32_01	2002Aug22
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359	r400vgt_edgeflags_polygon_01	2002Aug22
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360	r400vgt_edgeflags_quad_list_01	2002Aug22
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361	r400vgt_edgeflags_quad_strip_01	2002Aug22
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362	r400vgt_edgeflags_triangle_fan_01	2002Aug22
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363	r400vgt_edgeflags_triangle_list_01	2002Aug22
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364	r400vgt_edgeflags_triangle_strip_01	2002Aug22
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365	r400vgt_edgeflags_triangle_wflags_01	2002Aug22
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366	r400vgt_ext2int_index_line_list_01	2002Aug22
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367	r400vgt_ext2int_index_line_loop_01	2002Aug22
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370	r400vgt_ext2int_index_polygon_01	2002Aug22
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371	r400vgt_ext2int_index_quad_list_01	2002Aug22
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372	r400vgt_ext2int_index_quad_strip_01	2002Aug22
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373	r400vgt_ext2int_index_triangle_fan_01	2002Aug22
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374	r400vgt_ext2int_index_triangle_list_01	2002Aug22
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375	r400vgt_ext2int_index_triangle_strip_01	2002Aug22
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376	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug22
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377	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug22
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378	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug22
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379	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug22
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380	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug22

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381	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug22
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383	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug22
PASS		
384	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug22
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386	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug22
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387	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug22
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388	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug22
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389	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug22
PASS		
390	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug22
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391	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug22
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392	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug22
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INCOMPLETE		
394	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug22
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395	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug22
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397	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug22
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399	r400vgt_hos_auto_index_quad_list_01	2002Aug22
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402	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug22
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407	r400vgt_immed_index_polygon_01	2002Aug22
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415	r400vgt_immed_index_tri_wflags_01	2002Aug22
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417	r400vgt_index_dealloc_points_01	2002Aug22
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418	r400vgt_index_dealloc_triangle_list_01	2002Aug22
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419	r400vgt_index_min_max_01	2002Aug22
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420	r400vgt_index_min_max_02	2002Aug22
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424	r400vgt_index_size_01	2002Aug22
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425	r400vgt_index_source_switch_01	2002Aug22
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428	r400vgt_line_strip_01	2002Aug22

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444	r400vgt_provoking_vtx_all_01	2002Aug22
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445	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug22
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446	r400vgt_quad_list_01	2002Aug22
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447	r400vgt_quad_strip_01	2002Aug22
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448	r400vgt_rectangle_list_01	2002Aug22
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450	r400vgt_reuse_depth_point_list_01	2002Aug22
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451	r400vgt_reuse_depth_triangle_list_01	2002Aug22
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453 r400vgt_reuse_index_point_list_01 2002Aug22
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454 r400vgt_reuse_index_triangle_list_01 2002Aug22
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455 r400vgt_reuse_index_triangle_list_02 2002Aug22
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456 r400vgt_reuse_index_triangle_list_03 2002Aug22
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457 r400vgt_triangle_fan_01 2002Aug22
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458 r400vgt_triangle_list_01 2002Aug22
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459 r400vgt_triangle_strip_01 2002Aug22
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460 r400vgt_triangle_wflags_01 2002Aug22
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461 r400vgt_vtx_vector_packing_01 2002Aug22
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462 r400vte_coverage_01 2002Aug22
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463 r400vte_simple_point_01 2002Aug22
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464 r400vte_simple_triangle_01 2002Aug22
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465 r400vte_w0_fmt_01 2002Aug22
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466 r400vte_w0_fmt_03 2002Aug22
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467 r400vte_xy_fmt_01 2002Aug22
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468 r400vte_xyz_scale_01 2002Aug22
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469 r400vte_xyz_scale_02 2002Aug22
PASS
470 r400vte_z_fmt_01 2002Aug22
PASS

=====
Tests_Run_count = 470 Pass_count = 450 Fail_count = 0 Incomplete_count = 20
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep 23  7:53:46 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             170         170         153           90
              SC              274         274         274          100
              CL              115         115         115          100
              VTE              10          10          10          100
              SPECIALTY         6           6           6           100
              SU               85          85          84           98
              SANITY            2           2           2           100
+++++
              TOTAL            674         674         656           97
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_1st_tri_june15		2002Aug20
INCOMPLETE			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug20
INCOMPLETE			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug20
INCOMPLETE			
4	primlib_parameterized_simple_triangle		2002Aug20
INCOMPLETE			
5	primlib_template_simple_triangle		2002Aug20
INCOMPLETE			
6	primlib_tex_tri		2002Aug20
INCOMPLETE			
7	r400cl_clip_10_verts_01		2002Aug20
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8	r400cl_clip_disable_01		2002Aug20
INCOMPLETE			
9	r400cl_clip_space_dx_ogl_01		2002Aug20
INCOMPLETE			
10	r400cl_clip_ucp_6bits_01		2002Aug20
INCOMPLETE			
11	r400cl_cull_only_ena_01		2002Aug20
INCOMPLETE			
12	r400cl_edgeflags_01		2002Aug20
INCOMPLETE			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug20
INCOMPLETE			
14	r400cl_edgeflags_frustum_far_01		2002Aug20
INCOMPLETE			
15	r400cl_edgeflags_frustum_left_01		2002Aug20
INCOMPLETE			
16	r400cl_edgeflags_frustum_near_01		2002Aug20
INCOMPLETE			
17	r400cl_edgeflags_frustum_right_01		2002Aug20
INCOMPLETE			
18	r400cl_edgeflags_frustum_top_01		2002Aug20
INCOMPLETE			
19	r400cl_edgeflags_gband_01		2002Aug20
INCOMPLETE			
20	r400cl_edgeflags_gband_bottom_01		2002Aug20

INCOMPLETE
21 r400cl_edgeflags_gband_left_01 2002Aug20
INCOMPLETE
22 r400cl_edgeflags_gband_right_01 2002Aug20
INCOMPLETE
23 r400cl_edgeflags_gband_top_01 2002Aug20
INCOMPLETE
24 r400cl_frustum_01 2002Aug20
INCOMPLETE
25 r400cl_frustum_02 2002Aug20
INCOMPLETE
26 r400cl_frustum_03 2002Aug20
INCOMPLETE
27 r400cl_frustum_04 2002Aug20
INCOMPLETE
28 r400cl_frustum_05 2002Aug20
INCOMPLETE
29 r400cl_frustum_06 2002Aug20
INCOMPLETE
30 r400cl_frustum_07 2002Aug20
INCOMPLETE
31 r400cl_frustum_08 2002Aug20
INCOMPLETE
32 r400cl_frustum_09 2002Aug20
INCOMPLETE
33 r400cl_frustum_10 2002Aug20
INCOMPLETE
34 r400cl_frustum_11 2002Aug20
INCOMPLETE
35 r400cl_frustum_12 2002Aug20
INCOMPLETE
36 r400cl_frustum_13 2002Aug20
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37 r400cl_frustum_14 2002Aug20
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38 r400cl_frustum_15 2002Aug20
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39 r400cl_frustum_16 2002Aug20
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40 r400cl_frustum_17 2002Aug20
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41 r400cl_frustum_18 2002Aug20
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42 r400cl_frustum_19 2002Aug20
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43 r400cl_frustum_20 2002Aug20
INCOMPLETE
44 r400cl_frustum_21 2002Aug20

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50	r400cl_frustum_27	2002Aug20
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51	r400cl_frustum_28	2002Aug20
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52	r400cl_frustum_lines_01	2002Aug20
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53	r400cl_frustum_lines_02	2002Aug20
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54	r400cl_frustum_lines_03	2002Aug20
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55	r400cl_frustum_lines_04	2002Aug20
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56	r400cl_frustum_lines_05	2002Aug20
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73	r400cl_frustum_lines_22	2002Aug20
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74	r400cl_frustum_lines_23	2002Aug20
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75	r400cl_frustum_lines_24	2002Aug20
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77	r400cl_gband_tcl_01	2002Aug20
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79	r400cl_ucp_combos_01	2002Aug20
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80	r400cl_ucp_combos_02	2002Aug20
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81	r400cl_ucp_combos_03	2002Aug20
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82	r400cl_ucp_combos_04	2002Aug20
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83	r400cl_vertex_reuse_clip_01	2002Aug20
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84	r400cl_vtx_kill_01	2002Aug20
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85	r400cl_vtx_kill_02	2002Aug20
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88	r400sanity_vfd_texture_sample_01	2002Aug20
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92	r400sc_clip_rect_03	2002Aug20

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93	r400sc_clip_rect_04	2002Aug20
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97	r400sc_diamond_exit_04	2002Aug20
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106	r400sc_jss_2x1_02	2002Aug20
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123	r400sc_jss_4x1_02	2002Aug20
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124	r400sc_jss_4x2_01	2002Aug20
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134	r400sc_line_dx10_ge_0_01	2002Aug20
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148	r400sc_line_stipple_08	2002Aug20
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150	r400sc_line_strip_stipple_01	2002Aug20
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178	r400sc_prsp_byc_intrp_ref_pix_07	2002Aug20
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179	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug20
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184	r400sc_raster_fill_rule_05	2002Aug20
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212	r400sc_tri_walk_start_vertex_14	2002Aug20

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214	r400sc_tri_walk_start_vertex_16	2002Aug20
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219	r400sc_zbuffer_rectangle_list_01	2002Aug20
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220	r400sc_zbuffer_rectangle_list_02	2002Aug20
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221	r400sc_zbuffer_triangle_list_01	2002Aug20
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223	r400su_baryc_test_01	2002Aug20
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225	r400su_baryc_test_03	2002Aug20
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226	r400su_baryc_test_04	2002Aug20
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227	r400su_clip_edgeflag_polymode_01	2002Aug20
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228	r400su_clip_pointsize_test_01	2002Aug20
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229	r400su_clip_pointtest_01	2002Aug20
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232	r400su_clip_pointtest_04	2002Aug20
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233	r400su_clip_polymode_test_01	2002Aug20
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234	r400su_clipline_01	2002Aug20
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235	r400su_clippoint_01	2002Aug20
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236	r400su_clipvertexsortingfunctional_01	2002Aug20

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237	r400su_cullingfunctional_01	2002Aug20
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238	r400su_degentri_test_01	2002Aug20
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239	r400su_degentri_test_02	2002Aug20
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244	r400su_edgeflags_triangle_02	2002Aug20
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249	r400su_line_test_02	2002Aug20
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253	r400su_parallel_orientation_all_02	2002Aug20
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260	r400su_point_wl6_hl_functional_01	2002Aug20

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261 r400su_point_wl_h16_functional_01	2002Aug20
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262 r400su_pointsizepresent_01	2002Aug20
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263 r400su_pointsizepresent_02	2002Aug20
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264 r400su_polymode_rectangle_01	2002Aug20
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265 r400su_polymodeculling_01	2002Aug20
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266 r400su_polymodefunctional_01	2002Aug20
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267 r400su_provoking_vtx_edgeflags_triangle_01	2002Aug20
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268 r400su_provoking_vtx_edgeflags_triangle_02	2002Aug20
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269 r400su_provoking_vtx_edgeflags_triangle_03	2002Aug20
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270 r400su_provoking_vtx_edgeflags_triangle_04	2002Aug20
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272 r400su_provoking_vtx_point_01	2002Aug20
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273 r400su_provoking_vtx_polymode_rectangle_01	2002Aug20
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274 r400su_provoking_vtx_rectangle_01	2002Aug20
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275 r400su_provoking_vtx_triangle_01	2002Aug20
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277 r400su_rand_point_01	2002Aug20
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278 r400su_rand_tri_01	2002Aug20
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279 r400su_rectangle_list_01	2002Aug20
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280 r400su_sliver_01	2002Aug20
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281 r400su_triarea_test_01	2002Aug20
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282 r400su_triarea_test_02	2002Aug20
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283 r400su_triarea_test_03	2002Aug20
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284 r400su_triarea_test_04	2002Aug20

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288	r400vgt_auto_index_line_strip_01	2002Aug20
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289	r400vgt_auto_index_points_01	2002Aug20
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290	r400vgt_auto_index_polygon_01	2002Aug20
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291	r400vgt_auto_index_primitives_01	2002Aug20
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292	r400vgt_auto_index_quad_list_01	2002Aug20
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293	r400vgt_auto_index_quad_strip_01	2002Aug20
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294	r400vgt_auto_index_rectangle_list_01	2002Aug20
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295	r400vgt_auto_index_tri_fan_01	2002Aug20
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296	r400vgt_auto_index_tri_list_01	2002Aug20
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297	r400vgt_auto_index_tri_strip_01	2002Aug20
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298	r400vgt_auto_index_tri_wflags_01	2002Aug20
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301	r400vgt_dma_engine_03	2002Aug20
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302	r400vgt_dma_engine_04	2002Aug20
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303	r400vgt_dma_engine_05	2002Aug20
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304	r400vgt_dma_engine_06	2002Aug20
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305	r400vgt_dma_engine_07	2002Aug20
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306	r400vgt_dma_engine_08	2002Aug20
INCOMPLETE		
307	r400vgt_dma_engine_09	2002Aug20
INCOMPLETE		
308	r400vgt_dma_engine_10	2002Aug20

INCOMPLETE	
309 r400vgt_dma_index_line_list_01	2002Aug20
INCOMPLETE	
310 r400vgt_dma_index_line_loop_01	2002Aug20
INCOMPLETE	
311 r400vgt_dma_index_line_strip_01	2002Aug20
INCOMPLETE	
312 r400vgt_dma_index_points_01	2002Aug20
INCOMPLETE	
313 r400vgt_dma_index_polygon_01	2002Aug20
INCOMPLETE	
314 r400vgt_dma_index_primitives_01	2002Aug20
INCOMPLETE	
315 r400vgt_dma_index_quad_list_01	2002Aug20
INCOMPLETE	
316 r400vgt_dma_index_quad_strip_01	2002Aug20
INCOMPLETE	
317 r400vgt_dma_index_rectangle_list_01	2002Aug20
INCOMPLETE	
318 r400vgt_dma_index_tri_fan_01	2002Aug20
INCOMPLETE	
319 r400vgt_dma_index_tri_list_01	2002Aug20
INCOMPLETE	
320 r400vgt_dma_index_tri_strip_01	2002Aug20
INCOMPLETE	
321 r400vgt_dma_index_tri_wflags_01	2002Aug20
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322 r400vgt_dma_swap_indx16_01	2002Aug20
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323 r400vgt_dma_swap_indx32_01	2002Aug20
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324 r400vgt_edgeflags_polygon_01	2002Aug20
INCOMPLETE	
325 r400vgt_edgeflags_quad_list_01	2002Aug20
INCOMPLETE	
326 r400vgt_edgeflags_quad_strip_01	2002Aug20
INCOMPLETE	
327 r400vgt_edgeflags_triangle_fan_01	2002Aug20
INCOMPLETE	
328 r400vgt_edgeflags_triangle_list_01	2002Aug20
INCOMPLETE	
329 r400vgt_edgeflags_triangle_strip_01	2002Aug20
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330 r400vgt_edgeflags_triangle_wflags_01	2002Aug20
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331 r400vgt_ext2int_index_line_list_01	2002Aug20
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332 r400vgt_ext2int_index_line_loop_01	2002Aug20

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333 r400vgt_ext2int_index_line_strip_01	2002Aug20
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334 r400vgt_ext2int_index_points_01	2002Aug20
INCOMPLETE	
335 r400vgt_ext2int_index_polygon_01	2002Aug20
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336 r400vgt_ext2int_index_quad_list_01	2002Aug20
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337 r400vgt_ext2int_index_quad_strip_01	2002Aug20
INCOMPLETE	
338 r400vgt_ext2int_index_triangle_fan_01	2002Aug20
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339 r400vgt_ext2int_index_triangle_list_01	2002Aug20
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340 r400vgt_ext2int_index_triangle_strip_01	2002Aug20
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341 r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug20
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342 r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug20
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343 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug20
INCOMPLETE	
344 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug20
INCOMPLETE	
345 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug20
INCOMPLETE	
346 r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug20
INCOMPLETE	
347 r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug20
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348 r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug20
INCOMPLETE	
349 r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug20
INCOMPLETE	
350 r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug20
INCOMPLETE	
351 r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug20
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352 r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug20
INCOMPLETE	
353 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug20
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354 r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug20
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355 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug20
INCOMPLETE	
356 r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug20

INCOMPLETE
357 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Aug20
INCOMPLETE
358 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Aug20
INCOMPLETE
359 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Aug20
INCOMPLETE
360 r400vgt_hos_PNT_disc_cp_qn_precision_02 2002Aug20
INCOMPLETE
361 r400vgt_hos_PNT_edge_detection_01 2002Aug20
INCOMPLETE
362 r400vgt_hos_PNT_lp_cont_no_projection_01 2002Aug20
INCOMPLETE
363 r400vgt_hos_auto_index_line_list_01 2002Aug20
INCOMPLETE
364 r400vgt_hos_auto_index_quad_list_01 2002Aug20
INCOMPLETE
365 r400vgt_hos_auto_index_triangle_list_01 2002Aug20
INCOMPLETE
366 r400vgt_hos_cubic_pos_pnt_discrete_01 2002Aug20
INCOMPLETE
367 r400vgt_hos_simple_linear_PNT_discrete_01 2002Aug20
INCOMPLETE
368 r400vgt_immed_index_line_list_01 2002Aug20
INCOMPLETE
369 r400vgt_immed_index_line_loop_01 2002Aug20
INCOMPLETE
370 r400vgt_immed_index_line_strip_01 2002Aug20
INCOMPLETE
371 r400vgt_immed_index_points_01 2002Aug20
INCOMPLETE
372 r400vgt_immed_index_polygon_01 2002Aug20
INCOMPLETE
373 r400vgt_immed_index_primitives_01 2002Aug20
INCOMPLETE
374 r400vgt_immed_index_quad_list_01 2002Aug20
INCOMPLETE
375 r400vgt_immed_index_quad_strip_01 2002Aug20
INCOMPLETE
376 r400vgt_immed_index_rectangle_list_01 2002Aug20
INCOMPLETE
377 r400vgt_immed_index_tri_fan_01 2002Aug20
INCOMPLETE
378 r400vgt_immed_index_tri_list_01 2002Aug20
INCOMPLETE
379 r400vgt_immed_index_tri_strip_01 2002Aug20
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380 r400vgt_immed_index_tri_wflags_01 2002Aug20

INCOMPLETE
381 r400vgt_index_dealloc_line_list_01 2002Aug20
INCOMPLETE
382 r400vgt_index_dealloc_points_01 2002Aug20
INCOMPLETE
383 r400vgt_index_dealloc_triangle_list_01 2002Aug20
INCOMPLETE
384 r400vgt_index_min_max_01 2002Aug20
INCOMPLETE
385 r400vgt_index_min_max_02 2002Aug20
INCOMPLETE
386 r400vgt_index_offset_01 2002Aug20
INCOMPLETE
387 r400vgt_index_offset_02 2002Aug20
INCOMPLETE
388 r400vgt_index_offset_03 2002Aug20
INCOMPLETE
389 r400vgt_index_size_01 2002Aug20
INCOMPLETE
390 r400vgt_index_source_switch_01 2002Aug20
INCOMPLETE
391 r400vgt_line_list_01 2002Aug20
INCOMPLETE
392 r400vgt_line_loop_01 2002Aug20
INCOMPLETE
393 r400vgt_line_strip_01 2002Aug20
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394 r400vgt_pass_thru_all_prims_01 2002Aug20
INCOMPLETE
395 r400vgt_point_list_01 2002Aug20
INCOMPLETE
396 r400vgt_polygon_01 2002Aug20
INCOMPLETE
397 r400vgt_provoking_vtx_all_01 2002Aug20
INCOMPLETE
398 r400vgt_provoking_vtx_edgeflags_all_01 2002Aug20
INCOMPLETE
399 r400vgt_quad_list_01 2002Aug20
INCOMPLETE
400 r400vgt_quad_strip_01 2002Aug20
INCOMPLETE
401 r400vgt_rectangle_list_01 2002Aug20
INCOMPLETE
402 r400vgt_reuse_depth_line_list_01 2002Aug20
INCOMPLETE
403 r400vgt_reuse_depth_point_list_01 2002Aug20
INCOMPLETE
404 r400vgt_reuse_depth_triangle_list_01 2002Aug20

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INCOMPLETE
405  r400vgt_reuse_index_line_list_01                2002Aug20
INCOMPLETE
406  r400vgt_reuse_index_point_list_01              2002Aug20
INCOMPLETE
407  r400vgt_reuse_index_triangle_list_01          2002Aug20
INCOMPLETE
408  r400vgt_reuse_index_triangle_list_02          2002Aug20
INCOMPLETE
409  r400vgt_reuse_index_triangle_list_03          2002Aug20
INCOMPLETE
410  r400vgt_triangle_fan_01                      2002Aug20
INCOMPLETE
411  r400vgt_triangle_list_01                     2002Aug20
INCOMPLETE
412  r400vgt_triangle_strip_01                    2002Aug20
INCOMPLETE
413  r400vgt_triangle_wflags_01                   2002Aug20
INCOMPLETE
414  r400vgt_vtx_vector_packing_01                 2002Aug20
INCOMPLETE
415  r400vte_coverage_01                          2002Aug20
INCOMPLETE
416  r400vte_simple_point_01                      2002Aug20
INCOMPLETE
417  r400vte_simple_triangle_01                   2002Aug20
INCOMPLETE
418  r400vte_w0_fmt_01                            2002Aug20
INCOMPLETE
419  r400vte_w0_fmt_03                            2002Aug20
INCOMPLETE
420  r400vte_xy_fmt_01                            2002Aug20
INCOMPLETE
421  r400vte_xyz_scale_01                         2002Aug20
INCOMPLETE
422  r400vte_xyz_scale_02                         2002Aug20
INCOMPLETE
423  r400vte_z_fmt_01                             2002Aug20
INCOMPLETE

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=====
Tests_Run_count = 423  Pass_count = 0  Fail_count = 0  Incomplete_count = 423
=====

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Oct  3 21:09:51 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             180         180         179           99
              SC              299         299         298           99
              CL              115         115         115           100
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              PARAMETER         62          62          62           100
              SU               97          97          97           100
              SANITY            2           2           2           100
+++++
              TOTAL            783         783         781           99
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug29
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug29
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug29
PASS			
4	primlib_parameterized_simple_triangle		2002Aug29
PASS			
5	primlib_template_simple_triangle		2002Aug29
PASS			
6	primlib_tex_tri		2002Aug29
PASS			
7	r400cl_clip_10_verts_01		2002Aug29
PASS			
8	r400cl_clip_disable_01		2002Aug29
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Aug29
PASS			
10	r400cl_clip_ucp_6bits_01		2002Aug29
PASS			
11	r400cl_cull_only_ena_01		2002Aug29
PASS			
12	r400cl_edgeflags_01		2002Aug29
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug29
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Aug29
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Aug29
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Aug29
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Aug29
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Aug29
PASS			
19	r400cl_edgeflags_gband_01		2002Aug29
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Aug29

PASS		
21	r400cl_edgeflags_gband_left_01	2002Aug29
PASS		
22	r400cl_edgeflags_gband_right_01	2002Aug29
PASS		
23	r400cl_edgeflags_gband_top_01	2002Aug29
PASS		
24	r400cl_frustum_01	2002Aug29
PASS		
25	r400cl_frustum_02	2002Aug29
PASS		
26	r400cl_frustum_03	2002Aug29
PASS		
27	r400cl_frustum_04	2002Aug29
PASS		
28	r400cl_frustum_05	2002Aug29
PASS		
29	r400cl_frustum_06	2002Aug29
PASS		
30	r400cl_frustum_07	2002Aug29
PASS		
31	r400cl_frustum_08	2002Aug29
PASS		
32	r400cl_frustum_09	2002Aug29
PASS		
33	r400cl_frustum_10	2002Aug29
PASS		
34	r400cl_frustum_11	2002Aug29
PASS		
35	r400cl_frustum_12	2002Aug29
PASS		
36	r400cl_frustum_13	2002Aug29
PASS		
37	r400cl_frustum_14	2002Aug29
PASS		
38	r400cl_frustum_15	2002Aug29
PASS		
39	r400cl_frustum_16	2002Aug29
PASS		
40	r400cl_frustum_17	2002Aug29
PASS		
41	r400cl_frustum_18	2002Aug29
PASS		
42	r400cl_frustum_19	2002Aug29
PASS		
43	r400cl_frustum_20	2002Aug29
PASS		
44	r400cl_frustum_21	2002Aug29

PASS		
45	r400cl_frustum_22	2002Aug29
PASS		
46	r400cl_frustum_23	2002Aug29
PASS		
47	r400cl_frustum_24	2002Aug29
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48	r400cl_frustum_25	2002Aug29
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49	r400cl_frustum_26	2002Aug29
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50	r400cl_frustum_27	2002Aug29
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51	r400cl_frustum_28	2002Aug29
PASS		
52	r400cl_frustum_lines_01	2002Aug29
PASS		
53	r400cl_frustum_lines_02	2002Aug29
PASS		
54	r400cl_frustum_lines_03	2002Aug29
PASS		
55	r400cl_frustum_lines_04	2002Aug29
PASS		
56	r400cl_frustum_lines_05	2002Aug29
PASS		
57	r400cl_frustum_lines_06	2002Aug29
PASS		
58	r400cl_frustum_lines_07	2002Aug29
PASS		
59	r400cl_frustum_lines_08	2002Aug29
PASS		
60	r400cl_frustum_lines_09	2002Aug29
PASS		
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PASS		
62	r400cl_frustum_lines_11	2002Aug29
PASS		
63	r400cl_frustum_lines_12	2002Aug29
PASS		
64	r400cl_frustum_lines_13	2002Aug29
PASS		
65	r400cl_frustum_lines_14	2002Aug29
PASS		
66	r400cl_frustum_lines_15	2002Aug29
PASS		
67	r400cl_frustum_lines_16	2002Aug29
PASS		
68	r400cl_frustum_lines_17	2002Aug29

PASS		
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PASS		
70	r400cl_frustum_lines_19	2002Aug29
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71	r400cl_frustum_lines_20	2002Aug29
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73	r400cl_frustum_lines_22	2002Aug29
PASS		
74	r400cl_frustum_lines_23	2002Aug29
PASS		
75	r400cl_frustum_lines_24	2002Aug29
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76	r400cl_gband_04	2002Aug29
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77	r400cl_gband_tcl_01	2002Aug29
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78	r400cl_pointlist_vertex_state_ucp_01	2002Aug29
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79	r400cl_simple_triangle_01	2002Aug29
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80	r400cl_ucp_combos_01	2002Aug29
PASS		
81	r400cl_ucp_combos_02	2002Aug29
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82	r400cl_ucp_combos_03	2002Aug29
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83	r400cl_ucp_combos_04	2002Aug29
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84	r400cl_ucp_combos_05	2002Aug29
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85	r400cl_ucp_combos_06	2002Aug29
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86	r400cl_ucp_combos_07	2002Aug29
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87	r400cl_ucp_combos_08	2002Aug29
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88	r400cl_ucp_combos_09	2002Aug29
PASS		
89	r400cl_ucp_combos_10	2002Aug29
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91	r400cl_ucp_combos_12	2002Aug29
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92	r400cl_ucp_combos_13	2002Aug29

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105	r400cl_ucp_combos_26	2002Aug29
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107	r400cl_ucp_combos_28	2002Aug29
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108	r400cl_ucp_combos_29	2002Aug29
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109	r400cl_ucp_combos_30	2002Aug29
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110	r400cl_ucp_pointlist_01	2002Aug29
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111	r400cl_vertex_reuse_clip_01	2002Aug29
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112	r400cl_vtx_kill_01	2002Aug29
PASS		
113	r400cl_vtx_kill_02	2002Aug29
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114	r400cl_w_eq_0	2002Aug29
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115	r400sanity_persp_projection_01	2002Aug29
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116	r400sanity_vfd_texture_sample_01	2002Aug29

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117	r400sc_bres_cntl_01	2002Aug29
PASS		
118	r400sc_bres_cntl_02	2002Aug29
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119	r400sc_bres_cntl_03	2002Aug29
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120	r400sc_bres_cntl_04	2002Aug29
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122	r400sc_clip_rect_02	2002Aug29
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123	r400sc_clip_rect_03	2002Aug29
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124	r400sc_clip_rect_04	2002Aug29
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125	r400sc_clip_rect_fc_01	2002Aug29
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126	r400sc_diamond_exit_01	2002Aug29
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127	r400sc_diamond_exit_02	2002Aug29
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128	r400sc_diamond_exit_03	2002Aug29
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130	r400sc_diamond_exit_05	2002Aug29
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132	r400sc_jss_1x2_02	2002Aug29
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133	r400sc_jss_1x3_01	2002Aug29
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134	r400sc_jss_1x3_02	2002Aug29
PASS		
135	r400sc_jss_1x4_01	2002Aug29
PASS		
136	r400sc_jss_1x4_02	2002Aug29
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137	r400sc_jss_2x1_01	2002Aug29
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138	r400sc_jss_2x1_02	2002Aug29
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139	r400sc_jss_2x2_01	2002Aug29
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140	r400sc_jss_2x2_02	2002Aug29

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164	r400sc_jss_4x4_aa_mask_02	2002Aug29

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165 r400sc_jss_4x4_fc_01 2002Aug29
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166 r400sc_jss_4x4_max_dist_01 2002Aug29
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167 r400sc_line_dx10_eq_0_01 2002Aug29
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168 r400sc_line_dx10_ge_0_01 2002Aug29
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170 r400sc_line_dy10_eq_0_01 2002Aug29
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171 r400sc_line_dy10_ge_0_01 2002Aug29
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172 r400sc_line_dy10_lt_0_01 2002Aug29
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173 r400sc_line_expand_width_msa_8_01 2002Aug29
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174 r400sc_line_expand_width_msa_8_02 2002Aug29
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175 r400sc_line_expand_width_msa_8_03 2002Aug29
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176 r400sc_line_jss_3x4_01 2002Aug29
PASS
177 r400sc_line_list_01 2002Aug29
PASS
178 r400sc_line_list_02 2002Aug29
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179 r400sc_line_list_concentric_circle_01 2002Aug29
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180 r400sc_line_list_concentric_circle_02 2002Aug29
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181 r400sc_line_list_concentric_circle_03 2002Aug29
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182 r400sc_line_list_textured_01 2002Aug29
PASS
183 r400sc_line_list_verify_st_01 2002Aug29
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184 r400sc_line_msa_8_01 2002Aug29
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185 r400sc_line_stipple_01 2002Aug29
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186 r400sc_line_stipple_02 2002Aug29
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187 r400sc_line_stipple_03 2002Aug29
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188 r400sc_line_stipple_04 2002Aug29

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193	r400sc_line_stipple_09	2002Aug29
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194	r400sc_line_stipple_fc_08	2002Aug29
PASS		
195	r400sc_line_strip_stipple_01	2002Aug29
PASS		
196	r400sc_msaa_1_01	2002Aug29
PASS		
197	r400sc_msaa_4_01	2002Aug29
PASS		
198	r400sc_msaa_6_01	2002Aug29
PASS		
199	r400sc_msaa_8_01	2002Aug29
PASS		
200	r400sc_msaa_8_aa_mask_01	2002Aug29
PASS		
201	r400sc_msaa_8_aa_mask_02	2002Aug29
PASS		
202	r400sc_msaa_8_aa_mask_fc_02	2002Aug29
PASS		
203	r400sc_null_triangles_01	2002Aug29
PASS		
204	r400sc_null_triangles_fc_01	2002Aug29
PASS		
205	r400sc_point_jss_3x4_01	2002Aug29
PASS		
206	r400sc_point_list_01	2002Aug29
PASS		
207	r400sc_point_list_02	2002Aug29
PASS		
208	r400sc_point_list_03	2002Aug29
PASS		
209	r400sc_point_list_04	2002Aug29
PASS		
210	r400sc_point_list_05	2002Aug29
PASS		
211	r400sc_point_list_06	2002Aug28
INCOMPLETE		
212	r400sc_point_list_07	2002Aug29

PASS		
213	r400sc_point_list_08	2002Aug29
PASS		
214	r400sc_point_list_09	2002Aug29
PASS		
215	r400sc_point_msa_8_01	2002Aug29
PASS		
216	r400sc_poly_offset_01	2002Aug29
PASS		
217	r400sc_poly_offset_02	2002Aug29
PASS		
218	r400sc_poly_offset_fc_01	2002Aug29
PASS		
219	r400sc_polygon_stipple_01	2002Aug29
PASS		
220	r400sc_prsp_byc_intrp_ref_pix_01	2002Aug29
PASS		
221	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug29
PASS		
222	r400sc_prsp_byc_intrp_ref_pix_03	2002Aug29
PASS		
223	r400sc_prsp_byc_intrp_ref_pix_04	2002Aug29
PASS		
224	r400sc_prsp_byc_intrp_ref_pix_05	2002Aug29
PASS		
225	r400sc_prsp_byc_intrp_ref_pix_06	2002Aug29
PASS		
226	r400sc_prsp_byc_intrp_ref_pix_07	2002Aug29
PASS		
227	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug29
PASS		
228	r400sc_raster_fill_rule_01	2002Aug29
PASS		
229	r400sc_raster_fill_rule_02	2002Aug29
PASS		
230	r400sc_raster_fill_rule_03	2002Aug29
PASS		
231	r400sc_raster_fill_rule_04	2002Aug29
PASS		
232	r400sc_raster_fill_rule_05	2002Aug29
PASS		
233	r400sc_raster_fill_rule_fc_01	2002Aug29
PASS		
234	r400sc_rectangle_list_01	2002Aug29
PASS		
235	r400sc_rectangle_list_02	2002Aug29
PASS		
236	r400sc_rectangle_list_03	2002Aug29

PASS		
237	r400sc_rectangle_list_04	2002Aug29
PASS		
238	r400sc_rectangle_list_05	2002Aug29
PASS		
239	r400sc_rectangle_list_06	2002Aug29
PASS		
240	r400sc_rectangle_list_07	2002Aug29
PASS		
241	r400sc_rectangle_list_08	2002Aug29
PASS		
242	r400sc_scissor_rect_01	2002Aug29
PASS		
243	r400sc_scissor_rect_02	2002Aug29
PASS		
244	r400sc_scissor_rect_03	2002Aug29
PASS		
245	r400sc_scissor_rect_04	2002Aug29
PASS		
246	r400sc_scissor_rect_05	2002Aug29
PASS		
247	r400sc_scissor_rect_fc_01	2002Aug29
PASS		
248	r400sc_set_state_01	2002Aug29
PASS		
249	r400sc_tri_walk_start_vertex_01	2002Aug29
PASS		
250	r400sc_tri_walk_start_vertex_02	2002Aug29
PASS		
251	r400sc_tri_walk_start_vertex_03	2002Aug29
PASS		
252	r400sc_tri_walk_start_vertex_04	2002Aug29
PASS		
253	r400sc_tri_walk_start_vertex_05	2002Aug29
PASS		
254	r400sc_tri_walk_start_vertex_06	2002Aug29
PASS		
255	r400sc_tri_walk_start_vertex_07	2002Aug29
PASS		
256	r400sc_tri_walk_start_vertex_08	2002Aug29
PASS		
257	r400sc_tri_walk_start_vertex_09	2002Aug29
PASS		
258	r400sc_tri_walk_start_vertex_10	2002Aug29
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259	r400sc_tri_walk_start_vertex_11	2002Aug29
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260	r400sc_tri_walk_start_vertex_12	2002Aug29

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261	r400sc_tri_walk_start_vertex_13	2002Aug29
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262	r400sc_tri_walk_start_vertex_14	2002Aug29
PASS		
263	r400sc_tri_walk_start_vertex_15	2002Aug29
PASS		
264	r400sc_tri_walk_start_vertex_16	2002Aug29
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265	r400sc_triangle_stipple_01	2002Aug29
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266	r400sc_window_offset_01	2002Aug29
PASS		
267	r400sc_window_offset_fc_01	2002Aug29
PASS		
268	r400sc_zbuffer_line_list_01	2002Aug29
PASS		
269	r400sc_zbuffer_point_list_01	2002Aug29
PASS		
270	r400sc_zbuffer_rectangle_list_01	2002Aug29
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271	r400sc_zbuffer_rectangle_list_02	2002Aug29
PASS		
272	r400sc_zbuffer_rectangle_list_fc_02	2002Aug29
PASS		
273	r400sc_zbuffer_triangle_list_01	2002Aug29
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274	r400su_4tri_text_offscreen_01	2002Aug29
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275	r400su_baryc_test_01	2002Aug29
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276	r400su_baryc_test_02	2002Aug29
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277	r400su_baryc_test_03	2002Aug29
PASS		
278	r400su_baryc_test_04	2002Aug29
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279	r400su_clip_edgeflag_polymode_01	2002Aug29
PASS		
280	r400su_clip_pointsize_test_01	2002Aug29
PASS		
281	r400su_clip_pointttest_01	2002Aug29
PASS		
282	r400su_clip_pointttest_02	2002Aug29
PASS		
283	r400su_clip_pointttest_03	2002Aug29
PASS		
284	r400su_clip_pointttest_04	2002Aug29

PASS		
285	r400su_clip_polymode_test_01	2002Aug29
PASS		
286	r400su_clipline_01	2002Aug29
PASS		
287	r400su_clippoint_01	2002Aug29
PASS		
288	r400su_clipvertexsortingfunctional_01	2002Aug29
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289	r400su_cullingfunctional_01	2002Aug29
PASS		
290	r400su_degentri_test_01	2002Aug29
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291	r400su_degentri_test_02	2002Aug29
PASS		
292	r400su_degentri_test_03	2002Aug29
PASS		
293	r400su_edge_flag_01	2002Aug29
PASS		
294	r400su_edge_flag_02	2002Aug29
PASS		
295	r400su_edgeflags_triangle_01	2002Aug29
PASS		
296	r400su_edgeflags_triangle_02	2002Aug29
PASS		
297	r400su_geom_sort_01	2002Aug29
PASS		
298	r400su_line_end_cap_functional_01	2002Aug29
PASS		
299	r400su_line_end_cap_width_functional_02	2002Aug29
PASS		
300	r400su_line_test_01	2002Aug29
PASS		
301	r400su_line_test_02	2002Aug29
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302	r400su_longstrip_01	2002Aug29
PASS		
303	r400su_multi_prim_01	2002Aug29
PASS		
304	r400su_parallel_orientation_all_01	2002Aug29
PASS		
305	r400su_parallel_orientation_all_02	2002Aug29
PASS		
306	r400su_pc_management_01	2002Aug29
PASS		
307	r400su_pc_management_02	2002Aug29
PASS		
308	r400su_pc_management_03	2002Aug29

PASS		
309	r400su_point_sprite_01	2002Aug29
PASS		
310	r400su_point_sprite_02	2002Aug29
PASS		
311	r400su_point_sprite_03	2002Aug29
PASS		
312	r400su_point_sprite_04	2002Aug29
PASS		
313	r400su_point_sprite_05	2002Aug29
PASS		
314	r400su_point_sprite_06	2002Aug29
PASS		
315	r400su_point_sprite_07	2002Aug29
PASS		
316	r400su_point_sprite_08	2002Aug29
PASS		
317	r400su_point_w16_h1_functional_01	2002Aug29
PASS		
318	r400su_point_w1_h16_functional_01	2002Aug29
PASS		
319	r400su_pointsizepresent_01	2002Aug29
PASS		
320	r400su_pointsizepresent_02	2002Aug29
PASS		
321	r400su_polymode_lines_degen_triangle_01	2002Aug29
PASS		
322	r400su_polymode_lines_degen_triangle_02	2002Aug29
PASS		
323	r400su_polymode_lines_zero_area_triangle_01	2002Aug29
PASS		
324	r400su_polymode_lines_zero_area_triangle_02	2002Aug29
PASS		
325	r400su_polymode_points_degen_triangle_01	2002Aug29
PASS		
326	r400su_polymode_points_degen_triangle_02	2002Aug29
PASS		
327	r400su_polymode_points_zero_area_triangle_01	2002Aug29
PASS		
328	r400su_polymode_points_zero_area_triangle_02	2002Aug29
PASS		
329	r400su_polymode_rectangle_01	2002Aug29
PASS		
330	r400su_polymode_zero_area_triangle_01	2002Aug29
PASS		
331	r400su_polymode_zero_area_triangle_02	2002Aug29
PASS		
332	r400su_polymode_zero_area_triangle_03	2002Aug29

PASS		
333	r400su_polymode_zero_area_triangle_04	2002Aug29
PASS		
334	r400su_polymodeculling_01	2002Aug29
PASS		
335	r400su_polymodefunctional_01	2002Aug29
PASS		
336	r400su_provoking_vtx_edgeflags_triangle_01	2002Aug29
PASS		
337	r400su_provoking_vtx_edgeflags_triangle_02	2002Aug29
PASS		
338	r400su_provoking_vtx_edgeflags_triangle_03	2002Aug29
PASS		
339	r400su_provoking_vtx_edgeflags_triangle_04	2002Aug29
PASS		
340	r400su_provoking_vtx_line_01	2002Aug29
PASS		
341	r400su_provoking_vtx_point_01	2002Aug29
PASS		
342	r400su_provoking_vtx_polymode_rectangle_01	2002Aug29
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343	r400su_provoking_vtx_rectangle_01	2002Aug29
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344	r400su_provoking_vtx_triangle_01	2002Aug29
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345	r400su_rand_line_01	2002Aug29
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346	r400su_rand_point_01	2002Aug29
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347	r400su_rand_tri_01	2002Aug29
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348	r400su_rectangle_list_01	2002Aug29
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349	r400su_sliver_01	2002Aug29
PASS		
350	r400su_triarea_test_01	2002Aug29
PASS		
351	r400su_triarea_test_02	2002Aug29
PASS		
352	r400su_triarea_test_03	2002Aug29
PASS		
353	r400su_triarea_test_04	2002Aug29
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354	r400su_vertexsortingfunctional_01	2002Aug29
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355	r400vgt_auto_index_line_list_01	2002Aug29
PASS		
356	r400vgt_auto_index_line_loop_01	2002Aug29

PASS		
357	r400vgt_auto_index_line_strip_01	2002Aug29
PASS		
358	r400vgt_auto_index_points_01	2002Aug29
PASS		
359	r400vgt_auto_index_polygon_01	2002Aug29
PASS		
360	r400vgt_auto_index_primitives_01	2002Aug29
PASS		
361	r400vgt_auto_index_quad_list_01	2002Aug29
PASS		
362	r400vgt_auto_index_quad_strip_01	2002Aug29
PASS		
363	r400vgt_auto_index_rectangle_list_01	2002Aug29
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364	r400vgt_auto_index_tri_fan_01	2002Aug29
PASS		
365	r400vgt_auto_index_tri_list_01	2002Aug29
PASS		
366	r400vgt_auto_index_tri_strip_01	2002Aug29
PASS		
367	r400vgt_auto_index_tri_wflags_01	2002Aug29
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368	r400vgt_dma_engine_01	2002Aug29
PASS		
369	r400vgt_dma_engine_02	2002Aug29
PASS		
370	r400vgt_dma_engine_03	2002Aug29
PASS		
371	r400vgt_dma_engine_04	2002Aug29
PASS		
372	r400vgt_dma_engine_05	2002Aug29
PASS		
373	r400vgt_dma_engine_06	2002Aug29
PASS		
374	r400vgt_dma_engine_07	2002Aug29
PASS		
375	r400vgt_dma_engine_08	2002Aug29
PASS		
376	r400vgt_dma_engine_09	2002Aug29
PASS		
377	r400vgt_dma_engine_10	2002Aug29
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378	r400vgt_dma_index_line_list_01	2002Aug29
PASS		
379	r400vgt_dma_index_line_loop_01	2002Aug29
PASS		
380	r400vgt_dma_index_line_strip_01	2002Aug29

PASS		
381	r400vgt_dma_index_points_01	2002Aug29
PASS		
382	r400vgt_dma_index_polygon_01	2002Aug29
PASS		
383	r400vgt_dma_index_primitives_01	2002Aug29
PASS		
384	r400vgt_dma_index_quad_list_01	2002Aug29
PASS		
385	r400vgt_dma_index_quad_strip_01	2002Aug29
PASS		
386	r400vgt_dma_index_rectangle_list_01	2002Aug29
PASS		
387	r400vgt_dma_index_tri_fan_01	2002Aug29
PASS		
388	r400vgt_dma_index_tri_list_01	2002Aug29
PASS		
389	r400vgt_dma_index_tri_strip_01	2002Aug29
PASS		
390	r400vgt_dma_index_tri_wflags_01	2002Aug29
PASS		
391	r400vgt_dma_swap_indx16_01	2002Aug29
PASS		
392	r400vgt_dma_swap_indx32_01	2002Aug29
PASS		
393	r400vgt_edgeflags_polygon_01	2002Aug29
PASS		
394	r400vgt_edgeflags_quad_list_01	2002Aug29
PASS		
395	r400vgt_edgeflags_quad_strip_01	2002Aug29
PASS		
396	r400vgt_edgeflags_triangle_fan_01	2002Aug29
PASS		
397	r400vgt_edgeflags_triangle_list_01	2002Aug29
PASS		
398	r400vgt_edgeflags_triangle_strip_01	2002Aug29
PASS		
399	r400vgt_edgeflags_triangle_wflags_01	2002Aug29
PASS		
400	r400vgt_ext2int_index_line_list_01	2002Aug29
PASS		
401	r400vgt_ext2int_index_line_loop_01	2002Aug29
PASS		
402	r400vgt_ext2int_index_line_strip_01	2002Aug29
PASS		
403	r400vgt_ext2int_index_points_01	2002Aug29
PASS		
404	r400vgt_ext2int_index_polygon_01	2002Aug29

PASS
405 r400vgt_ext2int_index_quad_list_01 2002Aug29
PASS
406 r400vgt_ext2int_index_quad_strip_01 2002Aug29
PASS
407 r400vgt_ext2int_index_triangle_fan_01 2002Aug29
PASS
408 r400vgt_ext2int_index_triangle_list_01 2002Aug29
PASS
409 r400vgt_ext2int_index_triangle_strip_01 2002Aug29
PASS
410 r400vgt_hos_PNL_cp_ln_cont_no_projection_01 2002Aug29
PASS
411 r400vgt_hos_PNL_lp_ln_cont_no_projection_01 2002Aug29
PASS
412 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01 2002Aug29
PASS
413 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02 2002Aug29
PASS
414 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01 2002Aug29
PASS
415 r400vgt_hos_PNQ_lp_cont_no_projection_01 2002Aug29
PASS
416 r400vgt_hos_PNT_cont_cp_qn_complex_01 2002Aug29
PASS
417 r400vgt_hos_PNT_cont_cp_qn_precision_01 2002Aug29
PASS
418 r400vgt_hos_PNT_cont_cp_qn_precision_02 2002Aug29
PASS
419 r400vgt_hos_PNT_cp_qn_cont_light_texture_01 2002Aug29
PASS
420 r400vgt_hos_PNT_cp_qn_cont_light_texture_02 2002Aug29
PASS
421 r400vgt_hos_PNT_cp_qn_cont_light_texture_03 2002Aug29
PASS
422 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01 2002Aug29
PASS
423 r400vgt_hos_PNT_cp_qn_cont_no_projection_01 2002Aug29
PASS
424 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01 2002Aug29
PASS
425 r400vgt_hos_PNT_disc_cp_qn_complex_01 2002Aug29
PASS
426 r400vgt_hos_PNT_disc_cp_qn_light_texture_01 2002Aug29
PASS
427 r400vgt_hos_PNT_disc_cp_qn_no_projection_01 2002Aug29
PASS
428 r400vgt_hos_PNT_disc_cp_qn_precision_01 2002Aug29

PASS		
429	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug29
PASS		
430	r400vgt_hos_PNT_edge_detection_01	2002Aug29
PASS		
431	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug29
PASS		
432	r400vgt_hos_auto_index_line_list_01	2002Aug29
PASS		
433	r400vgt_hos_auto_index_quad_list_01	2002Aug29
PASS		
434	r400vgt_hos_auto_index_triangle_list_01	2002Aug29
PASS		
435	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug29
PASS		
436	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug29
PASS		
437	r400vgt_immed_index_line_list_01	2002Aug29
PASS		
438	r400vgt_immed_index_line_loop_01	2002Aug29
PASS		
439	r400vgt_immed_index_line_strip_01	2002Aug29
PASS		
440	r400vgt_immed_index_points_01	2002Aug29
PASS		
441	r400vgt_immed_index_polygon_01	2002Aug29
PASS		
442	r400vgt_immed_index_primitives_01	2002Aug29
PASS		
443	r400vgt_immed_index_quad_list_01	2002Aug29
PASS		
444	r400vgt_immed_index_quad_strip_01	2002Aug29
PASS		
445	r400vgt_immed_index_rectangle_list_01	2002Aug29
PASS		
446	r400vgt_immed_index_tri_fan_01	2002Aug29
PASS		
447	r400vgt_immed_index_tri_list_01	2002Aug29
PASS		
448	r400vgt_immed_index_tri_strip_01	2002Aug29
PASS		
449	r400vgt_immed_index_tri_wflags_01	2002Aug29
PASS		
450	r400vgt_index_dealloc_line_list_01	2002Aug29
PASS		
451	r400vgt_index_dealloc_points_01	2002Aug29
PASS		
452	r400vgt_index_dealloc_triangle_list_01	2002Aug29

PASS		
453	r400vgt_index_min_max_01	2002Aug29
PASS		
454	r400vgt_index_min_max_02	2002Aug29
PASS		
455	r400vgt_index_offset_01	2002Aug29
PASS		
456	r400vgt_index_offset_02	2002Aug29
PASS		
457	r400vgt_index_offset_03	2002Aug29
PASS		
458	r400vgt_index_size_01	2002Aug29
PASS		
459	r400vgt_index_source_switch_01	2002Aug29
PASS		
460	r400vgt_line_list_01	2002Aug29
PASS		
461	r400vgt_line_loop_01	2002Aug29
PASS		
462	r400vgt_line_strip_01	2002Aug29
PASS		
463	r400vgt_multi_context_01	2002Aug29
PASS		
464	r400vgt_multi_context_02	2002Aug29
PASS		
465	r400vgt_multi_context_03	2002Aug29
PASS		
466	r400vgt_multi_context_04	2002Aug29
PASS		
467	r400vgt_multi_context_05	2002Aug29
PASS		
468	r400vgt_multi_context_06	2002Aug29
PASS		
469	r400vgt_multi_context_07	2002Aug29
PASS		
470	r400vgt_multi_context_08	2002Aug29
PASS		
471	r400vgt_multi_context_09	2002Aug29
PASS		
472	r400vgt_multi_context_10	2002Aug29
PASS		
473	r400vgt_multi_context_11	2002Aug29
PASS		
474	r400vgt_multi_context_12	2002Aug29
PASS		
475	r400vgt_pass_thru_all_prims_01	2002Aug29
PASS		
476	r400vgt_point_list_01	2002Aug29

PASS		
477	r400vgt_polygon_01	2002Aug29
PASS		
478	r400vgt_provoking_vtx_all_01	2002Aug29
PASS		
479	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug29
PASS		
480	r400vgt_quad_list_01	2002Aug29
PASS		
481	r400vgt_quad_strip_01	2002Aug29
PASS		
482	r400vgt_rectangle_list_01	2002Aug29
PASS		
483	r400vgt_reuse_depth_line_list_01	2002Aug29
PASS		
484	r400vgt_reuse_depth_point_list_01	2002Aug29
PASS		
485	r400vgt_reuse_depth_triangle_list_01	2002Aug29
PASS		
486	r400vgt_reuse_index_line_list_01	2002Aug29
PASS		
487	r400vgt_reuse_index_point_list_01	2002Aug29
PASS		
488	r400vgt_reuse_index_triangle_list_01	2002Aug29
PASS		
489	r400vgt_reuse_index_triangle_list_02	2002Aug29
PASS		
490	r400vgt_reuse_index_triangle_list_03	2002Aug29
PASS		
491	r400vgt_triangle_fan_01	2002Aug29
PASS		
492	r400vgt_triangle_list_01	2002Aug29
PASS		
493	r400vgt_triangle_strip_01	2002Aug29
PASS		
494	r400vgt_triangle_wflags_01	2002Aug29
PASS		
495	r400vgt_vtx_vector_packing_01	2002Aug29
PASS		
496	r400vte_coverage_01	2002Aug29
PASS		
497	r400vte_simple_point_01	2002Aug29
PASS		
498	r400vte_simple_triangle_01	2002Aug29
PASS		
499	r400vte_w0_fmt_01	2002Aug29
PASS		
500	r400vte_w0_fmt_03	2002Aug29

```
PASS
501 r400vte_xy_fmt_01 2002Aug29
PASS
502 r400vte_xyz_scale_01 2002Aug29
PASS
503 r400vte_xyz_scale_02 2002Aug29
PASS
504 r400vte_z_fmt_01 2002Aug29
PASS
```

```
=====
Tests_Run_count = 504 Pass_count = 503 Fail_count = 0 Incomplete_count = 1
=====
```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Aug 24  3:48:17 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141          100
              SC           154          154          152           98
              CL           81           81           81          100
              VTE           9           9            9          100
SPECIALTY    SU           79           79           79          100
              SANITY       2            2            1           50
+++++
              TOTAL        472          472          469           99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Aug 14 18:06:06 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          124          96
              SC           133          133           1           0
              CL           80           80           0           0
              VTE           9            9            9          100
SPECIALTY    SU           64           64           64          100
              SANITY       2            2            2          100
+++++
              TOTAL        423          423          206          48
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug26
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug26
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug26
PASS			
4	primlib_parameterized_simple_triangle		2002Aug26
PASS			
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354 r400vgt_dma_index_quad_strip_01 2002Aug26
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355 r400vgt_dma_index_rectangle_list_01 2002Aug26
PASS
356 r400vgt_dma_index_tri_fan_01 2002Aug26

PASS		
357	r400vgt_dma_index_tri_list_01	2002Aug26
PASS		
358	r400vgt_dma_index_tri_strip_01	2002Aug26
PASS		
359	r400vgt_dma_index_tri_wflags_01	2002Aug26
PASS		
360	r400vgt_dma_swap_indx16_01	2002Aug26
PASS		
361	r400vgt_dma_swap_indx32_01	2002Aug26
PASS		
362	r400vgt_edgeflags_polygon_01	2002Aug26
PASS		
363	r400vgt_edgeflags_quad_list_01	2002Aug26
PASS		
364	r400vgt_edgeflags_quad_strip_01	2002Aug26
PASS		
365	r400vgt_edgeflags_triangle_fan_01	2002Aug26
PASS		
366	r400vgt_edgeflags_triangle_list_01	2002Aug26
PASS		
367	r400vgt_edgeflags_triangle_strip_01	2002Aug26
PASS		
368	r400vgt_edgeflags_triangle_wflags_01	2002Aug26
PASS		
369	r400vgt_ext2int_index_line_list_01	2002Aug26
PASS		
370	r400vgt_ext2int_index_line_loop_01	2002Aug26
PASS		
371	r400vgt_ext2int_index_line_strip_01	2002Aug26
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372	r400vgt_ext2int_index_points_01	2002Aug26
PASS		
373	r400vgt_ext2int_index_polygon_01	2002Aug26
PASS		
374	r400vgt_ext2int_index_quad_list_01	2002Aug26
PASS		
375	r400vgt_ext2int_index_quad_strip_01	2002Aug26
PASS		
376	r400vgt_ext2int_index_triangle_fan_01	2002Aug26
PASS		
377	r400vgt_ext2int_index_triangle_list_01	2002Aug26
PASS		
378	r400vgt_ext2int_index_triangle_strip_01	2002Aug26
PASS		
379	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug26
PASS		
380	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug26

PASS		
381	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug26
PASS		
382	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug26
PASS		
383	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug26
PASS		
384	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug26
PASS		
385	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug26
PASS		
386	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug26
PASS		
387	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug26
PASS		
388	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug26
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389	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug26
PASS		
390	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug26
PASS		
391	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug26
PASS		
392	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug26
PASS		
393	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug26
PASS		
394	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug26
PASS		
395	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug26
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396	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug26
PASS		
397	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug26
PASS		
398	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug26
PASS		
399	r400vgt_hos_PNT_edge_detection_01	2002Aug26
PASS		
400	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug26
PASS		
401	r400vgt_hos_auto_index_line_list_01	2002Aug26
PASS		
402	r400vgt_hos_auto_index_quad_list_01	2002Aug26
PASS		
403	r400vgt_hos_auto_index_triangle_list_01	2002Aug26
PASS		
404	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug26

PASS		
405	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug26
PASS		
406	r400vgt_immed_index_line_list_01	2002Aug26
PASS		
407	r400vgt_immed_index_line_loop_01	2002Aug26
PASS		
408	r400vgt_immed_index_line_strip_01	2002Aug26
PASS		
409	r400vgt_immed_index_points_01	2002Aug26
PASS		
410	r400vgt_immed_index_polygon_01	2002Aug26
PASS		
411	r400vgt_immed_index_primitives_01	2002Aug26
PASS		
412	r400vgt_immed_index_quad_list_01	2002Aug26
PASS		
413	r400vgt_immed_index_quad_strip_01	2002Aug26
PASS		
414	r400vgt_immed_index_rectangle_list_01	2002Aug26
PASS		
415	r400vgt_immed_index_tri_fan_01	2002Aug26
PASS		
416	r400vgt_immed_index_tri_list_01	2002Aug26
PASS		
417	r400vgt_immed_index_tri_strip_01	2002Aug26
PASS		
418	r400vgt_immed_index_tri_wflags_01	2002Aug26
PASS		
419	r400vgt_index_dealloc_line_list_01	2002Aug26
PASS		
420	r400vgt_index_dealloc_points_01	2002Aug26
PASS		
421	r400vgt_index_dealloc_triangle_list_01	2002Aug26
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422	r400vgt_index_min_max_01	2002Aug26
PASS		
423	r400vgt_index_min_max_02	2002Aug26
PASS		
424	r400vgt_index_offset_01	2002Aug26
PASS		
425	r400vgt_index_offset_02	2002Aug26
PASS		
426	r400vgt_index_offset_03	2002Aug26
PASS		
427	r400vgt_index_size_01	2002Aug26
PASS		
428	r400vgt_index_source_switch_01	2002Aug26

PASS		
429	r400vgt_line_list_01	2002Aug26
PASS		
430	r400vgt_line_loop_01	2002Aug26
PASS		
431	r400vgt_line_strip_01	2002Aug26
PASS		
432	r400vgt_multi_context_01	2002Aug26
PASS		
433	r400vgt_multi_context_02	2002Aug26
PASS		
434	r400vgt_multi_context_03	2002Aug26
PASS		
435	r400vgt_multi_context_04	2002Aug26
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436	r400vgt_multi_context_05	2002Aug26
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437	r400vgt_multi_context_06	2002Aug26
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438	r400vgt_multi_context_07	2002Aug26
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439	r400vgt_multi_context_08	2002Aug26
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440	r400vgt_multi_context_09	2002Aug26
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441	r400vgt_multi_context_10	2002Aug26
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442	r400vgt_multi_context_11	2002Aug26
PASS		
443	r400vgt_multi_context_12	2002Aug26
PASS		
444	r400vgt_pass_thru_all_prims_01	2002Aug26
PASS		
445	r400vgt_point_list_01	2002Aug26
PASS		
446	r400vgt_polygon_01	2002Aug26
PASS		
447	r400vgt_provoking_vtx_all_01	2002Aug26
PASS		
448	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug26
PASS		
449	r400vgt_quad_list_01	2002Aug26
PASS		
450	r400vgt_quad_strip_01	2002Aug26
PASS		
451	r400vgt_rectangle_list_01	2002Aug26
PASS		
452	r400vgt_reuse_depth_line_list_01	2002Aug26

PASS
453 r400vgt_reuse_depth_point_list_01 2002Aug26
PASS
454 r400vgt_reuse_depth_triangle_list_01 2002Aug26
PASS
455 r400vgt_reuse_index_line_list_01 2002Aug26
PASS
456 r400vgt_reuse_index_point_list_01 2002Aug26
PASS
457 r400vgt_reuse_index_triangle_list_01 2002Aug26
PASS
458 r400vgt_reuse_index_triangle_list_02 2002Aug26
PASS
459 r400vgt_reuse_index_triangle_list_03 2002Aug26
PASS
460 r400vgt_triangle_fan_01 2002Aug26
PASS
461 r400vgt_triangle_list_01 2002Aug26
PASS
462 r400vgt_triangle_strip_01 2002Aug26
PASS
463 r400vgt_triangle_wflags_01 2002Aug26
PASS
464 r400vgt_vtx_vector_packing_01 2002Aug26
PASS
465 r400vte_coverage_01 2002Aug26
PASS
466 r400vte_simple_point_01 2002Aug26
PASS
467 r400vte_simple_triangle_01 2002Aug26
PASS
468 r400vte_w0_fmt_01 2002Aug26
PASS
469 r400vte_w0_fmt_03 2002Aug26
PASS
470 r400vte_xy_fmt_01 2002Aug26
PASS
471 r400vte_xyz_scale_01 2002Aug26
PASS
472 r400vte_xyz_scale_02 2002Aug26
PASS
473 r400vte_z_fmt_01 2002Aug26
PASS

=====
Tests_Run_count = 473 Pass_count = 471 Fail_count = 0 Incomplete_count = 2
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++
++++      Date: Sat Sep 28 22:14:15 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
++++      CP          11          11          11          100
++++      RBBM        1           1           1           100
++++      VGT         175         175         173          98
++++      SC          284         284         284          100
++++      CL          115         115         115          100
++++      VTE         10          10          10          100
++++      SPECIALTY   6           6           6           100
++++      SU           91          91          91          100
++++      SANITY      2           2           2           100
+++++
++++      TOTAL        695         695         693          99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep23
	PASS		
2	cp_e2blit_brush_mt_ropcc		2002Sep23
	PASS		
3	cp_e2_hostdata_blt_pntr_8888		2002Sep23
	PASS		
4	cp_e2_one_blit		2002Sep23
	PASS		
5	cp_e2_one_line		2002Sep23
	PASS		
6	cp_e2paint_565		2002Sep23
	PASS		
7	cp_e2paint_multi		2002Sep23
	PASS		
8	cp_e2polyscanlines_brush		2002Sep23
	PASS		
9	cp_e2polyscanlines_brush_mt		2002Sep23
	PASS		
10	cp_e2smalltext_jcl		2002Sep23
	PASS		
11	cp_simple_triangle		2002Sep23
	PASS		
12	primlib_1st_tri_june15		2002Sep25
	PASS		
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep25
	PASS		
14	primlib_gouraud_triangles_2_draw_passes		2002Sep25
	PASS		
15	primlib_parameterized_simple_triangle		2002Sep25
	PASS		
16	primlib_template_simple_triangle		2002Sep25
	PASS		
17	primlib_tex_tri		2002Sep24
	INCOMPLETE		
18	r400cl_bary_texture_08_bug		2002Sep21
	PASS		
19	r400cl_clip_10_verts_01		2002Sep25
	PASS		
20	r400cl_clip_disable_01		2002Sep25

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep25
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep25
PASS		
23	r400cl_cull_only_ena_01	2002Sep25
PASS		
24	r400cl_edgeflags_01	2002Sep25
PASS		
25	r400cl_edgeflags_01_bug	2002Sep23
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep25
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep25
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep25
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep25
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep25
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep25
PASS		
32	r400cl_edgeflags_gband_01	2002Sep25
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep25
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep25
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep25
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep25
PASS		
37	r400cl_frustum_01	2002Sep25
PASS		
38	r400cl_frustum_02	2002Sep25
PASS		
39	r400cl_frustum_03	2002Sep25
PASS		
40	r400cl_frustum_04	2002Sep25
PASS		
41	r400cl_frustum_05	2002Sep25
PASS		
42	r400cl_frustum_06	2002Sep25
PASS		
43	r400cl_frustum_07	2002Sep25
PASS		
44	r400cl_frustum_08	2002Sep25

PASS		
45	r400cl_frustum_09	2002Sep25
PASS		
46	r400cl_frustum_10	2002Sep25
PASS		
47	r400cl_frustum_11	2002Sep25
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48	r400cl_frustum_12	2002Sep25
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49	r400cl_frustum_13	2002Sep25
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50	r400cl_frustum_14	2002Sep25
PASS		
51	r400cl_frustum_15	2002Sep25
PASS		
52	r400cl_frustum_16	2002Sep25
PASS		
53	r400cl_frustum_17	2002Sep25
PASS		
54	r400cl_frustum_18	2002Sep25
PASS		
55	r400cl_frustum_19	2002Sep25
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56	r400cl_frustum_20	2002Sep25
PASS		
57	r400cl_frustum_21	2002Sep25
PASS		
58	r400cl_frustum_22	2002Sep25
PASS		
59	r400cl_frustum_23	2002Sep25
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60	r400cl_frustum_24	2002Sep25
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61	r400cl_frustum_25	2002Sep25
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62	r400cl_frustum_26	2002Sep25
PASS		
63	r400cl_frustum_27	2002Sep25
PASS		
64	r400cl_frustum_28	2002Sep25
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep25
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep25
PASS		
67	r400cl_frustum_lines_01	2002Sep25
PASS		
68	r400cl_frustum_lines_02	2002Sep25

PASS		
69	r400cl_frustum_lines_03	2002Sep25
PASS		
70	r400cl_frustum_lines_04	2002Sep25
PASS		
71	r400cl_frustum_lines_05	2002Sep25
PASS		
72	r400cl_frustum_lines_06	2002Sep25
PASS		
73	r400cl_frustum_lines_07	2002Sep25
PASS		
74	r400cl_frustum_lines_08	2002Sep25
PASS		
75	r400cl_frustum_lines_09	2002Sep25
PASS		
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77	r400cl_frustum_lines_11	2002Sep25
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PASS		
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81	r400cl_frustum_lines_15	2002Sep25
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82	r400cl_frustum_lines_16	2002Sep25
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83	r400cl_frustum_lines_17	2002Sep25
PASS		
84	r400cl_frustum_lines_18	2002Sep25
PASS		
85	r400cl_frustum_lines_19	2002Sep25
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86	r400cl_frustum_lines_20	2002Sep25
PASS		
87	r400cl_frustum_lines_21	2002Sep25
PASS		
88	r400cl_frustum_lines_22	2002Sep25
PASS		
89	r400cl_frustum_lines_23	2002Sep25
PASS		
90	r400cl_frustum_lines_24	2002Sep25
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91	r400cl_gband_04	2002Sep25
PASS		
92	r400cl_gband_tcl_01	2002Sep25

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93	r400cl_nan_kill_01_bug	2002Sep23
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Sep25
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95	r400cl_polymode_line_fill_01_bug	2002Sep23
PASS		
96	r400cl_simple_triangle_01	2002Sep25
PASS		
97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep23
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98	r400cl_ucp_combos_01	2002Sep25
PASS		
99	r400cl_ucp_combos_02	2002Sep25
PASS		
100	r400cl_ucp_combos_03	2002Sep25
PASS		
101	r400cl_ucp_combos_04	2002Sep25
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PASS		
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129	r400cl_vertex_reuse_clip_01	2002Sep25
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130	r400cl_vtx_kill_01	2002Sep25
PASS		
131	r400cl_vtx_kill_02	2002Sep25
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132	r400cl_w_eq_0	2002Sep25
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133	r400rbm_simple_triangle_01	2002Sep23
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134	r400sanity_persp_projection_01	2002Sep25
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135	r400sanity_vfd_texture_sample_01	2002Sep25
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136	r400sc_baryc_01	2002Sep25
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137	r400sc_bres_cntl_01	2002Sep7
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138	r400sc_bres_cntl_02	2002Sep25
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142	r400sc_bres_cntl_w9x_01	2002Sep25
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144	r400sc_clip_rect_02	2002Sep25
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145	r400sc_clip_rect_03	2002Sep25
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147	r400sc_clip_rect_fc_01	2002Sep25
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149	r400sc_diamond_exit_02	2002Sep25
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154	r400sc_jss_1x2_01	2002Sep25
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155	r400sc_jss_1x2_02	2002Sep25
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156	r400sc_jss_1x2_primtypes_01	2002Sep25
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158	r400sc_jss_1x3_02	2002Sep25
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159	r400sc_jss_1x3_primtypes_01	2002Sep25
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161	r400sc_jss_1x4_02	2002Sep25
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162	r400sc_jss_1x4_primtypes_01	2002Sep25
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164	r400sc_jss_2x1_02	2002Sep25

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166	r400sc_jss_2x2_01	2002Sep25
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167	r400sc_jss_2x2_02	2002Sep25
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169	r400sc_jss_2x3_01	2002Sep25
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174	r400sc_jss_2x4_primtypes_01	2002Sep25
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178	r400sc_jss_3x2_01	2002Sep25
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179	r400sc_jss_3x2_02	2002Sep25
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186	r400sc_jss_3x4_03	2002Sep25
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187	r400sc_jss_3x4_primtypes_01	2002Sep25
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189	r400sc_jss_4x1_02	2002Sep25
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190	r400sc_jss_4x1_primitives_01	2002Sep25
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191	r400sc_jss_4x2_01	2002Sep25
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192	r400sc_jss_4x2_02	2002Sep25
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193	r400sc_jss_4x2_primitives_01	2002Sep25
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194	r400sc_jss_4x3_01	2002Sep25
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195	r400sc_jss_4x3_02	2002Sep25
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196	r400sc_jss_4x3_primitives_01	2002Sep25
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197	r400sc_jss_4x4_01	2002Sep25
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198	r400sc_jss_4x4_02	2002Sep25
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199	r400sc_jss_4x4_03	2002Sep25
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200	r400sc_jss_4x4_aa_mask_01	2002Sep25
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203	r400sc_jss_4x4_fc_02	2002Sep25
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204	r400sc_jss_4x4_max_dist_01	2002Sep25
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205	r400sc_jss_4x4_primitives_01	2002Sep25
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207	r400sc_line_dx10_ge_0_01	2002Sep25
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208	r400sc_line_dx10_lt_0_01	2002Sep25
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215	r400sc_line_jss_3x4_01	2002Sep25
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216	r400sc_line_list_01	2002Sep25
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217	r400sc_line_list_02	2002Sep25
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219	r400sc_line_list_concentric_circle_02	2002Sep25
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220	r400sc_line_list_concentric_circle_03	2002Sep25
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221	r400sc_line_list_textured_01	2002Sep25
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222	r400sc_line_list_verify_st_01	2002Sep25
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223	r400sc_line_list_verify_st_01_bug	2002Sep23
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224	r400sc_line_msa_8_01	2002Sep25
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321	r400sc_msaa_X_primtypes_02_bug	2002Sep23
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508	r400vgt_auto_index_tri_wflags_01	2002Sep25
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533	r400vgt_dma_swap_indx32_01	2002Sep25
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534	r400vgt_edgeflags_polygon_01	2002Sep25
PASS		
535	r400vgt_edgeflags_quad_list_01	2002Sep25
PASS		
536	r400vgt_edgeflags_quad_strip_01	2002Sep25
PASS		
537	r400vgt_edgeflags_triangle_fan_01	2002Sep25
PASS		
538	r400vgt_edgeflags_triangle_list_01	2002Sep25
PASS		
539	r400vgt_edgeflags_triangle_strip_01	2002Sep25
PASS		
540	r400vgt_edgeflags_triangle_wflags_01	2002Sep25
PASS		
541	r400vgt_event_handling_01	2002Sep25
PASS		
542	r400vgt_ext2int_index_line_list_01	2002Sep25
PASS		
543	r400vgt_ext2int_index_line_loop_01	2002Sep25
PASS		
544	r400vgt_ext2int_index_line_strip_01	2002Sep25
PASS		
545	r400vgt_ext2int_index_points_01	2002Sep25
PASS		
546	r400vgt_ext2int_index_polygon_01	2002Sep25
PASS		
547	r400vgt_ext2int_index_quad_list_01	2002Sep25
PASS		
548	r400vgt_ext2int_index_quad_strip_01	2002Sep25

PASS		
549	r400vgt_ext2int_index_rectangle_list_01	2002Sep25
PASS		
550	r400vgt_ext2int_index_triangle_fan_01	2002Sep25
PASS		
551	r400vgt_ext2int_index_triangle_list_01	2002Sep25
PASS		
552	r400vgt_ext2int_index_triangle_strip_01	2002Sep25
PASS		
553	r400vgt_ext2int_index_triangle_wflags_01	2002Sep25
PASS		
554	r400vgt_hos_auto_index_line_list_01	2002Sep25
PASS		
555	r400vgt_hos_auto_index_quad_list_01	2002Sep25
PASS		
556	r400vgt_hos_auto_index_triangle_list_01	2002Sep25
PASS		
557	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep25
PASS		
558	r400vgt_hos_LPatch_01	2002Sep25
PASS		
559	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep25
PASS		
560	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep25
PASS		
561	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep25
PASS		
562	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep25
PASS		
563	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep25
PASS		
564	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep25
PASS		
565	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep25
PASS		
566	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep25
PASS		
567	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep25
PASS		
568	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep25
PASS		
569	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep25
PASS		
570	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep25
PASS		
571	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep25
PASS		
572	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep25

PASS		
573	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep25
PASS		
574	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep25
PASS		
575	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep25
PASS		
576	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep25
PASS		
577	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep25
PASS		
578	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep25
PASS		
579	r400vgt_hos_PNT_edge_detection_01	2002Sep25
PASS		
580	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep25
PASS		
581	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep25
PASS		
582	r400vgt_hos_RPatch_cp_02	2002Sep25
PASS		
583	r400vgt_hos_RPatch_lp_02	2002Sep25
PASS		
584	r400vgt_immed_index_line_list_01	2002Sep25
PASS		
585	r400vgt_immed_index_line_loop_01	2002Sep25
PASS		
586	r400vgt_immed_index_line_strip_01	2002Sep25
PASS		
587	r400vgt_immed_index_points_01	2002Sep25
PASS		
588	r400vgt_immed_index_polygon_01	2002Sep25
PASS		
589	r400vgt_immed_index_primitives_01	2002Sep25
PASS		
590	r400vgt_immed_index_quad_list_01	2002Sep25
PASS		
591	r400vgt_immed_index_quad_strip_01	2002Sep25
PASS		
592	r400vgt_immed_index_rectangle_list_01	2002Sep25
PASS		
593	r400vgt_immed_index_tri_fan_01	2002Sep25
PASS		
594	r400vgt_immed_index_tri_list_01	2002Sep25
PASS		
595	r400vgt_immed_index_tri_strip_01	2002Sep25
PASS		
596	r400vgt_immed_index_tri_wflags_01	2002Sep25

PASS		
597	r400vgt_index_dealloc_line_list_01	2002Sep25
PASS		
598	r400vgt_index_dealloc_points_01	2002Sep25
PASS		
599	r400vgt_index_dealloc_triangle_list_01	2002Sep25
PASS		
600	r400vgt_index_min_max_01	2002Sep25
PASS		
601	r400vgt_index_min_max_02	2002Sep25
PASS		
602	r400vgt_index_offset_01	2002Sep25
PASS		
603	r400vgt_index_offset_02	2002Sep25
PASS		
604	r400vgt_index_offset_03	2002Sep25
PASS		
605	r400vgt_index_size_01	2002Sep25
PASS		
606	r400vgt_index_source_switch_01	2002Sep25
PASS		
607	r400vgt_line_list_01	2002Sep25
PASS		
608	r400vgt_line_list_02	2002Sep25
PASS		
609	r400vgt_line_loop_01	2002Sep25
PASS		
610	r400vgt_line_loop_02	2002Sep25
PASS		
611	r400vgt_line_strip_01	2002Sep25
PASS		
612	r400vgt_line_strip_02	2002Sep25
PASS		
613	r400vgt_multi_context_01	2002Sep25
PASS		
614	r400vgt_multi_context_02	2002Sep25
PASS		
615	r400vgt_multi_context_03	2002Sep25
PASS		
616	r400vgt_multi_context_04	2002Sep25
PASS		
617	r400vgt_multi_context_05	2002Sep25
PASS		
618	r400vgt_multi_context_06	2002Sep25
PASS		
619	r400vgt_multi_context_07	2002Sep25
PASS		
620	r400vgt_multi_context_08	2002Sep25

PASS		
621	r400vgt_multi_context_09	2002Sep25
PASS		
622	r400vgt_multi_context_10	2002Sep25
PASS		
623	r400vgt_multi_context_11	2002Sep25
PASS		
624	r400vgt_multi_context_12	2002Sep25
PASS		
625	r400vgt_multi_prim_reset_index_all_01	2002Sep25
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
626	r400vgt_pass_thru_all_prims_01	2002Sep25
PASS		
627	r400vgt_point_list_01	2002Sep25
PASS		
628	r400vgt_point_list_02	2002Sep25
PASS		
629	r400vgt_polygon_01	2002Sep25
PASS		
630	r400vgt_polygon_02	2002Sep25
PASS		
631	r400vgt_provoking_vtx_all_01	2002Sep25
PASS		
632	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep25
PASS		
633	r400vgt_provoking_vtx_polygon_01	2002Sep25
PASS		
634	r400vgt_provoking_vtx_quad_list_01	2002Sep25
PASS		
635	r400vgt_provoking_vtx_quad_strip_01	2002Sep25
PASS		
636	r400vgt_provoking_vtx_tri_fan_01	2002Sep25
PASS		
637	r400vgt_provoking_vtx_tri_strip_01	2002Sep25
PASS		
638	r400vgt_quad_list_01	2002Sep25
PASS		
639	r400vgt_quad_list_02	2002Sep25
PASS		
640	r400vgt_quad_strip_01	2002Sep25
PASS		
641	r400vgt_quad_strip_02	2002Sep25
PASS		
642	r400vgt_rectangle_list_01	2002Sep25
PASS		
643	r400vgt_rectangle_list_02	2002Sep25
PASS		
644	r400vgt_reuse_depth_line_list_01	2002Sep25

PASS		
645	r400vgt_reuse_depth_point_list_01	2002Sep25
PASS		
646	r400vgt_reuse_depth_triangle_list_01	2002Sep25
PASS		
647	r400vgt_reuse_index_line_list_01	2002Sep25
PASS		
648	r400vgt_reuse_index_point_list_01	2002Sep25
PASS		
649	r400vgt_reuse_index_triangle_list_01	2002Sep25
PASS		
650	r400vgt_reuse_index_triangle_list_02	2002Sep25
PASS		
651	r400vgt_reuse_index_triangle_list_03	2002Sep25
PASS		
652	r400vgt_suppress_eop_01	2002Sep25
PASS		
653	r400vgt_suppress_eop_02	2002Sep25
PASS		
654	r400vgt_suppress_eop_03	2002Sep25
PASS		
655	r400vgt_suppress_eop_04	2002Sep25
PASS		
656	r400vgt_triangle_fan_01	2002Sep25
PASS		
657	r400vgt_triangle_fan_02	2002Sep25
PASS		
658	r400vgt_triangle_list_01	2002Sep25
PASS		
659	r400vgt_triangle_list_02	2002Sep25
PASS		
660	r400vgt_triangle_strip_01	2002Sep25
PASS		
661	r400vgt_triangle_strip_02	2002Sep25
PASS		
662	r400vgt_triangle_wflags_01	2002Sep25
PASS		
663	r400vgt_triangle_wflags_02	2002Sep25
PASS		
664	r400vgt_viz_query_01	2002Sep25
PASS		
665	r400vgt_vtx_vector_packing_01	2002Sep25
PASS		
666	r400vte_coverage_01	2002Sep25
PASS		
667	r400vte_simple_point_01	2002Sep25
PASS		
668	r400vte_simple_triangle_01	2002Sep25

PASS
669 r400vte_w0_fmt_01 2002Sep25
PASS
670 r400vte_w0_fmt_03 2002Sep25
PASS
671 r400vte_xy_fmt_01 2002Sep25
PASS
672 r400vte_xy_fmt_mk_01_bug 2002Sep23
PASS
673 r400vte_xyz_scale_01 2002Sep25
PASS
674 r400vte_xyz_scale_02 2002Sep25
PASS
675 r400vte_z_fmt_01 2002Sep25
PASS

=====
Tests_Run_count = 675 Pass_count = 673 Fail_count = 1 Incomplete_count = 1
=====

Regression Report For VGT - Project = r400 Created on Mon Oct 14 22:49:41 2002

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc	0:0:3	2002Oct14
PASS			
2	cp_dma_interrupt	0:0:2	2002Oct14
PASS			
3	cp_dma_m2m_01	0:0:1	2002Oct14
PASS			
4	cp_dma_m2m_02	0:0:1	2002Oct14
PASS			
5	cp_dma_m2m_03	0:0:1	2002Oct14
PASS			
6	cp_dma_m2m_04	0:0:1	2002Oct14
PASS			
7	cp_dma_m2r_01	0:0:1	2002Oct14
PASS			
8	cp_dma_m2r_02	0:0:2	2002Oct14
PASS			
9	cp_dma_m2r_03	0:0:1	2002Oct14
PASS			
10	cp_dma_m2r_04	0:0:1	2002Oct14
PASS			
11	cp_dma_m2r_r2m	0:0:1	2002Oct14
PASS			
12	cp_dma_pio_simple	0:0:1	2002Oct14
PASS			
13	cp_dma_pio_stress	0:0:1	2002Oct14
PASS			
14	cp_dma_piobm_stress	0:0:1	2002Oct14
PASS			
15	cp_dma_r2m_01	0:0:1	2002Oct14
PASS			
16	cp_dma_r2m_02	0:0:2	2002Oct14
PASS			
17	cp_dma_r2m_03	0:0:2	2002Oct14
PASS			
18	cp_dma_r2m_04	0:0:2	2002Oct14
PASS			
19	cp_dma_r2r_01	0:0:1	2002Oct14
PASS			
20	cp_dma_r2r_02	0:0:1	2002Oct14

PASS			
21	cp_dma_r2r_03	0:0:2	2002Oct14
PASS			
22	cp_dma_r2r_r2m	0:0:1	2002Oct14
PASS			
23	cp_dma_r2r_r2m_m2m	0:0:2	2002Oct14
PASS			
24	cp_dma_r2r_r2m_m2m_r2m	0:0:1	2002Oct14
PASS			
25	cp_dma_simple	0:0:1	2002Oct14
PASS			
26	cp_e2_hostdata_blt_pntr_8888	0:0:1	2002Oct14
PASS			
27	cp_e2_one_blit	0:0:0	2002Oct14
PASS			
28	cp_e2_one_line	0:0:2	2002Oct14
PASS			
29	cp_e2blit_brush_mt_ropcc	0:0:1	2002Oct14
PASS			
30	cp_e2paint_565	0:0:1	2002Oct14
PASS			
31	cp_e2paint_multi	0:0:2	2002Oct14
PASS			
32	cp_e2polyscanlines_brush	0:0:1	2002Oct14
PASS			
33	cp_e2polyscanlines_brush_mt	0:0:1	2002Oct14
PASS			
34	cp_e2smalltext_jcl	0:0:3	2002Oct14
PASS			
35	cp_im_load_indirect	0:0:3	2002Oct14
PASS			
36	cp_push_aper_indirect1	0:0:3	2002Oct14
PASS			
37	cp_push_aper_primary	0:0:3	2002Oct14
PASS			
38	cp_queue_avail_01	0:0:3	2002Oct14
PASS			
39	cp_queue_avail_02	0:0:3	2002Oct14
PASS			
40	cp_queue_avail_03	0:0:3	2002Oct14
PASS			
41	cp_queue_avail_04	0:0:3	2002Oct14
PASS			
42	cp_queue_avail_05	0:0:3	2002Oct14
PASS			
43	cp_queue_avail_06	0:0:3	2002Oct14
PASS			
44	cp_queue_avail_07	0:0:3	2002Oct14

PASS			
45	cp_queue_avail_08	0:0:3	2002Oct14
PASS			
46	cp_queue_avail_09	0:0:3	2002Oct14
PASS			
47	cp_queue_avail_10	0:0:3	2002Oct14
PASS			
48	cp_queue_avail_11	0:0:3	2002Oct14
PASS			
49	cp_queue_avail_12	0:0:3	2002Oct14
PASS			
50	cp_simple_triangle	0:0:3	2002Oct14
PASS			
51	milestone_event_flush_tri1	0:0:3	2002Oct11
PASS			
52	milestone_event_flush_tri2	0:0:3	2002Oct11
PASS			
53	milestone_event_flush_tri3	0:0:3	2002Oct11
PASS			
54	milestone_event_flush_tri4	0:0:3	2002Oct11
PASS			
55	milestone_event_flush_tri5	0:0:3	2002Oct11
PASS			
56	milestone_event_flush_tri6	0:0:3	2002Oct11
PASS			
57	milestone_event_flush_tri7	0:0:3	2002Oct11
PASS			
58	milestone_event_flush_tri8	0:0:3	2002Oct11
PASS			
59	milestone_event_flush_tri9	0:0:3	2002Oct11
PASS			
60	milestone_event_randomized	0:0:3	2002Oct11
PASS			
61	milestone_event_standard	0:0:3	2002Oct11
PASS			
62	milestone_event_switch_flush_tri1	0:0:3	2002Oct11
PASS			
63	milestone_event_switch_flush_tri2	0:0:3	2002Oct11
PASS			
64	milestone_event_switch_flush_tri3	0:0:3	2002Oct11
PASS			
65	milestone_event_switch_flush_tri4	0:0:3	2002Oct11
PASS			
66	milestone_event_switch_flush_tri5	0:0:3	2002Oct11
PASS			
67	milestone_event_switch_flush_tri6	0:0:3	2002Oct11
PASS			
68	milestone_event_switch_flush_tri7	0:0:3	2002Oct11

PASS			
69	milestone_event_switch_flush_tri8	0:0:3	2002Oct11
PASS			
70	milestone_event_switch_flush_tri9	0:0:3	2002Oct11
PASS			
71	milestone_event_switch_tri2	0:0:3	2002Oct11
PASS			
72	milestone_event_switch_tri3	0:0:3	2002Oct11
PASS			
73	milestone_event_switch_tri4	0:0:3	2002Oct11
PASS			
74	milestone_event_switch_tri5	0:0:3	2002Oct11
PASS			
75	milestone_event_switch_tri6	0:0:3	2002Oct11
PASS			
76	milestone_event_switch_tri7	0:0:3	2002Oct11
PASS			
77	milestone_event_switch_tri8	0:0:3	2002Oct11
PASS			
78	milestone_event_switch_tri9	0:0:3	2002Oct11
PASS			
79	milestone_event_tri2	0:0:3	2002Oct11
PASS			
80	milestone_event_tri3	0:0:3	2002Oct11
PASS			
81	milestone_event_tri4	0:0:3	2002Oct11
PASS			
82	milestone_event_tri5	0:0:3	2002Oct11
PASS			
83	milestone_event_tri6	0:0:3	2002Oct11
PASS			
84	milestone_event_tri7	0:0:3	2002Oct11
PASS			
85	milestone_event_tri8	0:0:3	2002Oct11
PASS			
86	milestone_event_tri9	0:0:3	2002Oct11
PASS			
87	milestone_tri_random_rect	0:0:3	2002Oct11
PASS			
88	milestone_tri_random_tri	0:0:3	2002Oct11
PASS			
89	milestone_tri_randomized	0:0:3	2002Oct11
PASS			
90	milestone_tri_standard	0:0:3	2002Oct11
PASS			
91	milestone_tri_tril128_pix4	0:0:3	2002Oct11
PASS			
92	milestone_tri_tril_pix1	0:0:3	2002Oct11

PASS			
93	milestone_tri_tri1_pix2	0:0:3	2002Oct11
PASS			
94	milestone_tri_tri1_pix3	0:0:3	2002Oct11
PASS			
95	milestone_tri_tri1_pix4	0:0:3	2002Oct11
PASS			
96	milestone_tri_tri24x24	0:0:3	2002Oct11
PASS			
97	milestone_tri_tri28x28	0:0:3	2002Oct11
PASS			
98	milestone_tri_tri2_pix1	0:0:3	2002Oct11
PASS			
99	milestone_tri_tri2_pix16	0:0:3	2002Oct11
PASS			
100	milestone_tri_tri2_pix4	0:0:3	2002Oct11
PASS			
101	milestone_tri_tri32_pix1	0:0:3	2002Oct11
PASS			
102	milestone_tri_tri32_pix4	0:0:3	2002Oct11
PASS			
103	milestone_tri_tri32x32	0:0:3	2002Oct11
PASS			
104	milestone_tri_tri36x36	0:0:3	2002Oct11
PASS			
105	milestone_tri_tri40x40	0:0:3	2002Oct11
PASS			
106	milestone_tri_tri44x44	0:0:3	2002Oct11
PASS			
107	milestone_tri_tri48x48	0:0:3	2002Oct11
PASS			
108	milestone_tri_tri52x52	0:0:3	2002Oct11
PASS			
109	milestone_tri_tri56x56	0:0:3	2002Oct11
PASS			
110	milestone_tri_tri60x60	0:0:3	2002Oct11
PASS			
111	milestone_tri_tri64x64	0:0:3	2002Oct11
PASS			
112	milestone_tri_tri8_pix4	0:0:3	2002Oct11
PASS			
113	primlib_1st_tri_june15	0:0:3	2002Oct14
PASS			
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	0:0:3	2002Oct14
PASS			
115	primlib_gouraud_triangles_2_draw_passes	0:0:3	2002Oct14
PASS			
116	primlib_parameterized_simple_triangle	0:0:3	2002Oct14

PASS			
117	primlib_template_simple_triangle	0:0:3	2002Oct14
PASS			
118	primlib_tex_tri	0:0:3	2002Oct14
PASS			
119	r400cl_bary_texture_08_bug	0:0:3	2002Sep21
PASS			
120	r400cl_clip_10_verts_01	0:0:3	2002Oct14
PASS			
121	r400cl_clip_disable_01	0:0:3	2002Oct14
PASS			
122	r400cl_clip_space_dx_ogl_01	0:0:3	2002Oct14
PASS			
123	r400cl_clip_ucp_6bits_01	0:0:3	2002Oct14
PASS			
124	r400cl_cull_only_ena_01	0:0:3	2002Oct14
PASS			
125	r400cl_edgeflags_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp		compare file empty
126	r400cl_edgeflags_01_bug	0:0:3	2002Oct9
PASS			
127	r400cl_edgeflags_frustum_bottom_01	0:0:3	2002Oct14
PASS			
128	r400cl_edgeflags_frustum_far_01	0:0:3	2002Oct14
PASS			
129	r400cl_edgeflags_frustum_left_01	0:0:3	2002Oct14
PASS			
130	r400cl_edgeflags_frustum_near_01	0:0:3	2002Oct14
PASS			
131	r400cl_edgeflags_frustum_right_01	0:0:3	2002Oct14
PASS			
132	r400cl_edgeflags_frustum_top_01	0:0:3	2002Oct14
PASS			
133	r400cl_edgeflags_gband_01	0:0:3	2002Oct14
PASS			
134	r400cl_edgeflags_gband_bottom_01	0:0:3	2002Oct14
PASS			
135	r400cl_edgeflags_gband_left_01	0:0:3	2002Oct14
PASS			
136	r400cl_edgeflags_gband_right_01	0:0:3	2002Oct14
PASS			
137	r400cl_edgeflags_gband_top_01	0:0:3	2002Oct14
PASS			
138	r400cl_frustum_01	0:0:3	2002Oct14
PASS			
139	r400cl_frustum_02	0:0:3	2002Oct14
PASS			

140	r400cl_frustum_03	0:0:3	2002Oct14
	PASS		
141	r400cl_frustum_04	0:0:3	2002Oct14
	PASS		
142	r400cl_frustum_05	0:0:3	2002Oct14
	PASS		
143	r400cl_frustum_06	0:0:3	2002Oct14
	PASS		
144	r400cl_frustum_07	0:0:3	2002Oct14
	PASS		
145	r400cl_frustum_08	0:0:3	2002Oct14
	PASS		
146	r400cl_frustum_09	0:0:3	2002Oct14
	PASS		
147	r400cl_frustum_10	0:0:3	2002Oct14
	PASS		
148	r400cl_frustum_11	0:0:3	2002Oct14
	PASS		
149	r400cl_frustum_12	0:0:3	2002Oct14
	PASS		
150	r400cl_frustum_13	0:0:3	2002Oct14
	PASS		
151	r400cl_frustum_14	0:0:3	2002Oct14
	PASS		
152	r400cl_frustum_15	0:0:3	2002Oct14
	PASS		
153	r400cl_frustum_16	0:0:3	2002Oct14
	PASS		
154	r400cl_frustum_17	0:0:3	2002Oct14
	PASS		
155	r400cl_frustum_18	0:0:3	2002Oct14
	PASS		
156	r400cl_frustum_19	0:0:3	2002Oct14
	PASS		
157	r400cl_frustum_20	0:0:3	2002Oct14
	PASS		
158	r400cl_frustum_21	0:0:3	2002Oct14
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159	r400cl_frustum_22	0:0:3	2002Oct14
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160	r400cl_frustum_23	0:0:3	2002Oct14
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161	r400cl_frustum_24	0:0:3	2002Oct14
	PASS		
162	r400cl_frustum_25	0:0:3	2002Oct14
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163	r400cl_frustum_26	0:0:3	2002Oct14
	PASS		

164	r400cl_frustum_27	0:0:3	2002Oct14
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166	r400cl_frustum_29	0:0:3	2002Oct14
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167	r400cl_frustum_30	0:0:3	2002Oct14
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168	r400cl_frustum_31	0:0:3	2002Oct14
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170	r400cl_frustum_33	0:0:3	2002Oct14
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171	r400cl_frustum_34	0:0:3	2002Oct14
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172	r400cl_frustum_35	0:0:3	2002Oct14
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173	r400cl_frustum_36	0:0:3	2002Oct14
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174	r400cl_frustum_37	0:0:3	2002Oct14
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175	r400cl_frustum_38	0:0:3	2002Oct14
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176	r400cl_frustum_39	0:0:3	2002Oct14
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177	r400cl_frustum_40	0:0:3	2002Oct14
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178	r400cl_frustum_all_vols_lines	0:0:3	2002Oct14
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179	r400cl_frustum_all_vols_tris	0:0:3	2002Oct14
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180	r400cl_frustum_lines_01	0:0:3	2002Oct14
	PASS		
181	r400cl_frustum_lines_02	0:0:3	2002Oct14
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182	r400cl_frustum_lines_03	0:0:3	2002Oct14
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183	r400cl_frustum_lines_04	0:0:3	2002Oct14
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184	r400cl_frustum_lines_05	0:0:3	2002Oct14
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185	r400cl_frustum_lines_06	0:0:3	2002Oct14
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186	r400cl_frustum_lines_07	0:0:3	2002Oct14
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187	r400cl_frustum_lines_08	0:0:3	2002Oct14
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188	r400cl_frustum_lines_09	0:0:3	2002Oct14
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189	r400cl_frustum_lines_10	0:0:3	2002Oct14
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190	r400cl_frustum_lines_101	0:0:3	2002Oct14
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191	r400cl_frustum_lines_102	0:0:3	2002Oct14
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192	r400cl_frustum_lines_103	0:0:3	2002Oct14
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193	r400cl_frustum_lines_104	0:0:3	2002Oct14
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196	r400cl_frustum_lines_107	0:0:3	2002Oct14
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197	r400cl_frustum_lines_108	0:0:3	2002Oct14
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198	r400cl_frustum_lines_11	0:0:3	2002Oct14
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199	r400cl_frustum_lines_12	0:0:3	2002Oct14
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200	r400cl_frustum_lines_13	0:0:3	2002Oct14
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201	r400cl_frustum_lines_14	0:0:3	2002Oct14
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202	r400cl_frustum_lines_15	0:0:3	2002Oct14
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203	r400cl_frustum_lines_16	0:0:3	2002Oct14
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204	r400cl_frustum_lines_17	0:0:3	2002Oct14
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205	r400cl_frustum_lines_18	0:0:3	2002Oct14
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206	r400cl_frustum_lines_19	0:0:3	2002Oct14
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207	r400cl_frustum_lines_20	0:0:3	2002Oct14
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208	r400cl_frustum_lines_21	0:0:3	2002Oct14
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209	r400cl_frustum_lines_22	0:0:3	2002Oct14
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210	r400cl_frustum_lines_23	0:0:3	2002Oct14
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211	r400cl_frustum_lines_24	0:0:3	2002Oct14
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212	r400cl_frustum_lines_25	0:0:3	2002Oct14
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213	r400cl_frustum_lines_26	0:0:3	2002Oct14
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214	r400cl_frustum_lines_27	0:0:3	2002Oct14
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215	r400cl_frustum_lines_28	0:0:3	2002Oct14
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216	r400cl_frustum_lines_29	0:0:3	2002Oct14
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217	r400cl_frustum_lines_30	0:0:3	2002Oct14
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218	r400cl_frustum_lines_31	0:0:3	2002Oct14
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219	r400cl_frustum_lines_32	0:0:3	2002Oct14
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220	r400cl_frustum_lines_33	0:0:3	2002Oct14
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221	r400cl_frustum_lines_34	0:0:3	2002Oct14
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222	r400cl_frustum_lines_35	0:0:3	2002Oct14
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223	r400cl_frustum_lines_36	0:0:3	2002Oct14
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224	r400cl_frustum_lines_37	0:0:3	2002Oct14
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225	r400cl_frustum_lines_38	0:0:3	2002Oct14
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226	r400cl_frustum_lines_39	0:0:3	2002Oct14
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227	r400cl_frustum_lines_40	0:0:3	2002Oct14
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228	r400cl_frustum_lines_41	0:0:3	2002Oct14
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229	r400cl_frustum_lines_42	0:0:3	2002Oct14
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230	r400cl_frustum_lines_43	0:0:3	2002Oct14
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231	r400cl_frustum_lines_44	0:0:3	2002Oct14
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232	r400cl_frustum_lines_45	0:0:3	2002Oct14
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233	r400cl_frustum_lines_46	0:0:3	2002Oct14
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234	r400cl_frustum_lines_47	0:0:3	2002Oct14
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235	r400cl_frustum_lines_48	0:0:3	2002Oct14
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236	r400cl_frustum_lines_49	0:0:3	2002Oct14
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237	r400cl_frustum_lines_50	0:0:3	2002Oct14
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238	r400cl_frustum_lines_51	0:0:3	2002Oct14
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239	r400cl_frustum_lines_52	0:0:3	2002Oct14
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240	r400cl_frustum_lines_53	0:0:3	2002Oct14
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241	r400cl_frustum_lines_54	0:0:3	2002Oct14
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242	r400cl_frustum_lines_55	0:0:3	2002Oct14
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243	r400cl_frustum_lines_56	0:0:3	2002Oct14
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244	r400cl_frustum_lines_57	0:0:3	2002Oct14
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245	r400cl_frustum_lines_58	0:0:3	2002Oct14
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246	r400cl_frustum_lines_59	0:0:3	2002Oct14
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247	r400cl_frustum_lines_60	0:0:3	2002Oct14
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248	r400cl_frustum_lines_61	0:0:3	2002Oct14
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249	r400cl_frustum_lines_62	0:0:3	2002Oct14
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250	r400cl_frustum_lines_63	0:0:3	2002Oct14
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251	r400cl_frustum_lines_64	0:0:3	2002Oct14
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252	r400cl_frustum_lines_65	0:0:3	2002Oct14
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253	r400cl_frustum_lines_66	0:0:3	2002Oct14
	PASS		
254	r400cl_frustum_lines_67	0:0:3	2002Oct14
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255	r400cl_frustum_lines_68	0:0:3	2002Oct14
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256	r400cl_frustum_lines_69	0:0:3	2002Oct14
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257	r400cl_frustum_lines_70	0:0:3	2002Oct14
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258	r400cl_frustum_lines_71	0:0:3	2002Oct14
	PASS		
259	r400cl_frustum_lines_72	0:0:3	2002Oct14
	PASS		

260	r400cl_gband_04	0:0:3	2002Oct14
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261	r400cl_gband_tcl_01	0:0:3	2002Oct14
	PASS		
262	r400cl_nan_kill_01	0:0:3	2002Oct14
	PASS		
263	r400cl_nan_kill_01_bug	0:0:3	2002Oct9
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264	r400cl_point_ucp_combos_01	0:0:3	2002Oct14
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265	r400cl_pointlist_vertex_state_ucp_01	0:0:3	2002Oct14
	PASS		
266	r400cl_polymode_line_fill_01	0:0:3	2002Oct14
	PASS		
267	r400cl_polymode_line_fill_01_bug	0:0:3	2002Oct9
	PASS		
268	r400cl_simple_triangle_01	0:0:3	2002Oct14
	PASS		
269	r400cl_tri_polymode_line_stipple_ucp_combos_01	0:0:3	2002Oct14
	PASS		
270	r400cl_tri_polymode_line_ucp_combos_01	0:0:3	2002Oct14
	PASS		
271	r400cl_triangle_polymode_line_stippled_01	0:0:3	2002Oct14
	PASS		
272	r400cl_triangle_polymode_line_stippled_01_bug	0:0:3	2002Oct9
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273	r400cl_ucp_combos_01	0:0:3	2002Oct14
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274	r400cl_ucp_combos_02	0:0:3	2002Oct14
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275	r400cl_ucp_combos_03	0:0:3	2002Oct14
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276	r400cl_ucp_combos_04	0:0:3	2002Oct14
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277	r400cl_ucp_combos_05	0:0:3	2002Oct14
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278	r400cl_ucp_combos_06	0:0:3	2002Oct14
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279	r400cl_ucp_combos_07	0:0:3	2002Oct14
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280	r400cl_ucp_combos_08	0:0:3	2002Oct14
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281	r400cl_ucp_combos_09	0:0:3	2002Oct14
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282	r400cl_ucp_combos_10	0:0:3	2002Oct14
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283	r400cl_ucp_combos_11	0:0:3	2002Oct14
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284	r400cl_ucp_combos_12	0:0:3	2002Oct14
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285	r400cl_ucp_combos_13	0:0:3	2002Oct14
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286	r400cl_ucp_combos_14	0:0:3	2002Oct14
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287	r400cl_ucp_combos_15	0:0:3	2002Oct14
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288	r400cl_ucp_combos_16	0:0:3	2002Oct14
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289	r400cl_ucp_combos_17	0:0:3	2002Oct14
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290	r400cl_ucp_combos_18	0:0:3	2002Oct14
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291	r400cl_ucp_combos_19	0:0:3	2002Oct14
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292	r400cl_ucp_combos_20	0:0:3	2002Oct14
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293	r400cl_ucp_combos_21	0:0:3	2002Oct14
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294	r400cl_ucp_combos_22	0:0:3	2002Oct14
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295	r400cl_ucp_combos_23	0:0:3	2002Oct14
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296	r400cl_ucp_combos_24	0:0:3	2002Oct14
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297	r400cl_ucp_combos_25	0:0:3	2002Oct14
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298	r400cl_ucp_combos_26	0:0:3	2002Oct14
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299	r400cl_ucp_combos_27	0:0:3	2002Oct14
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300	r400cl_ucp_combos_28	0:0:3	2002Oct14
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301	r400cl_ucp_combos_29	0:0:3	2002Oct14
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302	r400cl_ucp_combos_30	0:0:3	2002Oct14
	PASS		
303	r400cl_ucp_pointlist_01	0:0:3	2002Oct14
	PASS		
304	r400cl_vertex_reuse_clip_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
305	r400cl_vtx_kill_01	0:0:3	2002Oct14
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306	r400cl_vtx_kill_02	0:0:3	2002Oct14
	PASS		
307	r400cl_w_eq_0	0:0:3	2002Oct14

PASS			
308	r400rbbm_simple_triangle_01	0:0:3	2002Oct14
PASS			
309	r400sanity_persp_projection_01	0:0:3	2002Oct14
PASS			
310	r400sanity_vfd_texture_sample_01	0:0:3	2002Oct14
PASS			
311	r400sc_baryc_01	0:0:3	2002Oct14
PASS			
312	r400sc_baryc_02	0:0:3	2002Oct14
PASS			
313	r400sc_bres_cntl_01	0:0:3	2002Sep7
PASS			
314	r400sc_bres_cntl_02	0:0:3	2002Oct14
PASS			
315	r400sc_bres_cntl_03	0:0:3	2002Oct14
PASS			
316	r400sc_bres_cntl_04	0:0:3	2002Oct14
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317	r400sc_bres_cntl_w2k_01	0:0:3	2002Oct14
PASS			
318	r400sc_bres_cntl_w9x_01	0:0:3	2002Oct14
PASS			
319	r400sc_clip_rect_01	0:0:3	2002Oct14
PASS			
320	r400sc_clip_rect_02	0:0:3	2002Oct14
PASS			
321	r400sc_clip_rect_03	0:0:3	2002Oct14
PASS			
322	r400sc_clip_rect_04	0:0:3	2002Oct14
PASS			
323	r400sc_clip_rect_fc_01	0:0:3	2002Oct14
PASS			
324	r400sc_diamond_exit_01	0:0:3	2002Oct14
PASS			
325	r400sc_diamond_exit_02	0:0:3	2002Oct14
PASS			
326	r400sc_diamond_exit_03	0:0:3	2002Oct14
PASS			
327	r400sc_diamond_exit_04	0:0:3	2002Oct14
PASS			
328	r400sc_diamond_exit_05	0:0:3	2002Oct14
PASS			
329	r400sc_jss_1x1_printypes_01	0:0:3	2002Oct14
PASS			
330	r400sc_jss_1x2_01	0:0:3	2002Oct14
PASS			
331	r400sc_jss_1x2_02	0:0:3	2002Oct14

PASS			
332	r400sc_jss_1x2_primtypes_01	0:0:3	2002Oct14
PASS			
333	r400sc_jss_1x3_01	0:0:3	2002Oct14
PASS			
334	r400sc_jss_1x3_02	0:0:3	2002Oct14
PASS			
335	r400sc_jss_1x3_primtypes_01	0:0:3	2002Oct14
PASS			
336	r400sc_jss_1x4_01	0:0:3	2002Oct14
PASS			
337	r400sc_jss_1x4_02	0:0:3	2002Oct14
PASS			
338	r400sc_jss_1x4_primtypes_01	0:0:3	2002Oct14
PASS			
339	r400sc_jss_2x1_01	0:0:3	2002Oct14
PASS			
340	r400sc_jss_2x1_02	0:0:3	2002Oct14
PASS			
341	r400sc_jss_2x1_primtypes_01	0:0:3	2002Oct14
PASS			
342	r400sc_jss_2x2_01	0:0:3	2002Oct14
PASS			
343	r400sc_jss_2x2_02	0:0:3	2002Oct14
PASS			
344	r400sc_jss_2x2_primtypes_01	0:0:3	2002Oct14
PASS			
345	r400sc_jss_2x3_01	0:0:3	2002Oct14
PASS			
346	r400sc_jss_2x3_02	0:0:3	2002Oct14
PASS			
347	r400sc_jss_2x3_primtypes_01	0:0:3	2002Oct14
PASS			
348	r400sc_jss_2x4_01	0:0:3	2002Oct14
PASS			
349	r400sc_jss_2x4_02	0:0:3	2002Oct14
PASS			
350	r400sc_jss_2x4_primtypes_01	0:0:3	2002Oct14
PASS			
351	r400sc_jss_3x1_01	0:0:3	2002Oct14
PASS			
352	r400sc_jss_3x1_02	0:0:3	2002Oct14
PASS			
353	r400sc_jss_3x1_primtypes_01	0:0:3	2002Oct14
PASS			
354	r400sc_jss_3x2_01	0:0:3	2002Oct14
PASS			
355	r400sc_jss_3x2_02	0:0:3	2002Oct14

PASS			
356	r400sc_jss_3x2_primitives_01	0:0:3	2002Oct14
PASS			
357	r400sc_jss_3x3_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
358	r400sc_jss_3x3_02	0:0:3	2002Oct14
PASS			
359	r400sc_jss_3x3_primitives_01	0:0:3	2002Oct14
PASS			
360	r400sc_jss_3x4_01	0:0:3	2002Oct14
PASS			
361	r400sc_jss_3x4_02	0:0:3	2002Oct14
PASS			
362	r400sc_jss_3x4_03	0:0:3	2002Oct14
PASS			
363	r400sc_jss_3x4_primitives_01	0:0:3	2002Oct14
PASS			
364	r400sc_jss_4x1_01	0:0:3	2002Oct14
PASS			
365	r400sc_jss_4x1_02	0:0:3	2002Oct14
PASS			
366	r400sc_jss_4x1_primitives_01	0:0:3	2002Oct14
PASS			
367	r400sc_jss_4x2_01	0:0:3	2002Oct14
PASS			
368	r400sc_jss_4x2_02	0:0:3	2002Oct14
PASS			
369	r400sc_jss_4x2_primitives_01	0:0:3	2002Oct14
PASS			
370	r400sc_jss_4x3_01	0:0:3	2002Oct14
PASS			
371	r400sc_jss_4x3_02	0:0:3	2002Oct14
PASS			
372	r400sc_jss_4x3_primitives_01	0:0:3	2002Oct14
PASS			
373	r400sc_jss_4x4_01	0:0:3	2002Oct14
PASS			
374	r400sc_jss_4x4_02	0:0:3	2002Oct14
PASS			
375	r400sc_jss_4x4_03	0:0:3	2002Oct14
PASS			
376	r400sc_jss_4x4_aa_mask_01	0:0:3	2002Oct14
PASS			
377	r400sc_jss_4x4_aa_mask_02	0:0:3	2002Oct14
PASS			
378	r400sc_jss_4x4_fc_01	0:0:3	2002Oct14
PASS			

379	r400sc_jss_4x4_fc_02	0:0:3	2002Oct14
	PASS		
380	r400sc_jss_4x4_max_dist_01	0:0:3	2002Oct14
	PASS		
381	r400sc_jss_4x4_primitives_01	0:0:3	2002Oct14
	PASS		
382	r400sc_line_dx10_eq_0_01	0:0:3	2002Oct14
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383	r400sc_line_dx10_ge_0_01	0:0:3	2002Oct14
	PASS		
384	r400sc_line_dx10_lt_0_01	0:0:3	2002Oct14
	PASS		
385	r400sc_line_dy10_eq_0_01	0:0:3	2002Oct14
	PASS		
386	r400sc_line_dy10_ge_0_01	0:0:3	2002Oct14
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387	r400sc_line_dy10_lt_0_01	0:0:3	2002Oct14
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388	r400sc_line_expand_width_msa_8_01	0:0:3	2002Oct14
	PASS		
389	r400sc_line_expand_width_msa_8_02	0:0:3	2002Oct14
	PASS		
390	r400sc_line_expand_width_msa_8_03	0:0:3	2002Oct14
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391	r400sc_line_jss_3x4_01	0:0:3	2002Oct14
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392	r400sc_line_list_01	0:0:3	2002Oct14
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393	r400sc_line_list_02	0:0:3	2002Oct14
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394	r400sc_line_list_concentric_circle_01	0:0:3	2002Oct14
	PASS		
395	r400sc_line_list_concentric_circle_02	0:0:3	2002Oct14
	PASS		
396	r400sc_line_list_concentric_circle_03	0:0:3	2002Oct14
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397	r400sc_line_list_textured_01	0:0:3	2002Oct14
	PASS		
398	r400sc_line_list_verify_st_01	0:0:3	2002Oct14
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399	r400sc_line_list_verify_st_01_bug	0:0:3	2002Oct9
	PASS		
400	r400sc_line_msa_8_01	0:0:3	2002Oct14
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401	r400sc_line_msa_8_textured_01	0:0:3	2002Oct14
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402	r400sc_line_stipple_01	0:0:3	2002Oct14
	PASS		

403	r400sc_line_stipple_02	0:0:3	2002Oct14
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404	r400sc_line_stipple_03	0:0:3	2002Oct14
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405	r400sc_line_stipple_04	0:0:3	2002Oct14
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406	r400sc_line_stipple_05	0:0:3	2002Oct14
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407	r400sc_line_stipple_06	0:0:3	2002Oct14
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408	r400sc_line_stipple_07	0:0:3	2002Oct14
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409	r400sc_line_stipple_08	0:0:3	2002Oct14
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410	r400sc_line_stipple_09	0:0:3	2002Oct14
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411	r400sc_line_stipple_10	0:0:3	2002Oct14
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412	r400sc_line_stipple_11	0:0:3	2002Oct14
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413	r400sc_line_stipple_12	0:0:3	2002Oct14
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414	r400sc_line_stipple_13	0:0:3	2002Oct14
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415	r400sc_line_stipple_14	0:0:3	2002Oct14
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416	r400sc_line_stipple_15	0:0:3	2002Oct14
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417	r400sc_line_stipple_16	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
418	r400sc_line_stipple_17	0:0:3	2002Oct14
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419	r400sc_line_stipple_18	0:0:3	2002Oct14
	PASS		
420	r400sc_line_stipple_19	0:0:3	2002Oct14
	PASS		
421	r400sc_line_stipple_20	0:0:3	2002Oct14
	PASS		
422	r400sc_line_stipple_21	0:0:3	2002Oct14
	PASS		
423	r400sc_line_stipple_22	0:0:3	2002Oct14
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
424	r400sc_line_stipple_23	0:0:3	2002Oct14
	PASS		
425	r400sc_line_stipple_fc_08	0:0:3	2002Oct14
	PASS		
426	r400sc_line_strip_stipple_01	0:0:3	2002Oct14

PASS			
427	r400sc_msaa_1_01	0:0:3	2002Oct14
PASS			
428	r400sc_msaa_1_primitives_01	0:0:3	2002Oct14
PASS			
429	r400sc_msaa_1_rectangle_list_01	0:0:3	2002Oct14
PASS			
430	r400sc_msaa_1_rectangle_list_02	0:0:3	2002Oct14
PASS			
431	r400sc_msaa_1_rectangle_list_03	0:0:3	2002Oct14
PASS			
432	r400sc_msaa_1_rectangle_list_04	0:0:3	2002Oct14
PASS			
433	r400sc_msaa_1_rectangle_list_05	0:0:3	2002Oct14
PASS			
434	r400sc_msaa_1_rectangle_list_06	0:0:3	2002Oct14
PASS			
435	r400sc_msaa_1_rectangle_list_07	0:0:3	2002Oct14
PASS			
436	r400sc_msaa_1_rectangle_list_08	0:0:3	2002Oct14
PASS			
437	r400sc_msaa_1_zbuffer_rectangle_list_01	0:0:3	2002Oct14
PASS			
438	r400sc_msaa_1_zbuffer_rectangle_list_02	0:0:3	2002Oct14
PASS			
439	r400sc_msaa_2_primitives_01	0:0:3	2002Oct14
PASS			
440	r400sc_msaa_2_rectangle_list_01	0:0:3	2002Oct14
PASS			
441	r400sc_msaa_2_rectangle_list_02	0:0:3	2002Oct14
PASS			
442	r400sc_msaa_2_rectangle_list_03	0:0:3	2002Oct14
PASS			
443	r400sc_msaa_2_rectangle_list_04	0:0:3	2002Oct14
PASS			
444	r400sc_msaa_2_rectangle_list_05	0:0:3	2002Oct14
PASS			
445	r400sc_msaa_2_rectangle_list_06	0:0:3	2002Oct14
PASS			
446	r400sc_msaa_2_rectangle_list_07	0:0:3	2002Oct14
PASS			
447	r400sc_msaa_2_rectangle_list_08	0:0:3	2002Oct14
PASS			
448	r400sc_msaa_2_zbuffer_rectangle_list_01	0:0:3	2002Oct14
PASS			
449	r400sc_msaa_2_zbuffer_rectangle_list_02	0:0:3	2002Oct14
PASS			
450	r400sc_msaa_3_primitives_01	0:0:3	2002Oct14

PASS			
451	r400sc_msa_3_rectangle_list_01	0:0:3	2002Oct14
PASS			
452	r400sc_msa_3_rectangle_list_02	0:0:3	2002Oct14
PASS			
453	r400sc_msa_3_rectangle_list_03	0:0:3	2002Oct14
PASS			
454	r400sc_msa_3_rectangle_list_04	0:0:3	2002Oct14
PASS			
455	r400sc_msa_3_rectangle_list_05	0:0:3	2002Oct14
PASS			
456	r400sc_msa_3_rectangle_list_06	0:0:3	2002Oct14
PASS			
457	r400sc_msa_3_rectangle_list_07	0:0:3	2002Oct14
PASS			
458	r400sc_msa_3_rectangle_list_08	0:0:3	2002Oct14
PASS			
459	r400sc_msa_3_zbuffer_rectangle_list_01	0:0:3	2002Oct14
PASS			
460	r400sc_msa_3_zbuffer_rectangle_list_02	0:0:3	2002Oct14
PASS			
461	r400sc_msa_4_01	0:0:3	2002Oct14
PASS			
462	r400sc_msa_4_primtypes_01	0:0:3	2002Oct14
PASS			
463	r400sc_msa_4_rectangle_list_01	0:0:3	2002Oct14
PASS			
464	r400sc_msa_4_rectangle_list_02	0:0:3	2002Oct14
PASS			
465	r400sc_msa_4_rectangle_list_03	0:0:3	2002Oct14
PASS			
466	r400sc_msa_4_rectangle_list_04	0:0:3	2002Oct14
PASS			
467	r400sc_msa_4_rectangle_list_05	0:0:3	2002Oct14
PASS			
468	r400sc_msa_4_rectangle_list_06	0:0:3	2002Oct14
PASS			
469	r400sc_msa_4_rectangle_list_07	0:0:3	2002Oct14
PASS			
470	r400sc_msa_4_rectangle_list_08	0:0:3	2002Oct14
PASS			
471	r400sc_msa_4_zbuffer_rectangle_list_01	0:0:3	2002Oct14
PASS			
472	r400sc_msa_4_zbuffer_rectangle_list_02	0:0:3	2002Oct14
PASS			
473	r400sc_msa_6_01	0:0:3	2002Oct14
PASS			
474	r400sc_msa_6_primtypes_01	0:0:3	2002Oct14

PASS			
475	r400sc_msa_6_rectangle_list_01	0:0:3	2002Oct14
PASS			
476	r400sc_msa_6_rectangle_list_02	0:0:3	2002Oct14
PASS			
477	r400sc_msa_6_rectangle_list_03	0:0:3	2002Oct14
PASS			
478	r400sc_msa_6_rectangle_list_04	0:0:3	2002Oct14
PASS			
479	r400sc_msa_6_rectangle_list_05	0:0:3	2002Oct14
PASS			
480	r400sc_msa_6_rectangle_list_06	0:0:3	2002Oct14
PASS			
481	r400sc_msa_6_rectangle_list_07	0:0:3	2002Oct14
PASS			
482	r400sc_msa_6_rectangle_list_08	0:0:3	2002Oct14
PASS			
483	r400sc_msa_6_zbuffer_rectangle_list_01	0:0:3	2002Oct14
PASS			
484	r400sc_msa_6_zbuffer_rectangle_list_02	0:0:3	2002Oct14
PASS			
485	r400sc_msa_8_01	0:0:3	2002Oct14
PASS			
486	r400sc_msa_8_02	0:0:3	2002Oct14
PASS			
487	r400sc_msa_8_03	0:0:3	2002Oct14
PASS			
488	r400sc_msa_8_04	0:0:3	2002Oct14
PASS			
489	r400sc_msa_8_05	0:0:3	2002Oct14
PASS			
490	r400sc_msa_8_aa_mask_01	0:0:3	2002Oct14
PASS			
491	r400sc_msa_8_aa_mask_02	0:0:3	2002Oct14
PASS			
492	r400sc_msa_8_aa_mask_fc_02	0:0:3	2002Oct14
PASS			
493	r400sc_msa_8_hos_PNT_cont_cp_qn_complex_01	0:0:3	2002Oct14
PASS			
494	r400sc_msa_8_primtypes_01	0:0:3	2002Oct14
PASS			
495	r400sc_msa_8_rectangle_list_01	0:0:3	2002Oct14
PASS			
496	r400sc_msa_8_rectangle_list_02	0:0:3	2002Oct14
PASS			
497	r400sc_msa_8_rectangle_list_03	0:0:3	2002Oct14
PASS			
498	r400sc_msa_8_rectangle_list_04	0:0:3	2002Oct14

PASS			
499	r400sc_msaa_8_rectangle_list_05	0:0:3	2002Oct14
PASS			
500	r400sc_msaa_8_rectangle_list_06	0:0:3	2002Oct14
PASS			
501	r400sc_msaa_8_rectangle_list_07	0:0:3	2002Oct14
PASS			
502	r400sc_msaa_8_rectangle_list_08	0:0:3	2002Oct14
PASS			
503	r400sc_msaa_8_zbuffer_rectangle_list_01	0:0:3	2002Oct14
PASS			
504	r400sc_msaa_8_zbuffer_rectangle_list_02	0:0:3	2002Oct14
PASS			
505	r400sc_msaa_X_primitives_01_bug	0:0:3	2002Oct9
PASS			
506	r400sc_msaa_X_primitives_02_bug	0:0:3	2002Oct9
PASS			
507	r400sc_multi_chip_prim_discard_00_07	0:0:3	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
508	r400sc_multi_chip_prim_discard_00_07_standard	0:0:3	2002Oct11
PASS			
509	r400sc_multi_chip_prim_discard_00_07_test00	0:0:3	2002Oct11
INCOMPLETE			
510	r400sc_multi_chip_prim_discard_00_07_test01	0:0:3	2002Oct11
PASS			
511	r400sc_multi_chip_prim_discard_00_07_test02	0:0:3	2002Oct11
INCOMPLETE			
512	r400sc_multi_chip_prim_discard_00_07_test03	0:0:3	2002Oct11
PASS			
513	r400sc_multi_chip_prim_discard_00_07_test04	0:0:3	2002Oct11
INCOMPLETE			
514	r400sc_multi_chip_prim_discard_00_07_test05	0:0:3	2002Oct11
INCOMPLETE			
515	r400sc_multi_chip_prim_discard_00_07_test06	0:0:3	2002Oct11
PASS			
516	r400sc_multi_chip_tile_discard_00_07	0:0:3	2002Oct7
PASS			
517	r400sc_multi_chip_tile_discard_00_07_standard	0:0:3	2002Oct11
PASS			
518	r400sc_multi_chip_tile_discard_00_07_test00	0:0:3	2002Oct11
PASS			
519	r400sc_multi_chip_tile_discard_00_07_test01	0:0:3	2002Oct11
PASS			
520	r400sc_multi_chip_tile_discard_00_07_test02	0:0:3	2002Oct11
PASS			
521	r400sc_multi_chip_tile_discard_00_07_test03	0:0:3	2002Oct11
PASS			
522	r400sc_multi_chip_tile_discard_00_07_test04	0:0:3	2002Oct11


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**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp VgtPaClipS_VgtSide.cmp
compare file empty
523  r400sc_multi_chip_tile_discard_00_07_test05      0:0:3      2002Oct11
INCOMPLETE
524  r400sc_multi_chip_tile_discard_00_07_test06      0:0:3      2002Oct11
INCOMPLETE
525  r400sc_null_triangles_01                          0:0:3      2002Oct14
PASS
526  r400sc_null_triangles_fc_01                       0:0:3      2002Oct14
PASS
527  r400sc_packed_color_01                            0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

528  r400sc_perf_01                                    0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

529  r400sc_perf_02                                    0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

530  r400sc_perf_03                                    0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

531  r400sc_pinwheel_01                               0:0:3      2002Oct14
PASS
532  r400sc_pinwheel_02                               0:0:3      2002Oct14
PASS
533  r400sc_pinwheel_02_bug                           0:0:3      2002Oct9
PASS
534  r400sc_point_jss_3x4_01                          0:0:3      2002Oct14
PASS
535  r400sc_point_list_01                             0:0:3      2002Oct14
PASS
536  r400sc_point_list_02                             0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

537  r400sc_point_list_03                             0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

538  r400sc_point_list_04                             0:0:3      2002Oct14
PASS
539  r400sc_point_list_05                             0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

540  r400sc_point_list_06                             0:0:3      2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

541  r400sc_point_list_06_bug                         0:0:3      2002Oct9
PASS

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542	r400sc_point_list_06_bug_02	0:0:3	2002Oct9
	PASS		
543	r400sc_point_list_07	0:0:3	2002Oct14
	PASS		
544	r400sc_point_list_08	0:0:3	2002Oct14
	PASS		
545	r400sc_point_list_09	0:0:3	2002Oct14
	PASS		
546	r400sc_point_msa_8_01	0:0:3	2002Oct14
	PASS		
547	r400sc_poly_offset_01	0:0:3	2002Oct14
	PASS		
548	r400sc_poly_offset_02	0:0:3	2002Oct14
	PASS		
549	r400sc_poly_offset_03	0:0:3	2002Oct14
	PASS		
550	r400sc_poly_offset_04	0:0:3	2002Oct14
	PASS		
551	r400sc_poly_offset_05	0:0:3	2002Oct14
	PASS		
552	r400sc_poly_offset_06	0:0:3	2002Oct14
	PASS		
553	r400sc_poly_offset_07	0:0:3	2002Oct14
	PASS		
554	r400sc_poly_offset_08	0:0:3	2002Oct14
	PASS		
555	r400sc_poly_offset_09	0:0:3	2002Oct14
	PASS		
556	r400sc_poly_offset_10	0:0:3	2002Oct14
	PASS		
557	r400sc_poly_offset_10_bug	0:0:3	2002Oct9
	PASS		
558	r400sc_poly_offset_fc_01	0:0:3	2002Oct14
	PASS		
559	r400sc_polygon_stipple_01	0:0:3	2002Oct14
	PASS		
560	r400sc_polymode_tri_fill_01	0:0:3	2002Oct14
	PASS		
561	r400sc_polymode_tri_fill_01_bug	0:0:3	2002Oct9
	PASS		
562	r400sc_prsp_byc_intrp_ref_pix_01	0:0:3	2002Oct14
	PASS		
563	r400sc_prsp_byc_intrp_ref_pix_02	0:0:3	2002Oct14
	PASS		
564	r400sc_prsp_byc_intrp_ref_pix_03	0:0:3	2002Oct14
	PASS		
565	r400sc_prsp_byc_intrp_ref_pix_04	0:0:3	2002Oct14
	PASS		

566	r400sc_prsp_byc_intrp_ref_pix_05	0:0:3	2002Oct14
	PASS		
567	r400sc_prsp_byc_intrp_ref_pix_06	0:0:3	2002Oct14
	PASS		
568	r400sc_prsp_byc_intrp_ref_pix_07	0:0:3	2002Oct14
	PASS		
569	r400sc_prsp_byc_intrp_ref_pix_08	0:0:3	2002Oct14
	PASS		
570	r400sc_raster_fill_rule_01	0:0:3	2002Oct14
	PASS		
571	r400sc_raster_fill_rule_02	0:0:3	2002Oct14
	PASS		
572	r400sc_raster_fill_rule_03	0:0:3	2002Oct14
	PASS		
573	r400sc_raster_fill_rule_04	0:0:3	2002Oct14
	PASS		
574	r400sc_raster_fill_rule_05	0:0:3	2002Oct14
	PASS		
575	r400sc_raster_fill_rule_fc_01	0:0:3	2002Oct14
	PASS		
576	r400sc_rectangle_list_01	0:0:3	2002Oct14
	PASS		
577	r400sc_rectangle_list_02	0:0:3	2002Oct14
	PASS		
578	r400sc_rectangle_list_03	0:0:3	2002Oct14
	PASS		
579	r400sc_rectangle_list_04	0:0:3	2002Oct14
	PASS		
580	r400sc_rectangle_list_05	0:0:3	2002Oct14
	PASS		
581	r400sc_rectangle_list_06	0:0:3	2002Oct14
	PASS		
582	r400sc_rectangle_list_07	0:0:3	2002Oct14
	PASS		
583	r400sc_rectangle_list_08	0:0:3	2002Oct14
	PASS		
584	r400sc_scissor_rect_01	0:0:3	2002Oct14
	PASS		
585	r400sc_scissor_rect_02	0:0:3	2002Oct14
	PASS		
586	r400sc_scissor_rect_03	0:0:3	2002Oct14
	PASS		
587	r400sc_scissor_rect_04	0:0:3	2002Oct14
	PASS		
588	r400sc_scissor_rect_05	0:0:3	2002Oct14
	PASS		
589	r400sc_scissor_rect_fc_01	0:0:3	2002Oct14
	PASS		

590	r400sc_set_state_01	0:0:3	2002Oct14
	PASS		
591	r400sc_sp_sample_cntl_01	0:0:3	2002Oct14
	PASS		
592	r400sc_sp_sample_cntl_02	0:0:3	2002Oct14
	PASS		
593	r400sc_sp_sample_cntl_03	0:0:3	2002Oct14
	PASS		
594	r400sc_sp_sample_cntl_04	0:0:3	2002Oct14
	PASS		
595	r400sc_sp_sample_cntl_05	0:0:3	2002Oct14
	PASS		
596	r400sc_sp_sample_cntl_06	0:0:3	2002Oct14
	PASS		
597	r400sc_sp_sample_cntl_07	0:0:3	2002Oct14
	PASS		
598	r400sc_sp_sample_cntl_08	0:0:3	2002Oct14
	PASS		
599	r400sc_tri_16_par_64_dwords_01	0:0:3	2002Oct14
	PASS		
600	r400sc_tri_8textures_01	0:0:3	2002Oct14
	PASS		
601	r400sc_tri_8textures_02	0:0:3	2002Oct14
	PASS		
602	r400sc_tri_walk_start_vertex_01	0:0:3	2002Oct14
	PASS		
603	r400sc_tri_walk_start_vertex_02	0:0:3	2002Oct14
	PASS		
604	r400sc_tri_walk_start_vertex_03	0:0:3	2002Oct14
	PASS		
605	r400sc_tri_walk_start_vertex_04	0:0:3	2002Oct14
	PASS		
606	r400sc_tri_walk_start_vertex_05	0:0:3	2002Oct14
	PASS		
607	r400sc_tri_walk_start_vertex_06	0:0:3	2002Oct14
	PASS		
608	r400sc_tri_walk_start_vertex_07	0:0:3	2002Oct14
	PASS		
609	r400sc_tri_walk_start_vertex_08	0:0:3	2002Oct14
	PASS		
610	r400sc_tri_walk_start_vertex_09	0:0:3	2002Oct14
	PASS		
611	r400sc_tri_walk_start_vertex_10	0:0:3	2002Oct14
	PASS		
612	r400sc_tri_walk_start_vertex_11	0:0:3	2002Oct14
	PASS		
613	r400sc_tri_walk_start_vertex_12	0:0:3	2002Oct14
	PASS		

614	r400sc_tri_walk_start_vertex_13	0:0:3	2002Oct14
	PASS		
615	r400sc_tri_walk_start_vertex_14	0:0:3	2002Oct14
	PASS		
616	r400sc_tri_walk_start_vertex_15	0:0:3	2002Oct14
	PASS		
617	r400sc_tri_walk_start_vertex_16	0:0:3	2002Oct14
	PASS		
618	r400sc_triangle_stipple_01	0:0:3	2002Oct14
	PASS		
619	r400sc_window_offset_01	0:0:3	2002Oct14
	PASS		
620	r400sc_window_offset_02	0:0:3	2002Oct14
	PASS		
621	r400sc_window_offset_03	0:0:3	2002Oct14
	PASS		
622	r400sc_window_offset_04	0:0:3	2002Oct14
	PASS		
623	r400sc_window_offset_05	0:0:3	2002Oct14
	PASS		
624	r400sc_window_offset_fc_01	0:0:3	2002Oct14
	PASS		
625	r400sc_zbuffer_line_list_01	0:0:3	2002Oct14
	PASS		
626	r400sc_zbuffer_point_list_01	0:0:3	2002Oct14
	PASS		
627	r400sc_zbuffer_rectangle_list_01	0:0:3	2002Oct14
	PASS		
628	r400sc_zbuffer_rectangle_list_02	0:0:3	2002Oct14
	PASS		
629	r400sc_zbuffer_rectangle_list_fc_02	0:0:3	2002Oct14
	PASS		
630	r400sc_zbuffer_triangle_list_01	0:0:3	2002Oct14
	PASS		
631	r400su_4tri_text_offscreen_01	0:0:3	2002Oct14
	PASS		
632	r400su_baryc_test_01	0:0:3	2002Oct14
	PASS		
633	r400su_baryc_test_02	0:0:3	2002Oct14
	PASS		
634	r400su_baryc_test_03	0:0:3	2002Oct14
	PASS		
635	r400su_baryc_test_04	0:0:3	2002Oct14
	PASS		
636	r400su_clip_baryc_test_01	0:0:3	2002Oct14
	PASS		
637	r400su_clip_baryc_test_02	0:0:3	2002Oct14
	PASS		

638	r400su_clip_edgeflag_polymode_01	0:0:3	2002Oct14
	PASS		
639	r400su_clip_pointsize_test_01	0:0:3	2002Oct14
	PASS		
640	r400su_clip_pointtest_01	0:0:3	2002Oct14
	PASS		
641	r400su_clip_pointtest_02	0:0:3	2002Oct14
	PASS		
642	r400su_clip_pointtest_03	0:0:3	2002Oct14
	PASS		
643	r400su_clip_pointtest_04	0:0:3	2002Oct14
	PASS		
644	r400su_clip_polymode_test_01	0:0:3	2002Oct14
	PASS		
645	r400su_clipline_01	0:0:3	2002Oct14
	PASS		
646	r400su_clippoint_01	0:0:3	2002Oct14
	PASS		
647	r400su_clipvertexsortingfunctional_01	0:0:3	2002Oct14
	PASS		
648	r400su_cullingfunctional_01	0:0:3	2002Oct14
	PASS		
649	r400su_degentri_test_01	0:0:3	2002Oct14
	PASS		
650	r400su_degentri_test_02	0:0:3	2002Oct14
	PASS		
651	r400su_degentri_test_03	0:0:3	2002Oct14
	PASS		
652	r400su_degentri_test_04	0:0:3	2002Oct14
	PASS		
653	r400su_edge_flag_01	0:0:3	2002Oct14
	PASS		
654	r400su_edge_flag_02	0:0:3	2002Oct14
	PASS		
655	r400su_edgeflags_triangle_01	0:0:3	2002Oct14
	PASS		
656	r400su_edgeflags_triangle_02	0:0:3	2002Oct14
	PASS		
657	r400su_geom_sort_01	0:0:3	2002Oct14
	PASS		
658	r400su_line_end_cap_functional_01	0:0:3	2002Oct14
	PASS		
659	r400su_line_end_cap_width_functional_02	0:0:3	2002Oct14
	PASS		
660	r400su_line_orientation_01	0:0:3	2002Oct14
	PASS		
661	r400su_line_orientation_02	0:0:3	2002Oct14
	PASS		

662	r400su_line_orientation_dx01_01	0:0:3	2002Oct14
	PASS		
663	r400su_line_orientation_dx01_02	0:0:3	2002Oct14
	PASS		
664	r400su_line_orientation_dy01_01	0:0:3	2002Oct14
	PASS		
665	r400su_line_orientation_dy01_02	0:0:3	2002Oct14
	PASS		
666	r400su_line_test_01	0:0:3	2002Oct14
	PASS		
667	r400su_line_test_02	0:0:3	2002Oct14
	PASS		
668	r400su_line_x_major_01	0:0:3	2002Oct14
	PASS		
669	r400su_line_x_major_02	0:0:3	2002Oct14
	PASS		
670	r400su_line_y_major_01	0:0:3	2002Oct14
	PASS		
671	r400su_line_y_major_02	0:0:3	2002Oct14
	PASS		
672	r400su_longstrip_01	0:0:3	2002Oct14
	PASS		
673	r400su_multi_context_01	0:0:3	2002Oct14
	PASS		
674	r400su_multi_prim_01	0:0:3	2002Oct14
	PASS		
675	r400su_multi_prim_02	0:0:3	2002Oct14
	PASS		
676	r400su_parallel_orientation_all_01	0:0:3	2002Oct14
	PASS		
677	r400su_parallel_orientation_all_02	0:0:3	2002Oct14
	PASS		
678	r400su_pc_management_01	0:0:3	2002Oct14
	PASS		
679	r400su_pc_management_02	0:0:3	2002Oct14
	PASS		
680	r400su_pc_management_03	0:0:3	2002Oct14
	PASS		
681	r400su_point_sprite_01	0:0:3	2002Oct14
	PASS		
682	r400su_point_sprite_02	0:0:3	2002Oct14
	PASS		
683	r400su_point_sprite_03	0:0:3	2002Oct14
	PASS		
684	r400su_point_sprite_04	0:0:3	2002Oct14
	PASS		
685	r400su_point_sprite_05	0:0:3	2002Oct14
	PASS		

686	r400su_point_sprite_06	0:0:3	2002Oct14
	PASS		
687	r400su_point_sprite_07	0:0:3	2002Oct14
	PASS		
688	r400su_point_sprite_08	0:0:3	2002Oct14
	PASS		
689	r400su_point_wl6_hl_functional_01	0:0:3	2002Oct14
	PASS		
690	r400su_point_wl_hl6_functional_01	0:0:3	2002Oct14
	PASS		
691	r400su_pointsizepresent_01	0:0:3	2002Oct14
	PASS		
692	r400su_pointsizepresent_02	0:0:3	2002Oct14
	PASS		
693	r400su_polymode_lines_degen_triangle_01	0:0:3	2002Oct14
	PASS		
694	r400su_polymode_lines_degen_triangle_02	0:0:3	2002Oct14
	PASS		
695	r400su_polymode_lines_zero_area_triangle_01	0:0:3	2002Oct14
	PASS		
696	r400su_polymode_lines_zero_area_triangle_02	0:0:3	2002Oct14
	PASS		
697	r400su_polymode_points_degen_triangle_01	0:0:3	2002Oct14
	PASS		
698	r400su_polymode_points_degen_triangle_02	0:0:3	2002Oct14
	PASS		
699	r400su_polymode_points_zero_area_triangle_01	0:0:3	2002Oct14
	PASS		
700	r400su_polymode_points_zero_area_triangle_02	0:0:3	2002Oct14
	PASS		
701	r400su_polymode_rectangle_01	0:0:3	2002Oct14
	PASS		
702	r400su_polymode_zero_area_triangle_01	0:0:3	2002Oct14
	PASS		
703	r400su_polymode_zero_area_triangle_02	0:0:3	2002Oct14
	PASS		
704	r400su_polymode_zero_area_triangle_03	0:0:3	2002Oct14
	PASS		
705	r400su_polymode_zero_area_triangle_04	0:0:3	2002Oct14
	PASS		
706	r400su_polymodeculling_01	0:0:3	2002Oct14
	PASS		
707	r400su_polymodefunctional_01	0:0:3	2002Oct14
	PASS		
708	r400su_provoking_vtx_edgeflags_triangle_01	0:0:3	2002Oct14
	PASS		
709	r400su_provoking_vtx_edgeflags_triangle_02	0:0:3	2002Oct14
	PASS		

710	r400su_provoking_vtx_edgeflags_triangle_03	0:0:3	2002Oct14
	PASS		
711	r400su_provoking_vtx_edgeflags_triangle_04	0:0:3	2002Oct14
	PASS		
712	r400su_provoking_vtx_line_01	0:0:3	2002Oct14
	PASS		
713	r400su_provoking_vtx_point_01	0:0:3	2002Oct14
	PASS		
714	r400su_provoking_vtx_polymode_rectangle_01	0:0:3	2002Oct14
	PASS		
715	r400su_provoking_vtx_rectangle_01	0:0:3	2002Oct14
	PASS		
716	r400su_provoking_vtx_triangle_01	0:0:3	2002Oct14
	PASS		
717	r400su_rand_line_01	0:0:3	2002Oct14
	PASS		
718	r400su_rand_point_01	0:0:3	2002Oct14
	PASS		
719	r400su_rand_tri_01	0:0:3	2002Oct14
	PASS		
720	r400su_rectangle_01	0:0:3	2002Oct14
	PASS		
721	r400su_rectangle_list_01	0:0:3	2002Oct14
	PASS		
722	r400su_sliver_01	0:0:3	2002Oct14
	PASS		
723	r400su_triarea_test_01	0:0:3	2002Oct14
	PASS		
724	r400su_triarea_test_02	0:0:3	2002Oct14
	PASS		
725	r400su_triarea_test_03	0:0:3	2002Oct14
	PASS		
726	r400su_triarea_test_04	0:0:3	2002Oct14
	PASS		
727	r400su_vertexsortingfunctional_01	0:0:3	2002Oct14
	PASS		
728	r400su_w_grad_test_01	0:0:3	2002Oct14
	PASS		
729	r400su_w_grad_test_02	0:0:3	2002Oct14
	PASS		
730	r400su_w_grad_test_03	0:0:3	2002Oct14
	PASS		
731	r400su_z_grad_test_01	0:0:3	2002Oct14
	PASS		
732	r400su_z_grad_test_02	0:0:3	2002Oct14
	PASS		
733	r400su_z_grad_test_03	0:0:3	2002Oct14
	PASS		

734	r400su_zero_area_test_01	0:0:3	2002Oct14
	PASS		
735	r400su_zero_area_test_02	0:0:3	2002Oct14
	PASS		
736	r400su_zero_area_test_03	0:0:3	2002Oct14
	PASS		
737	r400su_zero_area_test_04	0:0:3	2002Oct14
	PASS		
738	r400vgt_auto_index_line_list_01	0:0:3	2002Oct14
	PASS		
739	r400vgt_auto_index_line_loop_01	0:0:3	2002Oct14
	PASS		
740	r400vgt_auto_index_line_strip_01	0:0:3	2002Oct14
	PASS		
741	r400vgt_auto_index_points_01	0:0:3	2002Oct14
	PASS		
742	r400vgt_auto_index_polygon_01	0:0:3	2002Oct14
	PASS		
743	r400vgt_auto_index_primitives_01	0:0:3	2002Oct14
	PASS		
744	r400vgt_auto_index_quad_list_01	0:0:3	2002Oct14
	PASS		
745	r400vgt_auto_index_quad_strip_01	0:0:3	2002Oct14
	PASS		
746	r400vgt_auto_index_rectangle_list_01	0:0:3	2002Oct14
	PASS		
747	r400vgt_auto_index_tri_fan_01	0:0:3	2002Oct14
	PASS		
748	r400vgt_auto_index_tri_list_01	0:0:3	2002Oct14
	PASS		
749	r400vgt_auto_index_tri_strip_01	0:0:3	2002Oct14
	PASS		
750	r400vgt_auto_index_tri_wflags_01	0:0:3	2002Oct14
	PASS		
751	r400vgt_dma_engine_01	0:0:3	2002Oct14
	PASS		
752	r400vgt_dma_engine_02	0:0:3	2002Oct14
	PASS		
753	r400vgt_dma_engine_03	0:0:3	2002Oct14
	PASS		
754	r400vgt_dma_engine_04	0:0:3	2002Oct14
	PASS		
755	r400vgt_dma_engine_05	0:0:3	2002Oct14
	PASS		
756	r400vgt_dma_engine_06	0:0:3	2002Oct14
	PASS		
757	r400vgt_dma_engine_07	0:0:3	2002Oct14
	PASS		

758	r400vgt_dma_engine_08	0:0:3	2002Oct14
	PASS		
759	r400vgt_dma_engine_09	0:0:3	2002Oct14
	PASS		
760	r400vgt_dma_engine_10	0:0:3	2002Oct14
	PASS		
761	r400vgt_dma_index_line_list_01	0:0:3	2002Oct14
	PASS		
762	r400vgt_dma_index_line_loop_01	0:0:3	2002Oct14
	PASS		
763	r400vgt_dma_index_line_strip_01	0:0:3	2002Oct14
	PASS		
764	r400vgt_dma_index_points_01	0:0:3	2002Oct14
	PASS		
765	r400vgt_dma_index_polygon_01	0:0:3	2002Oct14
	PASS		
766	r400vgt_dma_index_primitives_01	0:0:3	2002Oct14
	PASS		
767	r400vgt_dma_index_quad_list_01	0:0:3	2002Oct14
	PASS		
768	r400vgt_dma_index_quad_strip_01	0:0:3	2002Oct14
	PASS		
769	r400vgt_dma_index_rectangle_list_01	0:0:3	2002Oct14
	PASS		
770	r400vgt_dma_index_tri_fan_01	0:0:3	2002Oct14
	PASS		
771	r400vgt_dma_index_tri_list_01	0:0:3	2002Oct14
	PASS		
772	r400vgt_dma_index_tri_strip_01	0:0:3	2002Oct14
	PASS		
773	r400vgt_dma_index_tri_wflags_01	0:0:3	2002Oct14
	PASS		
774	r400vgt_dma_swap_idx16_01	0:0:3	2002Oct14
	PASS		
775	r400vgt_dma_swap_idx32_01	0:0:3	2002Oct14
	PASS		
776	r400vgt_edgeflags_polygon_01	0:0:3	2002Oct14
	PASS		
777	r400vgt_edgeflags_quad_list_01	0:0:3	2002Oct14
	PASS		
778	r400vgt_edgeflags_quad_strip_01	0:0:3	2002Oct14
	PASS		
779	r400vgt_edgeflags_triangle_fan_01	0:0:3	2002Oct14
	PASS		
780	r400vgt_edgeflags_triangle_list_01	0:0:3	2002Oct14
	PASS		
781	r400vgt_edgeflags_triangle_strip_01	0:0:3	2002Oct14
	PASS		

782	r400vgt_edgeflags_triangle_wflags_01	0:0:3	2002Oct14
	PASS		
783	r400vgt_event_handling_01	0:0:3	2002Sep26
	PASS		
784	r400vgt_ext2int_index_line_list_01	0:0:3	2002Oct14
	PASS		
785	r400vgt_ext2int_index_line_loop_01	0:0:3	2002Oct14
	PASS		
786	r400vgt_ext2int_index_line_strip_01	0:0:3	2002Oct14
	PASS		
787	r400vgt_ext2int_index_points_01	0:0:3	2002Oct14
	PASS		
788	r400vgt_ext2int_index_polygon_01	0:0:3	2002Oct14
	PASS		
789	r400vgt_ext2int_index_quad_list_01	0:0:3	2002Oct14
	PASS		
790	r400vgt_ext2int_index_quad_strip_01	0:0:3	2002Oct14
	PASS		
791	r400vgt_ext2int_index_rectangle_list_01	0:0:3	2002Oct14
	PASS		
792	r400vgt_ext2int_index_triangle_fan_01	0:0:3	2002Oct14
	PASS		
793	r400vgt_ext2int_index_triangle_list_01	0:0:3	2002Oct14
	PASS		
794	r400vgt_ext2int_index_triangle_strip_01	0:0:3	2002Oct14
	PASS		
795	r400vgt_ext2int_index_triangle_wflags_01	0:0:3	2002Oct14
	PASS		
796	r400vgt_hos_LPatch_01	0:0:3	2002Oct14
	PASS		
797	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	0:0:3	2002Oct14
	PASS		
798	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	0:0:3	2002Oct14
	PASS		
799	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	0:0:3	2002Oct14
	PASS		
800	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	0:0:3	2002Oct14
	PASS		
801	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	0:0:3	2002Oct14
	PASS		
802	r400vgt_hos_PNQ_lp_cont_no_projection_01	0:0:3	2002Oct14
	PASS		
803	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	0:0:3	2002Oct14
	PASS		
804	r400vgt_hos_PNT_cont_cp_qn_complex_01	0:0:3	2002Oct14
	PASS		
805	r400vgt_hos_PNT_cont_cp_qn_precision_01	0:0:3	2002Oct14
	PASS		

806	r400vgt_hos_PNT_cont_cp_qn_precision_02	0:0:3	2002Oct14
	PASS		
807	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	0:0:3	2002Oct14
	PASS		
808	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	0:0:3	2002Oct14
	PASS		
809	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	0:0:3	2002Oct14
	PASS		
810	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	0:0:3	2002Oct14
	PASS		
811	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	0:0:3	2002Oct14
	PASS		
812	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	0:0:3	2002Oct14
	PASS		
813	r400vgt_hos_PNT_disc_cp_qn_complex_01	0:0:3	2002Oct14
	PASS		
814	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	0:0:3	2002Oct14
	PASS		
815	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	0:0:3	2002Oct14
	PASS		
816	r400vgt_hos_PNT_disc_cp_qn_precision_01	0:0:3	2002Oct14
	PASS		
817	r400vgt_hos_PNT_disc_cp_qn_precision_02	0:0:3	2002Oct14
	PASS		
818	r400vgt_hos_PNT_edge_detection_01	0:0:3	2002Oct14
	PASS		
819	r400vgt_hos_PNT_lp_cont_no_projection_01	0:0:3	2002Oct14
	PASS		
820	r400vgt_hos_RPatch_cp_02	0:0:3	2002Oct14
	PASS		
821	r400vgt_hos_RPatch_lp_02	0:0:3	2002Oct14
	PASS		
822	r400vgt_hos_TPatch_01	0:0:3	2002Oct14
	PASS		
823	r400vgt_hos_TPatch_02	0:0:3	2002Oct14
	PASS		
824	r400vgt_hos_auto_index_line_list_01	0:0:3	2002Oct14
	PASS		
825	r400vgt_hos_auto_index_quad_list_01	0:0:3	2002Oct14
	PASS		
826	r400vgt_hos_auto_index_triangle_list_01	0:0:3	2002Oct14
	PASS		
827	r400vgt_hos_cubic_pos_pnt_discrete_01	0:0:3	2002Oct14
	PASS		
828	r400vgt_hos_simple_linear_PNT_discrete_01	0:0:3	2002Oct14
	PASS		
829	r400vgt_immed_index_line_list_01	0:0:3	2002Oct14
	PASS		

830	r400vgt_immed_index_line_loop_01	0:0:3	2002Oct14
	PASS		
831	r400vgt_immed_index_line_strip_01	0:0:3	2002Oct14
	PASS		
832	r400vgt_immed_index_points_01	0:0:3	2002Oct14
	PASS		
833	r400vgt_immed_index_polygon_01	0:0:3	2002Oct14
	FAIL VgtPaClipP_VgtSide.cmp compare file empty		
834	r400vgt_immed_index_primitives_01	0:0:3	2002Oct14
	PASS		
835	r400vgt_immed_index_quad_list_01	0:0:3	2002Oct14
	PASS		
836	r400vgt_immed_index_quad_strip_01	0:0:3	2002Oct14
	PASS		
837	r400vgt_immed_index_rectangle_list_01	0:0:3	2002Oct14
	PASS		
838	r400vgt_immed_index_tri_fan_01	0:0:3	2002Oct14
	FAIL VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp		
839	r400vgt_immed_index_tri_list_01	0:0:3	2002Oct14
	FAIL VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
840	r400vgt_immed_index_tri_strip_01	0:0:3	2002Oct14
	FAIL VgtPaClipP_VgtSide.cmp compare file empty		
841	r400vgt_immed_index_tri_wflags_01	0:0:3	2002Oct14
	FAIL VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
842	r400vgt_index_dealloc_line_list_01	0:0:3	2002Oct14
	PASS		
843	r400vgt_index_dealloc_points_01	0:0:3	2002Oct14
	PASS		
844	r400vgt_index_dealloc_triangle_list_01	0:0:3	2002Oct14
	PASS		
845	r400vgt_index_min_max_01	0:0:3	2002Oct14
	PASS		
846	r400vgt_index_min_max_02	0:0:3	2002Oct14
	PASS		
847	r400vgt_index_min_max_03	0:0:3	2002Oct14
	PASS		
848	r400vgt_index_min_max_04	0:0:3	2002Oct14
	PASS		
849	r400vgt_index_offset_01	0:0:3	2002Oct14
	PASS		
850	r400vgt_index_offset_02	0:0:3	2002Oct14
	PASS		
851	r400vgt_index_offset_03	0:0:3	2002Oct14
	PASS		
852	r400vgt_index_offset_04	0:0:3	2002Oct14
	PASS		

853	r400vgt_index_offset_05	0:0:3	2002Oct14
FAIL	VgtPaClipP_VgtSide.cmp		
854	r400vgt_index_offset_06	0:0:3	2002Oct14
PASS			
855	r400vgt_index_offset_07	0:0:3	2002Oct14
PASS			
856	r400vgt_index_offset_08	0:0:3	2002Oct14
PASS			
857	r400vgt_index_size_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp VgtPaClipS_VgtSide.cmp		
858	r400vgt_index_size_02	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
859	r400vgt_index_source_switch_01	0:0:3	2002Oct14
PASS			
860	r400vgt_line_list_01	0:0:3	2002Oct14
PASS			
861	r400vgt_line_list_02	0:0:3	2002Oct14
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
862	r400vgt_line_loop_01	0:0:3	2002Oct14
PASS			
863	r400vgt_line_loop_02	0:0:3	2002Oct14
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
864	r400vgt_line_strip_01	0:0:3	2002Oct14
PASS			
865	r400vgt_line_strip_02	0:0:3	2002Oct14
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
866	r400vgt_multi_context_01	0:0:3	2002Oct14
PASS			
867	r400vgt_multi_context_02	0:0:3	2002Oct14
PASS			
868	r400vgt_multi_context_03	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
869	r400vgt_multi_context_04	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp		
870	r400vgt_multi_context_05	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
871	r400vgt_multi_context_06	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
872	r400vgt_multi_context_07	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
873	r400vgt_multi_context_08	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		

874	r400vgt_multi_context_09	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
875	r400vgt_multi_context_10	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
876	r400vgt_multi_context_11	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
877	r400vgt_multi_context_12	0:0:3	2002Oct14
PASS			
878	r400vgt_multi_prim_reset_index_all_01	0:0:3	2002Oct14
PASS			
879	r400vgt_multi_prim_reset_index_all_02	0:0:3	2002Oct14
PASS			
880	r400vgt_pass_thru_all_prims_01	0:0:3	2002Oct14
PASS			
881	r400vgt_point_list_01	0:0:3	2002Oct14
PASS			
882	r400vgt_point_list_02	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
883	r400vgt_polygon_01	0:0:3	2002Oct14
PASS			
884	r400vgt_polygon_02	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
885	r400vgt_provoking_vtx_all_01	0:0:3	2002Oct14
PASS			
886	r400vgt_provoking_vtx_edgeflags_all_01	0:0:3	2002Oct14
PASS			
887	r400vgt_provoking_vtx_polygon_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
888	r400vgt_provoking_vtx_quad_list_01	0:0:3	2002Oct14
PASS			
889	r400vgt_provoking_vtx_quad_strip_01	0:0:3	2002Oct14
PASS			
890	r400vgt_provoking_vtx_tri_fan_01	0:0:3	2002Oct14
PASS			
891	r400vgt_provoking_vtx_tri_strip_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp VgtPaClipS_VgtSide.cmp		
892	r400vgt_quad_list_01	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp		
893	r400vgt_quad_list_02	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		

894	r400vgt_quad_strip_01	0:0:3	2002Oct14
	PASS		
895	r400vgt_quad_strip_02	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
896	r400vgt_rectangle_list_01	0:0:3	2002Oct14
	PASS		
897	r400vgt_rectangle_list_02	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
898	r400vgt_reuse_depth_line_list_01	0:0:3	2002Oct14
	PASS		
899	r400vgt_reuse_depth_point_list_01	0:0:3	2002Oct14
	PASS		
900	r400vgt_reuse_depth_triangle_list_01	0:0:3	2002Oct14
	PASS		
901	r400vgt_reuse_index_line_list_01	0:0:3	2002Oct14
	PASS		
902	r400vgt_reuse_index_point_list_01	0:0:3	2002Oct14
	PASS		
903	r400vgt_reuse_index_triangle_list_01	0:0:3	2002Oct14
	PASS		
904	r400vgt_reuse_index_triangle_list_02	0:0:3	2002Oct14
	PASS		
905	r400vgt_reuse_index_triangle_list_03	0:0:3	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
906	r400vgt_suppress_eop_01	0:0:1	2002Oct14
	PASS		
907	r400vgt_suppress_eop_02	0:0:1	2002Oct14
	PASS		
908	r400vgt_suppress_eop_03	0:0:1	2002Oct14
	PASS		
909	r400vgt_suppress_eop_04	0:0:1	2002Oct14
	PASS		
910	r400vgt_suppress_eop_05	0:0:1	2002Sep26
	PASS		
911	r400vgt_triangle_fan_01	0:0:1	2002Oct14
	PASS		
912	r400vgt_triangle_fan_02	0:0:1	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		
913	r400vgt_triangle_list_01	0:0:1	2002Oct14
	PASS		
914	r400vgt_triangle_list_02	0:0:1	2002Oct14
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty		

```

915 r400vgt_triangle_strip_01          0:0:1    2002Oct14
PASS
916 r400vgt_triangle_strip_02          0:0:1    2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp
917 r400vgt_triangle_wflags_01         0:0:1    2002Oct14
PASS
918 r400vgt_triangle_wflags_02         0:0:1    2002Oct14
**FAIL**      VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp compare file empty

919 r400vgt_viz_query_01               0:0:1    2002Oct14
PASS
920 r400vgt_vtx_vector_packing_01       0:0:1    2002Oct14
PASS
921 r400vte_coverage_01                 0:0:1    2002Oct14
PASS
922 r400vte_simple_point_01             0:0:1    2002Oct14
PASS
923 r400vte_simple_triangle_01          0:0:1    2002Oct14
PASS
924 r400vte_w0_fmt_01                   0:0:1    2002Oct14
PASS
925 r400vte_w0_fmt_03                   0:0:1    2002Oct14
PASS
926 r400vte_xy_fmt_01                   0:0:1    2002Oct14
PASS
927 r400vte_xy_fmt_mk_01_bug            0:0:1    2002Oct9
PASS
928 r400vte_xyz_scale_01                0:0:1    2002Oct14
PASS
929 r400vte_xyz_scale_02                0:0:1    2002Oct14
PASS
930 r400vte_z_fmt_01                    0:0:1    2002Oct14
PASS

```

```

=====
Tests_Run_count = 930  Pass_count = 875 Fail_count = 48  Incomplete_count = 7
=====

```

=====
 Regression Report For VGT - Project = r400 Created on Wed Oct 9 22:16:29 2002
 =====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc		2002Oct9
	PASS		
2	cp_dma_interrupt		2002Oct9
	PASS		
3	cp_dma_m2m_01		2002Oct9
	PASS		
4	cp_dma_m2m_02		2002Oct9
	PASS		
5	cp_dma_m2m_03		2002Oct9
	PASS		
6	cp_dma_m2m_04		2002Oct9
	PASS		
7	cp_dma_m2r_01		2002Oct9
	PASS		
8	cp_dma_m2r_02		2002Oct9
	PASS		
9	cp_dma_m2r_03		2002Oct9
	PASS		
10	cp_dma_m2r_04		2002Oct9
	PASS		
11	cp_dma_m2r_r2m		2002Oct9
	PASS		
12	cp_dma_pio_simple		2002Oct9
	PASS		
13	cp_dma_pio_stress		2002Oct9
	PASS		
14	cp_dma_piobm_stress		2002Oct9
	PASS		
15	cp_dma_r2m_01		2002Oct9
	PASS		
16	cp_dma_r2m_02		2002Oct9
	PASS		
17	cp_dma_r2m_03		2002Oct9
	PASS		
18	cp_dma_r2m_04		2002Oct9
	PASS		
19	cp_dma_r2r_01		2002Oct9
	PASS		
20	cp_dma_r2r_02		2002Oct9

PASS		
21	cp_dma_r2r_03	2002Oct9
PASS		
22	cp_dma_r2r_r2m	2002Oct9
PASS		
23	cp_dma_r2r_r2m_m2m	2002Oct9
PASS		
24	cp_dma_r2r_r2m_m2m_r2m	2002Oct9
PASS		
25	cp_dma_simple	2002Oct9
PASS		
26	cp_e2_hostdata_blt_pntr_8888	2002Oct9
PASS		
27	cp_e2_one_blit	2002Oct9
PASS		
28	cp_e2_one_line	2002Oct9
PASS		
29	cp_e2blit_brush_mt_ropcc	2002Oct9
PASS		
30	cp_e2paint_565	2002Oct9
PASS		
31	cp_e2paint_multi	2002Oct9
PASS		
32	cp_e2polyscanlines_brush	2002Oct9
PASS		
33	cp_e2polyscanlines_brush_mt	2002Oct9
PASS		
34	cp_e2smalltext_jcl	2002Oct9
PASS		
35	cp_im_load_indirect	2002Oct9
PASS		
36	cp_push_aper_indirect1	2002Oct9
PASS		
37	cp_push_aper_primary	2002Oct9
PASS		
38	cp_queue_avail_01	2002Oct9
PASS		
39	cp_queue_avail_02	2002Oct9
PASS		
40	cp_queue_avail_03	2002Oct9
PASS		
41	cp_queue_avail_04	2002Oct9
PASS		
42	cp_queue_avail_05	2002Oct9
PASS		
43	cp_queue_avail_06	2002Oct9
PASS		
44	cp_queue_avail_07	2002Oct9

PASS		
45	cp_queue_avail_08	2002Oct9
PASS		
46	cp_queue_avail_09	2002Oct9
PASS		
47	cp_queue_avail_10	2002Oct9
PASS		
48	cp_queue_avail_11	2002Oct9
PASS		
49	cp_queue_avail_12	2002Oct9
PASS		
50	cp_simple_triangle	2002Oct9
PASS		
51	milestone_event_flush_tri1	2002Oct3
PASS		
52	milestone_event_flush_tri2	2002Oct3
PASS		
53	milestone_event_flush_tri3	2002Oct3
PASS		
54	milestone_event_flush_tri4	2002Oct3
PASS		
55	milestone_event_flush_tri5	2002Oct3
PASS		
56	milestone_event_flush_tri6	2002Oct3
PASS		
57	milestone_event_flush_tri7	2002Oct3
PASS		
58	milestone_event_flush_tri8	2002Oct3
PASS		
59	milestone_event_flush_tri9	2002Oct3
PASS		
60	milestone_event_randomized	2002Oct3
PASS		
61	milestone_event_standard	2002Oct3
PASS		
62	milestone_event_switch_flush_tri1	2002Oct3
PASS		
63	milestone_event_switch_flush_tri2	2002Oct3
PASS		
64	milestone_event_switch_flush_tri3	2002Oct3
PASS		
65	milestone_event_switch_flush_tri4	2002Oct3
PASS		
66	milestone_event_switch_flush_tri5	2002Oct3
PASS		
67	milestone_event_switch_flush_tri6	2002Oct3
PASS		
68	milestone_event_switch_flush_tri7	2002Oct3

PASS		
69	milestone_event_switch_flush_tri8	2002Oct3
PASS		
70	milestone_event_switch_flush_tri9	2002Oct3
PASS		
71	milestone_event_switch_tri2	2002Oct3
PASS		
72	milestone_event_switch_tri3	2002Oct3
PASS		
73	milestone_event_switch_tri4	2002Oct3
PASS		
74	milestone_event_switch_tri5	2002Oct3
PASS		
75	milestone_event_switch_tri6	2002Oct3
PASS		
76	milestone_event_switch_tri7	2002Oct3
PASS		
77	milestone_event_switch_tri8	2002Oct3
PASS		
78	milestone_event_switch_tri9	2002Oct3
PASS		
79	milestone_event_tri2	2002Oct3
PASS		
80	milestone_event_tri3	2002Oct3
PASS		
81	milestone_event_tri4	2002Oct3
PASS		
82	milestone_event_tri5	2002Oct3
PASS		
83	milestone_event_tri6	2002Oct3
PASS		
84	milestone_event_tri7	2002Oct3
PASS		
85	milestone_event_tri8	2002Oct3
PASS		
86	milestone_event_tri9	2002Oct3
PASS		
87	milestone_tri_random_rect	2002Oct3
PASS		
88	milestone_tri_random_tri	2002Oct3
PASS		
89	milestone_tri_randomized	2002Oct3
PASS		
90	milestone_tri_standard	2002Oct3
PASS		
91	milestone_tri_tril28_pix4	2002Oct3
PASS		
92	milestone_tri_tril_pix1	2002Oct3

PASS		
93	milestone_tri_tril_pix2	2002Oct3
PASS		
94	milestone_tri_tril_pix3	2002Oct3
PASS		
95	milestone_tri_tril_pix4	2002Oct3
PASS		
96	milestone_tri_tri24x24	2002Oct3
PASS		
97	milestone_tri_tri28x28	2002Oct3
PASS		
98	milestone_tri_tri2_pix1	2002Oct3
PASS		
99	milestone_tri_tri2_pix16	2002Oct3
PASS		
100	milestone_tri_tri2_pix4	2002Oct3
PASS		
101	milestone_tri_tri32_pix1	2002Oct3
PASS		
102	milestone_tri_tri32_pix4	2002Oct3
PASS		
103	milestone_tri_tri32x32	2002Oct3
PASS		
104	milestone_tri_tri36x36	2002Oct3
PASS		
105	milestone_tri_tri40x40	2002Oct3
PASS		
106	milestone_tri_tri44x44	2002Oct3
PASS		
107	milestone_tri_tri48x48	2002Oct3
PASS		
108	milestone_tri_tri52x52	2002Oct3
PASS		
109	milestone_tri_tri56x56	2002Oct3
PASS		
110	milestone_tri_tri60x60	2002Oct3
PASS		
111	milestone_tri_tri64x64	2002Oct3
PASS		
112	milestone_tri_tri8_pix4	2002Oct3
PASS		
113	primlib_1st_tri_june15	2002Oct9
PASS		
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct9
PASS		
115	primlib_gouraud_triangles_2_draw_passes	2002Oct9
PASS		
116	primlib_parameterized_simple_triangle	2002Oct9

PASS		
117	primlib_template_simple_triangle	2002Oct9
PASS		
118	primlib_tex_tri	2002Oct9
PASS		
119	r400cl_bary_texture_08_bug	2002Sep21
PASS		
120	r400cl_clip_10_verts_01	2002Oct9
PASS		
121	r400cl_clip_disable_01	2002Oct9
PASS		
122	r400cl_clip_space_dx_ogl_01	2002Oct9
PASS		
123	r400cl_clip_ucp_6bits_01	2002Oct9
PASS		
124	r400cl_cull_only_ena_01	2002Oct9
PASS		
125	r400cl_edgeflags_01	2002Oct9
PASS		
126	r400cl_edgeflags_01_bug	2002Oct9
PASS		
127	r400cl_edgeflags_frustum_bottom_01	2002Oct9
PASS		
128	r400cl_edgeflags_frustum_far_01	2002Oct9
PASS		
129	r400cl_edgeflags_frustum_left_01	2002Oct9
PASS		
130	r400cl_edgeflags_frustum_near_01	2002Oct9
PASS		
131	r400cl_edgeflags_frustum_right_01	2002Oct9
PASS		
132	r400cl_edgeflags_frustum_top_01	2002Oct9
PASS		
133	r400cl_edgeflags_gband_01	2002Oct9
PASS		
134	r400cl_edgeflags_gband_bottom_01	2002Oct9
PASS		
135	r400cl_edgeflags_gband_left_01	2002Oct9
PASS		
136	r400cl_edgeflags_gband_right_01	2002Oct9
PASS		
137	r400cl_edgeflags_gband_top_01	2002Oct9
PASS		
138	r400cl_frustum_01	2002Oct9
PASS		
139	r400cl_frustum_02	2002Oct9
PASS		
140	r400cl_frustum_03	2002Oct9

PASS		
141	r400cl_frustum_04	20020ct9
PASS		
142	r400cl_frustum_05	20020ct9
PASS		
143	r400cl_frustum_06	20020ct9
PASS		
144	r400cl_frustum_07	20020ct9
PASS		
145	r400cl_frustum_08	20020ct9
PASS		
146	r400cl_frustum_09	20020ct9
PASS		
147	r400cl_frustum_10	20020ct9
PASS		
148	r400cl_frustum_11	20020ct9
PASS		
149	r400cl_frustum_12	20020ct9
PASS		
150	r400cl_frustum_13	20020ct9
PASS		
151	r400cl_frustum_14	20020ct9
PASS		
152	r400cl_frustum_15	20020ct9
PASS		
153	r400cl_frustum_16	20020ct9
PASS		
154	r400cl_frustum_17	20020ct9
PASS		
155	r400cl_frustum_18	20020ct9
PASS		
156	r400cl_frustum_19	20020ct9
PASS		
157	r400cl_frustum_20	20020ct9
PASS		
158	r400cl_frustum_21	20020ct9
PASS		
159	r400cl_frustum_22	20020ct9
PASS		
160	r400cl_frustum_23	20020ct9
PASS		
161	r400cl_frustum_24	20020ct9
PASS		
162	r400cl_frustum_25	20020ct9
PASS		
163	r400cl_frustum_26	20020ct9
PASS		
164	r400cl_frustum_27	20020ct9

PASS		
165	r400cl_frustum_28	2002Oct9
PASS		
166	r400cl_frustum_all_vols_lines	2002Oct9
PASS		
167	r400cl_frustum_all_vols_tris	2002Oct9
PASS		
168	r400cl_frustum_lines_01	2002Oct9
PASS		
169	r400cl_frustum_lines_02	2002Oct9
PASS		
170	r400cl_frustum_lines_03	2002Oct9
PASS		
171	r400cl_frustum_lines_04	2002Oct9
PASS		
172	r400cl_frustum_lines_05	2002Oct9
PASS		
173	r400cl_frustum_lines_06	2002Oct9
PASS		
174	r400cl_frustum_lines_07	2002Oct9
PASS		
175	r400cl_frustum_lines_08	2002Oct9
PASS		
176	r400cl_frustum_lines_09	2002Oct9
PASS		
177	r400cl_frustum_lines_10	2002Oct9
PASS		
178	r400cl_frustum_lines_11	2002Oct9
PASS		
179	r400cl_frustum_lines_12	2002Oct9
PASS		
180	r400cl_frustum_lines_13	2002Oct9
PASS		
181	r400cl_frustum_lines_14	2002Oct9
PASS		
182	r400cl_frustum_lines_15	2002Oct9
PASS		
183	r400cl_frustum_lines_16	2002Oct9
PASS		
184	r400cl_frustum_lines_17	2002Oct9
PASS		
185	r400cl_frustum_lines_18	2002Oct9
PASS		
186	r400cl_frustum_lines_19	2002Oct9
PASS		
187	r400cl_frustum_lines_20	2002Oct9
PASS		
188	r400cl_frustum_lines_21	2002Oct9

PASS		
189	r400cl_frustum_lines_22	2002Oct9
PASS		
190	r400cl_frustum_lines_23	2002Oct9
PASS		
191	r400cl_frustum_lines_24	2002Oct9
PASS		
192	r400cl_frustum_lines_25	2002Oct9
PASS		
193	r400cl_frustum_lines_26	2002Oct9
PASS		
194	r400cl_frustum_lines_27	2002Oct9
PASS		
195	r400cl_frustum_lines_28	2002Oct9
PASS		
196	r400cl_frustum_lines_29	2002Oct9
PASS		
197	r400cl_frustum_lines_30	2002Oct9
PASS		
198	r400cl_frustum_lines_31	2002Oct9
PASS		
199	r400cl_frustum_lines_32	2002Oct9
PASS		
200	r400cl_frustum_lines_33	2002Oct9
PASS		
201	r400cl_frustum_lines_34	2002Oct9
PASS		
202	r400cl_frustum_lines_35	2002Oct9
PASS		
203	r400cl_frustum_lines_36	2002Oct9
PASS		
204	r400cl_frustum_lines_37	2002Oct9
PASS		
205	r400cl_frustum_lines_38	2002Oct9
PASS		
206	r400cl_frustum_lines_39	2002Oct9
PASS		
207	r400cl_frustum_lines_40	2002Oct9
PASS		
208	r400cl_frustum_lines_41	2002Oct9
PASS		
209	r400cl_frustum_lines_42	2002Oct9
PASS		
210	r400cl_frustum_lines_43	2002Oct9
PASS		
211	r400cl_frustum_lines_44	2002Oct9
PASS		
212	r400cl_frustum_lines_45	2002Oct9

PASS		
213	r400cl_frustum_lines_46	2002Oct9
PASS		
214	r400cl_frustum_lines_47	2002Oct9
PASS		
215	r400cl_frustum_lines_48	2002Oct9
PASS		
216	r400cl_frustum_lines_49	2002Oct9
PASS		
217	r400cl_frustum_lines_50	2002Oct9
PASS		
218	r400cl_frustum_lines_51	2002Oct9
PASS		
219	r400cl_frustum_lines_52	2002Oct9
PASS		
220	r400cl_gband_04	2002Oct9
PASS		
221	r400cl_gband_tcl_01	2002Oct9
PASS		
222	r400cl_nan_kill_01_bug	2002Oct9
PASS		
223	r400cl_point_ucp_combos_01	2002Oct9
INCOMPLETE		
224	r400cl_pointlist_vertex_state_ucp_01	2002Oct9
PASS		
225	r400cl_polymode_line_fill_01_bug	2002Oct9
PASS		
226	r400cl_simple_triangle_01	2002Oct9
PASS		
227	r400cl_tri_polymode_line_stipple_ucp_combos_01	2002Oct9
PASS		
228	r400cl_tri_polymode_line_ucp_combos_01	2002Oct9
PASS		
229	r400cl_triangle_polymode_line_stippled_01	2002Oct9
PASS		
230	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct9
PASS		
231	r400cl_ucp_combos_01	2002Oct9
PASS		
232	r400cl_ucp_combos_02	2002Oct9
PASS		
233	r400cl_ucp_combos_03	2002Oct9
PASS		
234	r400cl_ucp_combos_04	2002Oct9
PASS		
235	r400cl_ucp_combos_05	2002Oct9
PASS		
236	r400cl_ucp_combos_06	2002Oct9

PASS		
237	r400cl_ucp_combos_07	20020ct9
PASS		
238	r400cl_ucp_combos_08	20020ct9
PASS		
239	r400cl_ucp_combos_09	20020ct9
PASS		
240	r400cl_ucp_combos_10	20020ct9
PASS		
241	r400cl_ucp_combos_11	20020ct9
PASS		
242	r400cl_ucp_combos_12	20020ct9
PASS		
243	r400cl_ucp_combos_13	20020ct9
PASS		
244	r400cl_ucp_combos_14	20020ct9
PASS		
245	r400cl_ucp_combos_15	20020ct9
PASS		
246	r400cl_ucp_combos_16	20020ct9
PASS		
247	r400cl_ucp_combos_17	20020ct9
PASS		
248	r400cl_ucp_combos_18	20020ct9
PASS		
249	r400cl_ucp_combos_19	20020ct9
PASS		
250	r400cl_ucp_combos_20	20020ct9
PASS		
251	r400cl_ucp_combos_21	20020ct9
PASS		
252	r400cl_ucp_combos_22	20020ct9
PASS		
253	r400cl_ucp_combos_23	20020ct9
PASS		
254	r400cl_ucp_combos_24	20020ct9
PASS		
255	r400cl_ucp_combos_25	20020ct9
PASS		
256	r400cl_ucp_combos_26	20020ct9
PASS		
257	r400cl_ucp_combos_27	20020ct9
PASS		
258	r400cl_ucp_combos_28	20020ct9
PASS		
259	r400cl_ucp_combos_29	20020ct9
PASS		
260	r400cl_ucp_combos_30	20020ct9

PASS		
261	r400cl_ucp_pointlist_01	2002Oct9
PASS		
262	r400cl_vertex_reuse_clip_01	2002Oct9
PASS		
263	r400cl_vtx_kill_01	2002Oct9
PASS		
264	r400cl_vtx_kill_02	2002Oct9
PASS		
265	r400cl_w_eq_0	2002Oct9
PASS		
266	r400rbbm_simple_triangle_01	2002Oct9
PASS		
267	r400sanity_persp_projection_01	2002Oct9
PASS		
268	r400sanity_vfd_texture_sample_01	2002Oct9
PASS		
269	r400sc_baryc_01	2002Oct9
PASS		
270	r400sc_bres_cntl_01	2002Sep7
PASS		
271	r400sc_bres_cntl_02	2002Oct9
PASS		
272	r400sc_bres_cntl_03	2002Oct9
PASS		
273	r400sc_bres_cntl_04	2002Oct9
PASS		
274	r400sc_bres_cntl_w2k_01	2002Oct9
PASS		
275	r400sc_bres_cntl_w9x_01	2002Oct9
PASS		
276	r400sc_clip_rect_01	2002Oct9
PASS		
277	r400sc_clip_rect_02	2002Oct9
PASS		
278	r400sc_clip_rect_03	2002Oct9
PASS		
279	r400sc_clip_rect_04	2002Oct9
PASS		
280	r400sc_clip_rect_fc_01	2002Oct9
PASS		
281	r400sc_diamond_exit_01	2002Oct9
PASS		
282	r400sc_diamond_exit_02	2002Oct9
PASS		
283	r400sc_diamond_exit_03	2002Oct9
PASS		
284	r400sc_diamond_exit_04	2002Oct9

PASS		
285	r400sc_diamond_exit_05	2002Oct9
PASS		
286	r400sc_jss_1x1_primtypes_01	2002Oct9
PASS		
287	r400sc_jss_1x2_01	2002Oct9
PASS		
288	r400sc_jss_1x2_02	2002Oct9
PASS		
289	r400sc_jss_1x2_primtypes_01	2002Oct9
PASS		
290	r400sc_jss_1x3_01	2002Oct9
PASS		
291	r400sc_jss_1x3_02	2002Oct9
PASS		
292	r400sc_jss_1x3_primtypes_01	2002Oct9
PASS		
293	r400sc_jss_1x4_01	2002Oct9
PASS		
294	r400sc_jss_1x4_02	2002Oct9
PASS		
295	r400sc_jss_1x4_primtypes_01	2002Oct9
PASS		
296	r400sc_jss_2x1_01	2002Oct9
PASS		
297	r400sc_jss_2x1_02	2002Oct9
PASS		
298	r400sc_jss_2x1_primtypes_01	2002Oct9
PASS		
299	r400sc_jss_2x2_01	2002Oct9
PASS		
300	r400sc_jss_2x2_02	2002Oct9
PASS		
301	r400sc_jss_2x2_primtypes_01	2002Oct9
PASS		
302	r400sc_jss_2x3_01	2002Oct9
PASS		
303	r400sc_jss_2x3_02	2002Oct9
PASS		
304	r400sc_jss_2x3_primtypes_01	2002Oct9
PASS		
305	r400sc_jss_2x4_01	2002Oct9
PASS		
306	r400sc_jss_2x4_02	2002Oct9
PASS		
307	r400sc_jss_2x4_primtypes_01	2002Oct9
PASS		
308	r400sc_jss_3x1_01	2002Oct9

PASS		
309	r400sc_jss_3x1_02	20020ct9
PASS		
310	r400sc_jss_3x1_primtypes_01	20020ct9
PASS		
311	r400sc_jss_3x2_01	20020ct9
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312	r400sc_jss_3x2_02	20020ct9
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313	r400sc_jss_3x2_primtypes_01	20020ct9
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314	r400sc_jss_3x3_01	20020ct9
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315	r400sc_jss_3x3_02	20020ct9
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316	r400sc_jss_3x3_primtypes_01	20020ct9
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317	r400sc_jss_3x4_01	20020ct9
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318	r400sc_jss_3x4_02	20020ct9
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323	r400sc_jss_4x1_primtypes_01	20020ct9
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330	r400sc_jss_4x4_01	20020ct9
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331	r400sc_jss_4x4_02	20020ct9
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332	r400sc_jss_4x4_03	20020ct9

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333	r400sc_jss_4x4_aa_mask_01	20020ct9
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334	r400sc_jss_4x4_aa_mask_02	20020ct9
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335	r400sc_jss_4x4_fc_01	20020ct9
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336	r400sc_jss_4x4_fc_02	20020ct9
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337	r400sc_jss_4x4_max_dist_01	20020ct9
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338	r400sc_jss_4x4_primitives_01	20020ct9
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339	r400sc_line_dx10_eq_0_01	20020ct9
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340	r400sc_line_dx10_ge_0_01	20020ct9
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341	r400sc_line_dx10_lt_0_01	20020ct9
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342	r400sc_line_dy10_eq_0_01	20020ct9
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343	r400sc_line_dy10_ge_0_01	20020ct9
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344	r400sc_line_dy10_lt_0_01	20020ct9
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345	r400sc_line_expand_width_msa_8_01	20020ct9
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346	r400sc_line_expand_width_msa_8_02	20020ct9
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347	r400sc_line_expand_width_msa_8_03	20020ct9
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348	r400sc_line_jss_3x4_01	20020ct9
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349	r400sc_line_list_01	20020ct9
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350	r400sc_line_list_02	20020ct9
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351	r400sc_line_list_concentric_circle_01	20020ct9
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352	r400sc_line_list_concentric_circle_02	20020ct9
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353	r400sc_line_list_concentric_circle_03	20020ct9
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354	r400sc_line_list_textured_01	20020ct9
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355	r400sc_line_list_verify_st_01	20020ct9
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356	r400sc_line_list_verify_st_01_bug	20020ct9

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358	r400sc_line_msaa_8_textured_01	2002Oct9
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359	r400sc_line_stipple_01	2002Oct9
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360	r400sc_line_stipple_02	2002Oct9
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379	r400sc_line_stipple_21	2002Oct9
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380	r400sc_line_stipple_22	2002Oct9

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381	r400sc_line_stipple_23	2002Oct9
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382	r400sc_line_stipple_fc_08	2002Oct9
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383	r400sc_line_strip_stipple_01	2002Oct9
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384	r400sc_msaa_1_01	2002Oct9
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385	r400sc_msaa_1_primitives_01	2002Oct9
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386	r400sc_msaa_1_rectangle_list_01	2002Oct9
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395	r400sc_msaa_1_zbuffer_rectangle_list_02	2002Oct9
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396	r400sc_msaa_2_primitives_01	2002Oct9
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397	r400sc_msaa_2_rectangle_list_01	2002Oct9
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398	r400sc_msaa_2_rectangle_list_02	2002Oct9
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401	r400sc_msaa_2_rectangle_list_05	2002Oct9
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404	r400sc_msaa_2_rectangle_list_08	2002Oct9

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406	r400sc_msa_2_zbuffer_rectangle_list_02	2002Oct9
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409	r400sc_msa_3_rectangle_list_02	2002Oct9
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417	r400sc_msa_3_zbuffer_rectangle_list_02	2002Oct9
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429	r400sc_msa_4_zbuffer_rectangle_list_02	2002Oct9
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430	r400sc_msa_6_01	2002Oct9
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431	r400sc_msa_6_primitives_01	2002Oct9
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432	r400sc_msa_6_rectangle_list_01	2002Oct9
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433	r400sc_msa_6_rectangle_list_02	2002Oct9
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441	r400sc_msa_6_zbuffer_rectangle_list_02	2002Oct9
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444	r400sc_msa_8_03	2002Oct9
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446	r400sc_msa_8_05	2002Oct9
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447	r400sc_msa_8_aa_mask_01	2002Oct9
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448	r400sc_msa_8_aa_mask_02	2002Oct9
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449	r400sc_msa_8_aa_mask_fc_02	2002Oct9
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450	r400sc_msa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct9
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451	r400sc_msa_8_primitives_01	2002Oct9
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452	r400sc_msa_8_rectangle_list_01	2002Oct9

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453	r400sc_msa_8_rectangle_list_02	2002Oct9
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461	r400sc_msa_8_zbuffer_rectangle_list_02	2002Oct9
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462	r400sc_msa_X_primitives_01_bug	2002Oct9
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463	r400sc_msa_X_primitives_02_bug	2002Oct9
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464	r400sc_multi_chip_prim_discard_00_07	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp compare file empty	
465	r400sc_multi_chip_tile_discard_00_07	2002Oct7
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466	r400sc_null_triangles_01	2002Oct9
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467	r400sc_null_triangles_fc_01	2002Oct9
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468	r400sc_perf_01	2002Oct9
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469	r400sc_pinwheel_01	2002Oct9
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470	r400sc_pinwheel_02	2002Oct9
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471	r400sc_pinwheel_02_bug	2002Oct9
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472	r400sc_point_jss_3x4_01	2002Oct9
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473	r400sc_point_list_01	2002Oct9
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474	r400sc_point_list_02	2002Oct9
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475	r400sc_point_list_03	2002Oct9
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476	r400sc_point_list_04	2002Oct9

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478	r400sc_point_list_06	2002Oct9
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479	r400sc_point_list_06_bug	2002Oct9
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480	r400sc_point_list_06_bug_02	2002Oct9
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481	r400sc_point_list_07	2002Oct9
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483	r400sc_point_list_09	2002Oct9
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484	r400sc_point_msa_8_01	2002Oct9
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485	r400sc_poly_offset_01	2002Oct9
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486	r400sc_poly_offset_02	2002Oct9
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487	r400sc_poly_offset_03	2002Oct9
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493	r400sc_poly_offset_09	2002Oct9
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494	r400sc_poly_offset_10	2002Oct9
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495	r400sc_poly_offset_10_bug	2002Oct9
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496	r400sc_poly_offset_fc_01	2002Oct9
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497	r400sc_polygon_stipple_01	2002Oct9
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498	r400sc_polymode_tri_fill_01	2002Oct9
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499	r400sc_polymode_tri_fill_01_bug	2002Oct9
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500	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct9

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501	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct9
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502	r400sc_prsp_byc_intrp_ref_pix_03	2002Oct9
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503	r400sc_prsp_byc_intrp_ref_pix_04	2002Oct9
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506	r400sc_prsp_byc_intrp_ref_pix_07	2002Oct9
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507	r400sc_prsp_byc_intrp_ref_pix_08	2002Oct9
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508	r400sc_raster_fill_rule_01	2002Oct9
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509	r400sc_raster_fill_rule_02	2002Oct9
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510	r400sc_raster_fill_rule_03	2002Oct9
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511	r400sc_raster_fill_rule_04	2002Oct9
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512	r400sc_raster_fill_rule_05	2002Oct9
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513	r400sc_raster_fill_rule_fc_01	2002Oct9
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521	r400sc_rectangle_list_08	2002Oct9
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523	r400sc_scissor_rect_02	2002Oct9
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524	r400sc_scissor_rect_03	2002Oct9

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525	r400sc_scissor_rect_04	20020ct9
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526	r400sc_scissor_rect_05	20020ct9
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527	r400sc_scissor_rect_fc_01	20020ct9
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528	r400sc_set_state_01	20020ct9
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529	r400sc_sp_sample_cntl_01	20020ct9
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530	r400sc_sp_sample_cntl_02	20020ct9
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531	r400sc_sp_sample_cntl_03	20020ct9
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532	r400sc_sp_sample_cntl_04	20020ct9
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533	r400sc_sp_sample_cntl_05	20020ct9
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534	r400sc_sp_sample_cntl_06	20020ct9
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535	r400sc_sp_sample_cntl_07	20020ct9
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536	r400sc_sp_sample_cntl_08	20020ct9
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537	r400sc_tri_16_par_64_dwords_01	20020ct9
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538	r400sc_tri_8textures_01	20020ct9
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539	r400sc_tri_8textures_02	20020ct9
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540	r400sc_tri_walk_start_vertex_01	20020ct9
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541	r400sc_tri_walk_start_vertex_02	20020ct9
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542	r400sc_tri_walk_start_vertex_03	20020ct9
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543	r400sc_tri_walk_start_vertex_04	20020ct9
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544	r400sc_tri_walk_start_vertex_05	20020ct9
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545	r400sc_tri_walk_start_vertex_06	20020ct9
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546	r400sc_tri_walk_start_vertex_07	20020ct9
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548	r400sc_tri_walk_start_vertex_09	20020ct9

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554	r400sc_tri_walk_start_vertex_15	2002Oct9
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555	r400sc_tri_walk_start_vertex_16	2002Oct9
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556	r400sc_triangle_stipple_01	2002Oct9
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559	r400sc_window_offset_03	2002Oct9
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560	r400sc_window_offset_04	2002Oct9
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561	r400sc_window_offset_05	2002Oct9
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562	r400sc_window_offset_fc_01	2002Oct9
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564	r400sc_zbuffer_point_list_01	2002Oct9
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565	r400sc_zbuffer_rectangle_list_01	2002Oct9
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566	r400sc_zbuffer_rectangle_list_02	2002Oct9
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567	r400sc_zbuffer_rectangle_list_fc_02	2002Oct9
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568	r400sc_zbuffer_triangle_list_01	2002Oct9
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569	r400su_4tri_text_offscreen_01	2002Oct9
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570	r400su_baryc_test_01	2002Oct9
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571	r400su_baryc_test_02	2002Oct9
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572	r400su_baryc_test_03	2002Oct9

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574	r400su_clip_baryc_test_01	2002Oct9
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575	r400su_clip_baryc_test_02	2002Oct9
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576	r400su_clip_edgeflag_polymode_01	2002Oct9
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577	r400su_clip_pointsize_test_01	2002Oct9
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578	r400su_clip_pointtest_01	2002Oct9
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579	r400su_clip_pointtest_02	2002Oct9
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580	r400su_clip_pointtest_03	2002Oct9
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581	r400su_clip_pointtest_04	2002Oct9
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584	r400su_clippoint_01	2002Oct9
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585	r400su_clipvertexsortingfunctional_01	2002Oct9
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586	r400su_cullingfunctional_01	2002Oct9
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587	r400su_degentri_test_01	2002Oct9
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588	r400su_degentri_test_02	2002Oct9
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589	r400su_degentri_test_03	2002Oct9
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594	r400su_edgeflags_triangle_02	2002Oct9
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595	r400su_geom_sort_01	2002Oct9
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596	r400su_line_end_cap_functional_01	2002Oct9

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597	r400su_line_end_cap_width_functional_02	2002Oct9
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598	r400su_line_orientation_01	2002Oct9
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599	r400su_line_orientation_02	2002Oct9
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600	r400su_line_orientation_dx01_01	2002Oct9
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601	r400su_line_orientation_dx01_02	2002Oct9
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602	r400su_line_orientation_dy01_01	2002Oct9
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603	r400su_line_orientation_dy01_02	2002Oct9
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605	r400su_line_test_02	2002Oct9
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606	r400su_longstrip_01	2002Oct9
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609	r400su_multi_prim_02	2002Oct9
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610	r400su_parallel_orientation_all_01	2002Oct9
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611	r400su_parallel_orientation_all_02	2002Oct9
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613	r400su_pc_management_02	2002Oct9
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614	r400su_pc_management_03	2002Oct9
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615	r400su_point_sprite_01	2002Oct9
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622	r400su_point_sprite_08	2002Oct9
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624	r400su_point_wl_hl6_functional_01	2002Oct9
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625	r400su_pointsizepresent_01	2002Oct9
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626	r400su_pointsizepresent_02	2002Oct9
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627	r400su_polymode_lines_degen_triangle_01	2002Oct9
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628	r400su_polymode_lines_degen_triangle_02	2002Oct9
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630	r400su_polymode_lines_zero_area_triangle_02	2002Oct9
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631	r400su_polymode_points_degen_triangle_01	2002Oct9
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632	r400su_polymode_points_degen_triangle_02	2002Oct9
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633	r400su_polymode_points_zero_area_triangle_01	2002Oct9
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634	r400su_polymode_points_zero_area_triangle_02	2002Oct9
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635	r400su_polymode_rectangle_01	2002Oct9
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636	r400su_polymode_zero_area_triangle_01	2002Oct9
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637	r400su_polymode_zero_area_triangle_02	2002Oct9
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638	r400su_polymode_zero_area_triangle_03	2002Oct9
PASS		
639	r400su_polymode_zero_area_triangle_04	2002Oct9
PASS		
640	r400su_polymodeculling_01	2002Oct9
PASS		
641	r400su_polymodefunctional_01	2002Oct9
PASS		
642	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct9
PASS		
643	r400su_provoking_vtx_edgeflags_triangle_02	2002Oct9
PASS		
644	r400su_provoking_vtx_edgeflags_triangle_03	2002Oct9

PASS		
645	r400su_provoking_vtx_edgeflags_triangle_04	2002Oct9
PASS		
646	r400su_provoking_vtx_line_01	2002Oct9
PASS		
647	r400su_provoking_vtx_point_01	2002Oct9
PASS		
648	r400su_provoking_vtx_polymode_rectangle_01	2002Oct9
PASS		
649	r400su_provoking_vtx_rectangle_01	2002Oct9
PASS		
650	r400su_provoking_vtx_triangle_01	2002Oct9
PASS		
651	r400su_rand_line_01	2002Oct9
PASS		
652	r400su_rand_point_01	2002Oct9
PASS		
653	r400su_rand_tri_01	2002Oct9
PASS		
654	r400su_rectangle_01	2002Oct9
PASS		
655	r400su_rectangle_list_01	2002Oct9
PASS		
656	r400su_sliver_01	2002Oct9
PASS		
657	r400su_triarea_test_01	2002Oct9
PASS		
658	r400su_triarea_test_02	2002Oct9
PASS		
659	r400su_triarea_test_03	2002Oct9
PASS		
660	r400su_triarea_test_04	2002Oct9
PASS		
661	r400su_vertexsortingfunctional_01	2002Oct9
PASS		
662	r400su_w_grad_test_01	2002Oct9
PASS		
663	r400su_w_grad_test_02	2002Oct9
PASS		
664	r400su_w_grad_test_03	2002Oct9
PASS		
665	r400su_z_grad_test_01	2002Oct9
PASS		
666	r400su_z_grad_test_02	2002Oct9
PASS		
667	r400su_z_grad_test_03	2002Oct9
PASS		
668	r400su_zero_area_test_01	2002Oct9

PASS		
669	r400su_zero_area_test_02	2002Oct9
PASS		
670	r400su_zero_area_test_03	2002Oct9
PASS		
671	r400su_zero_area_test_04	2002Oct9
PASS		
672	r400vgt_auto_index_line_list_01	2002Oct9
PASS		
673	r400vgt_auto_index_line_loop_01	2002Oct9
PASS		
674	r400vgt_auto_index_line_strip_01	2002Oct9
PASS		
675	r400vgt_auto_index_points_01	2002Oct9
PASS		
676	r400vgt_auto_index_polygon_01	2002Oct9
PASS		
677	r400vgt_auto_index_primitives_01	2002Oct9
PASS		
678	r400vgt_auto_index_quad_list_01	2002Oct9
PASS		
679	r400vgt_auto_index_quad_strip_01	2002Oct9
PASS		
680	r400vgt_auto_index_rectangle_list_01	2002Oct9
PASS		
681	r400vgt_auto_index_tri_fan_01	2002Oct9
PASS		
682	r400vgt_auto_index_tri_list_01	2002Oct9
PASS		
683	r400vgt_auto_index_tri_strip_01	2002Oct9
PASS		
684	r400vgt_auto_index_tri_wflags_01	2002Oct9
PASS		
685	r400vgt_dma_engine_01	2002Oct9
PASS		
686	r400vgt_dma_engine_02	2002Oct9
PASS		
687	r400vgt_dma_engine_03	2002Oct9
PASS		
688	r400vgt_dma_engine_04	2002Oct9
PASS		
689	r400vgt_dma_engine_05	2002Oct9
PASS		
690	r400vgt_dma_engine_06	2002Oct9
PASS		
691	r400vgt_dma_engine_07	2002Oct9
PASS		
692	r400vgt_dma_engine_08	2002Oct9

PASS		
693	r400vgt_dma_engine_09	2002Oct9
PASS		
694	r400vgt_dma_engine_10	2002Oct9
PASS		
695	r400vgt_dma_index_line_list_01	2002Oct9
PASS		
696	r400vgt_dma_index_line_loop_01	2002Oct9
PASS		
697	r400vgt_dma_index_line_strip_01	2002Oct9
PASS		
698	r400vgt_dma_index_points_01	2002Oct9
PASS		
699	r400vgt_dma_index_polygon_01	2002Oct9
PASS		
700	r400vgt_dma_index_primitives_01	2002Oct9
PASS		
701	r400vgt_dma_index_quad_list_01	2002Oct9
PASS		
702	r400vgt_dma_index_quad_strip_01	2002Oct9
PASS		
703	r400vgt_dma_index_rectangle_list_01	2002Oct9
PASS		
704	r400vgt_dma_index_tri_fan_01	2002Oct9
PASS		
705	r400vgt_dma_index_tri_list_01	2002Oct9
PASS		
706	r400vgt_dma_index_tri_strip_01	2002Oct9
PASS		
707	r400vgt_dma_index_tri_wflags_01	2002Oct9
PASS		
708	r400vgt_dma_swap_indx16_01	2002Oct9
PASS		
709	r400vgt_dma_swap_indx32_01	2002Oct9
PASS		
710	r400vgt_edgeflags_polygon_01	2002Oct9
PASS		
711	r400vgt_edgeflags_quad_list_01	2002Oct9
PASS		
712	r400vgt_edgeflags_quad_strip_01	2002Oct9
PASS		
713	r400vgt_edgeflags_triangle_fan_01	2002Oct9
PASS		
714	r400vgt_edgeflags_triangle_list_01	2002Oct9
PASS		
715	r400vgt_edgeflags_triangle_strip_01	2002Oct9
PASS		
716	r400vgt_edgeflags_triangle_wflags_01	2002Oct9

PASS		
717	r400vgt_event_handling_01	2002Sep26
PASS		
718	r400vgt_ext2int_index_line_list_01	2002Oct9
PASS		
719	r400vgt_ext2int_index_line_loop_01	2002Oct9
PASS		
720	r400vgt_ext2int_index_line_strip_01	2002Oct9
PASS		
721	r400vgt_ext2int_index_points_01	2002Oct9
PASS		
722	r400vgt_ext2int_index_polygon_01	2002Oct9
PASS		
723	r400vgt_ext2int_index_quad_list_01	2002Oct9
PASS		
724	r400vgt_ext2int_index_quad_strip_01	2002Oct9
PASS		
725	r400vgt_ext2int_index_rectangle_list_01	2002Oct9
PASS		
726	r400vgt_ext2int_index_triangle_fan_01	2002Oct9
PASS		
727	r400vgt_ext2int_index_triangle_list_01	2002Oct9
PASS		
728	r400vgt_ext2int_index_triangle_strip_01	2002Oct9
PASS		
729	r400vgt_ext2int_index_triangle_wflags_01	2002Oct9
PASS		
730	r400vgt_hos_LPatch_01	2002Oct9
PASS		
731	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Oct9
PASS		
732	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct9
PASS		
733	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct9
PASS		
734	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Oct9
PASS		
735	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Oct9
PASS		
736	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct9
PASS		
737	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct9
PASS		
738	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct9
PASS		
739	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct9
PASS		
740	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct9

PASS		
741	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct9
PASS		
742	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct9
PASS		
743	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct9
PASS		
744	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct9
PASS		
745	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct9
PASS		
746	r400vgt_hos_PNT_cp_qn_disc_l4_04_lit_tex_proj_01	2002Oct9
PASS		
747	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct9
PASS		
748	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Oct9
PASS		
749	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Oct9
PASS		
750	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Oct9
PASS		
751	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct9
PASS		
752	r400vgt_hos_PNT_edge_detection_01	2002Oct9
PASS		
753	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct9
PASS		
754	r400vgt_hos_RPatch_cp_02	2002Oct9
PASS		
755	r400vgt_hos_RPatch_lp_02	2002Oct9
PASS		
756	r400vgt_hos_TPatch_01	2002Oct9
PASS		
757	r400vgt_hos_TPatch_02	2002Oct9
PASS		
758	r400vgt_hos_auto_index_line_list_01	2002Oct9
PASS		
759	r400vgt_hos_auto_index_quad_list_01	2002Oct9
PASS		
760	r400vgt_hos_auto_index_triangle_list_01	2002Oct9
PASS		
761	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Oct9
PASS		
762	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct9
PASS		
763	r400vgt_immed_index_line_list_01	2002Oct9
PASS		
764	r400vgt_immed_index_line_loop_01	2002Oct9

PASS		
765	r400vgt_immed_index_line_strip_01	2002Oct9
PASS		
766	r400vgt_immed_index_points_01	2002Oct9
PASS		
767	r400vgt_immed_index_polygon_01	2002Oct9
PASS		
768	r400vgt_immed_index_primitives_01	2002Oct9
PASS		
769	r400vgt_immed_index_quad_list_01	2002Oct9
PASS		
770	r400vgt_immed_index_quad_strip_01	2002Oct9
PASS		
771	r400vgt_immed_index_rectangle_list_01	2002Oct9
PASS		
772	r400vgt_immed_index_tri_fan_01	2002Oct9
PASS		
773	r400vgt_immed_index_tri_list_01	2002Oct9
PASS		
774	r400vgt_immed_index_tri_strip_01	2002Oct9
PASS		
775	r400vgt_immed_index_tri_wflags_01	2002Oct9
PASS		
776	r400vgt_index_dealloc_line_list_01	2002Oct9
PASS		
777	r400vgt_index_dealloc_points_01	2002Oct9
PASS		
778	r400vgt_index_dealloc_triangle_list_01	2002Oct9
PASS		
779	r400vgt_index_min_max_01	2002Oct9
PASS		
780	r400vgt_index_min_max_02	2002Oct9
PASS		
781	r400vgt_index_offset_01	2002Oct9
PASS		
782	r400vgt_index_offset_02	2002Oct9
PASS		
783	r400vgt_index_offset_03	2002Oct9
PASS		
784	r400vgt_index_offset_04	2002Oct9
PASS		
785	r400vgt_index_offset_05	2002Oct9
PASS		
786	r400vgt_index_offset_06	2002Oct9
PASS		
787	r400vgt_index_offset_07	2002Oct9
PASS		
788	r400vgt_index_offset_08	2002Oct9

PASS		
789	r400vgt_index_size_01	20020ct9
PASS		
790	r400vgt_index_size_02	20020ct9
PASS		
791	r400vgt_index_source_switch_01	20020ct9
PASS		
792	r400vgt_line_list_01	20020ct9
PASS		
793	r400vgt_line_list_02	20020ct9
PASS		
794	r400vgt_line_loop_01	20020ct9
PASS		
795	r400vgt_line_loop_02	20020ct9
PASS		
796	r400vgt_line_strip_01	20020ct9
PASS		
797	r400vgt_line_strip_02	20020ct9
PASS		
798	r400vgt_multi_context_01	20020ct9
PASS		
799	r400vgt_multi_context_02	20020ct9
PASS		
800	r400vgt_multi_context_03	20020ct9
PASS		
801	r400vgt_multi_context_04	20020ct9
PASS		
802	r400vgt_multi_context_05	20020ct9
PASS		
803	r400vgt_multi_context_06	20020ct9
PASS		
804	r400vgt_multi_context_07	20020ct9
PASS		
805	r400vgt_multi_context_08	20020ct9
PASS		
806	r400vgt_multi_context_09	20020ct9
PASS		
807	r400vgt_multi_context_10	20020ct9
PASS		
808	r400vgt_multi_context_11	20020ct9
PASS		
809	r400vgt_multi_context_12	20020ct9
PASS		
810	r400vgt_multi_prim_reset_index_all_01	20020ct9
PASS		
811	r400vgt_multi_prim_reset_index_all_02	20020ct9
PASS		
812	r400vgt_pass_thru_all_prims_01	20020ct9

PASS		
813	r400vgt_point_list_01	2002Oct9
PASS		
814	r400vgt_point_list_02	2002Oct9
PASS		
815	r400vgt_polygon_01	2002Oct9
PASS		
816	r400vgt_polygon_02	2002Oct9
PASS		
817	r400vgt_provoking_vtx_all_01	2002Oct9
PASS		
818	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct9
PASS		
819	r400vgt_provoking_vtx_polygon_01	2002Oct9
PASS		
820	r400vgt_provoking_vtx_quad_list_01	2002Oct9
PASS		
821	r400vgt_provoking_vtx_quad_strip_01	2002Oct9
PASS		
822	r400vgt_provoking_vtx_tri_fan_01	2002Oct9
PASS		
823	r400vgt_provoking_vtx_tri_strip_01	2002Oct9
PASS		
824	r400vgt_quad_list_01	2002Oct9
PASS		
825	r400vgt_quad_list_02	2002Oct9
PASS		
826	r400vgt_quad_strip_01	2002Oct9
PASS		
827	r400vgt_quad_strip_02	2002Oct9
PASS		
828	r400vgt_rectangle_list_01	2002Oct9
PASS		
829	r400vgt_rectangle_list_02	2002Oct9
PASS		
830	r400vgt_reuse_depth_line_list_01	2002Oct9
PASS		
831	r400vgt_reuse_depth_point_list_01	2002Oct9
PASS		
832	r400vgt_reuse_depth_triangle_list_01	2002Oct9
PASS		
833	r400vgt_reuse_index_line_list_01	2002Oct9
PASS		
834	r400vgt_reuse_index_point_list_01	2002Oct9
PASS		
835	r400vgt_reuse_index_triangle_list_01	2002Oct9
PASS		
836	r400vgt_reuse_index_triangle_list_02	2002Oct9

PASS			
837	r400vgt_reuse_index_triangle_list_03		2002Oct9
PASS			
838	r400vgt_suppress_eop_01	0:0:1	2002Oct9
PASS			
839	r400vgt_suppress_eop_02	0:0:1	2002Oct9
PASS			
840	r400vgt_suppress_eop_03	0:0:1	2002Oct9
PASS			
841	r400vgt_suppress_eop_04	0:0:1	2002Oct9
PASS			
842	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
843	r400vgt_triangle_fan_01	0:0:1	2002Oct9
PASS			
844	r400vgt_triangle_fan_02	0:0:1	2002Oct9
PASS			
845	r400vgt_triangle_list_01	0:0:1	2002Oct9
PASS			
846	r400vgt_triangle_list_02	0:0:1	2002Oct9
PASS			
847	r400vgt_triangle_strip_01	0:0:1	2002Oct9
PASS			
848	r400vgt_triangle_strip_02	0:0:1	2002Oct9
PASS			
849	r400vgt_triangle_wflags_01	0:0:1	2002Oct9
PASS			
850	r400vgt_triangle_wflags_02	0:0:1	2002Oct9
PASS			
851	r400vgt_viz_query_01	0:0:1	2002Oct9
PASS			
852	r400vgt_vtx_vector_packing_01	0:0:1	2002Oct9
PASS			
853	r400vte_coverage_01	0:0:1	2002Oct9
PASS			
854	r400vte_simple_point_01	0:0:1	2002Oct9
PASS			
855	r400vte_simple_triangle_01	0:0:1	2002Oct9
PASS			
856	r400vte_w0_fmt_01	0:0:1	2002Oct9
PASS			
857	r400vte_w0_fmt_03	0:0:1	2002Oct9
PASS			
858	r400vte_xy_fmt_01	0:0:1	2002Oct9
PASS			
859	r400vte_xy_fmt_mk_01_bug	0:0:1	2002Oct9
PASS			
860	r400vte_xyz_scale_01	0:0:1	2002Oct9

PASS

861 r400vte_xyz_scale_02

0:0:1

2002Oct9

PASS

862 r400vte_z_fmt_01

0:0:1

2002Oct9

PASS

=====

Tests_Run_count = 862 Pass_count = 860 Fail_count = 1 Incomplete_count = 1

=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Oct1
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Oct1
PASS			
3	cp_e2_one_blit		2002Oct1
PASS			
4	cp_e2_one_line		2002Oct1
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Oct1
PASS			
6	cp_e2paint_565		2002Oct1
PASS			
7	cp_e2paint_multi		2002Oct1
PASS			
8	cp_e2polyscanlines_brush		2002Oct1
PASS			
9	cp_e2polyscanlines_brush_mt		2002Oct1
PASS			
10	cp_e2smalltext_jcl		2002Oct1
PASS			
11	cp_simple_triangle		2002Oct1
PASS			
12	primlib_1st_tri_june15		2002Oct1
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Oct1
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Oct1
PASS			
15	primlib_parameterized_simple_triangle		2002Oct1
PASS			
16	primlib_template_simple_triangle		2002Oct1
PASS			
17	primlib_tex_tri		2002Oct1
PASS			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Oct1
PASS			
20	r400cl_clip_disable_01		2002Oct1

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Oct1
PASS		
22	r400cl_clip_ucp_6bits_01	2002Oct1
PASS		
23	r400cl_cull_only_ena_01	2002Oct1
PASS		
24	r400cl_edgeflags_01	2002Oct1
PASS		
25	r400cl_edgeflags_01_bug	2002Oct1
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Oct1
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Oct1
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Oct1
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Oct1
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Oct1
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Oct1
PASS		
32	r400cl_edgeflags_gband_01	2002Oct1
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Oct1
PASS		
34	r400cl_edgeflags_gband_left_01	2002Oct1
PASS		
35	r400cl_edgeflags_gband_right_01	2002Oct1
PASS		
36	r400cl_edgeflags_gband_top_01	2002Oct1
PASS		
37	r400cl_frustum_01	2002Oct1
PASS		
38	r400cl_frustum_02	2002Oct1
PASS		
39	r400cl_frustum_03	2002Oct1
PASS		
40	r400cl_frustum_04	2002Oct1
PASS		
41	r400cl_frustum_05	2002Oct1
PASS		
42	r400cl_frustum_06	2002Oct1
PASS		
43	r400cl_frustum_07	2002Oct1
PASS		
44	r400cl_frustum_08	2002Oct1

PASS		
45	r400cl_frustum_09	2002Oct1
PASS		
46	r400cl_frustum_10	2002Oct1
PASS		
47	r400cl_frustum_11	2002Oct1
PASS		
48	r400cl_frustum_12	2002Oct1
PASS		
49	r400cl_frustum_13	2002Oct1
PASS		
50	r400cl_frustum_14	2002Oct1
PASS		
51	r400cl_frustum_15	2002Oct1
PASS		
52	r400cl_frustum_16	2002Oct1
PASS		
53	r400cl_frustum_17	2002Oct1
PASS		
54	r400cl_frustum_18	2002Oct1
PASS		
55	r400cl_frustum_19	2002Oct1
PASS		
56	r400cl_frustum_20	2002Oct1
PASS		
57	r400cl_frustum_21	2002Oct1
PASS		
58	r400cl_frustum_22	2002Oct1
PASS		
59	r400cl_frustum_23	2002Oct1
PASS		
60	r400cl_frustum_24	2002Oct1
PASS		
61	r400cl_frustum_25	2002Oct1
PASS		
62	r400cl_frustum_26	2002Oct1
PASS		
63	r400cl_frustum_27	2002Oct1
PASS		
64	r400cl_frustum_28	2002Oct1
PASS		
65	r400cl_frustum_all_vols_lines	2002Oct1
PASS		
66	r400cl_frustum_all_vols_tris	2002Oct1
PASS		
67	r400cl_frustum_lines_01	2002Oct1
PASS		
68	r400cl_frustum_lines_02	2002Oct1

PASS		
69	r400cl_frustum_lines_03	2002Oct1
PASS		
70	r400cl_frustum_lines_04	2002Oct1
PASS		
71	r400cl_frustum_lines_05	2002Oct1
PASS		
72	r400cl_frustum_lines_06	2002Oct1
PASS		
73	r400cl_frustum_lines_07	2002Oct1
PASS		
74	r400cl_frustum_lines_08	2002Oct1
PASS		
75	r400cl_frustum_lines_09	2002Oct1
PASS		
76	r400cl_frustum_lines_10	2002Oct1
PASS		
77	r400cl_frustum_lines_11	2002Oct1
PASS		
78	r400cl_frustum_lines_12	2002Oct1
PASS		
79	r400cl_frustum_lines_13	2002Oct1
PASS		
80	r400cl_frustum_lines_14	2002Oct1
PASS		
81	r400cl_frustum_lines_15	2002Oct1
PASS		
82	r400cl_frustum_lines_16	2002Oct1
PASS		
83	r400cl_frustum_lines_17	2002Oct1
PASS		
84	r400cl_frustum_lines_18	2002Oct1
PASS		
85	r400cl_frustum_lines_19	2002Oct1
PASS		
86	r400cl_frustum_lines_20	2002Oct1
PASS		
87	r400cl_frustum_lines_21	2002Oct1
PASS		
88	r400cl_frustum_lines_22	2002Oct1
PASS		
89	r400cl_frustum_lines_23	2002Oct1
PASS		
90	r400cl_frustum_lines_24	2002Oct1
PASS		
91	r400cl_gband_04	2002Oct1
PASS		
92	r400cl_gband_tcl_01	2002Oct1

PASS		
93	r400cl_nan_kill_01_bug	2002Oct1
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Oct1
PASS		
95	r400cl_polymode_line_fill_01_bug	2002Oct1
PASS		
96	r400cl_simple_triangle_01	2002Oct1
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97	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct1
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98	r400cl_ucp_combos_01	2002Oct1
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99	r400cl_ucp_combos_02	2002Oct1
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100	r400cl_ucp_combos_03	2002Oct1
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125	r400cl_ucp_combos_28	2002Oct1
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126	r400cl_ucp_combos_29	2002Oct1
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127	r400cl_ucp_combos_30	2002Oct1
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128	r400cl_ucp_pointlist_01	2002Oct1
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129	r400cl_vertex_reuse_clip_01	2002Oct1
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130	r400cl_vtx_kill_01	2002Oct1
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131	r400cl_vtx_kill_02	2002Oct1
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132	r400cl_w_eq_0	2002Oct1
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133	r400rbm_simple_triangle_01	2002Oct1
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134	r400sanity_persp_projection_01	2002Oct1
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135	r400sanity_vfd_texture_sample_01	2002Oct1
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136	r400sc_baryc_01	2002Oct1
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137	r400sc_bres_cntl_01	2002Sep7
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138	r400sc_bres_cntl_02	2002Oct1
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139	r400sc_bres_cntl_03	2002Oct1
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140	r400sc_bres_cntl_04	2002Oct1

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141	r400sc_bres_cntl_w2k_01	2002Oct1
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142	r400sc_bres_cntl_w9x_01	2002Oct1
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143	r400sc_clip_rect_01	2002Oct1
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144	r400sc_clip_rect_02	2002Oct1
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147	r400sc_clip_rect_fc_01	2002Oct1
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148	r400sc_diamond_exit_01	2002Oct1
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149	r400sc_diamond_exit_02	2002Oct1
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151	r400sc_diamond_exit_04	2002Oct1
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152	r400sc_diamond_exit_05	2002Oct1
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153	r400sc_jss_1x1_primtypes_01	2002Oct1
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154	r400sc_jss_1x2_01	2002Oct1
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155	r400sc_jss_1x2_02	2002Oct1
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156	r400sc_jss_1x2_primtypes_01	2002Oct1
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157	r400sc_jss_1x3_01	2002Oct1
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158	r400sc_jss_1x3_02	2002Oct1
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159	r400sc_jss_1x3_primtypes_01	2002Oct1
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160	r400sc_jss_1x4_01	2002Oct1
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161	r400sc_jss_1x4_02	2002Oct1
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162	r400sc_jss_1x4_primtypes_01	2002Oct1
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163	r400sc_jss_2x1_01	2002Oct1
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164	r400sc_jss_2x1_02	2002Oct1

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167	r400sc_jss_2x2_02	2002Oct1
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169	r400sc_jss_2x3_01	2002Oct1
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170	r400sc_jss_2x3_02	2002Oct1
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171	r400sc_jss_2x3_primtypes_01	2002Oct1
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172	r400sc_jss_2x4_01	2002Oct1
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173	r400sc_jss_2x4_02	2002Oct1
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174	r400sc_jss_2x4_primtypes_01	2002Oct1
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175	r400sc_jss_3x1_01	2002Oct1
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176	r400sc_jss_3x1_02	2002Oct1
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177	r400sc_jss_3x1_primtypes_01	2002Oct1
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178	r400sc_jss_3x2_01	2002Oct1
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179	r400sc_jss_3x2_02	2002Oct1
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182	r400sc_jss_3x3_02	2002Oct1
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192	r400sc_jss_4x2_02	2002Oct1
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193	r400sc_jss_4x2_primitives_01	2002Oct1
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194	r400sc_jss_4x3_01	2002Oct1
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195	r400sc_jss_4x3_02	2002Oct1
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198	r400sc_jss_4x4_02	2002Oct1
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199	r400sc_jss_4x4_03	2002Oct1
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200	r400sc_jss_4x4_aa_mask_01	2002Oct1
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201	r400sc_jss_4x4_aa_mask_02	2002Oct1
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202	r400sc_jss_4x4_fc_01	2002Oct1
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203	r400sc_jss_4x4_fc_02	2002Oct1
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204	r400sc_jss_4x4_max_dist_01	2002Oct1
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205	r400sc_jss_4x4_primitives_01	2002Oct1
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206	r400sc_line_dx10_eq_0_01	2002Oct1
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207	r400sc_line_dx10_ge_0_01	2002Oct1
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208	r400sc_line_dx10_lt_0_01	2002Oct1
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209	r400sc_line_dy10_eq_0_01	2002Oct1
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210	r400sc_line_dy10_ge_0_01	2002Oct1
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211	r400sc_line_dy10_lt_0_01	2002Oct1
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212	r400sc_line_expand_width_msa_8_01	2002Oct1

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213	r400sc_line_expand_width_msa_8_02	2002Oct1
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214	r400sc_line_expand_width_msa_8_03	2002Oct1
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215	r400sc_line_jss_3x4_01	2002Oct1
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216	r400sc_line_list_01	2002Oct1
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217	r400sc_line_list_02	2002Oct1
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218	r400sc_line_list_concentric_circle_01	2002Oct1
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219	r400sc_line_list_concentric_circle_02	2002Oct1
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220	r400sc_line_list_concentric_circle_03	2002Oct1
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221	r400sc_line_list_textured_01	2002Oct1
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222	r400sc_line_list_verify_st_01	2002Oct1
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223	r400sc_line_list_verify_st_01_bug	2002Oct1
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224	r400sc_line_msa_8_01	2002Oct1
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225	r400sc_line_msa_8_textured_01	2002Oct1
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226	r400sc_line_stipple_01	2002Oct1
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227	r400sc_line_stipple_02	2002Oct1
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228	r400sc_line_stipple_03	2002Oct1
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229	r400sc_line_stipple_04	2002Oct1
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234	r400sc_line_stipple_09	2002Oct1
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235	r400sc_line_stipple_10	2002Oct1
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236	r400sc_line_stipple_11	2002Oct1

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238	r400sc_line_stipple_13	2002Oct1
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239	r400sc_line_stipple_14	2002Oct1
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241	r400sc_line_strip_stipple_01	2002Sep25
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242	r400sc_msa_1_01	2002Oct1
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275	r400sc_msa_3_zbuffer_rectangle_list_02	2002Oct1
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276	r400sc_msa_4_01	2002Oct1
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287	r400sc_msaa_4_zbuffer_rectangle_list_02	2002Oct1
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289	r400sc_msaa_6_primitives_01	2002Oct1
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305	r400sc_msaa_8_aa_mask_01	2002Oct1
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306	r400sc_msaa_8_aa_mask_02	2002Oct1
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307	r400sc_msaa_8_aa_mask_fc_02	2002Oct1
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319	r400sc_msaa_8_zbuffer_rectangle_list_02	2002Oct1
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321	r400sc_msaa_X_primtypes_02_bug	2002Oct1
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322	r400sc_multi_chip_prim_discard_00_07	2002Oct1
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323	r400sc_null_triangles_01	2002Oct1
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324	r400sc_null_triangles_fc_01	2002Oct1
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325	r400sc_pinwheel_01	2002Oct1
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326	r400sc_pinwheel_02	2002Oct1
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327	r400sc_pinwheel_02_bug	2002Oct1
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328	r400sc_point_jss_3x4_01	2002Oct1
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351	r400sc_poly_offset_10_bug	2002Oct1
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352	r400sc_poly_offset_fc_01	2002Oct1
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353	r400sc_polygon_stipple_01	2002Oct1
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354	r400sc_polymode_tri_fill_01	2002Oct1
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355	r400sc_polymode_tri_fill_01_bug	2002Oct1
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356	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct1

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357	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct1
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364	r400sc_raster_fill_rule_01	2002Oct1
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366	r400sc_raster_fill_rule_03	2002Oct1
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369	r400sc_raster_fill_rule_fc_01	2002Oct1
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378	r400sc_scissor_rect_01	2002Oct1
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379	r400sc_scissor_rect_02	2002Oct1
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380	r400sc_scissor_rect_03	2002Oct1

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382	r400sc_scissor_rect_05	2002Oct1
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383	r400sc_scissor_rect_fc_01	2002Oct1
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384	r400sc_set_state_01	2002Oct1
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385	r400sc_sp_sample_cntl_01	2002Oct1
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386	r400sc_sp_sample_cntl_02	2002Oct1
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387	r400sc_sp_sample_cntl_03	2002Oct1
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391	r400sc_sp_sample_cntl_07	2002Oct1
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392	r400sc_sp_sample_cntl_08	2002Oct1
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393	r400sc_tri_16_par_64_dwords_01	2002Oct1
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394	r400sc_tri_8textures_01	2002Oct1
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395	r400sc_tri_8textures_02	2002Oct1
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396	r400sc_tri_walk_start_vertex_01	2002Oct1
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397	r400sc_tri_walk_start_vertex_02	2002Oct1
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398	r400sc_tri_walk_start_vertex_03	2002Oct1
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404	r400sc_tri_walk_start_vertex_09	2002Oct1

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407	r400sc_tri_walk_start_vertex_12	2002Oct1
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408	r400sc_tri_walk_start_vertex_13	2002Oct1
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409	r400sc_tri_walk_start_vertex_14	2002Oct1
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410	r400sc_tri_walk_start_vertex_15	2002Oct1
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411	r400sc_tri_walk_start_vertex_16	2002Oct1
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412	r400sc_triangle_stipple_01	2002Oct1
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413	r400sc_window_offset_01	2002Oct1
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414	r400sc_window_offset_fc_01	2002Oct1
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415	r400sc_zbuffer_line_list_01	2002Oct1
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416	r400sc_zbuffer_point_list_01	2002Oct1
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417	r400sc_zbuffer_rectangle_list_01	2002Oct1
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418	r400sc_zbuffer_rectangle_list_02	2002Oct1
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419	r400sc_zbuffer_rectangle_list_fc_02	2002Oct1
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420	r400sc_zbuffer_triangle_list_01	2002Oct1
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421	r400su_4tri_text_offscreen_01	2002Oct1
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422	r400su_baryc_test_01	2002Oct1
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423	r400su_baryc_test_02	2002Oct1
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424	r400su_baryc_test_03	2002Oct1
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425	r400su_baryc_test_04	2002Oct1
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426	r400su_clip_baryc_test_01	2002Oct1
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427	r400su_clip_baryc_test_02	2002Oct1
PASS		
428	r400su_clip_edgeflag_polymode_01	2002Oct1

PASS		
429	r400su_clip_pointsize_test_01	2002Oct1
PASS		
430	r400su_clip_pointtest_01	2002Oct1
PASS		
431	r400su_clip_pointtest_02	2002Oct1
PASS		
432	r400su_clip_pointtest_03	2002Oct1
PASS		
433	r400su_clip_pointtest_04	2002Oct1
PASS		
434	r400su_clip_polymode_test_01	2002Oct1
PASS		
435	r400su_clipline_01	2002Oct1
PASS		
436	r400su_clippoint_01	2002Oct1
PASS		
437	r400su_clipvertexsortingfunctional_01	2002Oct1
PASS		
438	r400su_cullingfunctional_01	2002Oct1
PASS		
439	r400su_degentri_test_01	2002Oct1
PASS		
440	r400su_degentri_test_02	2002Oct1
PASS		
441	r400su_degentri_test_03	2002Oct1
PASS		
442	r400su_edge_flag_01	2002Oct1
PASS		
443	r400su_edge_flag_02	2002Oct1
PASS		
444	r400su_edgeflags_triangle_01	2002Oct1
PASS		
445	r400su_edgeflags_triangle_02	2002Oct1
PASS		
446	r400su_geom_sort_01	2002Oct1
PASS		
447	r400su_line_end_cap_functional_01	2002Oct1
PASS		
448	r400su_line_end_cap_width_functional_02	2002Oct1
PASS		
449	r400su_line_test_01	2002Oct1
PASS		
450	r400su_line_test_02	2002Oct1
PASS		
451	r400su_longstrip_01	2002Oct1
PASS		
452	r400su_multi_context_01	2002Oct1

PASS		
453	r400su_multi_prim_01	2002Oct1
PASS		
454	r400su_multi_prim_02	2002Oct1
PASS		
455	r400su_parallel_orientation_all_01	2002Oct1
PASS		
456	r400su_parallel_orientation_all_02	2002Oct1
PASS		
457	r400su_pc_management_01	2002Oct1
PASS		
458	r400su_pc_management_02	2002Oct1
PASS		
459	r400su_pc_management_03	2002Oct1
PASS		
460	r400su_point_sprite_01	2002Oct1
PASS		
461	r400su_point_sprite_02	2002Oct1
PASS		
462	r400su_point_sprite_03	2002Oct1
PASS		
463	r400su_point_sprite_04	2002Oct1
PASS		
464	r400su_point_sprite_05	2002Oct1
PASS		
465	r400su_point_sprite_06	2002Oct1
PASS		
466	r400su_point_sprite_07	2002Oct1
PASS		
467	r400su_point_sprite_08	2002Oct1
PASS		
468	r400su_point_wl6_h1_functional_01	2002Oct1
PASS		
469	r400su_point_wl_h16_functional_01	2002Oct1
PASS		
470	r400su_pointsizepresent_01	2002Oct1
PASS		
471	r400su_pointsizepresent_02	2002Oct1
PASS		
472	r400su_polymode_lines_degen_triangle_01	2002Oct1
PASS		
473	r400su_polymode_lines_degen_triangle_02	2002Oct1
PASS		
474	r400su_polymode_lines_zero_area_triangle_01	2002Oct1
PASS		
475	r400su_polymode_lines_zero_area_triangle_02	2002Oct1
PASS		
476	r400su_polymode_points_degen_triangle_01	2002Oct1

PASS		
477	r400su_polymode_points_degen_triangle_02	2002Oct1
PASS		
478	r400su_polymode_points_zero_area_triangle_01	2002Oct1
PASS		
479	r400su_polymode_points_zero_area_triangle_02	2002Oct1
PASS		
480	r400su_polymode_rectangle_01	2002Oct1
PASS		
481	r400su_polymode_zero_area_triangle_01	2002Oct1
PASS		
482	r400su_polymode_zero_area_triangle_02	2002Oct1
PASS		
483	r400su_polymode_zero_area_triangle_03	2002Oct1
PASS		
484	r400su_polymode_zero_area_triangle_04	2002Oct1
PASS		
485	r400su_polymodeculling_01	2002Oct1
PASS		
486	r400su_polymodefunctional_01	2002Oct1
PASS		
487	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct1
PASS		
488	r400su_provoking_vtx_edgeflags_triangle_02	2002Oct1
PASS		
489	r400su_provoking_vtx_edgeflags_triangle_03	2002Oct1
PASS		
490	r400su_provoking_vtx_edgeflags_triangle_04	2002Oct1
PASS		
491	r400su_provoking_vtx_line_01	2002Oct1
PASS		
492	r400su_provoking_vtx_point_01	2002Oct1
PASS		
493	r400su_provoking_vtx_polymode_rectangle_01	2002Oct1
PASS		
494	r400su_provoking_vtx_rectangle_01	2002Oct1
PASS		
495	r400su_provoking_vtx_triangle_01	2002Oct1
PASS		
496	r400su_rand_line_01	2002Oct1
PASS		
497	r400su_rand_point_01	2002Oct1
PASS		
498	r400su_rand_tri_01	2002Oct1
PASS		
499	r400su_rectangle_list_01	2002Oct1
PASS		
500	r400su_sliver_01	2002Oct1

PASS		
501	r400su_triarea_test_01	2002Oct1
PASS		
502	r400su_triarea_test_02	2002Oct1
PASS		
503	r400su_triarea_test_03	2002Oct1
PASS		
504	r400su_triarea_test_04	2002Oct1
PASS		
505	r400su_vertexsortingfunctional_01	2002Oct1
PASS		
506	r400su_w_grad_test_01	2002Oct1
PASS		
507	r400su_w_grad_test_02	2002Oct1
PASS		
508	r400su_w_grad_test_03	2002Oct1
PASS		
509	r400su_z_grad_test_01	2002Oct1
PASS		
510	r400su_z_grad_test_02	2002Oct1
PASS		
511	r400su_z_grad_test_03	2002Oct1
PASS		
512	r400vgt_auto_index_line_list_01	2002Oct1
PASS		
513	r400vgt_auto_index_line_loop_01	2002Oct1
PASS		
514	r400vgt_auto_index_line_strip_01	2002Oct1
PASS		
515	r400vgt_auto_index_points_01	2002Oct1
PASS		
516	r400vgt_auto_index_polygon_01	2002Oct1
PASS		
517	r400vgt_auto_index_primitives_01	2002Oct1
PASS		
518	r400vgt_auto_index_quad_list_01	2002Oct1
PASS		
519	r400vgt_auto_index_quad_strip_01	2002Oct1
PASS		
520	r400vgt_auto_index_rectangle_list_01	2002Oct1
PASS		
521	r400vgt_auto_index_tri_fan_01	2002Oct1
PASS		
522	r400vgt_auto_index_tri_list_01	2002Oct1
PASS		
523	r400vgt_auto_index_tri_strip_01	2002Oct1
PASS		
524	r400vgt_auto_index_tri_wflags_01	2002Oct1

PASS		
525	r400vgt_dma_engine_01	2002Oct1
PASS		
526	r400vgt_dma_engine_02	2002Oct1
PASS		
527	r400vgt_dma_engine_03	2002Oct1
PASS		
528	r400vgt_dma_engine_04	2002Oct1
PASS		
529	r400vgt_dma_engine_05	2002Oct1
PASS		
530	r400vgt_dma_engine_06	2002Oct1
PASS		
531	r400vgt_dma_engine_07	2002Oct1
PASS		
532	r400vgt_dma_engine_08	2002Oct1
PASS		
533	r400vgt_dma_engine_09	2002Oct1
PASS		
534	r400vgt_dma_engine_10	2002Oct1
PASS		
535	r400vgt_dma_index_line_list_01	2002Oct1
PASS		
536	r400vgt_dma_index_line_loop_01	2002Oct1
PASS		
537	r400vgt_dma_index_line_strip_01	2002Oct1
PASS		
538	r400vgt_dma_index_points_01	2002Oct1
PASS		
539	r400vgt_dma_index_polygon_01	2002Oct1
PASS		
540	r400vgt_dma_index_primitives_01	2002Oct1
PASS		
541	r400vgt_dma_index_quad_list_01	2002Oct1
PASS		
542	r400vgt_dma_index_quad_strip_01	2002Oct1
PASS		
543	r400vgt_dma_index_rectangle_list_01	2002Oct1
PASS		
544	r400vgt_dma_index_tri_fan_01	2002Oct1
PASS		
545	r400vgt_dma_index_tri_list_01	2002Oct1
PASS		
546	r400vgt_dma_index_tri_strip_01	2002Oct1
PASS		
547	r400vgt_dma_index_tri_wflags_01	2002Oct1
PASS		
548	r400vgt_dma_swap_idx16_01	2002Oct1

PASS		
549	r400vgt_dma_swap_idx32_01	2002Oct1
PASS		
550	r400vgt_edgeflags_polygon_01	2002Oct1
PASS		
551	r400vgt_edgeflags_quad_list_01	2002Oct1
PASS		
552	r400vgt_edgeflags_quad_strip_01	2002Oct1
PASS		
553	r400vgt_edgeflags_triangle_fan_01	2002Oct1
PASS		
554	r400vgt_edgeflags_triangle_list_01	2002Oct1
PASS		
555	r400vgt_edgeflags_triangle_strip_01	2002Oct1
PASS		
556	r400vgt_edgeflags_triangle_wflags_01	2002Oct1
PASS		
557	r400vgt_event_handling_01	2002Sep26
PASS		
558	r400vgt_ext2int_index_line_list_01	2002Oct1
PASS		
559	r400vgt_ext2int_index_line_loop_01	2002Oct1
PASS		
560	r400vgt_ext2int_index_line_strip_01	2002Oct1
PASS		
561	r400vgt_ext2int_index_points_01	2002Oct1
PASS		
562	r400vgt_ext2int_index_polygon_01	2002Oct1
PASS		
563	r400vgt_ext2int_index_quad_list_01	2002Oct1
PASS		
564	r400vgt_ext2int_index_quad_strip_01	2002Oct1
PASS		
565	r400vgt_ext2int_index_rectangle_list_01	2002Oct1
PASS		
566	r400vgt_ext2int_index_triangle_fan_01	2002Oct1
PASS		
567	r400vgt_ext2int_index_triangle_list_01	2002Oct1
PASS		
568	r400vgt_ext2int_index_triangle_strip_01	2002Oct1
PASS		
569	r400vgt_ext2int_index_triangle_wflags_01	2002Oct1
PASS		
570	r400vgt_hos_LPatch_01	2002Oct1
PASS		
571	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Oct1
PASS		
572	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct1

PASS		
573	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct1
PASS		
574	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Oct1
PASS		
575	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Oct1
PASS		
576	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct1
PASS		
577	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct1
PASS		
578	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct1
PASS		
579	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct1
PASS		
580	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct1
PASS		
581	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct1
PASS		
582	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct1
PASS		
583	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct1
PASS		
584	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct1
PASS		
585	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct1
PASS		
586	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Oct1
PASS		
587	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct1
PASS		
588	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Oct1
PASS		
589	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Oct1
PASS		
590	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Oct1
PASS		
591	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct1
PASS		
592	r400vgt_hos_PNT_edge_detection_01	2002Oct1
PASS		
593	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct1
PASS		
594	r400vgt_hos_RPatch_cp_02	2002Oct1
PASS		
595	r400vgt_hos_RPatch_lp_02	2002Oct1
PASS		
596	r400vgt_hos_TPatch_01	2002Oct1

PASS		
597	r400vgt_hos_TPatch_02	2002Oct1
PASS		
598	r400vgt_hos_auto_index_line_list_01	2002Oct1
PASS		
599	r400vgt_hos_auto_index_quad_list_01	2002Oct1
PASS		
600	r400vgt_hos_auto_index_triangle_list_01	2002Oct1
PASS		
601	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Oct1
PASS		
602	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct1
PASS		
603	r400vgt_immed_index_line_list_01	2002Oct1
PASS		
604	r400vgt_immed_index_line_loop_01	2002Oct1
PASS		
605	r400vgt_immed_index_line_strip_01	2002Oct1
PASS		
606	r400vgt_immed_index_points_01	2002Oct1
PASS		
607	r400vgt_immed_index_polygon_01	2002Oct1
PASS		
608	r400vgt_immed_index_primitives_01	2002Oct1
PASS		
609	r400vgt_immed_index_quad_list_01	2002Oct1
PASS		
610	r400vgt_immed_index_quad_strip_01	2002Oct1
PASS		
611	r400vgt_immed_index_rectangle_list_01	2002Oct1
PASS		
612	r400vgt_immed_index_tri_fan_01	2002Oct1
PASS		
613	r400vgt_immed_index_tri_list_01	2002Oct1
PASS		
614	r400vgt_immed_index_tri_strip_01	2002Oct1
PASS		
615	r400vgt_immed_index_tri_wflags_01	2002Oct1
PASS		
616	r400vgt_index_dealloc_line_list_01	2002Oct1
PASS		
617	r400vgt_index_dealloc_points_01	2002Oct1
PASS		
618	r400vgt_index_dealloc_triangle_list_01	2002Oct1
PASS		
619	r400vgt_index_min_max_01	2002Oct1
PASS		
620	r400vgt_index_min_max_02	2002Oct1

PASS		
621	r400vgt_index_offset_01	2002Oct1
PASS		
622	r400vgt_index_offset_02	2002Oct1
PASS		
623	r400vgt_index_offset_03	2002Oct1
PASS		
624	r400vgt_index_size_01	2002Oct1
PASS		
625	r400vgt_index_source_switch_01	2002Oct1
PASS		
626	r400vgt_line_list_01	2002Oct1
PASS		
627	r400vgt_line_list_02	2002Oct1
PASS		
628	r400vgt_line_loop_01	2002Oct1
PASS		
629	r400vgt_line_loop_02	2002Oct1
PASS		
630	r400vgt_line_strip_01	2002Oct1
PASS		
631	r400vgt_line_strip_02	2002Oct1
PASS		
632	r400vgt_multi_context_01	2002Oct1
PASS		
633	r400vgt_multi_context_02	2002Oct1
PASS		
634	r400vgt_multi_context_03	2002Oct1
PASS		
635	r400vgt_multi_context_04	2002Oct1
PASS		
636	r400vgt_multi_context_05	2002Oct1
PASS		
637	r400vgt_multi_context_06	2002Oct1
PASS		
638	r400vgt_multi_context_07	2002Oct1
PASS		
639	r400vgt_multi_context_08	2002Oct1
PASS		
640	r400vgt_multi_context_09	2002Oct1
PASS		
641	r400vgt_multi_context_10	2002Oct1
PASS		
642	r400vgt_multi_context_11	2002Oct1
PASS		
643	r400vgt_multi_context_12	2002Oct1
PASS		
644	r400vgt_multi_prim_reset_index_all_01	2002Oct1

PASS		
645	r400vgt_multi_prim_reset_index_all_02	2002Oct1
PASS		
646	r400vgt_pass_thru_all_prims_01	2002Oct1
PASS		
647	r400vgt_point_list_01	2002Oct1
PASS		
648	r400vgt_point_list_02	2002Oct1
PASS		
649	r400vgt_polygon_01	2002Oct1
PASS		
650	r400vgt_polygon_02	2002Oct1
PASS		
651	r400vgt_provoking_vtx_all_01	2002Oct1
PASS		
652	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct1
PASS		
653	r400vgt_provoking_vtx_polygon_01	2002Oct1
PASS		
654	r400vgt_provoking_vtx_quad_list_01	2002Oct1
PASS		
655	r400vgt_provoking_vtx_quad_strip_01	2002Oct1
PASS		
656	r400vgt_provoking_vtx_tri_fan_01	2002Oct1
PASS		
657	r400vgt_provoking_vtx_tri_strip_01	2002Oct1
PASS		
658	r400vgt_quad_list_01	2002Oct1
PASS		
659	r400vgt_quad_list_02	2002Oct1
PASS		
660	r400vgt_quad_strip_01	2002Oct1
PASS		
661	r400vgt_quad_strip_02	2002Oct1
PASS		
662	r400vgt_rectangle_list_01	2002Oct1
PASS		
663	r400vgt_rectangle_list_02	2002Oct1
PASS		
664	r400vgt_reuse_depth_line_list_01	2002Oct1
PASS		
665	r400vgt_reuse_depth_point_list_01	2002Oct1
PASS		
666	r400vgt_reuse_depth_triangle_list_01	2002Oct1
PASS		
667	r400vgt_reuse_index_line_list_01	2002Oct1
PASS		
668	r400vgt_reuse_index_point_list_01	2002Oct1

PASS			
669	r400vgt_reuse_index_triangle_list_01		2002Oct1
PASS			
670	r400vgt_reuse_index_triangle_list_02		2002Oct1
PASS			
671	r400vgt_reuse_index_triangle_list_03		2002Oct1
PASS			
672	r400vgt_suppress_eop_01	0:0:1	2002Oct1
PASS			
673	r400vgt_suppress_eop_02	0:0:1	2002Oct1
PASS			
674	r400vgt_suppress_eop_03	0:0:1	2002Oct1
PASS			
675	r400vgt_suppress_eop_04	0:0:1	2002Oct1
PASS			
676	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
677	r400vgt_triangle_fan_01	0:0:1	2002Oct1
PASS			
678	r400vgt_triangle_fan_02	0:0:1	2002Oct1
PASS			
679	r400vgt_triangle_list_01	0:0:1	2002Oct1
PASS			
680	r400vgt_triangle_list_02	0:0:1	2002Oct1
PASS			
681	r400vgt_triangle_strip_01	0:0:1	2002Oct1
PASS			
682	r400vgt_triangle_strip_02	0:0:1	2002Oct1
PASS			
683	r400vgt_triangle_wflags_01	0:0:1	2002Oct1
PASS			
684	r400vgt_triangle_wflags_02	0:0:1	2002Oct1
PASS			
685	r400vgt_viz_query_01	0:0:1	2002Oct1
PASS			
686	r400vgt_vtx_vector_packing_01	0:0:1	2002Oct1
PASS			
687	r400vte_coverage_01	0:0:1	2002Oct1
PASS			
688	r400vte_simple_point_01	0:0:1	2002Oct1
PASS			
689	r400vte_simple_triangle_01	0:0:1	2002Oct1
PASS			
690	r400vte_w0_fmt_01	0:0:1	2002Oct1
PASS			
691	r400vte_w0_fmt_03	0:0:1	2002Oct1
PASS			
692	r400vte_xy_fmt_01	0:0:1	2002Oct1

PASS
693 r400vte_xy_fmt_mk_01_bug 0:0:1 2002Oct1
PASS
694 r400vte_xyz_scale_01 0:0:1 2002Oct1
PASS
695 r400vte_xyz_scale_02 0:0:1 2002Oct1
PASS
696 r400vte_z_fmt_01 0:0:1 2002Oct1
PASS

=====
Tests_Run_count = 696 Pass_count = 696 Fail_count = 0 Incomplete_count = 0
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Aug 28 13:04:13 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141          100
              SC           156          156          155           99
              CL           82           82           82          100
              VTE           9           9            9          100
SPECIALTY    SU           81           81           81          100
              SANITY       2            2            1           50
+++++
              TOTAL        477          477          475           99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep10
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep10
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep10
PASS			
4	primlib_parameterized_simple_triangle		2002Sep10
PASS			
5	primlib_template_simple_triangle		2002Sep10
PASS			
6	primlib_tex_tri		2002Sep10
PASS			
7	r400cl_clip_10_verts_01		2002Sep8
PASS			
8	r400cl_clip_disable_01		2002Sep8
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep8
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep8
PASS			
11	r400cl_cull_only_ena_01		2002Sep8
PASS			
12	r400cl_edgeflags_01		2002Sep8
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep8
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep8
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep8
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep8
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep8
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep8
PASS			
19	r400cl_edgeflags_gband_01		2002Sep8
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep8

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep8
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep8
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep8
PASS		
24	r400cl_frustum_01	2002Sep8
PASS		
25	r400cl_frustum_02	2002Sep8
PASS		
26	r400cl_frustum_03	2002Sep8
PASS		
27	r400cl_frustum_04	2002Sep8
PASS		
28	r400cl_frustum_05	2002Sep8
PASS		
29	r400cl_frustum_06	2002Sep8
PASS		
30	r400cl_frustum_07	2002Sep8
PASS		
31	r400cl_frustum_08	2002Sep8
PASS		
32	r400cl_frustum_09	2002Sep8
PASS		
33	r400cl_frustum_10	2002Sep8
PASS		
34	r400cl_frustum_11	2002Sep8
PASS		
35	r400cl_frustum_12	2002Sep8
PASS		
36	r400cl_frustum_13	2002Sep8
PASS		
37	r400cl_frustum_14	2002Sep8
PASS		
38	r400cl_frustum_15	2002Sep8
PASS		
39	r400cl_frustum_16	2002Sep8
PASS		
40	r400cl_frustum_17	2002Sep8
PASS		
41	r400cl_frustum_18	2002Sep8
PASS		
42	r400cl_frustum_19	2002Sep8
PASS		
43	r400cl_frustum_20	2002Sep8
PASS		
44	r400cl_frustum_21	2002Sep8

PASS		
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173	r400sc_line_dx10_ge_0_01	2002Sep8
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380	r400su_line_test_01	2002Sep10

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381	r400su_line_test_02	2002Sep10
PASS		
382	r400su_longstrip_01	2002Sep10
PASS		
383	r400su_multi_prim_01	2002Sep10
PASS		
384	r400su_parallel_orientation_all_01	2002Sep10
PASS		
385	r400su_parallel_orientation_all_02	2002Sep10
PASS		
386	r400su_pc_management_01	2002Sep10
PASS		
387	r400su_pc_management_02	2002Sep10
PASS		
388	r400su_pc_management_03	2002Sep10
PASS		
389	r400su_point_sprite_01	2002Sep10
PASS		
390	r400su_point_sprite_02	2002Sep10
PASS		
391	r400su_point_sprite_03	2002Sep10
PASS		
392	r400su_point_sprite_04	2002Sep10
PASS		
393	r400su_point_sprite_05	2002Sep10
PASS		
394	r400su_point_sprite_06	2002Sep10
PASS		
395	r400su_point_sprite_07	2002Sep10
PASS		
396	r400su_point_sprite_08	2002Sep10
PASS		
397	r400su_point_wl6_h1_functional_01	2002Sep10
PASS		
398	r400su_point_wl_hl6_functional_01	2002Sep10
PASS		
399	r400su_pointsizepresent_01	2002Sep10
PASS		
400	r400su_pointsizepresent_02	2002Sep10
PASS		
401	r400su_polymode_lines_degen_triangle_01	2002Sep10
PASS		
402	r400su_polymode_lines_degen_triangle_02	2002Sep10
PASS		
403	r400su_polymode_lines_zero_area_triangle_01	2002Sep10
PASS		
404	r400su_polymode_lines_zero_area_triangle_02	2002Sep10

PASS		
405	r400su_polymode_points_degen_triangle_01	2002Sep10
PASS		
406	r400su_polymode_points_degen_triangle_02	2002Sep10
PASS		
407	r400su_polymode_points_zero_area_triangle_01	2002Sep10
PASS		
408	r400su_polymode_points_zero_area_triangle_02	2002Sep10
PASS		
409	r400su_polymode_rectangle_01	2002Sep10
PASS		
410	r400su_polymode_zero_area_triangle_01	2002Sep10
PASS		
411	r400su_polymode_zero_area_triangle_02	2002Sep10
PASS		
412	r400su_polymode_zero_area_triangle_03	2002Sep10
PASS		
413	r400su_polymode_zero_area_triangle_04	2002Sep10
PASS		
414	r400su_polymodeculling_01	2002Sep10
PASS		
415	r400su_polymodefunctional_01	2002Sep10
PASS		
416	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep10
PASS		
417	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep10
PASS		
418	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep10
PASS		
419	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep10
PASS		
420	r400su_provoking_vtx_line_01	2002Sep10
PASS		
421	r400su_provoking_vtx_point_01	2002Sep10
PASS		
422	r400su_provoking_vtx_polymode_rectangle_01	2002Sep10
PASS		
423	r400su_provoking_vtx_rectangle_01	2002Sep10
PASS		
424	r400su_provoking_vtx_triangle_01	2002Sep10
PASS		
425	r400su_rand_line_01	2002Sep10
PASS		
426	r400su_rand_point_01	2002Sep10
PASS		
427	r400su_rand_tri_01	2002Sep10
PASS		
428	r400su_rectangle_list_01	2002Sep10

PASS		
429	r400su_sliver_01	2002Sep10
PASS		
430	r400su_triarea_test_01	2002Sep10
PASS		
431	r400su_triarea_test_02	2002Sep10
PASS		
432	r400su_triarea_test_03	2002Sep10
PASS		
433	r400su_triarea_test_04	2002Sep10
PASS		
434	r400su_vertexsortingfunctional_01	2002Sep10
PASS		
435	r400vgt_auto_index_line_list_01	2002Sep8
PASS		
436	r400vgt_auto_index_line_loop_01	2002Sep8
PASS		
437	r400vgt_auto_index_line_strip_01	2002Sep8
PASS		
438	r400vgt_auto_index_points_01	2002Sep8
PASS		
439	r400vgt_auto_index_polygon_01	2002Sep8
PASS		
440	r400vgt_auto_index_primitives_01	2002Sep8
PASS		
441	r400vgt_auto_index_quad_list_01	2002Sep8
PASS		
442	r400vgt_auto_index_quad_strip_01	2002Sep8
PASS		
443	r400vgt_auto_index_rectangle_list_01	2002Sep8
PASS		
444	r400vgt_auto_index_tri_fan_01	2002Sep8
PASS		
445	r400vgt_auto_index_tri_list_01	2002Sep8
PASS		
446	r400vgt_auto_index_tri_strip_01	2002Sep8
PASS		
447	r400vgt_auto_index_tri_wflags_01	2002Sep8
PASS		
448	r400vgt_dma_engine_01	2002Sep8
PASS		
449	r400vgt_dma_engine_02	2002Sep8
PASS		
450	r400vgt_dma_engine_03	2002Sep8
PASS		
451	r400vgt_dma_engine_04	2002Sep8
PASS		
452	r400vgt_dma_engine_05	2002Sep8

PASS		
453	r400vgt_dma_engine_06	2002Sep8
PASS		
454	r400vgt_dma_engine_07	2002Sep8
PASS		
455	r400vgt_dma_engine_08	2002Sep8
PASS		
456	r400vgt_dma_engine_09	2002Sep8
PASS		
457	r400vgt_dma_engine_10	2002Sep8
PASS		
458	r400vgt_dma_index_line_list_01	2002Sep8
PASS		
459	r400vgt_dma_index_line_loop_01	2002Sep8
PASS		
460	r400vgt_dma_index_line_strip_01	2002Sep8
PASS		
461	r400vgt_dma_index_points_01	2002Sep8
PASS		
462	r400vgt_dma_index_polygon_01	2002Sep8
PASS		
463	r400vgt_dma_index_primitives_01	2002Sep8
PASS		
464	r400vgt_dma_index_quad_list_01	2002Sep8
PASS		
465	r400vgt_dma_index_quad_strip_01	2002Sep8
PASS		
466	r400vgt_dma_index_rectangle_list_01	2002Sep8
PASS		
467	r400vgt_dma_index_tri_fan_01	2002Sep8
PASS		
468	r400vgt_dma_index_tri_list_01	2002Sep8
PASS		
469	r400vgt_dma_index_tri_strip_01	2002Sep8
PASS		
470	r400vgt_dma_index_tri_wflags_01	2002Sep8
PASS		
471	r400vgt_dma_swap_idx16_01	2002Sep8
PASS		
472	r400vgt_dma_swap_idx32_01	2002Sep8
PASS		
473	r400vgt_edgeflags_polygon_01	2002Sep8
PASS		
474	r400vgt_edgeflags_quad_list_01	2002Sep8
PASS		
475	r400vgt_edgeflags_quad_strip_01	2002Sep8
PASS		
476	r400vgt_edgeflags_triangle_fan_01	2002Sep8

PASS		
477	r400vgt_edgeflags_triangle_list_01	2002Sep8
PASS		
478	r400vgt_edgeflags_triangle_strip_01	2002Sep8
PASS		
479	r400vgt_edgeflags_triangle_wflags_01	2002Sep8
PASS		
480	r400vgt_ext2int_index_line_list_01	2002Sep8
PASS		
481	r400vgt_ext2int_index_line_loop_01	2002Sep8
PASS		
482	r400vgt_ext2int_index_line_strip_01	2002Sep8
PASS		
483	r400vgt_ext2int_index_points_01	2002Sep8
PASS		
484	r400vgt_ext2int_index_polygon_01	2002Sep8
PASS		
485	r400vgt_ext2int_index_quad_list_01	2002Sep8
PASS		
486	r400vgt_ext2int_index_quad_strip_01	2002Sep8
PASS		
487	r400vgt_ext2int_index_triangle_fan_01	2002Sep8
PASS		
488	r400vgt_ext2int_index_triangle_list_01	2002Sep8
PASS		
489	r400vgt_ext2int_index_triangle_strip_01	2002Sep8
PASS		
490	r400vgt_hos_LPatch_01	2002Sep10
PASS		
491	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep8
PASS		
492	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep8
PASS		
493	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep8
PASS		
494	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep8
PASS		
495	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep8
PASS		
496	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep8
PASS		
497	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep9
PASS		
498	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep8
PASS		
499	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep9
PASS		
500	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep8

PASS		
501	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep8
PASS		
502	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep8
PASS		
503	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep8
PASS		
504	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep8
PASS		
505	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep8
PASS		
506	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep8
PASS		
507	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep8
PASS		
508	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep8
PASS		
509	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep8
PASS		
510	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep9
PASS		
511	r400vgt_hos_PNT_edge_detection_01	2002Sep10
PASS		
512	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep8
PASS		
513	r400vgt_hos_RPatch_cp_02	2002Sep10
PASS		
514	r400vgt_hos_RPatch_lp_02	2002Sep10
PASS		
515	r400vgt_hos_TPatch_01	2002Sep10
PASS		
516	r400vgt_hos_TPatch_02	2002Sep10
PASS		
517	r400vgt_hos_auto_index_line_list_01	2002Sep8
PASS		
518	r400vgt_hos_auto_index_quad_list_01	2002Sep9
PASS		
519	r400vgt_hos_auto_index_triangle_list_01	2002Sep8
PASS		
520	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep8
PASS		
521	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep10
PASS		
522	r400vgt_immed_index_line_list_01	2002Sep10
PASS		
523	r400vgt_immed_index_line_loop_01	2002Sep10
PASS		
524	r400vgt_immed_index_line_strip_01	2002Sep10

PASS		
525	r400vgt_immed_index_points_01	2002Sep10
PASS		
526	r400vgt_immed_index_polygon_01	2002Sep10
PASS		
527	r400vgt_immed_index_primitives_01	2002Sep10
PASS		
528	r400vgt_immed_index_quad_list_01	2002Sep10
PASS		
529	r400vgt_immed_index_quad_strip_01	2002Sep10
PASS		
530	r400vgt_immed_index_rectangle_list_01	2002Sep10
PASS		
531	r400vgt_immed_index_tri_fan_01	2002Sep10
PASS		
532	r400vgt_immed_index_tri_list_01	2002Sep10
PASS		
533	r400vgt_immed_index_tri_strip_01	2002Sep10
PASS		
534	r400vgt_immed_index_tri_wflags_01	2002Sep10
PASS		
535	r400vgt_index_dealloc_line_list_01	2002Sep10
PASS		
536	r400vgt_index_dealloc_points_01	2002Sep10
PASS		
537	r400vgt_index_dealloc_triangle_list_01	2002Sep10
PASS		
538	r400vgt_index_min_max_01	2002Sep10
PASS		
539	r400vgt_index_min_max_02	2002Sep10
PASS		
540	r400vgt_index_offset_01	2002Sep10
PASS		
541	r400vgt_index_offset_02	2002Sep10
PASS		
542	r400vgt_index_offset_03	2002Sep10
PASS		
543	r400vgt_index_size_01	2002Sep10
PASS		
544	r400vgt_index_source_switch_01	2002Sep10
PASS		
545	r400vgt_line_list_01	2002Sep10
PASS		
546	r400vgt_line_loop_01	2002Sep10
PASS		
547	r400vgt_line_strip_01	2002Sep10
PASS		
548	r400vgt_multi_context_01	2002Sep10

PASS		
549	r400vgt_multi_context_02	2002Sep10
PASS		
550	r400vgt_multi_context_03	2002Sep10
PASS		
551	r400vgt_multi_context_04	2002Sep10
PASS		
552	r400vgt_multi_context_05	2002Sep10
PASS		
553	r400vgt_multi_context_06	2002Sep10
PASS		
554	r400vgt_multi_context_07	2002Sep10
PASS		
555	r400vgt_multi_context_08	2002Sep10
PASS		
556	r400vgt_multi_context_09	2002Sep10
PASS		
557	r400vgt_multi_context_10	2002Sep10
PASS		
558	r400vgt_multi_context_11	2002Sep10
PASS		
559	r400vgt_multi_context_12	2002Sep10
PASS		
560	r400vgt_pass_thru_all_prims_01	2002Sep10
PASS		
561	r400vgt_point_list_01	2002Sep10
PASS		
562	r400vgt_polygon_01	2002Sep10
PASS		
563	r400vgt_provoking_vtx_all_01	2002Sep10
PASS		
564	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep10
PASS		
565	r400vgt_quad_list_01	2002Sep10
PASS		
566	r400vgt_quad_strip_01	2002Sep10
PASS		
567	r400vgt_rectangle_list_01	2002Sep10
PASS		
568	r400vgt_reuse_depth_line_list_01	2002Sep10
PASS		
569	r400vgt_reuse_depth_point_list_01	2002Sep10
PASS		
570	r400vgt_reuse_depth_triangle_list_01	2002Sep10
PASS		
571	r400vgt_reuse_index_line_list_01	2002Sep10
PASS		
572	r400vgt_reuse_index_point_list_01	2002Sep10

PASS		
573	r400vgt_reuse_index_triangle_list_01	2002Sep10
PASS		
574	r400vgt_reuse_index_triangle_list_02	2002Sep10
PASS		
575	r400vgt_reuse_index_triangle_list_03	2002Sep10
PASS		
576	r400vgt_suppress_eop_01	2002Sep10
PASS		
577	r400vgt_suppress_eop_02	2002Sep10
PASS		
578	r400vgt_suppress_eop_03	2002Sep10
PASS		
579	r400vgt_suppress_eop_04	2002Sep10
PASS		
580	r400vgt_triangle_fan_01	2002Sep10
PASS		
581	r400vgt_triangle_list_01	2002Sep10
PASS		
582	r400vgt_triangle_strip_01	2002Sep10
PASS		
583	r400vgt_triangle_wflags_01	2002Sep10
PASS		
584	r400vgt_viz_query_01	2002Sep10
PASS		
585	r400vgt_vtx_vector_packing_01	2002Sep10
PASS		
586	r400vte_coverage_01	2002Sep10
PASS		
587	r400vte_simple_point_01	2002Sep10
PASS		
588	r400vte_simple_triangle_01	2002Sep10
PASS		
589	r400vte_w0_fmt_01	2002Sep10
PASS		
590	r400vte_w0_fmt_03	2002Sep10
PASS		
591	r400vte_xy_fmt_01	2002Sep10
PASS		
592	r400vte_xyz_scale_01	2002Sep10
PASS		
593	r400vte_xyz_scale_02	2002Sep10
PASS		
594	r400vte_z_fmt_01	2002Sep10
PASS		

```

=====
Tests_Run_count = 594 Pass_count = 593 Fail_count = 0 Incomplete_count = 1
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Oct  8 11:50:05 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP          50          50          50          100
              RBBM        1           1           1           100
              VGT         180         180         180          100
              SC          300         300         295           98
              CL          147         147         146           99
              VTE         10          10          10           100
              SPECIALTY    6           6           6           100
              PARAMETER    62          62          62           100
              SU           98          98          98           100
              SANITY       2           2           2           100
+++++
              TOTAL          856         856         850           99
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep1
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep1
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep1
PASS			
4	primlib_parameterized_simple_triangle		2002Sep1
PASS			
5	primlib_template_simple_triangle		2002Sep1
PASS			
6	primlib_tex_tri		2002Sep1
PASS			
7	r400cl_clip_10_verts_01		2002Sep1
PASS			
8	r400cl_clip_disable_01		2002Sep1
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep1
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep1
PASS			
11	r400cl_cull_only_ena_01		2002Sep1
PASS			
12	r400cl_edgeflags_01		2002Sep1
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep1
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep1
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep1
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep1
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep1
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep1
PASS			
19	r400cl_edgeflags_gband_01		2002Sep1
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep1

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep1
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep1
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep1
PASS		
24	r400cl_frustum_01	2002Sep1
PASS		
25	r400cl_frustum_02	2002Sep1
PASS		
26	r400cl_frustum_03	2002Sep1
PASS		
27	r400cl_frustum_04	2002Sep1
PASS		
28	r400cl_frustum_05	2002Sep1
PASS		
29	r400cl_frustum_06	2002Sep1
PASS		
30	r400cl_frustum_07	2002Sep1
PASS		
31	r400cl_frustum_08	2002Sep1
PASS		
32	r400cl_frustum_09	2002Sep1
PASS		
33	r400cl_frustum_10	2002Sep1
PASS		
34	r400cl_frustum_11	2002Sep1
PASS		
35	r400cl_frustum_12	2002Sep1
PASS		
36	r400cl_frustum_13	2002Sep1
PASS		
37	r400cl_frustum_14	2002Sep1
PASS		
38	r400cl_frustum_15	2002Sep1
PASS		
39	r400cl_frustum_16	2002Sep1
PASS		
40	r400cl_frustum_17	2002Sep1
PASS		
41	r400cl_frustum_18	2002Sep1
PASS		
42	r400cl_frustum_19	2002Sep1
PASS		
43	r400cl_frustum_20	2002Sep1
PASS		
44	r400cl_frustum_21	2002Sep1

PASS		
45	r400cl_frustum_22	2002Sep1
PASS		
46	r400cl_frustum_23	2002Sep1
PASS		
47	r400cl_frustum_24	2002Sep1
PASS		
48	r400cl_frustum_25	2002Sep1
PASS		
49	r400cl_frustum_26	2002Sep1
PASS		
50	r400cl_frustum_27	2002Sep1
PASS		
51	r400cl_frustum_28	2002Sep1
PASS		
52	r400cl_frustum_lines_01	2002Sep1
PASS		
53	r400cl_frustum_lines_02	2002Sep1
PASS		
54	r400cl_frustum_lines_03	2002Sep1
PASS		
55	r400cl_frustum_lines_04	2002Sep1
PASS		
56	r400cl_frustum_lines_05	2002Sep1
PASS		
57	r400cl_frustum_lines_06	2002Sep1
PASS		
58	r400cl_frustum_lines_07	2002Sep1
PASS		
59	r400cl_frustum_lines_08	2002Sep1
PASS		
60	r400cl_frustum_lines_09	2002Sep1
PASS		
61	r400cl_frustum_lines_10	2002Sep1
PASS		
62	r400cl_frustum_lines_11	2002Sep1
PASS		
63	r400cl_frustum_lines_12	2002Sep1
PASS		
64	r400cl_frustum_lines_13	2002Sep1
PASS		
65	r400cl_frustum_lines_14	2002Sep1
PASS		
66	r400cl_frustum_lines_15	2002Sep1
PASS		
67	r400cl_frustum_lines_16	2002Sep1
PASS		
68	r400cl_frustum_lines_17	2002Sep1

PASS		
69	r400cl_frustum_lines_18	2002Sep1
PASS		
70	r400cl_frustum_lines_19	2002Sep1
PASS		
71	r400cl_frustum_lines_20	2002Sep1
PASS		
72	r400cl_frustum_lines_21	2002Sep1
PASS		
73	r400cl_frustum_lines_22	2002Sep1
PASS		
74	r400cl_frustum_lines_23	2002Sep1
PASS		
75	r400cl_frustum_lines_24	2002Sep1
PASS		
76	r400cl_gband_04	2002Sep1
PASS		
77	r400cl_gband_tcl_01	2002Sep1
PASS		
78	r400cl_pointlist_vertex_state_ucp_01	2002Sep1
PASS		
79	r400cl_simple_triangle_01	2002Sep1
PASS		
80	r400cl_ucp_combos_01	2002Sep1
PASS		
81	r400cl_ucp_combos_02	2002Sep1
PASS		
82	r400cl_ucp_combos_03	2002Sep1
PASS		
83	r400cl_ucp_combos_04	2002Sep1
PASS		
84	r400cl_ucp_combos_05	2002Sep1
PASS		
85	r400cl_ucp_combos_06	2002Sep1
PASS		
86	r400cl_ucp_combos_07	2002Sep1
PASS		
87	r400cl_ucp_combos_08	2002Sep1
PASS		
88	r400cl_ucp_combos_09	2002Sep1
PASS		
89	r400cl_ucp_combos_10	2002Sep1
PASS		
90	r400cl_ucp_combos_11	2002Sep1
PASS		
91	r400cl_ucp_combos_12	2002Sep1
PASS		
92	r400cl_ucp_combos_13	2002Sep1

PASS		
93	r400cl_ucp_combos_14	2002Sep1
PASS		
94	r400cl_ucp_combos_15	2002Sep1
PASS		
95	r400cl_ucp_combos_16	2002Sep1
PASS		
96	r400cl_ucp_combos_17	2002Sep1
PASS		
97	r400cl_ucp_combos_18	2002Sep1
PASS		
98	r400cl_ucp_combos_19	2002Sep1
PASS		
99	r400cl_ucp_combos_20	2002Sep1
PASS		
100	r400cl_ucp_combos_21	2002Sep1
PASS		
101	r400cl_ucp_combos_22	2002Sep1
PASS		
102	r400cl_ucp_combos_23	2002Sep1
PASS		
103	r400cl_ucp_combos_24	2002Sep1
PASS		
104	r400cl_ucp_combos_25	2002Sep1
PASS		
105	r400cl_ucp_combos_26	2002Sep1
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PASS		
438	r400vgt_hos_PNT_edge_detection_01	2002Sep1
PASS		
439	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep1
PASS		
440	r400vgt_hos_RPatch_cp_02	2002Sep1
PASS		
441	r400vgt_hos_RPatch_lp_02	2002Sep1
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442	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep1
PASS		
443	r400vgt_immed_index_line_list_01	2002Sep1
PASS		
444	r400vgt_immed_index_line_loop_01	2002Sep1
PASS		
445	r400vgt_immed_index_line_strip_01	2002Sep1
PASS		
446	r400vgt_immed_index_points_01	2002Sep1
PASS		
447	r400vgt_immed_index_polygon_01	2002Sep1
PASS		
448	r400vgt_immed_index_primitives_01	2002Sep1
PASS		
449	r400vgt_immed_index_quad_list_01	2002Sep1
PASS		
450	r400vgt_immed_index_quad_strip_01	2002Sep1
PASS		
451	r400vgt_immed_index_rectangle_list_01	2002Sep1
PASS		
452	r400vgt_immed_index_tri_fan_01	2002Sep1

PASS		
453	r400vgt_immed_index_tri_list_01	2002Sep1
PASS		
454	r400vgt_immed_index_tri_strip_01	2002Sep1
PASS		
455	r400vgt_immed_index_tri_wflags_01	2002Sep1
PASS		
456	r400vgt_index_dealloc_line_list_01	2002Sep1
PASS		
457	r400vgt_index_dealloc_points_01	2002Sep1
PASS		
458	r400vgt_index_dealloc_triangle_list_01	2002Sep1
PASS		
459	r400vgt_index_min_max_01	2002Sep1
PASS		
460	r400vgt_index_min_max_02	2002Sep1
PASS		
461	r400vgt_index_offset_01	2002Sep1
PASS		
462	r400vgt_index_offset_02	2002Sep1
PASS		
463	r400vgt_index_offset_03	2002Sep1
PASS		
464	r400vgt_index_size_01	2002Sep1
PASS		
465	r400vgt_index_source_switch_01	2002Sep1
PASS		
466	r400vgt_line_list_01	2002Sep1
PASS		
467	r400vgt_line_loop_01	2002Sep1
PASS		
468	r400vgt_line_strip_01	2002Sep1
PASS		
469	r400vgt_multi_context_01	2002Sep1
PASS		
470	r400vgt_multi_context_02	2002Sep1
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471	r400vgt_multi_context_03	2002Sep1
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472	r400vgt_multi_context_04	2002Sep1
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473	r400vgt_multi_context_05	2002Sep1
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474	r400vgt_multi_context_06	2002Sep1
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475	r400vgt_multi_context_07	2002Sep1
PASS		
476	r400vgt_multi_context_08	2002Sep1

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477	r400vgt_multi_context_09	2002Sep1
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478	r400vgt_multi_context_10	2002Sep1
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479	r400vgt_multi_context_11	2002Sep1
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480	r400vgt_multi_context_12	2002Sep1
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481	r400vgt_pass_thru_all_prims_01	2002Sep1
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482	r400vgt_point_list_01	2002Sep1
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483	r400vgt_polygon_01	2002Sep1
PASS		
484	r400vgt_provoking_vtx_all_01	2002Sep1
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485	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep1
PASS		
486	r400vgt_quad_list_01	2002Sep1
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487	r400vgt_quad_strip_01	2002Sep1
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488	r400vgt_rectangle_list_01	2002Sep1
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489	r400vgt_reuse_depth_line_list_01	2002Sep1
PASS		
490	r400vgt_reuse_depth_point_list_01	2002Sep1
PASS		
491	r400vgt_reuse_depth_triangle_list_01	2002Sep1
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492	r400vgt_reuse_index_line_list_01	2002Sep1
PASS		
493	r400vgt_reuse_index_point_list_01	2002Sep1
PASS		
494	r400vgt_reuse_index_triangle_list_01	2002Sep1
PASS		
495	r400vgt_reuse_index_triangle_list_02	2002Sep1
PASS		
496	r400vgt_reuse_index_triangle_list_03	2002Sep1
PASS		
497	r400vgt_suppress_eop_01	2002Sep1
PASS		
498	r400vgt_suppress_eop_02	2002Sep1
PASS		
499	r400vgt_suppress_eop_03	2002Sep1
PASS		
500	r400vgt_suppress_eop_04	2002Sep1

PASS
501 r400vgt_triangle_fan_01 2002Sep1
PASS
502 r400vgt_triangle_list_01 2002Sep1
PASS
503 r400vgt_triangle_strip_01 2002Sep1
PASS
504 r400vgt_triangle_wflags_01 2002Sep1
PASS
505 r400vgt_vtx_vector_packing_01 2002Sep1
PASS
506 r400vte_coverage_01 2002Sep1
PASS
507 r400vte_simple_point_01 2002Sep1
PASS
508 r400vte_simple_triangle_01 2002Sep1
PASS
509 r400vte_w0_fmt_01 2002Sep1
PASS
510 r400vte_w0_fmt_03 2002Sep1
PASS
511 r400vte_xy_fmt_01 2002Sep1
PASS
512 r400vte_xyz_scale_01 2002Sep1
PASS
513 r400vte_xyz_scale_02 2002Sep1
PASS
514 r400vte_z_fmt_01 2002Sep1
PASS

=====
Tests_Run_count = 514 Pass_count = 513 Fail_count = 0 Incomplete_count = 1
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep 25  9:02:31 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             170         170         169           99
              SC              275         275         275           100
              CL              115         115         115           100
              VTE              10          10          10           100
              SPECIALTY         6           6           5            83
              SU               85          85          85           100
              SANITY            2           2           2           100
+++++
              TOTAL            675         675         673           99
+++++

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Oct  5 21:34:29 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              50          50          50          100
              RBBM             1           1           1           100
              VGT             180         180         180          100
              SC              300         300         300          100
              CL              119         119         118           99
              VTE              10          10          10          100
              SPECIALTY         6           6           6           100
              PARAMETER         62          62          62          100
              SU               97          97          97          100
              SANITY            2           2           2           100
+++++
              TOTAL            827         827         826           99
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep6
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep6
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep6
PASS			
4	primlib_parameterized_simple_triangle		2002Sep6
PASS			
5	primlib_template_simple_triangle		2002Sep6
PASS			
6	primlib_tex_tri		2002Sep6
PASS			
7	r400cl_clip_10_verts_01		2002Sep6
PASS			
8	r400cl_clip_disable_01		2002Sep6
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep6
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep6
PASS			
11	r400cl_cull_only_ena_01		2002Sep6
PASS			
12	r400cl_edgeflags_01		2002Sep6
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep6
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep6
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep6
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep6
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep6
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep6
PASS			
19	r400cl_edgeflags_gband_01		2002Sep6
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep6

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep6
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep6
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep6
PASS		
24	r400cl_frustum_01	2002Sep6
PASS		
25	r400cl_frustum_02	2002Sep6
PASS		
26	r400cl_frustum_03	2002Sep6
PASS		
27	r400cl_frustum_04	2002Sep6
PASS		
28	r400cl_frustum_05	2002Sep6
PASS		
29	r400cl_frustum_06	2002Sep6
PASS		
30	r400cl_frustum_07	2002Sep6
PASS		
31	r400cl_frustum_08	2002Sep6
PASS		
32	r400cl_frustum_09	2002Sep6
PASS		
33	r400cl_frustum_10	2002Sep6
PASS		
34	r400cl_frustum_11	2002Sep6
PASS		
35	r400cl_frustum_12	2002Sep6
PASS		
36	r400cl_frustum_13	2002Sep6
PASS		
37	r400cl_frustum_14	2002Sep6
PASS		
38	r400cl_frustum_15	2002Sep6
PASS		
39	r400cl_frustum_16	2002Sep6
PASS		
40	r400cl_frustum_17	2002Sep6
PASS		
41	r400cl_frustum_18	2002Sep6
PASS		
42	r400cl_frustum_19	2002Sep6
PASS		
43	r400cl_frustum_20	2002Sep6
PASS		
44	r400cl_frustum_21	2002Sep6

PASS		
45	r400cl_frustum_22	2002Sep6
PASS		
46	r400cl_frustum_23	2002Sep6
PASS		
47	r400cl_frustum_24	2002Sep6
PASS		
48	r400cl_frustum_25	2002Sep6
PASS		
49	r400cl_frustum_26	2002Sep6
PASS		
50	r400cl_frustum_27	2002Sep6
PASS		
51	r400cl_frustum_28	2002Sep6
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52	r400cl_frustum_lines_01	2002Sep6
PASS		
53	r400cl_frustum_lines_02	2002Sep6
PASS		
54	r400cl_frustum_lines_03	2002Sep6
PASS		
55	r400cl_frustum_lines_04	2002Sep6
PASS		
56	r400cl_frustum_lines_05	2002Sep6
PASS		
57	r400cl_frustum_lines_06	2002Sep6
PASS		
58	r400cl_frustum_lines_07	2002Sep6
PASS		
59	r400cl_frustum_lines_08	2002Sep6
PASS		
60	r400cl_frustum_lines_09	2002Sep6
PASS		
61	r400cl_frustum_lines_10	2002Sep6
PASS		
62	r400cl_frustum_lines_11	2002Sep6
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63	r400cl_frustum_lines_12	2002Sep6
PASS		
64	r400cl_frustum_lines_13	2002Sep6
PASS		
65	r400cl_frustum_lines_14	2002Sep6
PASS		
66	r400cl_frustum_lines_15	2002Sep6
PASS		
67	r400cl_frustum_lines_16	2002Sep6
PASS		
68	r400cl_frustum_lines_17	2002Sep6

PASS		
69	r400cl_frustum_lines_18	2002Sep6
PASS		
70	r400cl_frustum_lines_19	2002Sep6
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71	r400cl_frustum_lines_20	2002Sep6
PASS		
72	r400cl_frustum_lines_21	2002Sep6
PASS		
73	r400cl_frustum_lines_22	2002Sep6
PASS		
74	r400cl_frustum_lines_23	2002Sep6
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75	r400cl_frustum_lines_24	2002Sep6
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76	r400cl_gband_04	2002Sep6
PASS		
77	r400cl_gband_tcl_01	2002Sep6
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78	r400cl_pointlist_vertex_state_ucp_01	2002Sep6
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79	r400cl_simple_triangle_01	2002Sep6
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80	r400cl_ucp_combos_01	2002Sep6
PASS		
81	r400cl_ucp_combos_02	2002Sep6
PASS		
82	r400cl_ucp_combos_03	2002Sep6
PASS		
83	r400cl_ucp_combos_04	2002Sep6
PASS		
84	r400cl_ucp_combos_05	2002Sep6
PASS		
85	r400cl_ucp_combos_06	2002Sep6
PASS		
86	r400cl_ucp_combos_07	2002Sep6
PASS		
87	r400cl_ucp_combos_08	2002Sep6
PASS		
88	r400cl_ucp_combos_09	2002Sep6
PASS		
89	r400cl_ucp_combos_10	2002Sep6
PASS		
90	r400cl_ucp_combos_11	2002Sep6
PASS		
91	r400cl_ucp_combos_12	2002Sep6
PASS		
92	r400cl_ucp_combos_13	2002Sep6

PASS		
93	r400cl_ucp_combos_14	2002Sep6
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94	r400cl_ucp_combos_15	2002Sep6
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95	r400cl_ucp_combos_16	2002Sep6
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97	r400cl_ucp_combos_18	2002Sep6
PASS		
98	r400cl_ucp_combos_19	2002Sep6
PASS		
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PASS		
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PASS		
101	r400cl_ucp_combos_22	2002Sep6
PASS		
102	r400cl_ucp_combos_23	2002Sep6
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103	r400cl_ucp_combos_24	2002Sep6
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104	r400cl_ucp_combos_25	2002Sep6
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105	r400cl_ucp_combos_26	2002Sep6
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106	r400cl_ucp_combos_27	2002Sep6
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107	r400cl_ucp_combos_28	2002Sep6
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108	r400cl_ucp_combos_29	2002Sep6
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109	r400cl_ucp_combos_30	2002Sep6
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110	r400cl_ucp_pointlist_01	2002Sep6
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111	r400cl_vertex_reuse_clip_01	2002Sep6
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112	r400cl_vtx_kill_01	2002Sep6
PASS		
113	r400cl_vtx_kill_02	2002Sep6
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114	r400cl_w_eq_0	2002Sep6
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115	r400sanity_persp_projection_01	2002Sep6
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116	r400sanity_vfd_texture_sample_01	2002Sep6

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117	r400sc_bres_cntl_01	2002Sep6
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118	r400sc_bres_cntl_02	2002Sep6
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119	r400sc_bres_cntl_03	2002Sep6
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120	r400sc_bres_cntl_04	2002Sep6
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122	r400sc_clip_rect_02	2002Sep6
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123	r400sc_clip_rect_03	2002Sep6
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124	r400sc_clip_rect_04	2002Sep6
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125	r400sc_clip_rect_fc_01	2002Sep6
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126	r400sc_diamond_exit_01	2002Sep6
PASS		
127	r400sc_diamond_exit_02	2002Sep6
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128	r400sc_diamond_exit_03	2002Sep6
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129	r400sc_diamond_exit_04	2002Sep6
PASS		
130	r400sc_diamond_exit_05	2002Sep6
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131	r400sc_jss_1x2_01	2002Sep6
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132	r400sc_jss_1x2_02	2002Sep6
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133	r400sc_jss_1x3_01	2002Sep6
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134	r400sc_jss_1x3_02	2002Sep6
PASS		
135	r400sc_jss_1x4_01	2002Sep6
PASS		
136	r400sc_jss_1x4_02	2002Sep6
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137	r400sc_jss_2x1_01	2002Sep6
PASS		
138	r400sc_jss_2x1_02	2002Sep6
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139	r400sc_jss_2x2_01	2002Sep6
PASS		
140	r400sc_jss_2x2_02	2002Sep6

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142	r400sc_jss_2x3_02	2002Sep6
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143	r400sc_jss_2x4_01	2002Sep6
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144	r400sc_jss_2x4_02	2002Sep6
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145	r400sc_jss_3x1_01	2002Sep6
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146	r400sc_jss_3x1_02	2002Sep6
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147	r400sc_jss_3x2_01	2002Sep6
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148	r400sc_jss_3x2_02	2002Sep6
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153	r400sc_jss_3x4_03	2002Sep6
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154	r400sc_jss_4x1_01	2002Sep6
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155	r400sc_jss_4x1_02	2002Sep6
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156	r400sc_jss_4x2_01	2002Sep6
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157	r400sc_jss_4x2_02	2002Sep6
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158	r400sc_jss_4x3_01	2002Sep6
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159	r400sc_jss_4x3_02	2002Sep6
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161	r400sc_jss_4x4_02	2002Sep6
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162	r400sc_jss_4x4_03	2002Sep6
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163	r400sc_jss_4x4_aa_mask_01	2002Sep6
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164	r400sc_jss_4x4_aa_mask_02	2002Sep6

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165	r400sc_jss_4x4_fc_01	2002Sep6
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166	r400sc_jss_4x4_fc_02	2002Sep6
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167	r400sc_jss_4x4_max_dist_01	2002Sep6
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169	r400sc_line_dx10_ge_0_01	2002Sep6
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170	r400sc_line_dx10_lt_0_01	2002Sep6
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171	r400sc_line_dy10_eq_0_01	2002Sep6
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172	r400sc_line_dy10_ge_0_01	2002Sep6
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173	r400sc_line_dy10_lt_0_01	2002Sep6
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174	r400sc_line_expand_width_msa_8_01	2002Sep6
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175	r400sc_line_expand_width_msa_8_02	2002Sep6
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176	r400sc_line_expand_width_msa_8_03	2002Sep6
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177	r400sc_line_jss_3x4_01	2002Sep6
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178	r400sc_line_list_01	2002Sep6
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179	r400sc_line_list_02	2002Sep6
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180	r400sc_line_list_concentric_circle_01	2002Sep6
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181	r400sc_line_list_concentric_circle_02	2002Sep6
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182	r400sc_line_list_concentric_circle_03	2002Sep6
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183	r400sc_line_list_textured_01	2002Sep6
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184	r400sc_line_list_verify_st_01	2002Sep6
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185	r400sc_line_msa_8_01	2002Sep6
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186	r400sc_line_msa_8_textured_01	2002Sep6
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187	r400sc_line_stipple_01	2002Sep6
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188	r400sc_line_stipple_02	2002Sep6

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189	r400sc_line_stipple_03	2002Sep6
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190	r400sc_line_stipple_04	2002Sep6
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191	r400sc_line_stipple_05	2002Sep6
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193	r400sc_line_stipple_07	2002Sep6
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194	r400sc_line_stipple_08	2002Sep6
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195	r400sc_line_stipple_09	2002Sep6
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196	r400sc_line_stipple_fc_08	2002Sep6
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197	r400sc_line_strip_stipple_01	2002Sep6
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199	r400sc_msaa_4_01	2002Sep6
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201	r400sc_msaa_8_01	2002Sep6
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202	r400sc_msaa_8_02	2002Sep6
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203	r400sc_msaa_8_03	2002Sep6
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204	r400sc_msaa_8_04	2002Sep6
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205	r400sc_msaa_8_05	2002Sep6
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206	r400sc_msaa_8_aa_mask_01	2002Sep6
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207	r400sc_msaa_8_aa_mask_02	2002Sep6
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208	r400sc_msaa_8_aa_mask_fc_02	2002Sep6
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209	r400sc_null_triangles_01	2002Sep6
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210	r400sc_null_triangles_fc_01	2002Sep6
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211	r400sc_point_jss_3x4_01	2002Sep6
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212	r400sc_point_list_01	2002Sep6

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213	r400sc_point_list_02	2002Sep6
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214	r400sc_point_list_03	2002Sep6
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215	r400sc_point_list_04	2002Sep6
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216	r400sc_point_list_05	2002Sep6
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217	r400sc_point_list_06	2002Aug28
INCOMPLETE		
218	r400sc_point_list_07	2002Sep6
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219	r400sc_point_list_08	2002Sep6
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220	r400sc_point_list_09	2002Sep6
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221	r400sc_point_msa_8_01	2002Sep6
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222	r400sc_poly_offset_01	2002Sep6
PASS		
223	r400sc_poly_offset_02	2002Sep6
PASS		
224	r400sc_poly_offset_fc_01	2002Sep6
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225	r400sc_polygon_stipple_01	2002Sep6
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226	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep6
PASS		
227	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep6
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228	r400sc_prsp_byc_intrp_ref_pix_03	2002Sep6
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229	r400sc_prsp_byc_intrp_ref_pix_04	2002Sep6
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230	r400sc_prsp_byc_intrp_ref_pix_05	2002Sep6
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231	r400sc_prsp_byc_intrp_ref_pix_06	2002Sep6
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232	r400sc_prsp_byc_intrp_ref_pix_07	2002Sep6
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233	r400sc_prsp_byc_intrp_ref_pix_08	2002Sep6
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234	r400sc_raster_fill_rule_01	2002Sep6
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235	r400sc_raster_fill_rule_02	2002Sep6
PASS		
236	r400sc_raster_fill_rule_03	2002Sep6

PASS		
237	r400sc_raster_fill_rule_04	2002Sep6
PASS		
238	r400sc_raster_fill_rule_05	2002Sep6
PASS		
239	r400sc_raster_fill_rule_fc_01	2002Sep6
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240	r400sc_rectangle_list_01	2002Sep6
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241	r400sc_rectangle_list_02	2002Sep6
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242	r400sc_rectangle_list_03	2002Sep6
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243	r400sc_rectangle_list_04	2002Sep6
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244	r400sc_rectangle_list_05	2002Sep6
PASS		
245	r400sc_rectangle_list_06	2002Sep6
PASS		
246	r400sc_rectangle_list_07	2002Sep6
PASS		
247	r400sc_rectangle_list_08	2002Sep6
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248	r400sc_scissor_rect_01	2002Sep6
PASS		
249	r400sc_scissor_rect_02	2002Sep6
PASS		
250	r400sc_scissor_rect_03	2002Sep6
PASS		
251	r400sc_scissor_rect_04	2002Sep6
PASS		
252	r400sc_scissor_rect_05	2002Sep6
PASS		
253	r400sc_scissor_rect_fc_01	2002Sep6
PASS		
254	r400sc_set_state_01	2002Sep6
PASS		
255	r400sc_tri_16_par_64_dwords_01	2002Sep6
PASS		
256	r400sc_tri_8textures_01	2002Sep6
PASS		
257	r400sc_tri_8textures_02	2002Sep6
PASS		
258	r400sc_tri_walk_start_vertex_01	2002Sep6
PASS		
259	r400sc_tri_walk_start_vertex_02	2002Sep6
PASS		
260	r400sc_tri_walk_start_vertex_03	2002Sep6

PASS		
261	r400sc_tri_walk_start_vertex_04	2002Sep6
PASS		
262	r400sc_tri_walk_start_vertex_05	2002Sep6
PASS		
263	r400sc_tri_walk_start_vertex_06	2002Sep6
PASS		
264	r400sc_tri_walk_start_vertex_07	2002Sep6
PASS		
265	r400sc_tri_walk_start_vertex_08	2002Sep6
PASS		
266	r400sc_tri_walk_start_vertex_09	2002Sep6
PASS		
267	r400sc_tri_walk_start_vertex_10	2002Sep6
PASS		
268	r400sc_tri_walk_start_vertex_11	2002Sep6
PASS		
269	r400sc_tri_walk_start_vertex_12	2002Sep6
PASS		
270	r400sc_tri_walk_start_vertex_13	2002Sep6
PASS		
271	r400sc_tri_walk_start_vertex_14	2002Sep6
PASS		
272	r400sc_tri_walk_start_vertex_15	2002Sep6
PASS		
273	r400sc_tri_walk_start_vertex_16	2002Sep6
PASS		
274	r400sc_triangle_stipple_01	2002Sep6
PASS		
275	r400sc_window_offset_01	2002Sep6
PASS		
276	r400sc_window_offset_fc_01	2002Sep6
PASS		
277	r400sc_zbuffer_line_list_01	2002Sep6
PASS		
278	r400sc_zbuffer_point_list_01	2002Sep6
PASS		
279	r400sc_zbuffer_rectangle_list_01	2002Sep6
PASS		
280	r400sc_zbuffer_rectangle_list_02	2002Sep6
PASS		
281	r400sc_zbuffer_rectangle_list_fc_02	2002Sep6
PASS		
282	r400sc_zbuffer_triangle_list_01	2002Sep6
PASS		
283	r400su_4tri_text_offscreen_01	2002Sep6
PASS		
284	r400su_baryc_test_01	2002Sep6

PASS		
285	r400su_baryc_test_02	2002Sep6
PASS		
286	r400su_baryc_test_03	2002Sep6
PASS		
287	r400su_baryc_test_04	2002Sep6
PASS		
288	r400su_clip_edgeflag_polymode_01	2002Sep6
PASS		
289	r400su_clip_pointsize_test_01	2002Sep6
PASS		
290	r400su_clip_pointtest_01	2002Sep6
PASS		
291	r400su_clip_pointtest_02	2002Sep6
PASS		
292	r400su_clip_pointtest_03	2002Sep6
PASS		
293	r400su_clip_pointtest_04	2002Sep6
PASS		
294	r400su_clip_polymode_test_01	2002Sep6
PASS		
295	r400su_clipline_01	2002Sep6
PASS		
296	r400su_clippoint_01	2002Sep6
PASS		
297	r400su_clipvertexsortingfunctional_01	2002Sep6
PASS		
298	r400su_cullingfunctional_01	2002Sep6
PASS		
299	r400su_degentri_test_01	2002Sep6
PASS		
300	r400su_degentri_test_02	2002Sep6
PASS		
301	r400su_degentri_test_03	2002Sep6
PASS		
302	r400su_edge_flag_01	2002Sep6
PASS		
303	r400su_edge_flag_02	2002Sep6
PASS		
304	r400su_edgeflags_triangle_01	2002Sep6
PASS		
305	r400su_edgeflags_triangle_02	2002Sep6
PASS		
306	r400su_geom_sort_01	2002Sep6
PASS		
307	r400su_line_end_cap_functional_01	2002Sep6
PASS		
308	r400su_line_end_cap_width_functional_02	2002Sep6

PASS		
309	r400su_line_test_01	2002Sep6
PASS		
310	r400su_line_test_02	2002Sep6
PASS		
311	r400su_longstrip_01	2002Sep6
PASS		
312	r400su_multi_prim_01	2002Sep6
PASS		
313	r400su_parallel_orientation_all_01	2002Sep6
PASS		
314	r400su_parallel_orientation_all_02	2002Sep6
PASS		
315	r400su_pc_management_01	2002Sep6
PASS		
316	r400su_pc_management_02	2002Sep6
PASS		
317	r400su_pc_management_03	2002Sep6
PASS		
318	r400su_point_sprite_01	2002Sep6
PASS		
319	r400su_point_sprite_02	2002Sep6
PASS		
320	r400su_point_sprite_03	2002Sep6
PASS		
321	r400su_point_sprite_04	2002Sep6
PASS		
322	r400su_point_sprite_05	2002Sep6
PASS		
323	r400su_point_sprite_06	2002Sep6
PASS		
324	r400su_point_sprite_07	2002Sep6
PASS		
325	r400su_point_sprite_08	2002Sep6
PASS		
326	r400su_point_wl6_h1_functional_01	2002Sep6
PASS		
327	r400su_point_wl_h16_functional_01	2002Sep6
PASS		
328	r400su_pointsizepresent_01	2002Sep6
PASS		
329	r400su_pointsizepresent_02	2002Sep6
PASS		
330	r400su_polymode_lines_degen_triangle_01	2002Sep6
PASS		
331	r400su_polymode_lines_degen_triangle_02	2002Sep6
PASS		
332	r400su_polymode_lines_zero_area_triangle_01	2002Sep6

PASS		
333	r400su_polymode_lines_zero_area_triangle_02	2002Sep6
PASS		
334	r400su_polymode_points_degen_triangle_01	2002Sep6
PASS		
335	r400su_polymode_points_degen_triangle_02	2002Sep6
PASS		
336	r400su_polymode_points_zero_area_triangle_01	2002Sep6
PASS		
337	r400su_polymode_points_zero_area_triangle_02	2002Sep6
PASS		
338	r400su_polymode_rectangle_01	2002Sep6
PASS		
339	r400su_polymode_zero_area_triangle_01	2002Sep6
PASS		
340	r400su_polymode_zero_area_triangle_02	2002Sep6
PASS		
341	r400su_polymode_zero_area_triangle_03	2002Sep6
PASS		
342	r400su_polymode_zero_area_triangle_04	2002Sep6
PASS		
343	r400su_polymodeculling_01	2002Sep6
PASS		
344	r400su_polymodefunctional_01	2002Sep6
PASS		
345	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep6
PASS		
346	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep6
PASS		
347	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep6
PASS		
348	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep6
PASS		
349	r400su_provoking_vtx_line_01	2002Sep6
PASS		
350	r400su_provoking_vtx_point_01	2002Sep6
PASS		
351	r400su_provoking_vtx_polymode_rectangle_01	2002Sep6
PASS		
352	r400su_provoking_vtx_rectangle_01	2002Sep6
PASS		
353	r400su_provoking_vtx_triangle_01	2002Sep6
PASS		
354	r400su_rand_line_01	2002Sep6
PASS		
355	r400su_rand_point_01	2002Sep6
PASS		
356	r400su_rand_tri_01	2002Sep6

PASS		
357	r400su_rectangle_list_01	2002Sep6
PASS		
358	r400su_sliver_01	2002Sep6
PASS		
359	r400su_triarea_test_01	2002Sep6
PASS		
360	r400su_triarea_test_02	2002Sep6
PASS		
361	r400su_triarea_test_03	2002Sep6
PASS		
362	r400su_triarea_test_04	2002Sep6
PASS		
363	r400su_vertexsortingfunctional_01	2002Sep6
PASS		
364	r400vgt_auto_index_line_list_01	2002Sep6
PASS		
365	r400vgt_auto_index_line_loop_01	2002Sep6
PASS		
366	r400vgt_auto_index_line_strip_01	2002Sep6
PASS		
367	r400vgt_auto_index_points_01	2002Sep6
PASS		
368	r400vgt_auto_index_polygon_01	2002Sep6
PASS		
369	r400vgt_auto_index_primitives_01	2002Sep6
PASS		
370	r400vgt_auto_index_quad_list_01	2002Sep6
PASS		
371	r400vgt_auto_index_quad_strip_01	2002Sep6
PASS		
372	r400vgt_auto_index_rectangle_list_01	2002Sep6
PASS		
373	r400vgt_auto_index_tri_fan_01	2002Sep6
PASS		
374	r400vgt_auto_index_tri_list_01	2002Sep6
PASS		
375	r400vgt_auto_index_tri_strip_01	2002Sep6
PASS		
376	r400vgt_auto_index_tri_wflags_01	2002Sep6
PASS		
377	r400vgt_dma_engine_01	2002Sep6
PASS		
378	r400vgt_dma_engine_02	2002Sep6
PASS		
379	r400vgt_dma_engine_03	2002Sep6
PASS		
380	r400vgt_dma_engine_04	2002Sep6

PASS		
381	r400vgt_dma_engine_05	2002Sep6
PASS		
382	r400vgt_dma_engine_06	2002Sep6
PASS		
383	r400vgt_dma_engine_07	2002Sep6
PASS		
384	r400vgt_dma_engine_08	2002Sep6
PASS		
385	r400vgt_dma_engine_09	2002Sep6
PASS		
386	r400vgt_dma_engine_10	2002Sep6
PASS		
387	r400vgt_dma_index_line_list_01	2002Sep6
PASS		
388	r400vgt_dma_index_line_loop_01	2002Sep6
PASS		
389	r400vgt_dma_index_line_strip_01	2002Sep6
PASS		
390	r400vgt_dma_index_points_01	2002Sep6
PASS		
391	r400vgt_dma_index_polygon_01	2002Sep6
PASS		
392	r400vgt_dma_index_primitives_01	2002Sep6
PASS		
393	r400vgt_dma_index_quad_list_01	2002Sep6
PASS		
394	r400vgt_dma_index_quad_strip_01	2002Sep6
PASS		
395	r400vgt_dma_index_rectangle_list_01	2002Sep6
PASS		
396	r400vgt_dma_index_tri_fan_01	2002Sep6
PASS		
397	r400vgt_dma_index_tri_list_01	2002Sep6
PASS		
398	r400vgt_dma_index_tri_strip_01	2002Sep6
PASS		
399	r400vgt_dma_index_tri_wflags_01	2002Sep6
PASS		
400	r400vgt_dma_swap_idx16_01	2002Sep6
PASS		
401	r400vgt_dma_swap_idx32_01	2002Sep6
PASS		
402	r400vgt_edgeflags_polygon_01	2002Sep6
PASS		
403	r400vgt_edgeflags_quad_list_01	2002Sep6
PASS		
404	r400vgt_edgeflags_quad_strip_01	2002Sep6

PASS		
405	r400vgt_edgeflags_triangle_fan_01	2002Sep6
PASS		
406	r400vgt_edgeflags_triangle_list_01	2002Sep6
PASS		
407	r400vgt_edgeflags_triangle_strip_01	2002Sep6
PASS		
408	r400vgt_edgeflags_triangle_wflags_01	2002Sep6
PASS		
409	r400vgt_ext2int_index_line_list_01	2002Sep6
PASS		
410	r400vgt_ext2int_index_line_loop_01	2002Sep6
PASS		
411	r400vgt_ext2int_index_line_strip_01	2002Sep6
PASS		
412	r400vgt_ext2int_index_points_01	2002Sep6
PASS		
413	r400vgt_ext2int_index_polygon_01	2002Sep6
PASS		
414	r400vgt_ext2int_index_quad_list_01	2002Sep6
PASS		
415	r400vgt_ext2int_index_quad_strip_01	2002Sep6
PASS		
416	r400vgt_ext2int_index_triangle_fan_01	2002Sep6
PASS		
417	r400vgt_ext2int_index_triangle_list_01	2002Sep6
PASS		
418	r400vgt_ext2int_index_triangle_strip_01	2002Sep6
PASS		
419	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep6
PASS		
420	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep6
PASS		
421	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep6
PASS		
422	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep6
PASS		
423	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep6
PASS		
424	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep6
PASS		
425	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep6
PASS		
426	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep6
PASS		
427	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep6
PASS		
428	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep6

PASS		
429	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep6
PASS		
430	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep6
PASS		
431	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep6
PASS		
432	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep6
PASS		
433	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep6
PASS		
434	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep6
PASS		
435	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep6
PASS		
436	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep6
PASS		
437	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep6
PASS		
438	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep6
PASS		
439	r400vgt_hos_PNT_edge_detection_01	2002Sep6
PASS		
440	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep6
PASS		
441	r400vgt_hos_RPatch_cp_02	2002Sep6
PASS		
442	r400vgt_hos_RPatch_lp_02	2002Sep6
PASS		
443	r400vgt_hos_auto_index_line_list_01	2002Sep6
PASS		
444	r400vgt_hos_auto_index_quad_list_01	2002Sep6
PASS		
445	r400vgt_hos_auto_index_triangle_list_01	2002Sep6
PASS		
446	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep6
PASS		
447	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep6
PASS		
448	r400vgt_immed_index_line_list_01	2002Sep6
PASS		
449	r400vgt_immed_index_line_loop_01	2002Sep6
PASS		
450	r400vgt_immed_index_line_strip_01	2002Sep6
PASS		
451	r400vgt_immed_index_points_01	2002Sep6
PASS		
452	r400vgt_immed_index_polygon_01	2002Sep6

PASS		
453	r400vgt_immed_index_primitives_01	2002Sep6
PASS		
454	r400vgt_immed_index_quad_list_01	2002Sep6
PASS		
455	r400vgt_immed_index_quad_strip_01	2002Sep6
PASS		
456	r400vgt_immed_index_rectangle_list_01	2002Sep6
PASS		
457	r400vgt_immed_index_tri_fan_01	2002Sep6
PASS		
458	r400vgt_immed_index_tri_list_01	2002Sep6
PASS		
459	r400vgt_immed_index_tri_strip_01	2002Sep6
PASS		
460	r400vgt_immed_index_tri_wflags_01	2002Sep6
PASS		
461	r400vgt_index_dealloc_line_list_01	2002Sep6
PASS		
462	r400vgt_index_dealloc_points_01	2002Sep6
PASS		
463	r400vgt_index_dealloc_triangle_list_01	2002Sep6
PASS		
464	r400vgt_index_min_max_01	2002Sep6
PASS		
465	r400vgt_index_min_max_02	2002Sep6
PASS		
466	r400vgt_index_offset_01	2002Sep6
PASS		
467	r400vgt_index_offset_02	2002Sep6
PASS		
468	r400vgt_index_offset_03	2002Sep6
PASS		
469	r400vgt_index_size_01	2002Sep6
PASS		
470	r400vgt_index_source_switch_01	2002Sep6
PASS		
471	r400vgt_line_list_01	2002Sep6
PASS		
472	r400vgt_line_loop_01	2002Sep6
PASS		
473	r400vgt_line_strip_01	2002Sep6
PASS		
474	r400vgt_multi_context_01	2002Sep6
PASS		
475	r400vgt_multi_context_02	2002Sep6
PASS		
476	r400vgt_multi_context_03	2002Sep6

PASS		
477	r400vgt_multi_context_04	2002Sep6
PASS		
478	r400vgt_multi_context_05	2002Sep6
PASS		
479	r400vgt_multi_context_06	2002Sep6
PASS		
480	r400vgt_multi_context_07	2002Sep6
PASS		
481	r400vgt_multi_context_08	2002Sep6
PASS		
482	r400vgt_multi_context_09	2002Sep6
PASS		
483	r400vgt_multi_context_10	2002Sep6
PASS		
484	r400vgt_multi_context_11	2002Sep6
PASS		
485	r400vgt_multi_context_12	2002Sep6
PASS		
486	r400vgt_pass_thru_all_prims_01	2002Sep6
PASS		
487	r400vgt_point_list_01	2002Sep6
PASS		
488	r400vgt_polygon_01	2002Sep6
PASS		
489	r400vgt_provoking_vtx_all_01	2002Sep6
PASS		
490	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep6
PASS		
491	r400vgt_quad_list_01	2002Sep6
PASS		
492	r400vgt_quad_strip_01	2002Sep6
PASS		
493	r400vgt_rectangle_list_01	2002Sep6
PASS		
494	r400vgt_reuse_depth_line_list_01	2002Sep6
PASS		
495	r400vgt_reuse_depth_point_list_01	2002Sep6
PASS		
496	r400vgt_reuse_depth_triangle_list_01	2002Sep6
PASS		
497	r400vgt_reuse_index_line_list_01	2002Sep6
PASS		
498	r400vgt_reuse_index_point_list_01	2002Sep6
PASS		
499	r400vgt_reuse_index_triangle_list_01	2002Sep6
PASS		
500	r400vgt_reuse_index_triangle_list_02	2002Sep6

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PASS
501 r400vgt_reuse_index_triangle_list_03 2002Sep6
PASS
502 r400vgt_suppress_eop_01 2002Sep6
PASS
503 r400vgt_suppress_eop_02 2002Sep6
PASS
504 r400vgt_suppress_eop_03 2002Sep6
PASS
505 r400vgt_suppress_eop_04 2002Sep6
PASS
506 r400vgt_triangle_fan_01 2002Sep6
PASS
507 r400vgt_triangle_list_01 2002Sep6
PASS
508 r400vgt_triangle_strip_01 2002Sep6
PASS
509 r400vgt_triangle_wflags_01 2002Sep6
PASS
510 r400vgt_vtx_vector_packing_01 2002Sep6
PASS
511 r400vte_coverage_01 2002Sep6
PASS
512 r400vte_simple_point_01 2002Sep6
PASS
513 r400vte_simple_triangle_01 2002Sep6
PASS
514 r400vte_w0_fmt_01 2002Sep6
PASS
515 r400vte_w0_fmt_03 2002Sep6
PASS
516 r400vte_xy_fmt_01 2002Sep6
PASS
517 r400vte_xyz_scale_01 2002Sep6
PASS
518 r400vte_xyz_scale_02 2002Sep6
PASS
519 r400vte_z_fmt_01 2002Sep6
PASS

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=====
Tests_Run_count = 519 Pass_count = 518 Fail_count = 0 Incomplete_count = 1
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sun Oct 13 21:42:18 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              50          50          50          100
              RBBM             1           1           1           100
              VGT             183         183         183          100
              SC              320         320         312           97
              CL              189         189         188           99
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              PARAMETER         62          62          62           100
              SU              107         107         107           100
              SANITY             2           2           2           100
+++++
              TOTAL            930         930         921           99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Sep 17  6:44:19 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          2          18
              RBBM             1           1           1          100
              VGT             157         157         154          98
              SC              244         244         241          98
              CL              110         110         110          100
              VTE              9           9           9          100
              SPECIALTY         6           6           6          100
              SU              85          85          85          100
              SANITY            2           2           2          100
+++++
              TOTAL            625         625         610          97
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Oct3
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Oct3
PASS			
3	cp_e2_one_blit		2002Oct3
PASS			
4	cp_e2_one_line		2002Oct3
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Oct3
PASS			
6	cp_e2paint_565		2002Oct3
PASS			
7	cp_e2paint_multi		2002Oct3
PASS			
8	cp_e2polyscanlines_brush		2002Oct3
PASS			
9	cp_e2polyscanlines_brush_mt		2002Oct3
PASS			
10	cp_e2smalltext_jcl		2002Oct3
PASS			
11	cp_simple_triangle		2002Oct3
PASS			
12	milestone_event_flush_tri1		2002Oct3
PASS			
13	milestone_event_flush_tri2		2002Oct3
PASS			
14	milestone_event_flush_tri3		2002Oct3
PASS			
15	milestone_event_flush_tri4		2002Oct3
PASS			
16	milestone_event_flush_tri5		2002Oct3
PASS			
17	milestone_event_flush_tri6		2002Oct3
PASS			
18	milestone_event_flush_tri7		2002Oct3
PASS			
19	milestone_event_flush_tri8		2002Oct3
PASS			
20	milestone_event_flush_tri9		2002Oct3

PASS		
21	milestone_event_randomized	2002Oct3
PASS		
22	milestone_event_standard	2002Oct3
PASS		
23	milestone_event_switch_flush_tri1	2002Oct3
PASS		
24	milestone_event_switch_flush_tri2	2002Oct3
PASS		
25	milestone_event_switch_flush_tri3	2002Oct3
PASS		
26	milestone_event_switch_flush_tri4	2002Oct3
PASS		
27	milestone_event_switch_flush_tri5	2002Oct3
PASS		
28	milestone_event_switch_flush_tri6	2002Oct3
PASS		
29	milestone_event_switch_flush_tri7	2002Oct3
PASS		
30	milestone_event_switch_flush_tri8	2002Oct3
PASS		
31	milestone_event_switch_flush_tri9	2002Oct3
PASS		
32	milestone_event_switch_tri2	2002Oct3
PASS		
33	milestone_event_switch_tri3	2002Oct3
PASS		
34	milestone_event_switch_tri4	2002Oct3
PASS		
35	milestone_event_switch_tri5	2002Oct3
PASS		
36	milestone_event_switch_tri6	2002Oct3
PASS		
37	milestone_event_switch_tri7	2002Oct3
PASS		
38	milestone_event_switch_tri8	2002Oct3
PASS		
39	milestone_event_switch_tri9	2002Oct3
PASS		
40	milestone_event_tri2	2002Oct3
PASS		
41	milestone_event_tri3	2002Oct3
PASS		
42	milestone_event_tri4	2002Oct3
PASS		
43	milestone_event_tri5	2002Oct3
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73	milestone_tri_tri8_pix4	2002Oct3
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74	primlib_1st_tri_june15	2002Oct3
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75	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct3
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76	primlib_gouraud_triangles_2_draw_passes	2002Oct3
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77	primlib_parameterized_simple_triangle	2002Oct3
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78	primlib_template_simple_triangle	2002Oct3
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79	primlib_tex_tri	2002Oct3
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80	r400cl_bary_texture_08_bug	2002Sep21
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81	r400cl_clip_10_verts_01	2002Oct3
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82	r400cl_clip_disable_01	2002Oct3
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83	r400cl_clip_space_dx_ogl_01	2002Oct3
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84	r400cl_clip_ucp_6bits_01	2002Oct3
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85	r400cl_cull_only_ena_01	2002Oct3
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86	r400cl_edgeflags_01	2002Oct3
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87	r400cl_edgeflags_01_bug	2002Oct3
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88	r400cl_edgeflags_frustum_bottom_01	2002Oct3
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89	r400cl_edgeflags_frustum_far_01	2002Oct3
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90	r400cl_edgeflags_frustum_left_01	2002Oct3
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91	r400cl_edgeflags_frustum_near_01	2002Oct3
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92	r400cl_edgeflags_frustum_right_01	2002Oct3

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93	r400cl_edgeflags_frustum_top_01	2002Oct3
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94	r400cl_edgeflags_gband_01	2002Oct3
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95	r400cl_edgeflags_gband_bottom_01	2002Oct3
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154	r400cl_gband_tcl_01	2002Oct3
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155	r400cl_nan_kill_01_bug	2002Oct3
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156	r400cl_pointlist_vertex_state_ucp_01	2002Oct3
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157	r400cl_polymode_line_fill_01_bug	2002Oct3
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158	r400cl_simple_triangle_01	2002Oct3
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159	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct3
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190	r400cl_ucp_pointlist_01	2002Oct3
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193	r400cl_vtx_kill_02	2002Oct3
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195	r400rbm_simple_triangle_01	2002Oct3
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196	r400sanity_persp_projection_01	2002Oct3
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197	r400sanity_vfd_texture_sample_01	2002Oct3
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204	r400sc_bres_cntl_w9x_01	2002Oct3
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225	r400sc_jss_2x1_01	2002Oct3
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281	r400sc_line_list_concentric_circle_02	2002Oct3
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282	r400sc_line_list_concentric_circle_03	2002Oct3
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283	r400sc_line_list_textured_01	2002Oct3
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284	r400sc_line_list_verify_st_01	2002Oct3

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285	r400sc_line_list_verify_st_01_bug	2002Oct3
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287	r400sc_line_msaa_8_textured_01	2002Oct3
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378	r400sc_msaa_8_aa_mask_fc_02	2002Oct3
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379	r400sc_msaa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct3
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380	r400sc_msaa_8_primitives_01	2002Oct3

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384	r400sc_msaa_8_rectangle_list_04	2002Oct3
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387	r400sc_msaa_8_rectangle_list_07	2002Oct3
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388	r400sc_msaa_8_rectangle_list_08	2002Oct3
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389	r400sc_msaa_8_zbuffer_rectangle_list_01	2002Oct3
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390	r400sc_msaa_8_zbuffer_rectangle_list_02	2002Oct3
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391	r400sc_msaa_X_primtypes_01_bug	2002Oct3
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392	r400sc_msaa_X_primtypes_02_bug	2002Oct3
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393	r400sc_multi_chip_prim_discard_00_07	2002Oct3
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394	r400sc_null_triangles_01	2002Oct3
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395	r400sc_null_triangles_fc_01	2002Oct3
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396	r400sc_pinwheel_01	2002Oct3
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397	r400sc_pinwheel_02	2002Oct3
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398	r400sc_pinwheel_02_bug	2002Oct3
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399	r400sc_point_jss_3x4_01	2002Oct3
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400	r400sc_point_list_01	2002Oct3
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412	r400sc_poly_offset_01	2002Oct3
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423	r400sc_poly_offset_fc_01	2002Oct3
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424	r400sc_polygon_stipple_01	2002Oct3
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425	r400sc_polymode_tri_fill_01	2002Oct3
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426	r400sc_polymode_tri_fill_01_bug	2002Oct3
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427	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct3
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428	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct3

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435	r400sc_raster_fill_rule_01	2002Oct3
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454	r400sc_scissor_rect_fc_01	2002Oct3
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455	r400sc_set_state_01	2002Oct3
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456	r400sc_sp_sample_cntl_01	2002Oct3
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463	r400sc_sp_sample_cntl_08	2002Oct3
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464	r400sc_tri_16_par_64_dwords_01	2002Oct3
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465	r400sc_tri_8textures_01	2002Oct3
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466	r400sc_tri_8textures_02	2002Oct3
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467	r400sc_tri_walk_start_vertex_01	2002Oct3
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468	r400sc_tri_walk_start_vertex_02	2002Oct3
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481	r400sc_tri_walk_start_vertex_15	2002Oct3
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482	r400sc_tri_walk_start_vertex_16	2002Oct3
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493	r400sc_zbuffer_rectangle_list_02	2002Oct3
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494	r400sc_zbuffer_rectangle_list_fc_02	2002Oct3
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495	r400sc_zbuffer_triangle_list_01	2002Oct3
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496	r400su_4tri_text_offscreen_01	2002Oct3
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497	r400su_baryc_test_01	2002Oct3
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498	r400su_baryc_test_02	2002Oct3
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502	r400su_clip_baryc_test_02	2002Oct3
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503	r400su_clip_edgeflag_polymode_01	2002Oct3
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504	r400su_clip_pointsize_test_01	2002Oct3
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505	r400su_clip_pointtest_01	2002Oct3
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508	r400su_clip_pointtest_04	2002Oct3
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511	r400su_clippoint_01	2002Oct3
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523	r400su_line_end_cap_width_functional_02	2002Oct3
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527	r400su_line_orientation_dx01_02	2002Oct3
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535	r400su_multi_prim_02	2002Oct3
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537	r400su_parallel_orientation_all_02	2002Oct3
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538	r400su_pc_management_01	2002Oct3
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552	r400su_pointsizepresent_02	2002Oct3
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554	r400su_polymode_lines_degen_triangle_02	2002Oct3
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583	r400su_triarea_test_02	2002Oct3
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595	r400vgt_auto_index_line_strip_01	2002Oct3
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604	r400vgt_auto_index_tri_strip_01	2002Oct3
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605	r400vgt_auto_index_tri_wflags_01	2002Oct3
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630	r400vgt_dma_swap_idx32_01	2002Oct3
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636	r400vgt_edgeflags_triangle_strip_01	2002Oct3
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637	r400vgt_edgeflags_triangle_wflags_01	2002Oct3
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638	r400vgt_event_handling_01	2002Sep26
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639	r400vgt_ext2int_index_line_list_01	2002Oct3
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640	r400vgt_ext2int_index_line_loop_01	2002Oct3
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650	r400vgt_ext2int_index_triangle_wflags_01	2002Oct3
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651	r400vgt_hos_LPatch_01	2002Oct3
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653	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct3
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654	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct3
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657	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct3
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658	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct3
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659	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct3
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660	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct3
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661	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct3
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662	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct3
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663	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct3
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664	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct3
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665	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct3
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666	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct3
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667	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Oct3
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668	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct3

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672	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct3
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673	r400vgt_hos_PNT_edge_detection_01	2002Oct3
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674	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct3
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675	r400vgt_hos_RPatch_cp_02	2002Oct3
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676	r400vgt_hos_RPatch_lp_02	2002Oct3
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677	r400vgt_hos_TPatch_01	2002Oct3
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678	r400vgt_hos_TPatch_02	2002Oct3
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681	r400vgt_hos_auto_index_triangle_list_01	2002Oct3
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683	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct3
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691	r400vgt_immed_index_quad_strip_01	2002Oct3
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696	r400vgt_immed_index_tri_wflags_01	2002Oct3
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699	r400vgt_index_dealloc_triangle_list_01	2002Oct3
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709	r400vgt_index_offset_08	2002Oct3
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710	r400vgt_index_size_01	2002Oct3
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711	r400vgt_index_source_switch_01	2002Oct3
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712	r400vgt_line_list_01	2002Oct3
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713	r400vgt_line_list_02	2002Oct3
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714	r400vgt_line_loop_01	2002Oct3
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715	r400vgt_line_loop_02	2002Oct3
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716	r400vgt_line_strip_01	2002Oct3

PASS		
717	r400vgt_line_strip_02	2002Oct3
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718	r400vgt_multi_context_01	2002Oct3
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719	r400vgt_multi_context_02	2002Oct3
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720	r400vgt_multi_context_03	2002Oct3
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721	r400vgt_multi_context_04	2002Oct3
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723	r400vgt_multi_context_06	2002Oct3
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729	r400vgt_multi_context_12	2002Oct3
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730	r400vgt_multi_prim_reset_index_all_01	2002Oct3
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731	r400vgt_multi_prim_reset_index_all_02	2002Oct3
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732	r400vgt_pass_thru_all_prims_01	2002Oct3
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733	r400vgt_point_list_01	2002Oct3
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734	r400vgt_point_list_02	2002Oct3
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735	r400vgt_polygon_01	2002Oct3
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736	r400vgt_polygon_02	2002Oct3
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737	r400vgt_provoking_vtx_all_01	2002Oct3
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738	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct3
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739	r400vgt_provoking_vtx_polygon_01	2002Oct3
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740	r400vgt_provoking_vtx_quad_list_01	2002Oct3

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742	r400vgt_provoking_vtx_tri_fan_01		2002Oct3
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743	r400vgt_provoking_vtx_tri_strip_01		2002Oct3
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747	r400vgt_quad_strip_02		2002Oct3
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748	r400vgt_rectangle_list_01		2002Oct3
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749	r400vgt_rectangle_list_02		2002Oct3
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750	r400vgt_reuse_depth_line_list_01		2002Oct3
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751	r400vgt_reuse_depth_point_list_01		2002Oct3
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752	r400vgt_reuse_depth_triangle_list_01		2002Oct3
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753	r400vgt_reuse_index_line_list_01		2002Oct3
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754	r400vgt_reuse_index_point_list_01		2002Oct3
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755	r400vgt_reuse_index_triangle_list_01		2002Oct3
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757	r400vgt_reuse_index_triangle_list_03		2002Oct3
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759	r400vgt_suppress_eop_02	0:0:1	2002Oct3
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760	r400vgt_suppress_eop_03	0:0:1	2002Oct3
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761	r400vgt_suppress_eop_04	0:0:1	2002Oct3
PASS			
762	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
763	r400vgt_triangle_fan_01	0:0:1	2002Oct3
PASS			
764	r400vgt_triangle_fan_02	0:0:1	2002Oct3


```

PASS
765 r400vgt_triangle_list_01          0:0:1    2002Oct3
PASS
766 r400vgt_triangle_list_02          0:0:1    2002Oct3
PASS
767 r400vgt_triangle_strip_01         0:0:1    2002Oct3
PASS
768 r400vgt_triangle_strip_02         0:0:1    2002Oct3
PASS
769 r400vgt_triangle_wflags_01        0:0:1    2002Oct3
PASS
770 r400vgt_triangle_wflags_02        0:0:1    2002Oct3
PASS
771 r400vgt_viz_query_01              0:0:1    2002Oct3
PASS
772 r400vgt_vtx_vector_packing_01     0:0:1    2002Oct3
PASS
773 r400vte_coverage_01               0:0:1    2002Oct3
PASS
774 r400vte_simple_point_01           0:0:1    2002Oct3
PASS
775 r400vte_simple_triangle_01        0:0:1    2002Oct3
PASS
776 r400vte_w0_fmt_01                 0:0:1    2002Oct3
PASS
777 r400vte_w0_fmt_03                 0:0:1    2002Oct3
PASS
778 r400vte_xy_fmt_01                 0:0:1    2002Oct3
PASS
779 r400vte_xy_fmt_mk_01_bug          0:0:1    2002Oct3
PASS
780 r400vte_xyz_scale_01              0:0:1    2002Oct3
PASS
781 r400vte_xyz_scale_02              0:0:1    2002Oct3
PASS
782 r400vte_z_fmt_01                  0:0:1    2002Oct3
PASS

```

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Tests_Run_count = 782 Pass_count = 781 Fail_count = 0 Incomplete_count = 1
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No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep23
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2	cp_e2blit_brush_mt_ropcc		2002Sep23
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3	cp_e2_hostdata_blt_pntr_8888		2002Sep23
PASS			
4	cp_e2_one_blit		2002Sep23
PASS			
5	cp_e2_one_line		2002Sep23
PASS			
6	cp_e2paint_565		2002Sep23
PASS			
7	cp_e2paint_multi		2002Sep23
PASS			
8	cp_e2polyscanlines_brush		2002Sep23
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep23
PASS			
10	cp_e2smalltext_jcl		2002Sep23
PASS			
11	cp_simple_triangle		2002Sep23
PASS			
12	primlib_1st_tri_june15		2002Sep23
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep23
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep23
PASS			
15	primlib_parameterized_simple_triangle		2002Sep23
PASS			
16	primlib_template_simple_triangle		2002Sep23
PASS			
17	primlib_tex_tri		2002Sep23
PASS			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep23
PASS			
20	r400cl_clip_disable_01		2002Sep23

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep23
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep23
PASS		
23	r400cl_cull_only_ena_01	2002Sep23
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24	r400cl_edgeflags_01	2002Sep23
PASS		
25	r400cl_edgeflags_01_bug	2002Sep23
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep23
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep23
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28	r400cl_edgeflags_frustum_left_01	2002Sep23
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep23
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep23
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep23
PASS		
32	r400cl_edgeflags_gband_01	2002Sep23
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33	r400cl_edgeflags_gband_bottom_01	2002Sep23
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34	r400cl_edgeflags_gband_left_01	2002Sep23
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep23
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36	r400cl_edgeflags_gband_top_01	2002Sep23
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37	r400cl_frustum_01	2002Sep23
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38	r400cl_frustum_02	2002Sep23
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44	r400cl_frustum_08	2002Sep23

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66	r400cl_frustum_all_vols_tris	2002Sep23
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68	r400cl_frustum_lines_02	2002Sep23

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92	r400cl_gband_tcl_01	2002Sep23

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93	r400cl_nan_kill_01_bug	2002Sep23
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95	r400cl_polymode_line_fill_01_bug	2002Sep23
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97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep23
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131	r400cl_vtx_kill_02	2002Sep23
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220	r400sc_line_list_concentric_circle_03	2002Sep23
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221	r400sc_line_list_textured_01	2002Sep23
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222	r400sc_line_list_verify_st_01	2002Sep23
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223	r400sc_line_list_verify_st_01_bug	2002Sep23
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224	r400sc_line_msa_8_01	2002Sep23
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225	r400sc_line_msa_8_textured_01	2002Sep23
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261	r400sc_msa_2_rectangle_list_07	2002Sep23
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262	r400sc_msa_2_rectangle_list_08	2002Sep23
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264	r400sc_msa_2_zbuffer_rectangle_list_02	2002Sep23
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265	r400sc_msa_3_primitives_01	2002Sep23
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275	r400sc_msa_3_zbuffer_rectangle_list_02	2002Sep23
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318	r400sc_msaa_8_zbuffer_rectangle_list_02	2002Sep23
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319	r400sc_msaa_X_primitives_01_bug	2002Sep23
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320	r400sc_msaa_X_primitives_02_bug	2002Sep23
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322	r400sc_null_triangles_fc_01	2002Sep23
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323	r400sc_pinwheel_01	2002Sep23
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324	r400sc_pinwheel_02	2002Sep23
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325	r400sc_pinwheel_02_bug	2002Sep23
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326	r400sc_point_jss_3x4_01	2002Sep23
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337	r400sc_point_list_09	2002Sep23
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338	r400sc_point_msa_8_01	2002Sep23
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339	r400sc_polygon_stipple_01	2002Sep23
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340	r400sc_polymode_tri_fill_01	2002Sep23
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341	r400sc_polymode_tri_fill_01_bug	2002Sep23
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342	r400sc_poly_offset_01	2002Sep23
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343	r400sc_poly_offset_02	2002Sep23
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344	r400sc_poly_offset_fc_01	2002Sep23
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345	r400sc_prsp_byc_intrp_ref_pix_01	2002Sep23
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346	r400sc_prsp_byc_intrp_ref_pix_02	2002Sep23
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352	r400sc_prsp_byc_intrp_ref_pix_08	2002Sep23
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354	r400sc_raster_fill_rule_02	2002Sep23
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355	r400sc_raster_fill_rule_03	2002Sep23
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356	r400sc_raster_fill_rule_04	2002Sep23

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370	r400sc_scissor_rect_04	2002Sep23
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380	r400sc_sp_sample_cntl_07	2002Sep23

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381	r400sc_sp_sample_cntl_08	2002Sep23
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383	r400sc_tri_8textures_01	2002Sep23
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384	r400sc_tri_8textures_02	2002Sep23
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385	r400sc_triangle_stipple_01	2002Sep23
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386	r400sc_tri_walk_start_vertex_01	2002Sep23
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399	r400sc_tri_walk_start_vertex_14	2002Sep23
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401	r400sc_tri_walk_start_vertex_16	2002Sep23
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416	r400su_clip_baryc_test_02	2002Sep23
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421	r400su_clip_pointttest_01	2002Sep23
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434	r400su_edgeflags_triangle_02	2002Sep23
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435	r400su_geom_sort_01	2002Sep23
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438	r400su_line_test_01	2002Sep23
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439	r400su_line_test_02	2002Sep23
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445	r400su_parallel_orientation_all_02	2002Sep23
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446	r400su_pc_management_01	2002Sep23
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460	r400su_point_wl_h16_functional_01	2002Sep23
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462	r400su_polymodefunctional_01	2002Sep23
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466	r400su_polymode_lines_zero_area_triangle_02	2002Sep23
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497	r400vgt_auto_index_line_strip_01	2002Sep23
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507	r400vgt_auto_index_tri_wflags_01	2002Sep23
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535	r400vgt_edgeflags_quad_strip_01	2002Sep23
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538	r400vgt_edgeflags_triangle_strip_01	2002Sep23
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539	r400vgt_edgeflags_triangle_wflags_01	2002Sep23
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559	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep23
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563	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep23
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571	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep23
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572	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep23

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577	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep23
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578	r400vgt_hos_PNT_edge_detection_01	2002Sep23
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580	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep23
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581	r400vgt_hos_RPatch_cp_02	2002Sep23
PASS		
582	r400vgt_hos_RPatch_lp_02	2002Sep23
PASS		
583	r400vgt_immed_index_line_list_01	2002Sep23
PASS		
584	r400vgt_immed_index_line_loop_01	2002Sep23
PASS		
585	r400vgt_immed_index_line_strip_01	2002Sep23
PASS		
586	r400vgt_immed_index_points_01	2002Sep23
PASS		
587	r400vgt_immed_index_polygon_01	2002Sep23
PASS		
588	r400vgt_immed_index_primitives_01	2002Sep23
PASS		
589	r400vgt_immed_index_quad_list_01	2002Sep23
PASS		
590	r400vgt_immed_index_quad_strip_01	2002Sep23
PASS		
591	r400vgt_immed_index_rectangle_list_01	2002Sep23
PASS		
592	r400vgt_immed_index_tri_fan_01	2002Sep23
PASS		
593	r400vgt_immed_index_tri_list_01	2002Sep23
PASS		
594	r400vgt_immed_index_tri_strip_01	2002Sep23
PASS		
595	r400vgt_immed_index_tri_wflags_01	2002Sep23
PASS		
596	r400vgt_index_dealloc_line_list_01	2002Sep23

PASS		
597	r400vgt_index_dealloc_points_01	2002Sep23
PASS		
598	r400vgt_index_dealloc_triangle_list_01	2002Sep23
PASS		
599	r400vgt_index_min_max_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
600	r400vgt_index_min_max_02	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
601	r400vgt_index_offset_01	2002Sep23
PASS		
602	r400vgt_index_offset_02	2002Sep23
PASS		
603	r400vgt_index_offset_03	2002Sep23
PASS		
604	r400vgt_index_size_01	2002Sep23
PASS		
605	r400vgt_index_source_switch_01	2002Sep23
PASS		
606	r400vgt_line_list_01	2002Sep23
PASS		
607	r400vgt_line_list_02	2002Sep23
PASS		
608	r400vgt_line_loop_01	2002Sep23
PASS		
609	r400vgt_line_loop_02	2002Sep23
PASS		
610	r400vgt_line_strip_01	2002Sep23
PASS		
611	r400vgt_line_strip_02	2002Sep23
PASS		
612	r400vgt_multi_context_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
613	r400vgt_multi_context_02	2002Sep23
PASS		
614	r400vgt_multi_context_03	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
615	r400vgt_multi_context_04	2002Sep23
PASS		
616	r400vgt_multi_context_05	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
617	r400vgt_multi_context_06	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
618	r400vgt_multi_context_07	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
619	r400vgt_multi_context_08	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
620	r400vgt_multi_context_09	2002Sep23

PASS		
621	r400vgt_multi_context_10	2002Sep23
PASS		
622	r400vgt_multi_context_11	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
623	r400vgt_multi_context_12	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
624	r400vgt_multi_prim_reset_index_all_01	2002Sep23
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
625	r400vgt_pass_thru_all_prims_01	2002Sep23
PASS		
626	r400vgt_point_list_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
627	r400vgt_point_list_02	2002Sep23
PASS		
628	r400vgt_polygon_01	2002Sep23
PASS		
629	r400vgt_polygon_02	2002Sep23
PASS		
630	r400vgt_provoking_vtx_all_01	2002Sep23
PASS		
631	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep23
PASS		
632	r400vgt_provoking_vtx_polygon_01	2002Sep23
PASS		
633	r400vgt_provoking_vtx_quad_list_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
634	r400vgt_provoking_vtx_quad_strip_01	2002Sep23
PASS		
635	r400vgt_provoking_vtx_tri_fan_01	2002Sep23
PASS		
636	r400vgt_provoking_vtx_tri_strip_01	2002Sep23
PASS		
637	r400vgt_quad_list_01	2002Sep23
PASS		
638	r400vgt_quad_list_02	2002Sep23
PASS		
639	r400vgt_quad_strip_01	2002Sep23
PASS		
640	r400vgt_quad_strip_02	2002Sep23
PASS		
641	r400vgt_rectangle_list_01	2002Sep23
PASS		
642	r400vgt_rectangle_list_02	2002Sep23
PASS		
643	r400vgt_reuse_depth_line_list_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
644	r400vgt_reuse_depth_point_list_01	2002Sep23

PASS		
645	r400vgt_reuse_depth_triangle_list_01	2002Sep23
PASS		
646	r400vgt_reuse_index_line_list_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
647	r400vgt_reuse_index_point_list_01	2002Sep23
FAIL	VgtPaClipS_VgtSide.cmp	
648	r400vgt_reuse_index_triangle_list_01	2002Sep23
PASS		
649	r400vgt_reuse_index_triangle_list_02	2002Sep23
PASS		
650	r400vgt_reuse_index_triangle_list_03	2002Sep23
PASS		
651	r400vgt_suppress_eop_01	2002Sep23
PASS		
652	r400vgt_suppress_eop_02	2002Sep23
PASS		
653	r400vgt_suppress_eop_03	2002Sep23
PASS		
654	r400vgt_suppress_eop_04	2002Sep23
PASS		
655	r400vgt_triangle_fan_01	2002Sep23
PASS		
656	r400vgt_triangle_fan_02	2002Sep23
PASS		
657	r400vgt_triangle_list_01	2002Sep23
PASS		
658	r400vgt_triangle_list_02	2002Sep23
PASS		
659	r400vgt_triangle_strip_01	2002Sep23
PASS		
660	r400vgt_triangle_strip_02	2002Sep23
PASS		
661	r400vgt_triangle_wflags_01	2002Sep23
PASS		
662	r400vgt_triangle_wflags_02	2002Sep23
PASS		
663	r400vgt_viz_query_01	2002Sep23
PASS		
664	r400vgt_vtx_vector_packing_01	2002Sep23
PASS		
665	r400vte_coverage_01	2002Sep23
PASS		
666	r400vte_simple_point_01	2002Sep23
PASS		
667	r400vte_simple_triangle_01	2002Sep23
PASS		
668	r400vte_w0_fmt_01	2002Sep23

PASS
669 r400vte_w0_fmt_03 2002Sep23
PASS
670 r400vte_xy_fmt_01 2002Sep23
PASS
671 r400vte_xy_fmt_mk_01_bug 2002Sep23
PASS
672 r400vte_xyz_scale_01 2002Sep23
PASS
673 r400vte_xyz_scale_02 2002Sep23
PASS
674 r400vte_z_fmt_01 2002Sep23
PASS

=====
Tests_Run_count = 674 Pass_count = 656 Fail_count = 18 Incomplete_count = 0
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep26
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Sep26
PASS			
3	cp_e2_one_blit		2002Sep26
PASS			
4	cp_e2_one_line		2002Sep26
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Sep26
PASS			
6	cp_e2paint_565		2002Sep26
PASS			
7	cp_e2paint_multi		2002Sep26
PASS			
8	cp_e2polyscanlines_brush		2002Sep26
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep26
PASS			
10	cp_e2smalltext_jcl		2002Sep26
PASS			
11	cp_simple_triangle		2002Sep26
PASS			
12	primlib_1st_tri_june15		2002Sep26
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep26
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep26
PASS			
15	primlib_parameterized_simple_triangle		2002Sep26
PASS			
16	primlib_template_simple_triangle		2002Sep26
PASS			
17	primlib_tex_tri		2002Sep26
PASS			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep26
PASS			
20	r400cl_clip_disable_01		2002Sep26

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep26
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep26
PASS		
23	r400cl_cull_only_ena_01	2002Sep26
PASS		
24	r400cl_edgeflags_01	2002Sep26
PASS		
25	r400cl_edgeflags_01_bug	2002Sep26
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep26
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep26
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep26
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep26
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep26
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep26
PASS		
32	r400cl_edgeflags_gband_01	2002Sep26
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep26
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep26
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep26
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep26
PASS		
37	r400cl_frustum_01	2002Sep26
PASS		
38	r400cl_frustum_02	2002Sep26
PASS		
39	r400cl_frustum_03	2002Sep26
PASS		
40	r400cl_frustum_04	2002Sep26
PASS		
41	r400cl_frustum_05	2002Sep26
PASS		
42	r400cl_frustum_06	2002Sep26
PASS		
43	r400cl_frustum_07	2002Sep26
PASS		
44	r400cl_frustum_08	2002Sep26

PASS		
45	r400cl_frustum_09	2002Sep26
PASS		
46	r400cl_frustum_10	2002Sep26
PASS		
47	r400cl_frustum_11	2002Sep26
PASS		
48	r400cl_frustum_12	2002Sep26
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49	r400cl_frustum_13	2002Sep26
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50	r400cl_frustum_14	2002Sep26
PASS		
51	r400cl_frustum_15	2002Sep26
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52	r400cl_frustum_16	2002Sep26
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53	r400cl_frustum_17	2002Sep26
PASS		
54	r400cl_frustum_18	2002Sep26
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55	r400cl_frustum_19	2002Sep26
PASS		
56	r400cl_frustum_20	2002Sep26
PASS		
57	r400cl_frustum_21	2002Sep26
PASS		
58	r400cl_frustum_22	2002Sep26
PASS		
59	r400cl_frustum_23	2002Sep26
PASS		
60	r400cl_frustum_24	2002Sep26
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61	r400cl_frustum_25	2002Sep26
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62	r400cl_frustum_26	2002Sep26
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63	r400cl_frustum_27	2002Sep26
PASS		
64	r400cl_frustum_28	2002Sep26
PASS		
65	r400cl_frustum_all_vols_lines	2002Sep26
PASS		
66	r400cl_frustum_all_vols_tris	2002Sep26
PASS		
67	r400cl_frustum_lines_01	2002Sep26
PASS		
68	r400cl_frustum_lines_02	2002Sep26

PASS		
69	r400cl_frustum_lines_03	2002Sep26
PASS		
70	r400cl_frustum_lines_04	2002Sep26
PASS		
71	r400cl_frustum_lines_05	2002Sep26
PASS		
72	r400cl_frustum_lines_06	2002Sep26
PASS		
73	r400cl_frustum_lines_07	2002Sep26
PASS		
74	r400cl_frustum_lines_08	2002Sep26
PASS		
75	r400cl_frustum_lines_09	2002Sep26
PASS		
76	r400cl_frustum_lines_10	2002Sep26
PASS		
77	r400cl_frustum_lines_11	2002Sep26
PASS		
78	r400cl_frustum_lines_12	2002Sep26
PASS		
79	r400cl_frustum_lines_13	2002Sep26
PASS		
80	r400cl_frustum_lines_14	2002Sep26
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81	r400cl_frustum_lines_15	2002Sep26
PASS		
82	r400cl_frustum_lines_16	2002Sep26
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83	r400cl_frustum_lines_17	2002Sep26
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84	r400cl_frustum_lines_18	2002Sep26
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85	r400cl_frustum_lines_19	2002Sep26
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86	r400cl_frustum_lines_20	2002Sep26
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87	r400cl_frustum_lines_21	2002Sep26
PASS		
88	r400cl_frustum_lines_22	2002Sep26
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89	r400cl_frustum_lines_23	2002Sep26
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90	r400cl_frustum_lines_24	2002Sep26
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91	r400cl_gband_04	2002Sep26
PASS		
92	r400cl_gband_tcl_01	2002Sep26

PASS		
93	r400cl_nan_kill_01_bug	2002Sep26
PASS		
94	r400cl_pointlist_vertex_state_ucp_01	2002Sep26
PASS		
95	r400cl_polymode_line_fill_01_bug	2002Sep26
PASS		
96	r400cl_simple_triangle_01	2002Sep26
PASS		
97	r400cl_triangle_polymode_line_stippled_01_bug	2002Sep26
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98	r400cl_ucp_combos_01	2002Sep26
PASS		
99	r400cl_ucp_combos_02	2002Sep26
PASS		
100	r400cl_ucp_combos_03	2002Sep26
PASS		
101	r400cl_ucp_combos_04	2002Sep26
PASS		
102	r400cl_ucp_combos_05	2002Sep26
PASS		
103	r400cl_ucp_combos_06	2002Sep26
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104	r400cl_ucp_combos_07	2002Sep26
PASS		
105	r400cl_ucp_combos_08	2002Sep26
PASS		
106	r400cl_ucp_combos_09	2002Sep26
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107	r400cl_ucp_combos_10	2002Sep26
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108	r400cl_ucp_combos_11	2002Sep26
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109	r400cl_ucp_combos_12	2002Sep26
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110	r400cl_ucp_combos_13	2002Sep26
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111	r400cl_ucp_combos_14	2002Sep26
PASS		
112	r400cl_ucp_combos_15	2002Sep26
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113	r400cl_ucp_combos_16	2002Sep26
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114	r400cl_ucp_combos_17	2002Sep26
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115	r400cl_ucp_combos_18	2002Sep26
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116	r400cl_ucp_combos_19	2002Sep26

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118	r400cl_ucp_combos_21	2002Sep26
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119	r400cl_ucp_combos_22	2002Sep26
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121	r400cl_ucp_combos_24	2002Sep26
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122	r400cl_ucp_combos_25	2002Sep26
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123	r400cl_ucp_combos_26	2002Sep26
PASS		
124	r400cl_ucp_combos_27	2002Sep26
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125	r400cl_ucp_combos_28	2002Sep26
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126	r400cl_ucp_combos_29	2002Sep26
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127	r400cl_ucp_combos_30	2002Sep26
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128	r400cl_ucp_pointlist_01	2002Sep26
PASS		
129	r400cl_vertex_reuse_clip_01	2002Sep26
PASS		
130	r400cl_vtx_kill_01	2002Sep26
PASS		
131	r400cl_vtx_kill_02	2002Sep26
PASS		
132	r400cl_w_eq_0	2002Sep26
PASS		
133	r400rbm_simple_triangle_01	2002Sep26
PASS		
134	r400sanity_persp_projection_01	2002Sep26
PASS		
135	r400sanity_vfd_texture_sample_01	2002Sep26
PASS		
136	r400sc_baryc_01	2002Sep26
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137	r400sc_bres_cntl_01	2002Sep7
PASS		
138	r400sc_bres_cntl_02	2002Sep26
PASS		
139	r400sc_bres_cntl_03	2002Sep26
PASS		
140	r400sc_bres_cntl_04	2002Sep26

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141	r400sc_bres_cntl_w2k_01	2002Sep26
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142	r400sc_bres_cntl_w9x_01	2002Sep26
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143	r400sc_clip_rect_01	2002Sep26
PASS		
144	r400sc_clip_rect_02	2002Sep26
PASS		
145	r400sc_clip_rect_03	2002Sep26
PASS		
146	r400sc_clip_rect_04	2002Sep26
PASS		
147	r400sc_clip_rect_fc_01	2002Sep26
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148	r400sc_diamond_exit_01	2002Sep26
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149	r400sc_diamond_exit_02	2002Sep26
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150	r400sc_diamond_exit_03	2002Sep26
PASS		
151	r400sc_diamond_exit_04	2002Sep26
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152	r400sc_diamond_exit_05	2002Sep26
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153	r400sc_jss_1x1_primtypes_01	2002Sep26
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154	r400sc_jss_1x2_01	2002Sep26
PASS		
155	r400sc_jss_1x2_02	2002Sep26
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156	r400sc_jss_1x2_primtypes_01	2002Sep26
PASS		
157	r400sc_jss_1x3_01	2002Sep26
PASS		
158	r400sc_jss_1x3_02	2002Sep26
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159	r400sc_jss_1x3_primtypes_01	2002Sep26
PASS		
160	r400sc_jss_1x4_01	2002Sep26
PASS		
161	r400sc_jss_1x4_02	2002Sep26
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162	r400sc_jss_1x4_primtypes_01	2002Sep26
PASS		
163	r400sc_jss_2x1_01	2002Sep26
PASS		
164	r400sc_jss_2x1_02	2002Sep26

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165	r400sc_jss_2x1_primtypes_01	2002Sep26
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166	r400sc_jss_2x2_01	2002Sep26
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167	r400sc_jss_2x2_02	2002Sep26
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168	r400sc_jss_2x2_primtypes_01	2002Sep26
PASS		
169	r400sc_jss_2x3_01	2002Sep26
PASS		
170	r400sc_jss_2x3_02	2002Sep26
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171	r400sc_jss_2x3_primtypes_01	2002Sep26
PASS		
172	r400sc_jss_2x4_01	2002Sep26
PASS		
173	r400sc_jss_2x4_02	2002Sep26
PASS		
174	r400sc_jss_2x4_primtypes_01	2002Sep26
PASS		
175	r400sc_jss_3x1_01	2002Sep26
PASS		
176	r400sc_jss_3x1_02	2002Sep26
PASS		
177	r400sc_jss_3x1_primtypes_01	2002Sep26
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178	r400sc_jss_3x2_01	2002Sep26
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179	r400sc_jss_3x2_02	2002Sep26
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180	r400sc_jss_3x2_primtypes_01	2002Sep26
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181	r400sc_jss_3x3_01	2002Sep26
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182	r400sc_jss_3x3_02	2002Sep26
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183	r400sc_jss_3x3_primtypes_01	2002Sep26
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184	r400sc_jss_3x4_01	2002Sep26
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185	r400sc_jss_3x4_02	2002Sep26
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186	r400sc_jss_3x4_03	2002Sep26
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187	r400sc_jss_3x4_primtypes_01	2002Sep26
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188	r400sc_jss_4x1_01	2002Sep26

PASS		
189	r400sc_jss_4x1_02	2002Sep26
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190	r400sc_jss_4x1_primitives_01	2002Sep26
PASS		
191	r400sc_jss_4x2_01	2002Sep26
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192	r400sc_jss_4x2_02	2002Sep26
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193	r400sc_jss_4x2_primitives_01	2002Sep26
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194	r400sc_jss_4x3_01	2002Sep26
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195	r400sc_jss_4x3_02	2002Sep26
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196	r400sc_jss_4x3_primitives_01	2002Sep26
PASS		
197	r400sc_jss_4x4_01	2002Sep26
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198	r400sc_jss_4x4_02	2002Sep26
PASS		
199	r400sc_jss_4x4_03	2002Sep26
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200	r400sc_jss_4x4_aa_mask_01	2002Sep26
PASS		
201	r400sc_jss_4x4_aa_mask_02	2002Sep26
PASS		
202	r400sc_jss_4x4_fc_01	2002Sep26
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203	r400sc_jss_4x4_fc_02	2002Sep26
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204	r400sc_jss_4x4_max_dist_01	2002Sep26
PASS		
205	r400sc_jss_4x4_primitives_01	2002Sep26
PASS		
206	r400sc_line_dx10_eq_0_01	2002Sep26
PASS		
207	r400sc_line_dx10_ge_0_01	2002Sep26
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208	r400sc_line_dx10_lt_0_01	2002Sep26
PASS		
209	r400sc_line_dy10_eq_0_01	2002Sep26
PASS		
210	r400sc_line_dy10_ge_0_01	2002Sep26
PASS		
211	r400sc_line_dy10_lt_0_01	2002Sep26
PASS		
212	r400sc_line_expand_width_maa_8_01	2002Sep26

PASS		
213	r400sc_line_expand_width_msa_8_02	2002Sep26
PASS		
214	r400sc_line_expand_width_msa_8_03	2002Sep26
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215	r400sc_line_jss_3x4_01	2002Sep26
PASS		
216	r400sc_line_list_01	2002Sep26
PASS		
217	r400sc_line_list_02	2002Sep26
PASS		
218	r400sc_line_list_concentric_circle_01	2002Sep26
PASS		
219	r400sc_line_list_concentric_circle_02	2002Sep26
PASS		
220	r400sc_line_list_concentric_circle_03	2002Sep26
PASS		
221	r400sc_line_list_textured_01	2002Sep26
PASS		
222	r400sc_line_list_verify_st_01	2002Sep26
PASS		
223	r400sc_line_list_verify_st_01_bug	2002Sep26
PASS		
224	r400sc_line_msa_8_01	2002Sep26
PASS		
225	r400sc_line_msa_8_textured_01	2002Sep26
PASS		
226	r400sc_line_stipple_01	2002Sep26
PASS		
227	r400sc_line_stipple_02	2002Sep26
PASS		
228	r400sc_line_stipple_03	2002Sep26
PASS		
229	r400sc_line_stipple_04	2002Sep26
PASS		
230	r400sc_line_stipple_05	2002Sep26
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231	r400sc_line_stipple_06	2002Sep26
PASS		
232	r400sc_line_stipple_07	2002Sep26
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233	r400sc_line_stipple_08	2002Sep26
PASS		
234	r400sc_line_stipple_09	2002Sep26
PASS		
235	r400sc_line_stipple_10	2002Sep26
PASS		
236	r400sc_line_stipple_11	2002Sep26

PASS		
237	r400sc_line_stipple_12	2002Sep26
PASS		
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564	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep26
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566	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep26
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571	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep26
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572	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep26

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573	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep26
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575	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep26
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577	r400vgt_hos_PNT_edge_detection_01	2002Sep26
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578	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep26
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579	r400vgt_hos_RPatch_cp_02	2002Sep26
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580	r400vgt_hos_RPatch_lp_02	2002Sep26
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581	r400vgt_hos_auto_index_line_list_01	2002Sep26
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627	r400vgt_multi_prim_reset_index_all_02	2002Sep26
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642	r400vgt_quad_strip_01	2002Sep26
PASS		
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644	r400vgt_rectangle_list_01	2002Sep26

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645	r400vgt_rectangle_list_02	2002Sep26
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668	r400vgt_vtx_vector_packing_01	2002Sep26

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671 r400vte_simple_triangle_01 2002Sep26
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673 r400vte_w0_fmt_03 2002Sep26
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674 r400vte_xy_fmt_01 2002Sep26
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675 r400vte_xy_fmt_mk_01_bug 2002Sep26
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676 r400vte_xyz_scale_01 2002Sep26
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677 r400vte_xyz_scale_02 2002Sep26
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678 r400vte_z_fmt_01 2002Sep26
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Test_Status  Probable_Cause_of_Failures                LastPass
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2     primlib_gouraud_tri_1_xyz_vb_1_rgb_vb     2002Aug16
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3     primlib_gouraud_triangles_2_draw_passes   2002Aug16
INCOMPLETE
4     primlib_parameterized_simple_triangle     2002Aug16
INCOMPLETE
5     primlib_template_simple_triangle         2002Aug16
INCOMPLETE
6     primlib_tex_tri                           2002Aug16
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7     r400cl_clip_10_verts_01                  2002Aug16
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8     r400cl_clip_disable_01                   2002Aug16
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9     r400cl_clip_space_dx_ogl_01              2002Aug16
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10    r400cl_clip_ucp_6bits_01                 2002Aug16
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11    r400cl_cull_only_ena_01                  2002Aug16
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12    r400cl_edgeflags_01                      2002Aug16
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13    r400cl_edgeflags_frustum_bottom_01      2002Aug16
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14    r400cl_edgeflags_frustum_far_01         2002Aug16
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15    r400cl_edgeflags_frustum_left_01        2002Aug16
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16    r400cl_edgeflags_frustum_near_01        2002Aug16
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17    r400cl_edgeflags_frustum_right_01       2002Aug16
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18    r400cl_edgeflags_frustum_top_01         2002Aug16
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19    r400cl_edgeflags_gband_01               2002Aug16
INCOMPLETE
20    r400cl_edgeflags_gband_bottom_01        2002Aug16

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196	r400sc_scissor_rect_04	2002Aug16
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197	r400sc_scissor_rect_05	2002Aug16
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198	r400sc_set_state_01	2002Aug16
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199	r400sc_tri_walk_start_vertex_01	2002Aug16
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200	r400sc_tri_walk_start_vertex_02	2002Aug16
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201	r400sc_tri_walk_start_vertex_03	2002Aug16
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206	r400sc_tri_walk_start_vertex_08	2002Aug16
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207	r400sc_tri_walk_start_vertex_09	2002Aug16
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209	r400sc_tri_walk_start_vertex_11	2002Aug16
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210	r400sc_tri_walk_start_vertex_12	2002Aug16
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211	r400sc_tri_walk_start_vertex_13	2002Aug16
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212	r400sc_tri_walk_start_vertex_14	2002Aug16

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213 r400sc_tri_walk_start_vertex_15	2002Aug16
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214 r400sc_tri_walk_start_vertex_16	2002Aug16
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215 r400sc_triangle_stipple_01	2002Aug16
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216 r400sc_window_offset_01	2002Aug16
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217 r400sc_zbuffer_line_list_01	2002Aug16
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218 r400sc_zbuffer_point_list_01	2002Aug16
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219 r400sc_zbuffer_rectangle_list_01	2002Aug16
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220 r400sc_zbuffer_rectangle_list_02	2002Aug16
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221 r400sc_zbuffer_triangle_list_01	2002Aug16
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222 r400su_4tri_text_offscreen_01	2002Aug16
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223 r400su_baryc_test_01	2002Aug16
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224 r400su_baryc_test_02	2002Aug16
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225 r400su_baryc_test_03	2002Aug16
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226 r400su_baryc_test_04	2002Aug16
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227 r400su_clip_edgeflag_polymode_01	2002Aug16
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228 r400su_clip_pointsize_test_01	2002Aug16
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229 r400su_clip_pointtest_01	2002Aug16
INCOMPLETE	
230 r400su_clip_pointtest_02	2002Aug16
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231 r400su_clip_pointtest_03	2002Aug16
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232 r400su_clip_pointtest_04	2002Aug16
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233 r400su_clip_polymode_test_01	2002Aug16
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234 r400su_clipline_01	2002Aug16
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235 r400su_clippoint_01	2002Aug16
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236 r400su_clipvertexsortingfunctional_01	2002Aug16

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237	r400su_cullingfunctional_01	2002Aug16
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238	r400su_degentri_test_01	2002Aug16
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239	r400su_degentri_test_02	2002Aug16
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240	r400su_degentri_test_03	2002Aug16
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241	r400su_edge_flag_01	2002Aug16
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242	r400su_edge_flag_02	2002Aug16
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243	r400su_edgeflags_triangle_01	2002Aug16
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244	r400su_edgeflags_triangle_02	2002Aug16
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245	r400su_geom_sort_01	2002Aug16
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246	r400su_line_end_cap_functional_01	2002Aug16
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247	r400su_line_end_cap_width_functional_02	2002Aug16
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248	r400su_line_test_01	2002Aug16
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249	r400su_line_test_02	2002Aug16
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250	r400su_longstrip_01	2002Aug16
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251	r400su_multi_prim_01	2002Aug16
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252	r400su_parallel_orientation_all_01	2002Aug16
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253	r400su_parallel_orientation_all_02	2002Aug16
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254	r400su_point_sprite_01	2002Aug16
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255	r400su_point_sprite_02	2002Aug16
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256	r400su_point_sprite_03	2002Aug16
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257	r400su_point_sprite_04	2002Aug16
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258	r400su_point_sprite_05	2002Aug16
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259	r400su_point_sprite_06	2002Aug16
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260	r400su_point_wl6_hl_functional_01	2002Aug16

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261 r400su_point_wl_h16_functional_01	2002Aug16
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262 r400su_pointsizepresent_01	2002Aug16
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263 r400su_pointsizepresent_02	2002Aug16
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264 r400su_polymode_rectangle_01	2002Aug16
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265 r400su_polymodeculling_01	2002Aug16
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266 r400su_polymodefunctional_01	2002Aug16
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267 r400su_provoking_vtx_edgeflags_triangle_01	2002Aug16
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268 r400su_provoking_vtx_edgeflags_triangle_02	2002Aug16
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269 r400su_provoking_vtx_edgeflags_triangle_03	2002Aug16
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270 r400su_provoking_vtx_edgeflags_triangle_04	2002Aug16
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271 r400su_provoking_vtx_line_01	2002Aug16
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272 r400su_provoking_vtx_point_01	2002Aug16
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273 r400su_provoking_vtx_polymode_rectangle_01	2002Aug16
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274 r400su_provoking_vtx_rectangle_01	2002Aug16
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275 r400su_provoking_vtx_triangle_01	2002Aug16
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276 r400su_rand_line_01	2002Aug16
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277 r400su_rand_point_01	2002Aug16
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278 r400su_rand_tri_01	2002Aug16
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279 r400su_rectangle_list_01	2002Aug16
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280 r400su_sliver_01	2002Aug16
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281 r400su_triarea_test_01	2002Aug16
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282 r400su_triarea_test_02	2002Aug16
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283 r400su_triarea_test_03	2002Aug16
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284 r400su_triarea_test_04	2002Aug16

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287	r400vgt_auto_index_line_loop_01	2002Aug16
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288	r400vgt_auto_index_line_strip_01	2002Aug16
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289	r400vgt_auto_index_points_01	2002Aug16
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290	r400vgt_auto_index_polygon_01	2002Aug16
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291	r400vgt_auto_index_primitives_01	2002Aug16
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292	r400vgt_auto_index_quad_list_01	2002Aug16
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293	r400vgt_auto_index_quad_strip_01	2002Aug16
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294	r400vgt_auto_index_rectangle_list_01	2002Aug16
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295	r400vgt_auto_index_tri_fan_01	2002Aug16
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296	r400vgt_auto_index_tri_list_01	2002Aug16
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297	r400vgt_auto_index_tri_strip_01	2002Aug16
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298	r400vgt_auto_index_tri_wflags_01	2002Aug16
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299	r400vgt_dma_engine_01	2002Aug16
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300	r400vgt_dma_engine_02	2002Aug16
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301	r400vgt_dma_engine_03	2002Aug16
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302	r400vgt_dma_engine_04	2002Aug16
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303	r400vgt_dma_engine_05	2002Aug16
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304	r400vgt_dma_engine_06	2002Aug16
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305	r400vgt_dma_engine_07	2002Aug16
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306	r400vgt_dma_engine_08	2002Aug16
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307	r400vgt_dma_engine_09	2002Aug16
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308	r400vgt_dma_engine_10	2002Aug16

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309 r400vgt_dma_index_line_list_01	2002Aug16
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310 r400vgt_dma_index_line_loop_01	2002Aug16
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311 r400vgt_dma_index_line_strip_01	2002Aug16
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312 r400vgt_dma_index_points_01	2002Aug16
INCOMPLETE	
313 r400vgt_dma_index_polygon_01	2002Aug16
INCOMPLETE	
314 r400vgt_dma_index_primitives_01	2002Aug16
INCOMPLETE	
315 r400vgt_dma_index_quad_list_01	2002Aug16
INCOMPLETE	
316 r400vgt_dma_index_quad_strip_01	2002Aug16
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317 r400vgt_dma_index_rectangle_list_01	2002Aug16
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318 r400vgt_dma_index_tri_fan_01	2002Aug16
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319 r400vgt_dma_index_tri_list_01	2002Aug16
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320 r400vgt_dma_index_tri_strip_01	2002Aug16
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321 r400vgt_dma_index_tri_wflags_01	2002Aug16
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322 r400vgt_dma_swap_indx16_01	2002Aug16
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323 r400vgt_dma_swap_indx32_01	2002Aug16
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324 r400vgt_edgeflags_polygon_01	2002Aug16
INCOMPLETE	
325 r400vgt_edgeflags_quad_list_01	2002Aug16
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326 r400vgt_edgeflags_quad_strip_01	2002Aug16
INCOMPLETE	
327 r400vgt_edgeflags_triangle_fan_01	2002Aug16
INCOMPLETE	
328 r400vgt_edgeflags_triangle_list_01	2002Aug16
INCOMPLETE	
329 r400vgt_edgeflags_triangle_strip_01	2002Aug16
INCOMPLETE	
330 r400vgt_edgeflags_triangle_wflags_01	2002Aug16
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331 r400vgt_ext2int_index_line_list_01	2002Aug16
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332 r400vgt_ext2int_index_line_loop_01	2002Aug16

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333 r400vgt_ext2int_index_line_strip_01	2002Aug16
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334 r400vgt_ext2int_index_points_01	2002Aug16
INCOMPLETE	
335 r400vgt_ext2int_index_polygon_01	2002Aug16
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336 r400vgt_ext2int_index_quad_list_01	2002Aug16
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337 r400vgt_ext2int_index_quad_strip_01	2002Aug16
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338 r400vgt_ext2int_index_triangle_fan_01	2002Aug16
INCOMPLETE	
339 r400vgt_ext2int_index_triangle_list_01	2002Aug16
INCOMPLETE	
340 r400vgt_ext2int_index_triangle_strip_01	2002Aug16
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341 r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug16
INCOMPLETE	
342 r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug16
INCOMPLETE	
343 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug16
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344 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug16
INCOMPLETE	
345 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug16
INCOMPLETE	
346 r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug16
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347 r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug16
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348 r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug16
INCOMPLETE	
349 r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug16
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350 r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug16
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351 r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug16
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352 r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug16
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353 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug16
INCOMPLETE	
354 r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug16
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355 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug16
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356 r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug16

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357	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug16
INCOMPLETE		
358	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug16
INCOMPLETE		
359	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug16
INCOMPLETE		
360	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug16
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361	r400vgt_hos_PNT_edge_detection_01	2002Aug16
INCOMPLETE		
362	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug16
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363	r400vgt_hos_auto_index_line_list_01	2002Aug16
INCOMPLETE		
364	r400vgt_hos_auto_index_quad_list_01	2002Aug16
INCOMPLETE		
365	r400vgt_hos_auto_index_triangle_list_01	2002Aug16
INCOMPLETE		
366	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug16
INCOMPLETE		
367	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug16
INCOMPLETE		
368	r400vgt_immed_index_line_list_01	2002Aug16
INCOMPLETE		
369	r400vgt_immed_index_line_loop_01	2002Aug16
INCOMPLETE		
370	r400vgt_immed_index_line_strip_01	2002Aug16
INCOMPLETE		
371	r400vgt_immed_index_points_01	2002Aug16
INCOMPLETE		
372	r400vgt_immed_index_polygon_01	2002Aug16
INCOMPLETE		
373	r400vgt_immed_index_primitives_01	2002Aug16
INCOMPLETE		
374	r400vgt_immed_index_quad_list_01	2002Aug16
INCOMPLETE		
375	r400vgt_immed_index_quad_strip_01	2002Aug16
INCOMPLETE		
376	r400vgt_immed_index_rectangle_list_01	2002Aug16
INCOMPLETE		
377	r400vgt_immed_index_tri_fan_01	2002Aug16
INCOMPLETE		
378	r400vgt_immed_index_tri_list_01	2002Aug16
INCOMPLETE		
379	r400vgt_immed_index_tri_strip_01	2002Aug16
INCOMPLETE		
380	r400vgt_immed_index_tri_wflags_01	2002Aug16

INCOMPLETE		
381	r400vgt_index_dealloc_line_list_01	2002Aug16
INCOMPLETE		
382	r400vgt_index_dealloc_points_01	2002Aug16
INCOMPLETE		
383	r400vgt_index_dealloc_triangle_list_01	2002Aug16
INCOMPLETE		
384	r400vgt_index_min_max_01	2002Aug16
INCOMPLETE		
385	r400vgt_index_min_max_02	2002Aug16
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386	r400vgt_index_offset_01	2002Aug16
INCOMPLETE		
387	r400vgt_index_offset_02	2002Aug16
INCOMPLETE		
388	r400vgt_index_offset_03	2002Aug16
INCOMPLETE		
389	r400vgt_index_size_01	2002Aug16
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390	r400vgt_index_source_switch_01	2002Aug16
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391	r400vgt_line_list_01	2002Aug16
INCOMPLETE		
392	r400vgt_line_loop_01	2002Aug16
INCOMPLETE		
393	r400vgt_line_strip_01	2002Aug16
INCOMPLETE		
394	r400vgt_pass_thru_all_prims_01	2002Aug16
INCOMPLETE		
395	r400vgt_point_list_01	2002Aug16
INCOMPLETE		
396	r400vgt_polygon_01	2002Aug16
INCOMPLETE		
397	r400vgt_provoking_vtx_all_01	2002Aug16
INCOMPLETE		
398	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug16
INCOMPLETE		
399	r400vgt_quad_list_01	2002Aug16
INCOMPLETE		
400	r400vgt_quad_strip_01	2002Aug16
INCOMPLETE		
401	r400vgt_rectangle_list_01	2002Aug16
INCOMPLETE		
402	r400vgt_reuse_depth_line_list_01	2002Aug16
INCOMPLETE		
403	r400vgt_reuse_depth_point_list_01	2002Aug16
INCOMPLETE		
404	r400vgt_reuse_depth_triangle_list_01	2002Aug16

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INCOMPLETE
405  r400vgt_reuse_index_line_list_01          2002Aug16
INCOMPLETE
406  r400vgt_reuse_index_point_list_01        2002Aug16
INCOMPLETE
407  r400vgt_reuse_index_triangle_list_01     2002Aug16
INCOMPLETE
408  r400vgt_reuse_index_triangle_list_02     2002Aug16
INCOMPLETE
409  r400vgt_reuse_index_triangle_list_03     2002Aug16
INCOMPLETE
410  r400vgt_triangle_fan_01                 2002Aug16
INCOMPLETE
411  r400vgt_triangle_list_01                2002Aug16
INCOMPLETE
412  r400vgt_triangle_strip_01               2002Aug16
INCOMPLETE
413  r400vgt_triangle_wflags_01             2002Aug16
INCOMPLETE
414  r400vgt_vtx_vector_packing_01          2002Aug16
INCOMPLETE
415  r400vte_coverage_01                    2002Aug16
INCOMPLETE
416  r400vte_simple_point_01                2002Aug16
INCOMPLETE
417  r400vte_simple_triangle_01             2002Aug16
INCOMPLETE
418  r400vte_w0_fmt_01                      2002Aug16
INCOMPLETE
419  r400vte_w0_fmt_03                      2002Aug16
INCOMPLETE
420  r400vte_xy_fmt_01                      2002Aug16
INCOMPLETE
421  r400vte_xyz_scale_01                   2002Aug16
INCOMPLETE
422  r400vte_xyz_scale_02                   2002Aug16
INCOMPLETE
423  r400vte_z_fmt_01                       2002Aug16
INCOMPLETE

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=====
Tests_Run_count = 423  Pass_count = 0  Fail_count = 0  Incomplete_count = 423
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```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep  5  3:50:33 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          147          147          147          100
              SC           164          164          163           99
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY    SU           81           81           81          100
              SANITY       2           2           2           100
+++++
              TOTAL        517          517          516           99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Fri Aug 16  3:10:05 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          0          0
              SC           133          133          0          0
              CL           80           80           0          0
              VTE          9            9            0          0
SPECIALTY    SU           64           64           0          0
              SANITY       2            2            0          0
+++++
              TOTAL        423          423          0          0
+++++

```

=====
Regression Report For VGT - Project = r400 Created on Sat Sep 28 22:14:15 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

1	cp_dma_simple		2002Sep28
PASS			
2	cp_e2_hostdata_blt_pntr_8888		2002Sep28
PASS			
3	cp_e2_one_blit		2002Sep28
PASS			
4	cp_e2_one_line		2002Sep28
PASS			
5	cp_e2blit_brush_mt_ropcc		2002Sep28
PASS			
6	cp_e2paint_565		2002Sep28
PASS			
7	cp_e2paint_multi		2002Sep28
PASS			
8	cp_e2polyscanlines_brush		2002Sep28
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep28
PASS			
10	cp_e2smalltext_jcl		2002Sep28
PASS			
11	cp_simple_triangle		2002Sep28
PASS			
12	primlib_1st_tri_june15		2002Sep28
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep28
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep28
PASS			
15	primlib_parameterized_simple_triangle		2002Sep28
PASS			
16	primlib_template_simple_triangle		2002Sep28
PASS			
17	primlib_tex_tri		2002Sep28
PASS			
18	r400cl_bary_texture_08_bug		2002Sep21
PASS			
19	r400cl_clip_10_verts_01		2002Sep28
PASS			
20	r400cl_clip_disable_01		2002Sep28

PASS		
21	r400cl_clip_space_dx_ogl_01	2002Sep28
PASS		
22	r400cl_clip_ucp_6bits_01	2002Sep28
PASS		
23	r400cl_cull_only_ena_01	2002Sep28
PASS		
24	r400cl_edgeflags_01	2002Sep28
PASS		
25	r400cl_edgeflags_01_bug	2002Sep28
PASS		
26	r400cl_edgeflags_frustum_bottom_01	2002Sep28
PASS		
27	r400cl_edgeflags_frustum_far_01	2002Sep28
PASS		
28	r400cl_edgeflags_frustum_left_01	2002Sep28
PASS		
29	r400cl_edgeflags_frustum_near_01	2002Sep28
PASS		
30	r400cl_edgeflags_frustum_right_01	2002Sep28
PASS		
31	r400cl_edgeflags_frustum_top_01	2002Sep28
PASS		
32	r400cl_edgeflags_gband_01	2002Sep28
PASS		
33	r400cl_edgeflags_gband_bottom_01	2002Sep28
PASS		
34	r400cl_edgeflags_gband_left_01	2002Sep28
PASS		
35	r400cl_edgeflags_gband_right_01	2002Sep28
PASS		
36	r400cl_edgeflags_gband_top_01	2002Sep28
PASS		
37	r400cl_frustum_01	2002Sep28
PASS		
38	r400cl_frustum_02	2002Sep28
PASS		
39	r400cl_frustum_03	2002Sep28
PASS		
40	r400cl_frustum_04	2002Sep28
PASS		
41	r400cl_frustum_05	2002Sep28
PASS		
42	r400cl_frustum_06	2002Sep28
PASS		
43	r400cl_frustum_07	2002Sep28
PASS		
44	r400cl_frustum_08	2002Sep28

PASS		
45	r400cl_frustum_09	2002Sep28
PASS		
46	r400cl_frustum_10	2002Sep28
PASS		
47	r400cl_frustum_11	2002Sep28
PASS		
48	r400cl_frustum_12	2002Sep28
PASS		
49	r400cl_frustum_13	2002Sep28
PASS		
50	r400cl_frustum_14	2002Sep28
PASS		
51	r400cl_frustum_15	2002Sep28
PASS		
52	r400cl_frustum_16	2002Sep28
PASS		
53	r400cl_frustum_17	2002Sep28
PASS		
54	r400cl_frustum_18	2002Sep28
PASS		
55	r400cl_frustum_19	2002Sep28
PASS		
56	r400cl_frustum_20	2002Sep28
PASS		
57	r400cl_frustum_21	2002Sep28
PASS		
58	r400cl_frustum_22	2002Sep28
PASS		
59	r400cl_frustum_23	2002Sep28
PASS		
60	r400cl_frustum_24	2002Sep28
PASS		
61	r400cl_frustum_25	2002Sep28
PASS		
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92	r400cl_gband_tcl_01	2002Sep28

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220	r400sc_line_list_concentric_circle_03	2002Sep28
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395	r400sc_tri_walk_start_vertex_01	2002Sep28
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396	r400sc_tri_walk_start_vertex_02	2002Sep28
PASS		
397	r400sc_tri_walk_start_vertex_03	2002Sep28
PASS		
398	r400sc_tri_walk_start_vertex_04	2002Sep28
PASS		
399	r400sc_tri_walk_start_vertex_05	2002Sep28
PASS		
400	r400sc_tri_walk_start_vertex_06	2002Sep28
PASS		
401	r400sc_tri_walk_start_vertex_07	2002Sep28
PASS		
402	r400sc_tri_walk_start_vertex_08	2002Sep28
PASS		
403	r400sc_tri_walk_start_vertex_09	2002Sep28
PASS		
404	r400sc_tri_walk_start_vertex_10	2002Sep28

PASS		
405	r400sc_tri_walk_start_vertex_11	2002Sep28
PASS		
406	r400sc_tri_walk_start_vertex_12	2002Sep28
PASS		
407	r400sc_tri_walk_start_vertex_13	2002Sep28
PASS		
408	r400sc_tri_walk_start_vertex_14	2002Sep28
PASS		
409	r400sc_tri_walk_start_vertex_15	2002Sep28
PASS		
410	r400sc_tri_walk_start_vertex_16	2002Sep28
PASS		
411	r400sc_triangle_stipple_01	2002Sep28
PASS		
412	r400sc_window_offset_01	2002Sep28
PASS		
413	r400sc_window_offset_fc_01	2002Sep28
PASS		
414	r400sc_zbuffer_line_list_01	2002Sep28
PASS		
415	r400sc_zbuffer_point_list_01	2002Sep28
PASS		
416	r400sc_zbuffer_rectangle_list_01	2002Sep28
PASS		
417	r400sc_zbuffer_rectangle_list_02	2002Sep28
PASS		
418	r400sc_zbuffer_rectangle_list_fc_02	2002Sep28
PASS		
419	r400sc_zbuffer_triangle_list_01	2002Sep28
PASS		
420	r400su_4tri_text_offscreen_01	2002Sep28
PASS		
421	r400su_baryc_test_01	2002Sep28
PASS		
422	r400su_baryc_test_02	2002Sep28
PASS		
423	r400su_baryc_test_03	2002Sep28
PASS		
424	r400su_baryc_test_04	2002Sep28
PASS		
425	r400su_clip_baryc_test_01	2002Sep28
PASS		
426	r400su_clip_baryc_test_02	2002Sep28
PASS		
427	r400su_clip_edgeflag_polymode_01	2002Sep28
PASS		
428	r400su_clip_pointsize_test_01	2002Sep28

PASS		
429	r400su_clip_pointttest_01	2002Sep28
PASS		
430	r400su_clip_pointttest_02	2002Sep28
PASS		
431	r400su_clip_pointttest_03	2002Sep28
PASS		
432	r400su_clip_pointttest_04	2002Sep28
PASS		
433	r400su_clip_polymode_test_01	2002Sep28
PASS		
434	r400su_clipline_01	2002Sep28
PASS		
435	r400su_clippoint_01	2002Sep28
PASS		
436	r400su_clipvertexsortingfunctional_01	2002Sep28
PASS		
437	r400su_cullingfunctional_01	2002Sep28
PASS		
438	r400su_degentri_test_01	2002Sep28
PASS		
439	r400su_degentri_test_02	2002Sep28
PASS		
440	r400su_degentri_test_03	2002Sep28
PASS		
441	r400su_edge_flag_01	2002Sep28
PASS		
442	r400su_edge_flag_02	2002Sep28
PASS		
443	r400su_edgeflags_triangle_01	2002Sep28
PASS		
444	r400su_edgeflags_triangle_02	2002Sep28
PASS		
445	r400su_geom_sort_01	2002Sep28
PASS		
446	r400su_line_end_cap_functional_01	2002Sep28
PASS		
447	r400su_line_end_cap_width_functional_02	2002Sep28
PASS		
448	r400su_line_test_01	2002Sep28
PASS		
449	r400su_line_test_02	2002Sep28
PASS		
450	r400su_longstrip_01	2002Sep28
PASS		
451	r400su_multi_context_01	2002Sep28
PASS		
452	r400su_multi_prim_01	2002Sep28

PASS		
453	r400su_multi_prim_02	2002Sep28
PASS		
454	r400su_parallel_orientation_all_01	2002Sep28
PASS		
455	r400su_parallel_orientation_all_02	2002Sep28
PASS		
456	r400su_pc_management_01	2002Sep28
PASS		
457	r400su_pc_management_02	2002Sep28
PASS		
458	r400su_pc_management_03	2002Sep28
PASS		
459	r400su_point_sprite_01	2002Sep28
PASS		
460	r400su_point_sprite_02	2002Sep28
PASS		
461	r400su_point_sprite_03	2002Sep28
PASS		
462	r400su_point_sprite_04	2002Sep28
PASS		
463	r400su_point_sprite_05	2002Sep28
PASS		
464	r400su_point_sprite_06	2002Sep28
PASS		
465	r400su_point_sprite_07	2002Sep28
PASS		
466	r400su_point_sprite_08	2002Sep28
PASS		
467	r400su_point_w16_h1_functional_01	2002Sep28
PASS		
468	r400su_point_w1_h16_functional_01	2002Sep28
PASS		
469	r400su_pointsizepresent_01	2002Sep28
PASS		
470	r400su_pointsizepresent_02	2002Sep28
PASS		
471	r400su_polymode_lines_degen_triangle_01	2002Sep28
PASS		
472	r400su_polymode_lines_degen_triangle_02	2002Sep28
PASS		
473	r400su_polymode_lines_zero_area_triangle_01	2002Sep28
PASS		
474	r400su_polymode_lines_zero_area_triangle_02	2002Sep28
PASS		
475	r400su_polymode_points_degen_triangle_01	2002Sep28
PASS		
476	r400su_polymode_points_degen_triangle_02	2002Sep28

PASS		
477	r400su_polymode_points_zero_area_triangle_01	2002Sep28
PASS		
478	r400su_polymode_points_zero_area_triangle_02	2002Sep28
PASS		
479	r400su_polymode_rectangle_01	2002Sep28
PASS		
480	r400su_polymode_zero_area_triangle_01	2002Sep28
PASS		
481	r400su_polymode_zero_area_triangle_02	2002Sep28
PASS		
482	r400su_polymode_zero_area_triangle_03	2002Sep28
PASS		
483	r400su_polymode_zero_area_triangle_04	2002Sep28
PASS		
484	r400su_polymodeculling_01	2002Sep28
PASS		
485	r400su_polymodefunctional_01	2002Sep28
PASS		
486	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep28
PASS		
487	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep28
PASS		
488	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep28
PASS		
489	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep28
PASS		
490	r400su_provoking_vtx_line_01	2002Sep28
PASS		
491	r400su_provoking_vtx_point_01	2002Sep28
PASS		
492	r400su_provoking_vtx_polymode_rectangle_01	2002Sep28
PASS		
493	r400su_provoking_vtx_rectangle_01	2002Sep28
PASS		
494	r400su_provoking_vtx_triangle_01	2002Sep28
PASS		
495	r400su_rand_line_01	2002Sep28
PASS		
496	r400su_rand_point_01	2002Sep28
PASS		
497	r400su_rand_tri_01	2002Sep28
PASS		
498	r400su_rectangle_list_01	2002Sep28
PASS		
499	r400su_sliver_01	2002Sep28
PASS		
500	r400su_triarea_test_01	2002Sep28

PASS		
501	r400su_triarea_test_02	2002Sep28
PASS		
502	r400su_triarea_test_03	2002Sep28
PASS		
503	r400su_triarea_test_04	2002Sep28
PASS		
504	r400su_vertexsortingfunctional_01	2002Sep28
PASS		
505	r400su_w_grad_test_01	2002Sep28
PASS		
506	r400su_w_grad_test_02	2002Sep28
PASS		
507	r400su_w_grad_test_03	2002Sep28
PASS		
508	r400su_z_grad_test_01	2002Sep28
PASS		
509	r400su_z_grad_test_02	2002Sep28
PASS		
510	r400su_z_grad_test_03	2002Sep28
PASS		
511	r400vgt_auto_index_line_list_01	2002Sep28
PASS		
512	r400vgt_auto_index_line_loop_01	2002Sep28
PASS		
513	r400vgt_auto_index_line_strip_01	2002Sep28
PASS		
514	r400vgt_auto_index_points_01	2002Sep28
PASS		
515	r400vgt_auto_index_polygon_01	2002Sep28
PASS		
516	r400vgt_auto_index_primitives_01	2002Sep28
PASS		
517	r400vgt_auto_index_quad_list_01	2002Sep28
PASS		
518	r400vgt_auto_index_quad_strip_01	2002Sep28
PASS		
519	r400vgt_auto_index_rectangle_list_01	2002Sep28
PASS		
520	r400vgt_auto_index_tri_fan_01	2002Sep28
PASS		
521	r400vgt_auto_index_tri_list_01	2002Sep28
PASS		
522	r400vgt_auto_index_tri_strip_01	2002Sep28
PASS		
523	r400vgt_auto_index_tri_wflags_01	2002Sep28
PASS		
524	r400vgt_dma_engine_01	2002Sep28

PASS		
525	r400vgt_dma_engine_02	2002Sep28
PASS		
526	r400vgt_dma_engine_03	2002Sep28
PASS		
527	r400vgt_dma_engine_04	2002Sep28
PASS		
528	r400vgt_dma_engine_05	2002Sep28
PASS		
529	r400vgt_dma_engine_06	2002Sep28
PASS		
530	r400vgt_dma_engine_07	2002Sep28
PASS		
531	r400vgt_dma_engine_08	2002Sep28
PASS		
532	r400vgt_dma_engine_09	2002Sep28
PASS		
533	r400vgt_dma_engine_10	2002Sep28
PASS		
534	r400vgt_dma_index_line_list_01	2002Sep28
PASS		
535	r400vgt_dma_index_line_loop_01	2002Sep28
PASS		
536	r400vgt_dma_index_line_strip_01	2002Sep28
PASS		
537	r400vgt_dma_index_points_01	2002Sep28
PASS		
538	r400vgt_dma_index_polygon_01	2002Sep28
PASS		
539	r400vgt_dma_index_primitives_01	2002Sep28
PASS		
540	r400vgt_dma_index_quad_list_01	2002Sep28
PASS		
541	r400vgt_dma_index_quad_strip_01	2002Sep28
PASS		
542	r400vgt_dma_index_rectangle_list_01	2002Sep28
PASS		
543	r400vgt_dma_index_tri_fan_01	2002Sep28
PASS		
544	r400vgt_dma_index_tri_list_01	2002Sep28
PASS		
545	r400vgt_dma_index_tri_strip_01	2002Sep28
PASS		
546	r400vgt_dma_index_tri_wflags_01	2002Sep28
PASS		
547	r400vgt_dma_swap_idx16_01	2002Sep28
PASS		
548	r400vgt_dma_swap_idx32_01	2002Sep28

PASS		
549	r400vgt_edgeflags_polygon_01	2002Sep28
PASS		
550	r400vgt_edgeflags_quad_list_01	2002Sep28
PASS		
551	r400vgt_edgeflags_quad_strip_01	2002Sep28
PASS		
552	r400vgt_edgeflags_triangle_fan_01	2002Sep28
PASS		
553	r400vgt_edgeflags_triangle_list_01	2002Sep28
PASS		
554	r400vgt_edgeflags_triangle_strip_01	2002Sep28
PASS		
555	r400vgt_edgeflags_triangle_wflags_01	2002Sep28
PASS		
556	r400vgt_event_handling_01	2002Sep26
PASS		
557	r400vgt_ext2int_index_line_list_01	2002Sep28
PASS		
558	r400vgt_ext2int_index_line_loop_01	2002Sep28
PASS		
559	r400vgt_ext2int_index_line_strip_01	2002Sep28
PASS		
560	r400vgt_ext2int_index_points_01	2002Sep28
PASS		
561	r400vgt_ext2int_index_polygon_01	2002Sep28
PASS		
562	r400vgt_ext2int_index_quad_list_01	2002Sep28
PASS		
563	r400vgt_ext2int_index_quad_strip_01	2002Sep28
PASS		
564	r400vgt_ext2int_index_rectangle_list_01	2002Sep28
PASS		
565	r400vgt_ext2int_index_triangle_fan_01	2002Sep28
PASS		
566	r400vgt_ext2int_index_triangle_list_01	2002Sep28
PASS		
567	r400vgt_ext2int_index_triangle_strip_01	2002Sep28
PASS		
568	r400vgt_ext2int_index_triangle_wflags_01	2002Sep28
PASS		
569	r400vgt_hos_LPatch_01	2002Sep28
PASS		
570	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep28
PASS		
571	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep28
PASS		
572	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep28

PASS		
573	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep28
PASS		
574	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep28
PASS		
575	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep28
PASS		
576	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep28
PASS		
577	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep28
PASS		
578	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep28
PASS		
579	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep28
PASS		
580	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep28
PASS		
581	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep28
PASS		
582	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep28
PASS		
583	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep28
PASS		
584	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep28
PASS		
585	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep28
PASS		
586	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep28
PASS		
587	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep28
PASS		
588	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep28
PASS		
589	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep28
PASS		
590	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep28
PASS		
591	r400vgt_hos_PNT_edge_detection_01	2002Sep28
PASS		
592	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep28
PASS		
593	r400vgt_hos_RPatch_cp_02	2002Sep28
PASS		
594	r400vgt_hos_RPatch_lp_02	2002Sep28
PASS		
595	r400vgt_hos_TPatch_01	2002Sep28
INCOMPLETE		
596	r400vgt_hos_TPatch_02	2002Sep28

INCOMPLETE

597	r400vgt_hos_auto_index_line_list_01	2002Sep28
	PASS	
598	r400vgt_hos_auto_index_quad_list_01	2002Sep28
	PASS	
599	r400vgt_hos_auto_index_triangle_list_01	2002Sep28
	PASS	
600	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep28
	PASS	
601	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep28
	PASS	
602	r400vgt_immed_index_line_list_01	2002Sep28
	PASS	
603	r400vgt_immed_index_line_loop_01	2002Sep28
	PASS	
604	r400vgt_immed_index_line_strip_01	2002Sep28
	PASS	
605	r400vgt_immed_index_points_01	2002Sep28
	PASS	
606	r400vgt_immed_index_polygon_01	2002Sep28
	PASS	
607	r400vgt_immed_index_primitives_01	2002Sep28
	PASS	
608	r400vgt_immed_index_quad_list_01	2002Sep28
	PASS	
609	r400vgt_immed_index_quad_strip_01	2002Sep28
	PASS	
610	r400vgt_immed_index_rectangle_list_01	2002Sep28
	PASS	
611	r400vgt_immed_index_tri_fan_01	2002Sep28
	PASS	
612	r400vgt_immed_index_tri_list_01	2002Sep28
	PASS	
613	r400vgt_immed_index_tri_strip_01	2002Sep28
	PASS	
614	r400vgt_immed_index_tri_wflags_01	2002Sep28
	PASS	
615	r400vgt_index_dealloc_line_list_01	2002Sep28
	PASS	
616	r400vgt_index_dealloc_points_01	2002Sep28
	PASS	
617	r400vgt_index_dealloc_triangle_list_01	2002Sep28
	PASS	
618	r400vgt_index_min_max_01	2002Sep28
	PASS	
619	r400vgt_index_min_max_02	2002Sep28
	PASS	
620	r400vgt_index_offset_01	2002Sep28

PASS		
621	r400vgt_index_offset_02	2002Sep28
PASS		
622	r400vgt_index_offset_03	2002Sep28
PASS		
623	r400vgt_index_size_01	2002Sep28
PASS		
624	r400vgt_index_source_switch_01	2002Sep28
PASS		
625	r400vgt_line_list_01	2002Sep28
PASS		
626	r400vgt_line_list_02	2002Sep28
PASS		
627	r400vgt_line_loop_01	2002Sep28
PASS		
628	r400vgt_line_loop_02	2002Sep28
PASS		
629	r400vgt_line_strip_01	2002Sep28
PASS		
630	r400vgt_line_strip_02	2002Sep28
PASS		
631	r400vgt_multi_context_01	2002Sep28
PASS		
632	r400vgt_multi_context_02	2002Sep28
PASS		
633	r400vgt_multi_context_03	2002Sep28
PASS		
634	r400vgt_multi_context_04	2002Sep28
PASS		
635	r400vgt_multi_context_05	2002Sep28
PASS		
636	r400vgt_multi_context_06	2002Sep28
PASS		
637	r400vgt_multi_context_07	2002Sep28
PASS		
638	r400vgt_multi_context_08	2002Sep28
PASS		
639	r400vgt_multi_context_09	2002Sep28
PASS		
640	r400vgt_multi_context_10	2002Sep28
PASS		
641	r400vgt_multi_context_11	2002Sep28
PASS		
642	r400vgt_multi_context_12	2002Sep28
PASS		
643	r400vgt_multi_prim_reset_index_all_01	2002Sep28
PASS		
644	r400vgt_multi_prim_reset_index_all_02	2002Sep28

PASS		
645	r400vgt_pass_thru_all_prims_01	2002Sep28
PASS		
646	r400vgt_point_list_01	2002Sep28
PASS		
647	r400vgt_point_list_02	2002Sep28
PASS		
648	r400vgt_polygon_01	2002Sep28
PASS		
649	r400vgt_polygon_02	2002Sep28
PASS		
650	r400vgt_provoking_vtx_all_01	2002Sep28
PASS		
651	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep28
PASS		
652	r400vgt_provoking_vtx_polygon_01	2002Sep28
PASS		
653	r400vgt_provoking_vtx_quad_list_01	2002Sep28
PASS		
654	r400vgt_provoking_vtx_quad_strip_01	2002Sep28
PASS		
655	r400vgt_provoking_vtx_tri_fan_01	2002Sep28
PASS		
656	r400vgt_provoking_vtx_tri_strip_01	2002Sep28
PASS		
657	r400vgt_quad_list_01	2002Sep28
PASS		
658	r400vgt_quad_list_02	2002Sep28
PASS		
659	r400vgt_quad_strip_01	2002Sep28
PASS		
660	r400vgt_quad_strip_02	2002Sep28
PASS		
661	r400vgt_rectangle_list_01	2002Sep28
PASS		
662	r400vgt_rectangle_list_02	2002Sep28
PASS		
663	r400vgt_reuse_depth_line_list_01	2002Sep28
PASS		
664	r400vgt_reuse_depth_point_list_01	2002Sep28
PASS		
665	r400vgt_reuse_depth_triangle_list_01	2002Sep28
PASS		
666	r400vgt_reuse_index_line_list_01	2002Sep28
PASS		
667	r400vgt_reuse_index_point_list_01	2002Sep28
PASS		
668	r400vgt_reuse_index_triangle_list_01	2002Sep28

PASS			
669	r400vgt_reuse_index_triangle_list_02		2002Sep28
PASS			
670	r400vgt_reuse_index_triangle_list_03		2002Sep28
PASS			
671	r400vgt_suppress_eop_01	0:0:1	2002Sep28
PASS			
672	r400vgt_suppress_eop_02	0:0:1	2002Sep28
PASS			
673	r400vgt_suppress_eop_03	0:0:1	2002Sep28
PASS			
674	r400vgt_suppress_eop_04	0:0:1	2002Sep28
PASS			
675	r400vgt_suppress_eop_05	0:0:1	2002Sep26
PASS			
676	r400vgt_triangle_fan_01	0:0:1	2002Sep28
PASS			
677	r400vgt_triangle_fan_02	0:0:1	2002Sep28
PASS			
678	r400vgt_triangle_list_01	0:0:1	2002Sep28
PASS			
679	r400vgt_triangle_list_02	0:0:1	2002Sep28
PASS			
680	r400vgt_triangle_strip_01	0:0:1	2002Sep28
PASS			
681	r400vgt_triangle_strip_02	0:0:1	2002Sep28
PASS			
682	r400vgt_triangle_wflags_01	0:0:1	2002Sep28
PASS			
683	r400vgt_triangle_wflags_02	0:0:1	2002Sep28
PASS			
684	r400vgt_viz_query_01	0:0:1	2002Sep28
PASS			
685	r400vgt_vtx_vector_packing_01	0:0:1	2002Sep28
PASS			
686	r400vte_coverage_01	0:0:1	2002Sep28
PASS			
687	r400vte_simple_point_01	0:0:1	2002Sep28
PASS			
688	r400vte_simple_triangle_01	0:0:1	2002Sep28
PASS			
689	r400vte_w0_fmt_01	0:0:1	2002Sep28
PASS			
690	r400vte_w0_fmt_03	0:0:1	2002Sep28
PASS			
691	r400vte_xy_fmt_01	0:0:1	2002Sep28
PASS			
692	r400vte_xy_fmt_mk_01_bug	0:0:1	2002Sep28

PASS
693 r400vte_xyz_scale_01 0:0:1 2002Sep28
PASS
694 r400vte_xyz_scale_02 0:0:1 2002Sep28
PASS
695 r400vte_z_fmt_01 0:0:1 2002Sep28
PASS

=====
Tests_Run_count = 695 Pass_count = 693 Fail_count = 0 Incomplete_count = 2
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Sep 14 11:04:43 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          157          157          150          95
              SC           241          241          238          98
              CL           110          110          110          100
              VTE           9           9           9           100
SPECIALTY    SU           85           85           85          100
              SANITY       2           2           2           100
+++++
              TOTAL        610          610          600          98
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep12
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep12
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep12
PASS			
4	primlib_parameterized_simple_triangle		2002Sep12
PASS			
5	primlib_template_simple_triangle		2002Sep12
PASS			
6	primlib_tex_tri		2002Sep12
PASS			
7	r400cl_clip_10_verts_01		2002Sep12
PASS			
8	r400cl_clip_disable_01		2002Sep12
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep12
PASS			
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345	r400sc_tri_walk_start_vertex_14	2002Sep12
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346	r400sc_tri_walk_start_vertex_15	2002Sep12
PASS		
347	r400sc_tri_walk_start_vertex_16	2002Sep12
PASS		
348	r400sc_window_offset_01	2002Sep12
PASS		
349	r400sc_window_offset_fc_01	2002Sep12
PASS		
350	r400sc_zbuffer_line_list_01	2002Sep12
PASS		
351	r400sc_zbuffer_point_list_01	2002Sep12
PASS		
352	r400sc_zbuffer_rectangle_list_01	2002Sep12
PASS		
353	r400sc_zbuffer_rectangle_list_02	2002Sep12
PASS		
354	r400sc_zbuffer_rectangle_list_fc_02	2002Sep12
PASS		
355	r400sc_zbuffer_triangle_list_01	2002Sep12
PASS		
356	r400su_4tri_text_offscreen_01	2002Sep12

PASS		
357	r400su_baryc_test_01	2002Sep12
PASS		
358	r400su_baryc_test_02	2002Sep12
PASS		
359	r400su_baryc_test_03	2002Sep12
PASS		
360	r400su_baryc_test_04	2002Sep12
PASS		
361	r400su_clip_baryc_test_01	2002Sep12
PASS		
362	r400su_clip_baryc_test_02	2002Sep12
PASS		
363	r400su_clip_edgeflag_polymode_01	2002Sep12
PASS		
364	r400su_clipline_01	2002Sep12
PASS		
365	r400su_clippoint_01	2002Sep12
PASS		
366	r400su_clip_pointsize_test_01	2002Sep12
PASS		
367	r400su_clip_pointtest_01	2002Sep12
PASS		
368	r400su_clip_pointtest_02	2002Sep12
PASS		
369	r400su_clip_pointtest_03	2002Sep12
PASS		
370	r400su_clip_pointtest_04	2002Sep12
PASS		
371	r400su_clip_polymode_test_01	2002Sep12
PASS		
372	r400su_clipvertexsortingfunctional_01	2002Sep12
PASS		
373	r400su_cullingfunctional_01	2002Sep12
PASS		
374	r400su_degentri_test_01	2002Sep12
PASS		
375	r400su_degentri_test_02	2002Sep12
PASS		
376	r400su_degentri_test_03	2002Sep12
PASS		
377	r400su_edge_flag_01	2002Sep12
PASS		
378	r400su_edge_flag_02	2002Sep12
PASS		
379	r400su_edgeflags_triangle_01	2002Sep12
PASS		
380	r400su_edgeflags_triangle_02	2002Sep12

PASS		
381	r400su_geom_sort_01	2002Sep12
PASS		
382	r400su_line_end_cap_functional_01	2002Sep12
PASS		
383	r400su_line_end_cap_width_functional_02	2002Sep12
PASS		
384	r400su_line_test_01	2002Sep12
PASS		
385	r400su_line_test_02	2002Sep12
PASS		
386	r400su_longstrip_01	2002Sep12
PASS		
387	r400su_multi_context_01	2002Sep12
PASS		
388	r400su_multi_prim_01	2002Sep12
PASS		
389	r400su_multi_prim_02	2002Sep12
PASS		
390	r400su_parallel_orientation_all_01	2002Sep12
PASS		
391	r400su_parallel_orientation_all_02	2002Sep12
PASS		
392	r400su_pc_management_01	2002Sep12
PASS		
393	r400su_pc_management_02	2002Sep12
PASS		
394	r400su_pc_management_03	2002Sep12
PASS		
395	r400su_pointsizepresent_01	2002Sep12
PASS		
396	r400su_pointsizepresent_02	2002Sep12
PASS		
397	r400su_point_sprite_01	2002Sep12
PASS		
398	r400su_point_sprite_02	2002Sep12
PASS		
399	r400su_point_sprite_03	2002Sep12
PASS		
400	r400su_point_sprite_04	2002Sep12
PASS		
401	r400su_point_sprite_05	2002Sep12
PASS		
402	r400su_point_sprite_06	2002Sep12
PASS		
403	r400su_point_sprite_07	2002Sep12
PASS		
404	r400su_point_sprite_08	2002Sep12

PASS		
405	r400su_point_w16_h1_functional_01	2002Sep12
PASS		
406	r400su_point_w1_h16_functional_01	2002Sep12
PASS		
407	r400su_polymodeculling_01	2002Sep12
PASS		
408	r400su_polymodefunctional_01	2002Sep12
PASS		
409	r400su_polymode_lines_degen_triangle_01	2002Sep12
PASS		
410	r400su_polymode_lines_degen_triangle_02	2002Sep12
PASS		
411	r400su_polymode_lines_zero_area_triangle_01	2002Sep12
PASS		
412	r400su_polymode_lines_zero_area_triangle_02	2002Sep12
PASS		
413	r400su_polymode_points_degen_triangle_01	2002Sep12
PASS		
414	r400su_polymode_points_degen_triangle_02	2002Sep12
PASS		
415	r400su_polymode_points_zero_area_triangle_01	2002Sep12
PASS		
416	r400su_polymode_points_zero_area_triangle_02	2002Sep12
PASS		
417	r400su_polymode_rectangle_01	2002Sep12
PASS		
418	r400su_polymode_zero_area_triangle_01	2002Sep12
PASS		
419	r400su_polymode_zero_area_triangle_02	2002Sep12
PASS		
420	r400su_polymode_zero_area_triangle_03	2002Sep12
PASS		
421	r400su_polymode_zero_area_triangle_04	2002Sep12
PASS		
422	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep12
PASS		
423	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep12
PASS		
424	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep12
PASS		
425	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep12
PASS		
426	r400su_provoking_vtx_line_01	2002Sep12
PASS		
427	r400su_provoking_vtx_point_01	2002Sep12
PASS		
428	r400su_provoking_vtx_polymode_rectangle_01	2002Sep12

PASS		
429	r400su_provoking_vtx_rectangle_01	2002Sep12
PASS		
430	r400su_provoking_vtx_triangle_01	2002Sep12
PASS		
431	r400su_rand_line_01	2002Sep12
PASS		
432	r400su_rand_point_01	2002Sep12
PASS		
433	r400su_rand_tri_01	2002Sep12
PASS		
434	r400su_rectangle_list_01	2002Sep12
PASS		
435	r400su_sliver_01	2002Sep12
PASS		
436	r400su_triarea_test_01	2002Sep12
PASS		
437	r400su_triarea_test_02	2002Sep12
PASS		
438	r400su_triarea_test_03	2002Sep12
PASS		
439	r400su_triarea_test_04	2002Sep12
PASS		
440	r400su_vertexsortingfunctional_01	2002Sep12
PASS		
441	r400vgt_auto_index_line_list_01	2002Sep12
PASS		
442	r400vgt_auto_index_line_loop_01	2002Sep12
PASS		
443	r400vgt_auto_index_line_strip_01	2002Sep12
PASS		
444	r400vgt_auto_index_points_01	2002Sep12
PASS		
445	r400vgt_auto_index_polygon_01	2002Sep12
PASS		
446	r400vgt_auto_index_primitives_01	2002Sep12
PASS		
447	r400vgt_auto_index_quad_list_01	2002Sep12
PASS		
448	r400vgt_auto_index_quad_strip_01	2002Sep12
PASS		
449	r400vgt_auto_index_rectangle_list_01	2002Sep12
PASS		
450	r400vgt_auto_index_tri_fan_01	2002Sep12
PASS		
451	r400vgt_auto_index_tri_list_01	2002Sep12
PASS		
452	r400vgt_auto_index_tri_strip_01	2002Sep12

PASS		
453	r400vgt_auto_index_tri_wflags_01	2002Sep12
PASS		
454	r400vgt_dma_engine_01	2002Sep12
PASS		
455	r400vgt_dma_engine_02	2002Sep12
PASS		
456	r400vgt_dma_engine_03	2002Sep12
PASS		
457	r400vgt_dma_engine_04	2002Sep12
PASS		
458	r400vgt_dma_engine_05	2002Sep12
PASS		
459	r400vgt_dma_engine_06	2002Sep12
PASS		
460	r400vgt_dma_engine_07	2002Sep12
PASS		
461	r400vgt_dma_engine_08	2002Sep12
PASS		
462	r400vgt_dma_engine_09	2002Sep12
PASS		
463	r400vgt_dma_engine_10	2002Sep12
PASS		
464	r400vgt_dma_index_line_list_01	2002Sep12
PASS		
465	r400vgt_dma_index_line_loop_01	2002Sep12
PASS		
466	r400vgt_dma_index_line_strip_01	2002Sep12
PASS		
467	r400vgt_dma_index_points_01	2002Sep12
PASS		
468	r400vgt_dma_index_polygon_01	2002Sep12
PASS		
469	r400vgt_dma_index_primitives_01	2002Sep12
PASS		
470	r400vgt_dma_index_quad_list_01	2002Sep12
PASS		
471	r400vgt_dma_index_quad_strip_01	2002Sep12
PASS		
472	r400vgt_dma_index_rectangle_list_01	2002Sep12
PASS		
473	r400vgt_dma_index_tri_fan_01	2002Sep12
PASS		
474	r400vgt_dma_index_tri_list_01	2002Sep12
PASS		
475	r400vgt_dma_index_tri_strip_01	2002Sep12
PASS		
476	r400vgt_dma_index_tri_wflags_01	2002Sep12

PASS		
477	r400vgt_dma_swap_idx16_01	2002Sep12
PASS		
478	r400vgt_dma_swap_idx32_01	2002Sep12
PASS		
479	r400vgt_edgeflags_polygon_01	2002Sep12
PASS		
480	r400vgt_edgeflags_quad_list_01	2002Sep12
PASS		
481	r400vgt_edgeflags_quad_strip_01	2002Sep12
PASS		
482	r400vgt_edgeflags_triangle_fan_01	2002Sep12
PASS		
483	r400vgt_edgeflags_triangle_list_01	2002Sep12
PASS		
484	r400vgt_edgeflags_triangle_strip_01	2002Sep12
PASS		
485	r400vgt_edgeflags_triangle_wflags_01	2002Sep12
PASS		
486	r400vgt_ext2int_index_line_list_01	2002Sep12
PASS		
487	r400vgt_ext2int_index_line_loop_01	2002Sep12
PASS		
488	r400vgt_ext2int_index_line_strip_01	2002Sep12
PASS		
489	r400vgt_ext2int_index_points_01	2002Sep12
PASS		
490	r400vgt_ext2int_index_polygon_01	2002Sep12
PASS		
491	r400vgt_ext2int_index_quad_list_01	2002Sep12
PASS		
492	r400vgt_ext2int_index_quad_strip_01	2002Sep12
PASS		
493	r400vgt_ext2int_index_triangle_fan_01	2002Sep12
PASS		
494	r400vgt_ext2int_index_triangle_list_01	2002Sep12
PASS		
495	r400vgt_ext2int_index_triangle_strip_01	2002Sep12
PASS		
496	r400vgt_hos_auto_index_line_list_01	2002Sep12
PASS		
497	r400vgt_hos_auto_index_quad_list_01	2002Sep12
PASS		
498	r400vgt_hos_auto_index_triangle_list_01	2002Sep12
PASS		
499	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep12
PASS		
500	r400vgt_hos_LPatch_01	2002Sep12

PASS		
501	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep12
PASS		
502	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep12
PASS		
503	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep12
PASS		
504	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep12
PASS		
505	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep12
PASS		
506	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep12
PASS		
507	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep12
PASS		
508	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep12
PASS		
509	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep12
PASS		
510	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep12
PASS		
511	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep12
PASS		
512	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep12
PASS		
513	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep12
PASS		
514	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep12
PASS		
515	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep12
PASS		
516	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep12
PASS		
517	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep12
PASS		
518	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep12
PASS		
519	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep12
PASS		
520	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep12
PASS		
521	r400vgt_hos_PNT_edge_detection_01	2002Sep12
PASS		
522	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep12
PASS		
523	r400vgt_hos_RPatch_cp_02	2002Sep12
PASS		
524	r400vgt_hos_RPatch_lp_02	2002Sep12

PASS		
525	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep12
PASS		
526	r400vgt_hos_TPatch_01	2002Sep11
INCOMPLETE		
527	r400vgt_hos_TPatch_02	2002Sep11
INCOMPLETE		
528	r400vgt_immed_index_line_list_01	2002Sep12
PASS		
529	r400vgt_immed_index_line_loop_01	2002Sep12
PASS		
530	r400vgt_immed_index_line_strip_01	2002Sep12
PASS		
531	r400vgt_immed_index_points_01	2002Sep12
PASS		
532	r400vgt_immed_index_polygon_01	2002Sep12
PASS		
533	r400vgt_immed_index_primitives_01	2002Sep12
PASS		
534	r400vgt_immed_index_quad_list_01	2002Sep12
PASS		
535	r400vgt_immed_index_quad_strip_01	2002Sep12
PASS		
536	r400vgt_immed_index_rectangle_list_01	2002Sep12
PASS		
537	r400vgt_immed_index_tri_fan_01	2002Sep12
PASS		
538	r400vgt_immed_index_tri_list_01	2002Sep12
PASS		
539	r400vgt_immed_index_tri_strip_01	2002Sep12
PASS		
540	r400vgt_immed_index_tri_wflags_01	2002Sep12
PASS		
541	r400vgt_index_dealloc_line_list_01	2002Sep12
PASS		
542	r400vgt_index_dealloc_points_01	2002Sep12
PASS		
543	r400vgt_index_dealloc_triangle_list_01	2002Sep12
PASS		
544	r400vgt_index_min_max_01	2002Sep12
PASS		
545	r400vgt_index_min_max_02	2002Sep12
PASS		
546	r400vgt_index_offset_01	2002Sep12
PASS		
547	r400vgt_index_offset_02	2002Sep12
PASS		
548	r400vgt_index_offset_03	2002Sep12

PASS		
549	r400vgt_index_size_01	2002Sep12
PASS		
550	r400vgt_index_source_switch_01	2002Sep12
PASS		
551	r400vgt_line_list_01	2002Sep12
PASS		
552	r400vgt_line_loop_01	2002Sep12
PASS		
553	r400vgt_line_strip_01	2002Sep12
PASS		
554	r400vgt_multi_context_01	2002Sep12
PASS		
555	r400vgt_multi_context_02	2002Sep12
PASS		
556	r400vgt_multi_context_03	2002Sep12
PASS		
557	r400vgt_multi_context_04	2002Sep12
PASS		
558	r400vgt_multi_context_05	2002Sep12
PASS		
559	r400vgt_multi_context_06	2002Sep12
PASS		
560	r400vgt_multi_context_07	2002Sep12
PASS		
561	r400vgt_multi_context_08	2002Sep12
PASS		
562	r400vgt_multi_context_09	2002Sep12
PASS		
563	r400vgt_multi_context_10	2002Sep12
PASS		
564	r400vgt_multi_context_11	2002Sep12
PASS		
565	r400vgt_multi_context_12	2002Sep12
PASS		
566	r400vgt_pass_thru_all_prims_01	2002Sep12
PASS		
567	r400vgt_point_list_01	2002Sep12
PASS		
568	r400vgt_polygon_01	2002Sep12
PASS		
569	r400vgt_provoking_vtx_all_01	2002Sep12
PASS		
570	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep12
PASS		
571	r400vgt_quad_list_01	2002Sep12
PASS		
572	r400vgt_quad_strip_01	2002Sep12

PASS		
573	r400vgt_rectangle_list_01	2002Sep12
PASS		
574	r400vgt_reuse_depth_line_list_01	2002Sep12
PASS		
575	r400vgt_reuse_depth_point_list_01	2002Sep12
PASS		
576	r400vgt_reuse_depth_triangle_list_01	2002Sep12
PASS		
577	r400vgt_reuse_index_line_list_01	2002Sep12
PASS		
578	r400vgt_reuse_index_point_list_01	2002Sep12
PASS		
579	r400vgt_reuse_index_triangle_list_01	2002Sep12
PASS		
580	r400vgt_reuse_index_triangle_list_02	2002Sep12
PASS		
581	r400vgt_reuse_index_triangle_list_03	2002Sep12
PASS		
582	r400vgt_suppress_eop_01	2002Sep12
PASS		
583	r400vgt_suppress_eop_02	2002Sep12
PASS		
584	r400vgt_suppress_eop_03	2002Sep12
PASS		
585	r400vgt_suppress_eop_04	2002Sep12
PASS		
586	r400vgt_triangle_fan_01	2002Sep12
PASS		
587	r400vgt_triangle_list_01	2002Sep12
PASS		
588	r400vgt_triangle_strip_01	2002Sep12
PASS		
589	r400vgt_triangle_wflags_01	2002Sep12
PASS		
590	r400vgt_viz_query_01	2002Sep12
PASS		
591	r400vgt_vtx_vector_packing_01	2002Sep12
PASS		
592	r400vte_coverage_01	2002Sep12
PASS		
593	r400vte_simple_point_01	2002Sep12
PASS		
594	r400vte_simple_triangle_01	2002Sep12
PASS		
595	r400vte_w0_fmt_01	2002Sep12
PASS		
596	r400vte_w0_fmt_03	2002Sep12

PASS
597 r400vte_xy_fmt_01 2002Sep12
PASS
598 r400vte_xyz_scale_01 2002Sep12
PASS
599 r400vte_xyz_scale_02 2002Sep12
PASS
600 r400vte_z_fmt_01 2002Sep12
PASS

=====
Tests_Run_count = 600 Pass_count = 597 Fail_count = 0 Incomplete_count = 3
=====


```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Oct 15 21:58:14 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP          50          50          50          100
              RBBM        1           1           1           100
              VGT         186         186         186          100
              SC          328         328         319           97
              CL          189         189         188           99
              VTE         10           10           10           100
              SPECIALTY    6           6           6           100
              PARAMETER    62          62          62           100
              SU          111         111         111           100
              SANITY       2           2           2           100
+++++
              TOTAL          945         945         935           98
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc	0:0:3	2002Oct13
PASS			
2	cp_dma_interrupt	0:0:2	2002Oct13
PASS			
3	cp_dma_m2m_01	0:0:1	2002Oct13
PASS			
4	cp_dma_m2m_02	0:0:1	2002Oct13
PASS			
5	cp_dma_m2m_03	0:0:1	2002Oct13
PASS			
6	cp_dma_m2m_04	0:0:1	2002Oct13
PASS			
7	cp_dma_m2r_01	0:0:1	2002Oct13
PASS			
8	cp_dma_m2r_02	0:0:2	2002Oct13
PASS			
9	cp_dma_m2r_03	0:0:1	2002Oct13
PASS			
10	cp_dma_m2r_04	0:0:1	2002Oct13
PASS			
11	cp_dma_m2r_r2m	0:0:1	2002Oct13
PASS			
12	cp_dma_pio_simple	0:0:1	2002Oct13
PASS			
13	cp_dma_pio_stress	0:0:1	2002Oct13
PASS			
14	cp_dma_piobm_stress	0:0:1	2002Oct13
PASS			
15	cp_dma_r2m_01	0:0:1	2002Oct13
PASS			
16	cp_dma_r2m_02	0:0:2	2002Oct13
PASS			
17	cp_dma_r2m_03	0:0:2	2002Oct13
PASS			
18	cp_dma_r2m_04	0:0:2	2002Oct13
PASS			
19	cp_dma_r2r_01	0:0:1	2002Oct13
PASS			
20	cp_dma_r2r_02	0:0:1	2002Oct13

PASS			
21	cp_dma_r2r_03	0:0:2	2002Oct13
PASS			
22	cp_dma_r2r_r2m	0:0:1	2002Oct13
PASS			
23	cp_dma_r2r_r2m_m2m	0:0:2	2002Oct13
PASS			
24	cp_dma_r2r_r2m_m2m_r2m	0:0:1	2002Oct13
PASS			
25	cp_dma_simple	0:0:1	2002Oct13
PASS			
26	cp_e2_hostdata_blt_pntr_8888	0:0:1	2002Oct13
PASS			
27	cp_e2_one_blit	0:0:0	2002Oct13
PASS			
28	cp_e2_one_line	0:0:2	2002Oct13
PASS			
29	cp_e2blit_brush_mt_ropcc	0:0:1	2002Oct13
PASS			
30	cp_e2paint_565	0:0:1	2002Oct13
PASS			
31	cp_e2paint_multi	0:0:2	2002Oct13
PASS			
32	cp_e2polyscanlines_brush	0:0:1	2002Oct13
PASS			
33	cp_e2polyscanlines_brush_mt	0:0:1	2002Oct13
PASS			
34	cp_e2smalltext_jcl	0:0:3	2002Oct13
PASS			
35	cp_im_load_indirect	0:0:3	2002Oct13
PASS			
36	cp_push_aper_indirect1	0:0:3	2002Oct13
PASS			
37	cp_push_aper_primary	0:0:3	2002Oct13
PASS			
38	cp_queue_avail_01	0:0:3	2002Oct13
PASS			
39	cp_queue_avail_02	0:0:3	2002Oct13
PASS			
40	cp_queue_avail_03	0:0:3	2002Oct13
PASS			
41	cp_queue_avail_04	0:0:3	2002Oct13
PASS			
42	cp_queue_avail_05	0:0:3	2002Oct13
PASS			
43	cp_queue_avail_06	0:0:3	2002Oct13
PASS			
44	cp_queue_avail_07	0:0:3	2002Oct13

PASS			
45	cp_queue_avail_08	0:0:3	2002Oct13
PASS			
46	cp_queue_avail_09	0:0:3	2002Oct13
PASS			
47	cp_queue_avail_10	0:0:3	2002Oct13
PASS			
48	cp_queue_avail_11	0:0:3	2002Oct13
PASS			
49	cp_queue_avail_12	0:0:3	2002Oct13
PASS			
50	cp_simple_triangle	0:0:3	2002Oct13
PASS			
51	milestone_event_flush_tri1	0:0:3	2002Oct11
PASS			
52	milestone_event_flush_tri2	0:0:3	2002Oct11
PASS			
53	milestone_event_flush_tri3	0:0:3	2002Oct11
PASS			
54	milestone_event_flush_tri4	0:0:3	2002Oct11
PASS			
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61	milestone_event_standard	0:0:3	2002Oct11
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63	milestone_event_switch_flush_tri2	0:0:3	2002Oct11
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339	r400sc_jss_2x1_01	0:0:3	2002Oct13
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340	r400sc_jss_2x1_02	0:0:3	2002Oct13
PASS			
341	r400sc_jss_2x1_primtypes_01	0:0:3	2002Oct13
PASS			
342	r400sc_jss_2x2_01	0:0:3	2002Oct13
PASS			
343	r400sc_jss_2x2_02	0:0:3	2002Oct13
PASS			
344	r400sc_jss_2x2_primtypes_01	0:0:3	2002Oct13
PASS			
345	r400sc_jss_2x3_01	0:0:3	2002Oct13
PASS			
346	r400sc_jss_2x3_02	0:0:3	2002Oct13
PASS			
347	r400sc_jss_2x3_primtypes_01	0:0:3	2002Oct13
PASS			
348	r400sc_jss_2x4_01	0:0:3	2002Oct13
PASS			
349	r400sc_jss_2x4_02	0:0:3	2002Oct13
PASS			
350	r400sc_jss_2x4_primtypes_01	0:0:3	2002Oct13
PASS			
351	r400sc_jss_3x1_01	0:0:3	2002Oct13
PASS			
352	r400sc_jss_3x1_02	0:0:3	2002Oct13
PASS			
353	r400sc_jss_3x1_primtypes_01	0:0:3	2002Oct13
PASS			
354	r400sc_jss_3x2_01	0:0:3	2002Oct13
PASS			
355	r400sc_jss_3x2_02	0:0:3	2002Oct13
PASS			
356	r400sc_jss_3x2_primtypes_01	0:0:3	2002Oct13

PASS			
357	r400sc_jss_3x3_01	0:0:3	2002Oct13
PASS			
358	r400sc_jss_3x3_02	0:0:3	2002Oct13
PASS			
359	r400sc_jss_3x3_primtypes_01	0:0:3	2002Oct13
PASS			
360	r400sc_jss_3x4_01	0:0:3	2002Oct13
PASS			
361	r400sc_jss_3x4_02	0:0:3	2002Oct13
PASS			
362	r400sc_jss_3x4_03	0:0:3	2002Oct13
PASS			
363	r400sc_jss_3x4_primtypes_01	0:0:3	2002Oct13
PASS			
364	r400sc_jss_4x1_01	0:0:3	2002Oct13
PASS			
365	r400sc_jss_4x1_02	0:0:3	2002Oct13
PASS			
366	r400sc_jss_4x1_primtypes_01	0:0:3	2002Oct13
PASS			
367	r400sc_jss_4x2_01	0:0:3	2002Oct13
PASS			
368	r400sc_jss_4x2_02	0:0:3	2002Oct13
PASS			
369	r400sc_jss_4x2_primtypes_01	0:0:3	2002Oct13
PASS			
370	r400sc_jss_4x3_01	0:0:3	2002Oct13
PASS			
371	r400sc_jss_4x3_02	0:0:3	2002Oct13
PASS			
372	r400sc_jss_4x3_primtypes_01	0:0:3	2002Oct13
PASS			
373	r400sc_jss_4x4_01	0:0:3	2002Oct13
PASS			
374	r400sc_jss_4x4_02	0:0:3	2002Oct13
PASS			
375	r400sc_jss_4x4_03	0:0:3	2002Oct13
PASS			
376	r400sc_jss_4x4_aa_mask_01	0:0:3	2002Oct13
PASS			
377	r400sc_jss_4x4_aa_mask_02	0:0:3	2002Oct13
PASS			
378	r400sc_jss_4x4_fc_01	0:0:3	2002Oct13
PASS			
379	r400sc_jss_4x4_fc_02	0:0:3	2002Oct13
PASS			
380	r400sc_jss_4x4_max_dist_01	0:0:3	2002Oct13

PASS			
381	r400sc_jss_4x4_primitives_01	0:0:3	2002Oct13
PASS			
382	r400sc_line_dx10_eq_0_01	0:0:3	2002Oct13
PASS			
383	r400sc_line_dx10_ge_0_01	0:0:3	2002Oct13
PASS			
384	r400sc_line_dx10_lt_0_01	0:0:3	2002Oct13
PASS			
385	r400sc_line_dy10_eq_0_01	0:0:3	2002Oct13
PASS			
386	r400sc_line_dy10_ge_0_01	0:0:3	2002Oct13
PASS			
387	r400sc_line_dy10_lt_0_01	0:0:3	2002Oct13
PASS			
388	r400sc_line_expand_width_msa_8_01	0:0:3	2002Oct13
PASS			
389	r400sc_line_expand_width_msa_8_02	0:0:3	2002Oct13
PASS			
390	r400sc_line_expand_width_msa_8_03	0:0:3	2002Oct13
PASS			
391	r400sc_line_jss_3x4_01	0:0:3	2002Oct13
PASS			
392	r400sc_line_list_01	0:0:3	2002Oct13
PASS			
393	r400sc_line_list_02	0:0:3	2002Oct13
PASS			
394	r400sc_line_list_concentric_circle_01	0:0:3	2002Oct13
PASS			
395	r400sc_line_list_concentric_circle_02	0:0:3	2002Oct13
PASS			
396	r400sc_line_list_concentric_circle_03	0:0:3	2002Oct13
PASS			
397	r400sc_line_list_textured_01	0:0:3	2002Oct13
PASS			
398	r400sc_line_list_verify_st_01	0:0:3	2002Oct13
PASS			
399	r400sc_line_list_verify_st_01_bug	0:0:3	2002Oct9
PASS			
400	r400sc_line_msa_8_01	0:0:3	2002Oct13
PASS			
401	r400sc_line_msa_8_textured_01	0:0:3	2002Oct13
PASS			
402	r400sc_line_stipple_01	0:0:3	2002Oct13
PASS			
403	r400sc_line_stipple_02	0:0:3	2002Oct13
PASS			
404	r400sc_line_stipple_03	0:0:3	2002Oct13

PASS			
405	r400sc_line_stipple_04	0:0:3	2002Oct13
PASS			
406	r400sc_line_stipple_05	0:0:3	2002Oct13
PASS			
407	r400sc_line_stipple_06	0:0:3	2002Oct13
PASS			
408	r400sc_line_stipple_07	0:0:3	2002Oct13
PASS			
409	r400sc_line_stipple_08	0:0:3	2002Oct13
PASS			
410	r400sc_line_stipple_09	0:0:3	2002Oct13
PASS			
411	r400sc_line_stipple_10	0:0:3	2002Oct13
PASS			
412	r400sc_line_stipple_11	0:0:3	2002Oct13
PASS			
413	r400sc_line_stipple_12	0:0:3	2002Oct13
PASS			
414	r400sc_line_stipple_13	0:0:3	2002Oct13
PASS			
415	r400sc_line_stipple_14	0:0:3	2002Oct13
PASS			
416	r400sc_line_stipple_15	0:0:3	2002Oct13
PASS			
417	r400sc_line_stipple_16	0:0:3	2002Oct13
PASS			
418	r400sc_line_stipple_17	0:0:3	2002Oct13
PASS			
419	r400sc_line_stipple_18	0:0:3	2002Oct13
PASS			
420	r400sc_line_stipple_19	0:0:3	2002Oct13
PASS			
421	r400sc_line_stipple_20	0:0:3	2002Oct13
PASS			
422	r400sc_line_stipple_21	0:0:3	2002Oct13
PASS			
423	r400sc_line_stipple_22	0:0:3	2002Oct13
PASS			
424	r400sc_line_stipple_23	0:0:3	2002Oct13
PASS			
425	r400sc_line_stipple_fc_08	0:0:3	2002Oct13
PASS			
426	r400sc_line_strip_stipple_01	0:0:3	2002Oct13
PASS			
427	r400sc_msaa_1_01	0:0:3	2002Oct13
PASS			
428	r400sc_msaa_1_primitives_01	0:0:3	2002Oct13

PASS			
429	r400sc_msa_1_rectangle_list_01	0:0:3	2002Oct13
PASS			
430	r400sc_msa_1_rectangle_list_02	0:0:3	2002Oct13
PASS			
431	r400sc_msa_1_rectangle_list_03	0:0:3	2002Oct13
PASS			
432	r400sc_msa_1_rectangle_list_04	0:0:3	2002Oct13
PASS			
433	r400sc_msa_1_rectangle_list_05	0:0:3	2002Oct13
PASS			
434	r400sc_msa_1_rectangle_list_06	0:0:3	2002Oct13
PASS			
435	r400sc_msa_1_rectangle_list_07	0:0:3	2002Oct13
PASS			
436	r400sc_msa_1_rectangle_list_08	0:0:3	2002Oct13
PASS			
437	r400sc_msa_1_zbuffer_rectangle_list_01	0:0:3	2002Oct13
PASS			
438	r400sc_msa_1_zbuffer_rectangle_list_02	0:0:3	2002Oct13
PASS			
439	r400sc_msa_2_primitives_01	0:0:3	2002Oct13
PASS			
440	r400sc_msa_2_rectangle_list_01	0:0:3	2002Oct13
PASS			
441	r400sc_msa_2_rectangle_list_02	0:0:3	2002Oct13
PASS			
442	r400sc_msa_2_rectangle_list_03	0:0:3	2002Oct13
PASS			
443	r400sc_msa_2_rectangle_list_04	0:0:3	2002Oct13
PASS			
444	r400sc_msa_2_rectangle_list_05	0:0:3	2002Oct13
PASS			
445	r400sc_msa_2_rectangle_list_06	0:0:3	2002Oct13
PASS			
446	r400sc_msa_2_rectangle_list_07	0:0:3	2002Oct13
PASS			
447	r400sc_msa_2_rectangle_list_08	0:0:3	2002Oct13
PASS			
448	r400sc_msa_2_zbuffer_rectangle_list_01	0:0:3	2002Oct13
PASS			
449	r400sc_msa_2_zbuffer_rectangle_list_02	0:0:3	2002Oct13
PASS			
450	r400sc_msa_3_primitives_01	0:0:3	2002Oct13
PASS			
451	r400sc_msa_3_rectangle_list_01	0:0:3	2002Oct13
PASS			
452	r400sc_msa_3_rectangle_list_02	0:0:3	2002Oct13

PASS			
453	r400sc_msaa_3_rectangle_list_03	0:0:3	2002Oct13
PASS			
454	r400sc_msaa_3_rectangle_list_04	0:0:3	2002Oct13
PASS			
455	r400sc_msaa_3_rectangle_list_05	0:0:3	2002Oct13
PASS			
456	r400sc_msaa_3_rectangle_list_06	0:0:3	2002Oct13
PASS			
457	r400sc_msaa_3_rectangle_list_07	0:0:3	2002Oct13
PASS			
458	r400sc_msaa_3_rectangle_list_08	0:0:3	2002Oct13
PASS			
459	r400sc_msaa_3_zbuffer_rectangle_list_01	0:0:3	2002Oct13
PASS			
460	r400sc_msaa_3_zbuffer_rectangle_list_02	0:0:3	2002Oct13
PASS			
461	r400sc_msaa_4_01	0:0:3	2002Oct13
PASS			
462	r400sc_msaa_4_primitives_01	0:0:3	2002Oct13
PASS			
463	r400sc_msaa_4_rectangle_list_01	0:0:3	2002Oct13
PASS			
464	r400sc_msaa_4_rectangle_list_02	0:0:3	2002Oct13
PASS			
465	r400sc_msaa_4_rectangle_list_03	0:0:3	2002Oct13
PASS			
466	r400sc_msaa_4_rectangle_list_04	0:0:3	2002Oct13
PASS			
467	r400sc_msaa_4_rectangle_list_05	0:0:3	2002Oct13
PASS			
468	r400sc_msaa_4_rectangle_list_06	0:0:3	2002Oct13
PASS			
469	r400sc_msaa_4_rectangle_list_07	0:0:3	2002Oct13
PASS			
470	r400sc_msaa_4_rectangle_list_08	0:0:3	2002Oct13
PASS			
471	r400sc_msaa_4_zbuffer_rectangle_list_01	0:0:3	2002Oct13
PASS			
472	r400sc_msaa_4_zbuffer_rectangle_list_02	0:0:3	2002Oct13
PASS			
473	r400sc_msaa_6_01	0:0:3	2002Oct13
PASS			
474	r400sc_msaa_6_primitives_01	0:0:3	2002Oct13
PASS			
475	r400sc_msaa_6_rectangle_list_01	0:0:3	2002Oct13
PASS			
476	r400sc_msaa_6_rectangle_list_02	0:0:3	2002Oct13

PASS			
477	r400sc_msaa_6_rectangle_list_03	0:0:3	2002Oct13
PASS			
478	r400sc_msaa_6_rectangle_list_04	0:0:3	2002Oct13
PASS			
479	r400sc_msaa_6_rectangle_list_05	0:0:3	2002Oct13
PASS			
480	r400sc_msaa_6_rectangle_list_06	0:0:3	2002Oct13
PASS			
481	r400sc_msaa_6_rectangle_list_07	0:0:3	2002Oct13
PASS			
482	r400sc_msaa_6_rectangle_list_08	0:0:3	2002Oct13
PASS			
483	r400sc_msaa_6_zbuffer_rectangle_list_01	0:0:3	2002Oct13
PASS			
484	r400sc_msaa_6_zbuffer_rectangle_list_02	0:0:3	2002Oct13
PASS			
485	r400sc_msaa_8_01	0:0:3	2002Oct13
PASS			
486	r400sc_msaa_8_02	0:0:3	2002Oct13
PASS			
487	r400sc_msaa_8_03	0:0:3	2002Oct13
PASS			
488	r400sc_msaa_8_04	0:0:3	2002Oct13
PASS			
489	r400sc_msaa_8_05	0:0:3	2002Oct13
PASS			
490	r400sc_msaa_8_aa_mask_01	0:0:3	2002Oct13
PASS			
491	r400sc_msaa_8_aa_mask_02	0:0:3	2002Oct13
PASS			
492	r400sc_msaa_8_aa_mask_fc_02	0:0:3	2002Oct13
PASS			
493	r400sc_msaa_8_hos_PNT_cont_cp_qn_complex_01	0:0:3	2002Oct13
PASS			
494	r400sc_msaa_8_primitives_01	0:0:3	2002Oct13
PASS			
495	r400sc_msaa_8_rectangle_list_01	0:0:3	2002Oct13
PASS			
496	r400sc_msaa_8_rectangle_list_02	0:0:3	2002Oct13
PASS			
497	r400sc_msaa_8_rectangle_list_03	0:0:3	2002Oct13
PASS			
498	r400sc_msaa_8_rectangle_list_04	0:0:3	2002Oct13
PASS			
499	r400sc_msaa_8_rectangle_list_05	0:0:3	2002Oct13
PASS			
500	r400sc_msaa_8_rectangle_list_06	0:0:3	2002Oct13

PASS			
501	r400sc_msa_8_rectangle_list_07	0:0:3	2002Oct13
PASS			
502	r400sc_msa_8_rectangle_list_08	0:0:3	2002Oct13
PASS			
503	r400sc_msa_8_zbuffer_rectangle_list_01	0:0:3	2002Oct13
PASS			
504	r400sc_msa_8_zbuffer_rectangle_list_02	0:0:3	2002Oct13
PASS			
505	r400sc_msa_X_primitives_01_bug	0:0:3	2002Oct9
PASS			
506	r400sc_msa_X_primitives_02_bug	0:0:3	2002Oct9
PASS			
507	r400sc_multi_chip_prim_discard_00_07	0:0:3	2002Oct7
FAIL	VgtPaClipP_VgtSide.cmp compare file empty		
508	r400sc_multi_chip_prim_discard_00_07_standard	0:0:3	2002Oct11
PASS			
509	r400sc_multi_chip_prim_discard_00_07_test00	0:0:3	2002Oct11
INCOMPLETE			
510	r400sc_multi_chip_prim_discard_00_07_test01	0:0:3	2002Oct11
PASS			
511	r400sc_multi_chip_prim_discard_00_07_test02	0:0:3	2002Oct11
INCOMPLETE			
512	r400sc_multi_chip_prim_discard_00_07_test03	0:0:3	2002Oct11
PASS			
513	r400sc_multi_chip_prim_discard_00_07_test04	0:0:3	2002Oct11
INCOMPLETE			
514	r400sc_multi_chip_prim_discard_00_07_test05	0:0:3	2002Oct11
INCOMPLETE			
515	r400sc_multi_chip_prim_discard_00_07_test06	0:0:3	2002Oct11
PASS			
516	r400sc_multi_chip_tile_discard_00_07	0:0:3	2002Oct7
PASS			
517	r400sc_multi_chip_tile_discard_00_07_standard	0:0:3	2002Oct11
PASS			
518	r400sc_multi_chip_tile_discard_00_07_test00	0:0:3	2002Oct11
PASS			
519	r400sc_multi_chip_tile_discard_00_07_test01	0:0:3	2002Oct11
PASS			
520	r400sc_multi_chip_tile_discard_00_07_test02	0:0:3	2002Oct11
PASS			
521	r400sc_multi_chip_tile_discard_00_07_test03	0:0:3	2002Oct11
PASS			
522	r400sc_multi_chip_tile_discard_00_07_test04	0:0:3	2002Oct11
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp VgtPaClipS_VgtSide.cmp		
compare file empty			
523	r400sc_multi_chip_tile_discard_00_07_test05	0:0:3	2002Oct11
INCOMPLETE			

524	r400sc_multi_chip_tile_discard_00_07_test06	0:0:3	2002Oct11
	INCOMPLETE		
525	r400sc_null_triangles_01	0:0:3	2002Oct13
	PASS		
526	r400sc_null_triangles_fc_01	0:0:3	2002Oct13
	PASS		
527	r400sc_packed_color_01	0:0:3	2002Oct13
	PASS		
528	r400sc_perf_01	0:0:3	2002Oct13
	PASS		
529	r400sc_perf_02	0:0:3	2002Oct13
	PASS		
530	r400sc_perf_03	0:0:3	2002Oct13
	PASS		
531	r400sc_pinwheel_01	0:0:3	2002Oct13
	PASS		
532	r400sc_pinwheel_02	0:0:3	2002Oct13
	PASS		
533	r400sc_pinwheel_02_bug	0:0:3	2002Oct9
	PASS		
534	r400sc_point_jss_3x4_01	0:0:3	2002Oct13
	PASS		
535	r400sc_point_list_01	0:0:3	2002Oct13
	PASS		
536	r400sc_point_list_02	0:0:3	2002Oct13
	PASS		
537	r400sc_point_list_03	0:0:3	2002Oct13
	PASS		
538	r400sc_point_list_04	0:0:3	2002Oct13
	PASS		
539	r400sc_point_list_05	0:0:3	2002Oct13
	PASS		
540	r400sc_point_list_06	0:0:3	2002Oct13
	PASS		
541	r400sc_point_list_06_bug	0:0:3	2002Oct9
	PASS		
542	r400sc_point_list_06_bug_02	0:0:3	2002Oct9
	PASS		
543	r400sc_point_list_07	0:0:3	2002Oct13
	PASS		
544	r400sc_point_list_08	0:0:3	2002Oct13
	PASS		
545	r400sc_point_list_09	0:0:3	2002Oct13
	PASS		
546	r400sc_point_msa_8_01	0:0:3	2002Oct13
	PASS		
547	r400sc_poly_offset_01	0:0:3	2002Oct13
	PASS		

548	r400sc_poly_offset_02	0:0:3	2002Oct13
	PASS		
549	r400sc_poly_offset_03	0:0:3	2002Oct13
	PASS		
550	r400sc_poly_offset_04	0:0:3	2002Oct13
	PASS		
551	r400sc_poly_offset_05	0:0:3	2002Oct13
	PASS		
552	r400sc_poly_offset_06	0:0:3	2002Oct13
	PASS		
553	r400sc_poly_offset_07	0:0:3	2002Oct13
	PASS		
554	r400sc_poly_offset_08	0:0:3	2002Oct13
	PASS		
555	r400sc_poly_offset_09	0:0:3	2002Oct13
	PASS		
556	r400sc_poly_offset_10	0:0:3	2002Oct13
	PASS		
557	r400sc_poly_offset_10_bug	0:0:3	2002Oct9
	PASS		
558	r400sc_poly_offset_fc_01	0:0:3	2002Oct13
	PASS		
559	r400sc_polygon_stipple_01	0:0:3	2002Oct13
	PASS		
560	r400sc_polymode_tri_fill_01	0:0:3	2002Oct13
	PASS		
561	r400sc_polymode_tri_fill_01_bug	0:0:3	2002Oct9
	PASS		
562	r400sc_prsp_byc_intrp_ref_pix_01	0:0:3	2002Oct13
	PASS		
563	r400sc_prsp_byc_intrp_ref_pix_02	0:0:3	2002Oct13
	PASS		
564	r400sc_prsp_byc_intrp_ref_pix_03	0:0:3	2002Oct13
	PASS		
565	r400sc_prsp_byc_intrp_ref_pix_04	0:0:3	2002Oct13
	PASS		
566	r400sc_prsp_byc_intrp_ref_pix_05	0:0:3	2002Oct13
	PASS		
567	r400sc_prsp_byc_intrp_ref_pix_06	0:0:3	2002Oct13
	PASS		
568	r400sc_prsp_byc_intrp_ref_pix_07	0:0:3	2002Oct13
	PASS		
569	r400sc_prsp_byc_intrp_ref_pix_08	0:0:3	2002Oct13
	PASS		
570	r400sc_raster_fill_rule_01	0:0:3	2002Oct13
	PASS		
571	r400sc_raster_fill_rule_02	0:0:3	2002Oct13
	PASS		

572	r400sc_raster_fill_rule_03	0:0:3	2002Oct13
	PASS		
573	r400sc_raster_fill_rule_04	0:0:3	2002Oct13
	PASS		
574	r400sc_raster_fill_rule_05	0:0:3	2002Oct13
	PASS		
575	r400sc_raster_fill_rule_fc_01	0:0:3	2002Oct13
	PASS		
576	r400sc_rectangle_list_01	0:0:3	2002Oct13
	PASS		
577	r400sc_rectangle_list_02	0:0:3	2002Oct13
	PASS		
578	r400sc_rectangle_list_03	0:0:3	2002Oct13
	PASS		
579	r400sc_rectangle_list_04	0:0:3	2002Oct13
	PASS		
580	r400sc_rectangle_list_05	0:0:3	2002Oct13
	PASS		
581	r400sc_rectangle_list_06	0:0:3	2002Oct13
	PASS		
582	r400sc_rectangle_list_07	0:0:3	2002Oct13
	PASS		
583	r400sc_rectangle_list_08	0:0:3	2002Oct13
	PASS		
584	r400sc_scissor_rect_01	0:0:3	2002Oct13
	PASS		
585	r400sc_scissor_rect_02	0:0:3	2002Oct13
	PASS		
586	r400sc_scissor_rect_03	0:0:3	2002Oct13
	PASS		
587	r400sc_scissor_rect_04	0:0:3	2002Oct13
	PASS		
588	r400sc_scissor_rect_05	0:0:3	2002Oct13
	PASS		
589	r400sc_scissor_rect_fc_01	0:0:3	2002Oct13
	PASS		
590	r400sc_set_state_01	0:0:3	2002Oct13
	PASS		
591	r400sc_sp_sample_cntl_01	0:0:3	2002Oct13
	PASS		
592	r400sc_sp_sample_cntl_02	0:0:3	2002Oct13
	PASS		
593	r400sc_sp_sample_cntl_03	0:0:3	2002Oct13
	PASS		
594	r400sc_sp_sample_cntl_04	0:0:3	2002Oct13
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845	r400vgt_index_min_max_01	0:0:3	2002Oct13
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846	r400vgt_index_min_max_02	0:0:3	2002Oct13
	PASS		
847	r400vgt_index_min_max_03	0:0:3	2002Oct13
	PASS		
848	r400vgt_index_min_max_04	0:0:3	2002Oct13
	PASS		
849	r400vgt_index_offset_01	0:0:3	2002Oct13
	PASS		
850	r400vgt_index_offset_02	0:0:3	2002Oct13
	PASS		
851	r400vgt_index_offset_03	0:0:3	2002Oct13
	PASS		
852	r400vgt_index_offset_04	0:0:3	2002Oct13
	PASS		
853	r400vgt_index_offset_05	0:0:3	2002Oct13
	PASS		
854	r400vgt_index_offset_06	0:0:3	2002Oct13
	PASS		
855	r400vgt_index_offset_07	0:0:3	2002Oct13
	PASS		
856	r400vgt_index_offset_08	0:0:3	2002Oct13
	PASS		
857	r400vgt_index_size_01	0:0:3	2002Oct13
	PASS		
858	r400vgt_index_size_02	0:0:3	2002Oct13
	PASS		
859	r400vgt_index_source_switch_01	0:0:3	2002Oct13
	PASS		

860	r400vgt_line_list_01	0:0:3	2002Oct13
	PASS		
861	r400vgt_line_list_02	0:0:3	2002Oct13
	PASS		
862	r400vgt_line_loop_01	0:0:3	2002Oct13
	PASS		
863	r400vgt_line_loop_02	0:0:3	2002Oct13
	PASS		
864	r400vgt_line_strip_01	0:0:3	2002Oct13
	PASS		
865	r400vgt_line_strip_02	0:0:3	2002Oct13
	PASS		
866	r400vgt_multi_context_01	0:0:3	2002Oct13
	PASS		
867	r400vgt_multi_context_02	0:0:3	2002Oct13
	PASS		
868	r400vgt_multi_context_03	0:0:3	2002Oct13
	PASS		
869	r400vgt_multi_context_04	0:0:3	2002Oct13
	PASS		
870	r400vgt_multi_context_05	0:0:3	2002Oct13
	PASS		
871	r400vgt_multi_context_06	0:0:3	2002Oct13
	PASS		
872	r400vgt_multi_context_07	0:0:3	2002Oct13
	PASS		
873	r400vgt_multi_context_08	0:0:3	2002Oct13
	PASS		
874	r400vgt_multi_context_09	0:0:3	2002Oct13
	PASS		
875	r400vgt_multi_context_10	0:0:3	2002Oct13
	PASS		
876	r400vgt_multi_context_11	0:0:3	2002Oct13
	PASS		
877	r400vgt_multi_context_12	0:0:3	2002Oct13
	PASS		
878	r400vgt_multi_prim_reset_index_all_01	0:0:3	2002Oct13
	PASS		
879	r400vgt_multi_prim_reset_index_all_02	0:0:3	2002Oct13
	PASS		
880	r400vgt_pass_thru_all_prims_01	0:0:3	2002Oct13
	PASS		
881	r400vgt_point_list_01	0:0:3	2002Oct13
	PASS		
882	r400vgt_point_list_02	0:0:3	2002Oct13
	PASS		
883	r400vgt_polygon_01	0:0:3	2002Oct13
	PASS		

884	r400vgt_polygon_02	0:0:3	2002Oct13
	PASS		
885	r400vgt_provoking_vtx_all_01	0:0:3	2002Oct13
	PASS		
886	r400vgt_provoking_vtx_edgeflags_all_01	0:0:3	2002Oct13
	PASS		
887	r400vgt_provoking_vtx_polygon_01	0:0:3	2002Oct13
	PASS		
888	r400vgt_provoking_vtx_quad_list_01	0:0:3	2002Oct13
	PASS		
889	r400vgt_provoking_vtx_quad_strip_01	0:0:3	2002Oct13
	PASS		
890	r400vgt_provoking_vtx_tri_fan_01	0:0:3	2002Oct13
	PASS		
891	r400vgt_provoking_vtx_tri_strip_01	0:0:3	2002Oct13
	PASS		
892	r400vgt_quad_list_01	0:0:3	2002Oct13
	PASS		
893	r400vgt_quad_list_02	0:0:3	2002Oct13
	PASS		
894	r400vgt_quad_strip_01	0:0:3	2002Oct13
	PASS		
895	r400vgt_quad_strip_02	0:0:3	2002Oct13
	PASS		
896	r400vgt_rectangle_list_01	0:0:3	2002Oct13
	PASS		
897	r400vgt_rectangle_list_02	0:0:3	2002Oct13
	PASS		
898	r400vgt_reuse_depth_line_list_01	0:0:3	2002Oct13
	PASS		
899	r400vgt_reuse_depth_point_list_01	0:0:3	2002Oct13
	PASS		
900	r400vgt_reuse_depth_triangle_list_01	0:0:3	2002Oct13
	PASS		
901	r400vgt_reuse_index_line_list_01	0:0:3	2002Oct13
	PASS		
902	r400vgt_reuse_index_point_list_01	0:0:3	2002Oct13
	PASS		
903	r400vgt_reuse_index_triangle_list_01	0:0:3	2002Oct13
	PASS		
904	r400vgt_reuse_index_triangle_list_02	0:0:3	2002Oct13
	PASS		
905	r400vgt_reuse_index_triangle_list_03	0:0:3	2002Oct13
	PASS		
906	r400vgt_suppress_eop_01	0:0:1	2002Oct13
	PASS		
907	r400vgt_suppress_eop_02	0:0:1	2002Oct13
	PASS		

908	r400vgt_suppress_eop_03	0:0:1	2002Oct13
	PASS		
909	r400vgt_suppress_eop_04	0:0:1	2002Oct13
	PASS		
910	r400vgt_suppress_eop_05	0:0:1	2002Sep26
	PASS		
911	r400vgt_triangle_fan_01	0:0:1	2002Oct13
	PASS		
912	r400vgt_triangle_fan_02	0:0:1	2002Oct13
	PASS		
913	r400vgt_triangle_list_01	0:0:1	2002Oct13
	PASS		
914	r400vgt_triangle_list_02	0:0:1	2002Oct13
	PASS		
915	r400vgt_triangle_strip_01	0:0:1	2002Oct13
	PASS		
916	r400vgt_triangle_strip_02	0:0:1	2002Oct13
	PASS		
917	r400vgt_triangle_wflags_01	0:0:1	2002Oct13
	PASS		
918	r400vgt_triangle_wflags_02	0:0:1	2002Oct13
	PASS		
919	r400vgt_viz_query_01	0:0:1	2002Oct13
	PASS		
920	r400vgt_vtx_vector_packing_01	0:0:1	2002Oct13
	PASS		
921	r400vte_coverage_01	0:0:1	2002Oct13
	PASS		
922	r400vte_simple_point_01	0:0:1	2002Oct13
	PASS		
923	r400vte_simple_triangle_01	0:0:1	2002Oct13
	PASS		
924	r400vte_w0_fmt_01	0:0:1	2002Oct13
	PASS		
925	r400vte_w0_fmt_03	0:0:1	2002Oct13
	PASS		
926	r400vte_xy_fmt_01	0:0:1	2002Oct13
	PASS		
927	r400vte_xy_fmt_mk_01_bug	0:0:1	2002Oct9
	PASS		
928	r400vte_xyz_scale_01	0:0:1	2002Oct13
	PASS		
929	r400vte_xyz_scale_02	0:0:1	2002Oct13
	PASS		
930	r400vte_z_fmt_01	0:0:1	2002Oct13
	PASS		

=====
Tests_Run_count = 930 Pass_count = 921 Fail_count = 2 Incomplete_count = 7

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Regression Report For VGT - Project = r400 Created on Tue Oct 8 11:50:05 2002
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No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_2desc		2002Oct8
PASS			
2	cp_dma_interrupt		2002Oct8
PASS			
3	cp_dma_m2m_01		2002Oct8
PASS			
4	cp_dma_m2m_02		2002Oct8
PASS			
5	cp_dma_m2m_03		2002Oct8
PASS			
6	cp_dma_m2m_04		2002Oct8
PASS			
7	cp_dma_m2r_01		2002Oct8
PASS			
8	cp_dma_m2r_02		2002Oct8
PASS			
9	cp_dma_m2r_03		2002Oct8
PASS			
10	cp_dma_m2r_04		2002Oct8
PASS			
11	cp_dma_m2r_r2m		2002Oct8
PASS			
12	cp_dma_piobm_stress		2002Oct8
PASS			
13	cp_dma_pio_simple		2002Oct8
PASS			
14	cp_dma_pio_stress		2002Oct8
PASS			
15	cp_dma_r2m_01		2002Oct8
PASS			
16	cp_dma_r2m_02		2002Oct8
PASS			
17	cp_dma_r2m_03		2002Oct8
PASS			
18	cp_dma_r2m_04		2002Oct8
PASS			
19	cp_dma_r2r_01		2002Oct8
PASS			
20	cp_dma_r2r_02		2002Oct8

PASS		
21	cp_dma_r2r_03	20020ct8
PASS		
22	cp_dma_r2r_r2m	20020ct8
PASS		
23	cp_dma_r2r_r2m_m2m	20020ct8
PASS		
24	cp_dma_r2r_r2m_m2m_r2m	20020ct8
PASS		
25	cp_dma_simple	20020ct8
PASS		
26	cp_e2blit_brush_mt_ropcc	20020ct8
PASS		
27	cp_e2_hostdata_blt_pntr_8888	20020ct8
PASS		
28	cp_e2_one_blit	20020ct8
PASS		
29	cp_e2_one_line	20020ct8
PASS		
30	cp_e2paint_565	20020ct8
PASS		
31	cp_e2paint_multi	20020ct8
PASS		
32	cp_e2polyscanlines_brush	20020ct8
PASS		
33	cp_e2polyscanlines_brush_mt	20020ct8
PASS		
34	cp_e2smalltext_jcl	20020ct8
PASS		
35	cp_im_load_indirect	20020ct8
PASS		
36	cp_push_aper_indirect1	20020ct8
PASS		
37	cp_push_aper_primary	20020ct8
PASS		
38	cp_queue_avail_01	20020ct8
PASS		
39	cp_queue_avail_02	20020ct8
PASS		
40	cp_queue_avail_03	20020ct8
PASS		
41	cp_queue_avail_04	20020ct8
PASS		
42	cp_queue_avail_05	20020ct8
PASS		
43	cp_queue_avail_06	20020ct8
PASS		
44	cp_queue_avail_07	20020ct8

PASS		
45	cp_queue_avail_08	2002Oct8
PASS		
46	cp_queue_avail_09	2002Oct8
PASS		
47	cp_queue_avail_10	2002Oct8
PASS		
48	cp_queue_avail_11	2002Oct8
PASS		
49	cp_queue_avail_12	2002Oct8
PASS		
50	cp_simple_triangle	2002Oct8
PASS		
51	milestone_event_flush_tri1	2002Oct3
PASS		
52	milestone_event_flush_tri2	2002Oct3
PASS		
53	milestone_event_flush_tri3	2002Oct3
PASS		
54	milestone_event_flush_tri4	2002Oct3
PASS		
55	milestone_event_flush_tri5	2002Oct3
PASS		
56	milestone_event_flush_tri6	2002Oct3
PASS		
57	milestone_event_flush_tri7	2002Oct3
PASS		
58	milestone_event_flush_tri8	2002Oct3
PASS		
59	milestone_event_flush_tri9	2002Oct3
PASS		
60	milestone_event_randomized	2002Oct3
PASS		
61	milestone_event_standard	2002Oct3
PASS		
62	milestone_event_switch_flush_tri1	2002Oct3
PASS		
63	milestone_event_switch_flush_tri2	2002Oct3
PASS		
64	milestone_event_switch_flush_tri3	2002Oct3
PASS		
65	milestone_event_switch_flush_tri4	2002Oct3
PASS		
66	milestone_event_switch_flush_tri5	2002Oct3
PASS		
67	milestone_event_switch_flush_tri6	2002Oct3
PASS		
68	milestone_event_switch_flush_tri7	2002Oct3

PASS		
69	milestone_event_switch_flush_tri8	2002Oct3
PASS		
70	milestone_event_switch_flush_tri9	2002Oct3
PASS		
71	milestone_event_switch_tri2	2002Oct3
PASS		
72	milestone_event_switch_tri3	2002Oct3
PASS		
73	milestone_event_switch_tri4	2002Oct3
PASS		
74	milestone_event_switch_tri5	2002Oct3
PASS		
75	milestone_event_switch_tri6	2002Oct3
PASS		
76	milestone_event_switch_tri7	2002Oct3
PASS		
77	milestone_event_switch_tri8	2002Oct3
PASS		
78	milestone_event_switch_tri9	2002Oct3
PASS		
79	milestone_event_tri2	2002Oct3
PASS		
80	milestone_event_tri3	2002Oct3
PASS		
81	milestone_event_tri4	2002Oct3
PASS		
82	milestone_event_tri5	2002Oct3
PASS		
83	milestone_event_tri6	2002Oct3
PASS		
84	milestone_event_tri7	2002Oct3
PASS		
85	milestone_event_tri8	2002Oct3
PASS		
86	milestone_event_tri9	2002Oct3
PASS		
87	milestone_tri_randomized	2002Oct3
PASS		
88	milestone_tri_random_rect	2002Oct3
PASS		
89	milestone_tri_random_tri	2002Oct3
PASS		
90	milestone_tri_standard	2002Oct3
PASS		
91	milestone_tri_tril28_pix4	2002Oct3
PASS		
92	milestone_tri_tril_pix1	2002Oct3

PASS		
93	milestone_tri_tril_pix2	2002Oct3
PASS		
94	milestone_tri_tril_pix3	2002Oct3
PASS		
95	milestone_tri_tril_pix4	2002Oct3
PASS		
96	milestone_tri_tri24x24	2002Oct3
PASS		
97	milestone_tri_tri28x28	2002Oct3
PASS		
98	milestone_tri_tri2_pix1	2002Oct3
PASS		
99	milestone_tri_tri2_pix16	2002Oct3
PASS		
100	milestone_tri_tri2_pix4	2002Oct3
PASS		
101	milestone_tri_tri32_pix1	2002Oct3
PASS		
102	milestone_tri_tri32_pix4	2002Oct3
PASS		
103	milestone_tri_tri32x32	2002Oct3
PASS		
104	milestone_tri_tri36x36	2002Oct3
PASS		
105	milestone_tri_tri40x40	2002Oct3
PASS		
106	milestone_tri_tri44x44	2002Oct3
PASS		
107	milestone_tri_tri48x48	2002Oct3
PASS		
108	milestone_tri_tri52x52	2002Oct3
PASS		
109	milestone_tri_tri56x56	2002Oct3
PASS		
110	milestone_tri_tri60x60	2002Oct3
PASS		
111	milestone_tri_tri64x64	2002Oct3
PASS		
112	milestone_tri_tri8_pix4	2002Oct3
PASS		
113	primlib_1st_tri_june15	2002Oct8
PASS		
114	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb	2002Oct8
PASS		
115	primlib_gouraud_triangles_2_draw_passes	2002Oct8
PASS		
116	primlib_parameterized_simple_triangle	2002Oct8

PASS		
117	primlib_template_simple_triangle	2002Oct8
PASS		
118	primlib_tex_tri	2002Oct8
PASS		
119	r400cl_bary_texture_08_bug	2002Sep21
PASS		
120	r400cl_clip_10_verts_01	2002Oct8
PASS		
121	r400cl_clip_disable_01	2002Oct8
PASS		
122	r400cl_clip_space_dx_ogl_01	2002Oct8
PASS		
123	r400cl_clip_ucp_6bits_01	2002Oct8
PASS		
124	r400cl_cull_only_ena_01	2002Oct8
PASS		
125	r400cl_edgeflags_01	2002Oct8
PASS		
126	r400cl_edgeflags_01_bug	2002Oct8
PASS		
127	r400cl_edgeflags_frustum_bottom_01	2002Oct8
PASS		
128	r400cl_edgeflags_frustum_far_01	2002Oct8
PASS		
129	r400cl_edgeflags_frustum_left_01	2002Oct8
PASS		
130	r400cl_edgeflags_frustum_near_01	2002Oct8
PASS		
131	r400cl_edgeflags_frustum_right_01	2002Oct8
PASS		
132	r400cl_edgeflags_frustum_top_01	2002Oct8
PASS		
133	r400cl_edgeflags_gband_01	2002Oct8
PASS		
134	r400cl_edgeflags_gband_bottom_01	2002Oct8
PASS		
135	r400cl_edgeflags_gband_left_01	2002Oct8
PASS		
136	r400cl_edgeflags_gband_right_01	2002Oct8
PASS		
137	r400cl_edgeflags_gband_top_01	2002Oct8
PASS		
138	r400cl_frustum_01	2002Oct8
PASS		
139	r400cl_frustum_02	2002Oct8
PASS		
140	r400cl_frustum_03	2002Oct8

PASS		
141	r400cl_frustum_04	20020ct8
PASS		
142	r400cl_frustum_05	20020ct8
PASS		
143	r400cl_frustum_06	20020ct8
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144	r400cl_frustum_07	20020ct8
PASS		
145	r400cl_frustum_08	20020ct8
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146	r400cl_frustum_09	20020ct8
PASS		
147	r400cl_frustum_10	20020ct8
PASS		
148	r400cl_frustum_11	20020ct8
PASS		
149	r400cl_frustum_12	20020ct8
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150	r400cl_frustum_13	20020ct8
PASS		
151	r400cl_frustum_14	20020ct8
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152	r400cl_frustum_15	20020ct8
PASS		
153	r400cl_frustum_16	20020ct8
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154	r400cl_frustum_17	20020ct8
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155	r400cl_frustum_18	20020ct8
PASS		
156	r400cl_frustum_19	20020ct8
PASS		
157	r400cl_frustum_20	20020ct8
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158	r400cl_frustum_21	20020ct8
PASS		
159	r400cl_frustum_22	20020ct8
PASS		
160	r400cl_frustum_23	20020ct8
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161	r400cl_frustum_24	20020ct8
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162	r400cl_frustum_25	20020ct8
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163	r400cl_frustum_26	20020ct8
PASS		
164	r400cl_frustum_27	20020ct8

PASS		
165	r400cl_frustum_28	2002Oct8
PASS		
166	r400cl_frustum_all_vols_lines	2002Oct8
PASS		
167	r400cl_frustum_all_vols_tris	2002Oct8
PASS		
168	r400cl_frustum_lines_01	2002Oct8
PASS		
169	r400cl_frustum_lines_02	2002Oct8
PASS		
170	r400cl_frustum_lines_03	2002Oct8
PASS		
171	r400cl_frustum_lines_04	2002Oct8
PASS		
172	r400cl_frustum_lines_05	2002Oct8
PASS		
173	r400cl_frustum_lines_06	2002Oct8
PASS		
174	r400cl_frustum_lines_07	2002Oct8
PASS		
175	r400cl_frustum_lines_08	2002Oct8
PASS		
176	r400cl_frustum_lines_09	2002Oct8
PASS		
177	r400cl_frustum_lines_10	2002Oct8
PASS		
178	r400cl_frustum_lines_11	2002Oct8
PASS		
179	r400cl_frustum_lines_12	2002Oct8
PASS		
180	r400cl_frustum_lines_13	2002Oct8
PASS		
181	r400cl_frustum_lines_14	2002Oct8
PASS		
182	r400cl_frustum_lines_15	2002Oct8
PASS		
183	r400cl_frustum_lines_16	2002Oct8
PASS		
184	r400cl_frustum_lines_17	2002Oct8
PASS		
185	r400cl_frustum_lines_18	2002Oct8
PASS		
186	r400cl_frustum_lines_19	2002Oct8
PASS		
187	r400cl_frustum_lines_20	2002Oct8
PASS		
188	r400cl_frustum_lines_21	2002Oct8

PASS		
189	r400cl_frustum_lines_22	2002Oct8
PASS		
190	r400cl_frustum_lines_23	2002Oct8
PASS		
191	r400cl_frustum_lines_24	2002Oct8
PASS		
192	r400cl_frustum_lines_25	2002Oct8
PASS		
193	r400cl_frustum_lines_26	2002Oct8
PASS		
194	r400cl_frustum_lines_27	2002Oct8
PASS		
195	r400cl_frustum_lines_28	2002Oct8
PASS		
196	r400cl_frustum_lines_29	2002Oct8
PASS		
197	r400cl_frustum_lines_30	2002Oct8
PASS		
198	r400cl_frustum_lines_31	2002Oct8
PASS		
199	r400cl_frustum_lines_32	2002Oct8
PASS		
200	r400cl_frustum_lines_33	2002Oct8
PASS		
201	r400cl_frustum_lines_34	2002Oct8
PASS		
202	r400cl_frustum_lines_35	2002Oct8
PASS		
203	r400cl_frustum_lines_36	2002Oct8
PASS		
204	r400cl_frustum_lines_37	2002Oct8
PASS		
205	r400cl_frustum_lines_38	2002Oct8
PASS		
206	r400cl_frustum_lines_39	2002Oct8
PASS		
207	r400cl_frustum_lines_40	2002Oct8
PASS		
208	r400cl_frustum_lines_41	2002Oct8
PASS		
209	r400cl_frustum_lines_42	2002Oct8
PASS		
210	r400cl_frustum_lines_43	2002Oct8
PASS		
211	r400cl_frustum_lines_44	2002Oct8
PASS		
212	r400cl_frustum_lines_45	2002Oct8

PASS		
213	r400cl_frustum_lines_46	2002Oct8
PASS		
214	r400cl_frustum_lines_47	2002Oct8
PASS		
215	r400cl_frustum_lines_48	2002Oct8
PASS		
216	r400cl_frustum_lines_49	2002Oct8
PASS		
217	r400cl_frustum_lines_50	2002Oct8
PASS		
218	r400cl_frustum_lines_51	2002Oct8
PASS		
219	r400cl_frustum_lines_52	2002Oct8
PASS		
220	r400cl_gband_04	2002Oct8
PASS		
221	r400cl_gband_tcl_01	2002Oct8
PASS		
222	r400cl_nan_kill_01_bug	2002Oct8
PASS		
223	r400cl_pointlist_vertex_state_ucp_01	2002Oct8
PASS		
224	r400cl_point_ucp_combos_01	2002Oct8
INCOMPLETE		
225	r400cl_polymode_line_fill_01_bug	2002Oct8
PASS		
226	r400cl_simple_triangle_01	2002Oct8
PASS		
227	r400cl_triangle_polymode_line_stippled_01	2002Oct8
PASS		
228	r400cl_triangle_polymode_line_stippled_01_bug	2002Oct8
PASS		
229	r400cl_tri_polymode_line_stipple_ucp_combos_01	2002Oct8
PASS		
230	r400cl_tri_polymode_line_ucp_combos_01	2002Oct8
PASS		
231	r400cl_ucp_combos_01	2002Oct8
PASS		
232	r400cl_ucp_combos_02	2002Oct8
PASS		
233	r400cl_ucp_combos_03	2002Oct8
PASS		
234	r400cl_ucp_combos_04	2002Oct8
PASS		
235	r400cl_ucp_combos_05	2002Oct8
PASS		
236	r400cl_ucp_combos_06	2002Oct8

PASS		
237	r400cl_ucp_combos_07	20020ct8
PASS		
238	r400cl_ucp_combos_08	20020ct8
PASS		
239	r400cl_ucp_combos_09	20020ct8
PASS		
240	r400cl_ucp_combos_10	20020ct8
PASS		
241	r400cl_ucp_combos_11	20020ct8
PASS		
242	r400cl_ucp_combos_12	20020ct8
PASS		
243	r400cl_ucp_combos_13	20020ct8
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244	r400cl_ucp_combos_14	20020ct8
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245	r400cl_ucp_combos_15	20020ct8
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246	r400cl_ucp_combos_16	20020ct8
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247	r400cl_ucp_combos_17	20020ct8
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248	r400cl_ucp_combos_18	20020ct8
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249	r400cl_ucp_combos_19	20020ct8
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250	r400cl_ucp_combos_20	20020ct8
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251	r400cl_ucp_combos_21	20020ct8
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252	r400cl_ucp_combos_22	20020ct8
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253	r400cl_ucp_combos_23	20020ct8
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255	r400cl_ucp_combos_25	20020ct8
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256	r400cl_ucp_combos_26	20020ct8
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257	r400cl_ucp_combos_27	20020ct8
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258	r400cl_ucp_combos_28	20020ct8
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259	r400cl_ucp_combos_29	20020ct8
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260	r400cl_ucp_combos_30	20020ct8

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261	r400cl_ucp_pointlist_01	2002Oct8
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262	r400cl_vertex_reuse_clip_01	2002Oct8
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263	r400cl_vtx_kill_01	2002Oct8
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264	r400cl_vtx_kill_02	2002Oct8
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265	r400cl_w_eq_0	2002Oct8
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266	r400rbbm_simple_triangle_01	2002Oct8
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267	r400sanity_persp_projection_01	2002Oct8
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268	r400sanity_vfd_texture_sample_01	2002Oct8
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269	r400sc_baryc_01	2002Oct8
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270	r400sc_bres_cntl_01	2002Sep7
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271	r400sc_bres_cntl_02	2002Oct8
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272	r400sc_bres_cntl_03	2002Oct8
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273	r400sc_bres_cntl_04	2002Oct8
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274	r400sc_bres_cntl_w2k_01	2002Oct8
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275	r400sc_bres_cntl_w9x_01	2002Oct8
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276	r400sc_clip_rect_01	2002Oct8
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277	r400sc_clip_rect_02	2002Oct8
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278	r400sc_clip_rect_03	2002Oct8
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279	r400sc_clip_rect_04	2002Oct8
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280	r400sc_clip_rect_fc_01	2002Oct8
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281	r400sc_diamond_exit_01	2002Oct8
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282	r400sc_diamond_exit_02	2002Oct8
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283	r400sc_diamond_exit_03	2002Oct8
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284	r400sc_diamond_exit_04	2002Oct8

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285	r400sc_diamond_exit_05	2002Oct8
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286	r400sc_jss_1x1_primtypes_01	2002Oct8
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287	r400sc_jss_1x2_01	2002Oct8
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288	r400sc_jss_1x2_02	2002Oct8
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289	r400sc_jss_1x2_primtypes_01	2002Oct8
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290	r400sc_jss_1x3_01	2002Oct8
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291	r400sc_jss_1x3_02	2002Oct8
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292	r400sc_jss_1x3_primtypes_01	2002Oct8
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293	r400sc_jss_1x4_01	2002Oct8
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294	r400sc_jss_1x4_02	2002Oct8
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295	r400sc_jss_1x4_primtypes_01	2002Oct8
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296	r400sc_jss_2x1_01	2002Oct8
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297	r400sc_jss_2x1_02	2002Oct8
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298	r400sc_jss_2x1_primtypes_01	2002Oct8
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299	r400sc_jss_2x2_01	2002Oct8
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300	r400sc_jss_2x2_02	2002Oct8
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301	r400sc_jss_2x2_primtypes_01	2002Oct8
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302	r400sc_jss_2x3_01	2002Oct8
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303	r400sc_jss_2x3_02	2002Oct8
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304	r400sc_jss_2x3_primtypes_01	2002Oct8
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305	r400sc_jss_2x4_01	2002Oct8
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306	r400sc_jss_2x4_02	2002Oct8
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307	r400sc_jss_2x4_primtypes_01	2002Oct8
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308	r400sc_jss_3x1_01	2002Oct8

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315	r400sc_jss_3x3_02	20020ct8
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331	r400sc_jss_4x4_02	20020ct8
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332	r400sc_jss_4x4_03	20020ct8

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333	r400sc_jss_4x4_aa_mask_01	2002Oct8
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334	r400sc_jss_4x4_aa_mask_02	2002Oct8
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335	r400sc_jss_4x4_fc_01	2002Oct8
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336	r400sc_jss_4x4_fc_02	2002Oct8
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337	r400sc_jss_4x4_max_dist_01	2002Oct8
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338	r400sc_jss_4x4_primitives_01	2002Oct8
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339	r400sc_line_dx10_eq_0_01	2002Oct8
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340	r400sc_line_dx10_ge_0_01	2002Oct8
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341	r400sc_line_dx10_lt_0_01	2002Oct8
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342	r400sc_line_dy10_eq_0_01	2002Oct8
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343	r400sc_line_dy10_ge_0_01	2002Oct8
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344	r400sc_line_dy10_lt_0_01	2002Oct8
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345	r400sc_line_expand_width_msa_8_01	2002Oct8
PASS		
346	r400sc_line_expand_width_msa_8_02	2002Oct8
PASS		
347	r400sc_line_expand_width_msa_8_03	2002Oct8
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348	r400sc_line_jss_3x4_01	2002Oct8
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349	r400sc_line_list_01	2002Oct8
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350	r400sc_line_list_02	2002Oct8
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351	r400sc_line_list_concentric_circle_01	2002Oct8
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352	r400sc_line_list_concentric_circle_02	2002Oct8
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353	r400sc_line_list_concentric_circle_03	2002Oct8
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354	r400sc_line_list_textured_01	2002Oct8
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355	r400sc_line_list_verify_st_01	2002Oct8
PASS		
356	r400sc_line_list_verify_st_01_bug	2002Oct8

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357	r400sc_line_msa_8_01	2002Oct8
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358	r400sc_line_msa_8_textured_01	2002Oct8
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359	r400sc_line_stipple_01	2002Oct8
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360	r400sc_line_stipple_02	2002Oct8
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361	r400sc_line_stipple_03	2002Oct8
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362	r400sc_line_stipple_04	2002Oct8
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363	r400sc_line_stipple_05	2002Oct8
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364	r400sc_line_stipple_06	2002Oct8
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365	r400sc_line_stipple_07	2002Oct8
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366	r400sc_line_stipple_08	2002Oct8
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367	r400sc_line_stipple_09	2002Oct8
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368	r400sc_line_stipple_10	2002Oct8
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369	r400sc_line_stipple_11	2002Oct8
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370	r400sc_line_stipple_12	2002Oct8
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379	r400sc_line_stipple_21	2002Oct8
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380	r400sc_line_stipple_22	2002Oct8

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381	r400sc_line_stipple_23	2002Oct8
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382	r400sc_line_stipple_fc_08	2002Oct8
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383	r400sc_line_strip_stipple_01	2002Oct8
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384	r400sc_msaa_1_01	2002Oct8
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385	r400sc_msaa_1_primitives_01	2002Oct8
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386	r400sc_msaa_1_rectangle_list_01	2002Oct8
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395	r400sc_msaa_1_zbuffer_rectangle_list_02	2002Oct8
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396	r400sc_msaa_2_primitives_01	2002Oct8
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398	r400sc_msaa_2_rectangle_list_02	2002Oct8
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404	r400sc_msaa_2_rectangle_list_08	2002Oct8

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406	r400sc_msa_2_zbuffer_rectangle_list_02	2002Oct8
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409	r400sc_msa_3_rectangle_list_02	2002Oct8
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417	r400sc_msa_3_zbuffer_rectangle_list_02	2002Oct8
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420	r400sc_msa_4_rectangle_list_01	2002Oct8
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421	r400sc_msa_4_rectangle_list_02	2002Oct8
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427	r400sc_msa_4_rectangle_list_08	2002Oct8
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429	r400sc_msa_4_zbuffer_rectangle_list_02	2002Oct8
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430	r400sc_msa_6_01	2002Oct8
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431	r400sc_msa_6_primitives_01	2002Oct8
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432	r400sc_msa_6_rectangle_list_01	2002Oct8
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433	r400sc_msa_6_rectangle_list_02	2002Oct8
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441	r400sc_msa_6_zbuffer_rectangle_list_02	2002Oct8
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443	r400sc_msa_8_02	2002Oct8
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444	r400sc_msa_8_03	2002Oct8
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445	r400sc_msa_8_04	2002Oct8
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446	r400sc_msa_8_05	2002Oct8
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447	r400sc_msa_8_aa_mask_01	2002Oct8
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448	r400sc_msa_8_aa_mask_02	2002Oct8
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449	r400sc_msa_8_aa_mask_fc_02	2002Oct8
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450	r400sc_msa_8_hos_PNT_cont_cp_qn_complex_01	2002Oct8
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451	r400sc_msa_8_primitives_01	2002Oct8
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452	r400sc_msa_8_rectangle_list_01	2002Oct8

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453	r400sc_msa_8_rectangle_list_02	2002Oct8
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454	r400sc_msa_8_rectangle_list_03	2002Oct8
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459	r400sc_msa_8_rectangle_list_08	2002Oct8
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460	r400sc_msa_8_zbuffer_rectangle_list_01	2002Oct8
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461	r400sc_msa_8_zbuffer_rectangle_list_02	2002Oct8
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462	r400sc_msa_X_primitives_01_bug	2002Oct8
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463	r400sc_msa_X_primitives_02_bug	2002Oct8
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464	r400sc_multi_chip_prim_discard_00_07	2002Oct7
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465	r400sc_multi_chip_tile_discard_00_07	2002Oct7
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466	r400sc_null_triangles_01	2002Oct8
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467	r400sc_null_triangles_fc_01	2002Oct8
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468	r400sc_perf_01	2002Oct8
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469	r400sc_pinwheel_01	2002Oct8
PASS		
470	r400sc_pinwheel_02	2002Oct8
PASS		
471	r400sc_pinwheel_02_bug	2002Oct8
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472	r400sc_point_jss_3x4_01	2002Oct8
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473	r400sc_point_list_01	2002Oct8
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474	r400sc_point_list_02	2002Oct8
INCOMPLETE		
475	r400sc_point_list_03	2002Oct8
INCOMPLETE		
476	r400sc_point_list_04	2002Oct8

INCOMPLETE

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478	r400sc_point_list_06	2002Oct8
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479	r400sc_point_list_06_bug	2002Oct8
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480	r400sc_point_list_06_bug_02	2002Oct8
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481	r400sc_point_list_07	2002Oct8
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482	r400sc_point_list_08	2002Oct8
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483	r400sc_point_list_09	2002Oct8
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484	r400sc_point_msa_8_01	2002Oct8
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485	r400sc_polygon_stipple_01	2002Oct8
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486	r400sc_polymode_tri_fill_01	2002Oct8
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487	r400sc_polymode_tri_fill_01_bug	2002Oct8
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488	r400sc_poly_offset_01	2002Oct8
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489	r400sc_poly_offset_02	2002Oct8
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491	r400sc_poly_offset_04	2002Oct8
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493	r400sc_poly_offset_06	2002Oct8
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494	r400sc_poly_offset_07	2002Oct8
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496	r400sc_poly_offset_09	2002Oct8
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497	r400sc_poly_offset_10	2002Oct8
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498	r400sc_poly_offset_10_bug	2002Oct8
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499	r400sc_poly_offset_fc_01	2002Oct8
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500	r400sc_prsp_byc_intrp_ref_pix_01	2002Oct8

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501	r400sc_prsp_byc_intrp_ref_pix_02	2002Oct8
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502	r400sc_prsp_byc_intrp_ref_pix_03	2002Oct8
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503	r400sc_prsp_byc_intrp_ref_pix_04	2002Oct8
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506	r400sc_prsp_byc_intrp_ref_pix_07	2002Oct8
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507	r400sc_prsp_byc_intrp_ref_pix_08	2002Oct8
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508	r400sc_raster_fill_rule_01	2002Oct8
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509	r400sc_raster_fill_rule_02	2002Oct8
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510	r400sc_raster_fill_rule_03	2002Oct8
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511	r400sc_raster_fill_rule_04	2002Oct8
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512	r400sc_raster_fill_rule_05	2002Oct8
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513	r400sc_raster_fill_rule_fc_01	2002Oct8
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514	r400sc_rectangle_list_01	2002Oct8
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515	r400sc_rectangle_list_02	2002Oct8
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520	r400sc_rectangle_list_07	2002Oct8
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521	r400sc_rectangle_list_08	2002Oct8
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522	r400sc_scissor_rect_01	2002Oct8
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523	r400sc_scissor_rect_02	2002Oct8
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524	r400sc_scissor_rect_03	2002Oct8

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525	r400sc_scissor_rect_04	2002Oct8
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526	r400sc_scissor_rect_05	2002Oct8
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527	r400sc_scissor_rect_fc_01	2002Oct8
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528	r400sc_set_state_01	2002Oct8
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529	r400sc_sp_sample_cntl_01	2002Oct8
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530	r400sc_sp_sample_cntl_02	2002Oct8
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531	r400sc_sp_sample_cntl_03	2002Oct8
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532	r400sc_sp_sample_cntl_04	2002Oct8
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533	r400sc_sp_sample_cntl_05	2002Oct8
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534	r400sc_sp_sample_cntl_06	2002Oct8
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535	r400sc_sp_sample_cntl_07	2002Oct8
PASS		
536	r400sc_sp_sample_cntl_08	2002Oct8
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537	r400sc_tri_16_par_64_dwords_01	2002Oct8
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538	r400sc_tri_8textures_01	2002Oct8
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539	r400sc_tri_8textures_02	2002Oct8
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540	r400sc_triangle_stipple_01	2002Oct8
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541	r400sc_tri_walk_start_vertex_01	2002Oct8
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542	r400sc_tri_walk_start_vertex_02	2002Oct8
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543	r400sc_tri_walk_start_vertex_03	2002Oct8
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544	r400sc_tri_walk_start_vertex_04	2002Oct8
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545	r400sc_tri_walk_start_vertex_05	2002Oct8
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546	r400sc_tri_walk_start_vertex_06	2002Oct8
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547	r400sc_tri_walk_start_vertex_07	2002Oct8
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548	r400sc_tri_walk_start_vertex_08	2002Oct8

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549	r400sc_tri_walk_start_vertex_09	2002Oct8
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550	r400sc_tri_walk_start_vertex_10	2002Oct8
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551	r400sc_tri_walk_start_vertex_11	2002Oct8
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552	r400sc_tri_walk_start_vertex_12	2002Oct8
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553	r400sc_tri_walk_start_vertex_13	2002Oct8
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554	r400sc_tri_walk_start_vertex_14	2002Oct8
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555	r400sc_tri_walk_start_vertex_15	2002Oct8
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556	r400sc_tri_walk_start_vertex_16	2002Oct8
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557	r400sc_window_offset_01	2002Oct8
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559	r400sc_window_offset_03	2002Oct8
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561	r400sc_window_offset_05	2002Oct8
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562	r400sc_window_offset_fc_01	2002Oct8
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563	r400sc_zbuffer_line_list_01	2002Oct8
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564	r400sc_zbuffer_point_list_01	2002Oct8
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565	r400sc_zbuffer_rectangle_list_01	2002Oct8
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566	r400sc_zbuffer_rectangle_list_02	2002Oct8
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567	r400sc_zbuffer_rectangle_list_fc_02	2002Oct8
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568	r400sc_zbuffer_triangle_list_01	2002Oct8
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569	r400su_4tri_text_offscreen_01	2002Oct8
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570	r400su_baryc_test_01	2002Oct8
PASS		
571	r400su_baryc_test_02	2002Oct8
PASS		
572	r400su_baryc_test_03	2002Oct8

PASS		
573	r400su_baryc_test_04	2002Oct8
PASS		
574	r400su_clip_baryc_test_01	2002Oct8
PASS		
575	r400su_clip_baryc_test_02	2002Oct8
PASS		
576	r400su_clip_edgeflag_polymode_01	2002Oct8
PASS		
577	r400su_clipline_01	2002Oct8
PASS		
578	r400su_clippoint_01	2002Oct8
PASS		
579	r400su_clip_pointsize_test_01	2002Oct8
PASS		
580	r400su_clip_pointttest_01	2002Oct8
PASS		
581	r400su_clip_pointttest_02	2002Oct8
PASS		
582	r400su_clip_pointttest_03	2002Oct8
PASS		
583	r400su_clip_pointttest_04	2002Oct8
PASS		
584	r400su_clip_polymode_test_01	2002Oct8
PASS		
585	r400su_clipvertexsortingfunctional_01	2002Oct8
PASS		
586	r400su_cullingfunctional_01	2002Oct8
PASS		
587	r400su_degentri_test_01	2002Oct8
PASS		
588	r400su_degentri_test_02	2002Oct8
PASS		
589	r400su_degentri_test_03	2002Oct8
PASS		
590	r400su_edge_flag_01	2002Oct8
PASS		
591	r400su_edge_flag_02	2002Oct8
PASS		
592	r400su_edgeflags_triangle_01	2002Oct8
PASS		
593	r400su_edgeflags_triangle_02	2002Oct8
PASS		
594	r400su_geom_sort_01	2002Oct8
PASS		
595	r400su_line_end_cap_functional_01	2002Oct8
PASS		
596	r400su_line_end_cap_width_functional_02	2002Oct8

PASS		
597	r400su_line_orientation_01	2002Oct8
PASS		
598	r400su_line_orientation_02	2002Oct8
PASS		
599	r400su_line_orientation_dx01_01	2002Oct8
PASS		
600	r400su_line_orientation_dx01_02	2002Oct8
PASS		
601	r400su_line_orientation_dy01_01	2002Oct8
PASS		
602	r400su_line_orientation_dy01_02	2002Oct8
PASS		
603	r400su_line_test_01	2002Oct8
PASS		
604	r400su_line_test_02	2002Oct8
PASS		
605	r400su_longstrip_01	2002Oct8
PASS		
606	r400su_multi_context_01	2002Oct8
PASS		
607	r400su_multi_prim_01	2002Oct8
PASS		
608	r400su_multi_prim_02	2002Oct8
PASS		
609	r400su_parallel_orientation_all_01	2002Oct8
PASS		
610	r400su_parallel_orientation_all_02	2002Oct8
PASS		
611	r400su_pc_management_01	2002Oct8
PASS		
612	r400su_pc_management_02	2002Oct8
PASS		
613	r400su_pc_management_03	2002Oct8
PASS		
614	r400su_pointsizepresent_01	2002Oct8
PASS		
615	r400su_pointsizepresent_02	2002Oct8
PASS		
616	r400su_point_sprite_01	2002Oct8
PASS		
617	r400su_point_sprite_02	2002Oct8
PASS		
618	r400su_point_sprite_03	2002Oct8
PASS		
619	r400su_point_sprite_04	2002Oct8
PASS		
620	r400su_point_sprite_05	2002Oct8

PASS		
621	r400su_point_sprite_06	2002Oct8
PASS		
622	r400su_point_sprite_07	2002Oct8
PASS		
623	r400su_point_sprite_08	2002Oct8
PASS		
624	r400su_point_wl6_h1_functional_01	2002Oct8
PASS		
625	r400su_point_wl_h16_functional_01	2002Oct8
PASS		
626	r400su_polymodeculling_01	2002Oct8
PASS		
627	r400su_polymodefunctional_01	2002Oct8
PASS		
628	r400su_polymode_lines_degen_triangle_01	2002Oct8
PASS		
629	r400su_polymode_lines_degen_triangle_02	2002Oct8
PASS		
630	r400su_polymode_lines_zero_area_triangle_01	2002Oct8
PASS		
631	r400su_polymode_lines_zero_area_triangle_02	2002Oct8
PASS		
632	r400su_polymode_points_degen_triangle_01	2002Oct8
PASS		
633	r400su_polymode_points_degen_triangle_02	2002Oct8
PASS		
634	r400su_polymode_points_zero_area_triangle_01	2002Oct8
PASS		
635	r400su_polymode_points_zero_area_triangle_02	2002Oct8
PASS		
636	r400su_polymode_rectangle_01	2002Oct8
PASS		
637	r400su_polymode_zero_area_triangle_01	2002Oct8
PASS		
638	r400su_polymode_zero_area_triangle_02	2002Oct8
PASS		
639	r400su_polymode_zero_area_triangle_03	2002Oct8
PASS		
640	r400su_polymode_zero_area_triangle_04	2002Oct8
PASS		
641	r400su_provoking_vtx_edgeflags_triangle_01	2002Oct8
PASS		
642	r400su_provoking_vtx_edgeflags_triangle_02	2002Oct8
PASS		
643	r400su_provoking_vtx_edgeflags_triangle_03	2002Oct8
PASS		
644	r400su_provoking_vtx_edgeflags_triangle_04	2002Oct8

PASS		
645	r400su_provoking_vtx_line_01	2002Oct8
PASS		
646	r400su_provoking_vtx_point_01	2002Oct8
PASS		
647	r400su_provoking_vtx_polymode_rectangle_01	2002Oct8
PASS		
648	r400su_provoking_vtx_rectangle_01	2002Oct8
PASS		
649	r400su_provoking_vtx_triangle_01	2002Oct8
PASS		
650	r400su_rand_line_01	2002Oct8
PASS		
651	r400su_rand_point_01	2002Oct8
PASS		
652	r400su_rand_tri_01	2002Oct8
PASS		
653	r400su_rectangle_01	2002Oct8
PASS		
654	r400su_rectangle_list_01	2002Oct8
PASS		
655	r400su_sliver_01	2002Oct8
PASS		
656	r400su_triarea_test_01	2002Oct8
PASS		
657	r400su_triarea_test_02	2002Oct8
PASS		
658	r400su_triarea_test_03	2002Oct8
PASS		
659	r400su_triarea_test_04	2002Oct8
PASS		
660	r400su_vertexsortingfunctional_01	2002Oct8
PASS		
661	r400su_w_grad_test_01	2002Oct8
PASS		
662	r400su_w_grad_test_02	2002Oct8
PASS		
663	r400su_w_grad_test_03	2002Oct8
PASS		
664	r400su_z_grad_test_01	2002Oct8
PASS		
665	r400su_z_grad_test_02	2002Oct8
PASS		
666	r400su_z_grad_test_03	2002Oct8
PASS		
667	r400vgt_auto_index_line_list_01	2002Oct8
PASS		
668	r400vgt_auto_index_line_loop_01	2002Oct8

PASS		
669	r400vgt_auto_index_line_strip_01	2002Oct8
PASS		
670	r400vgt_auto_index_points_01	2002Oct8
PASS		
671	r400vgt_auto_index_polygon_01	2002Oct8
PASS		
672	r400vgt_auto_index_primitives_01	2002Oct8
PASS		
673	r400vgt_auto_index_quad_list_01	2002Oct8
PASS		
674	r400vgt_auto_index_quad_strip_01	2002Oct8
PASS		
675	r400vgt_auto_index_rectangle_list_01	2002Oct8
PASS		
676	r400vgt_auto_index_tri_fan_01	2002Oct8
PASS		
677	r400vgt_auto_index_tri_list_01	2002Oct8
PASS		
678	r400vgt_auto_index_tri_strip_01	2002Oct8
PASS		
679	r400vgt_auto_index_tri_wflags_01	2002Oct8
PASS		
680	r400vgt_dma_engine_01	2002Oct8
PASS		
681	r400vgt_dma_engine_02	2002Oct8
PASS		
682	r400vgt_dma_engine_03	2002Oct8
PASS		
683	r400vgt_dma_engine_04	2002Oct8
PASS		
684	r400vgt_dma_engine_05	2002Oct8
PASS		
685	r400vgt_dma_engine_06	2002Oct8
PASS		
686	r400vgt_dma_engine_07	2002Oct8
PASS		
687	r400vgt_dma_engine_08	2002Oct8
PASS		
688	r400vgt_dma_engine_09	2002Oct8
PASS		
689	r400vgt_dma_engine_10	2002Oct8
PASS		
690	r400vgt_dma_index_line_list_01	2002Oct8
PASS		
691	r400vgt_dma_index_line_loop_01	2002Oct8
PASS		
692	r400vgt_dma_index_line_strip_01	2002Oct8

PASS		
693	r400vgt_dma_index_points_01	2002Oct8
PASS		
694	r400vgt_dma_index_polygon_01	2002Oct8
PASS		
695	r400vgt_dma_index_primitives_01	2002Oct8
PASS		
696	r400vgt_dma_index_quad_list_01	2002Oct8
PASS		
697	r400vgt_dma_index_quad_strip_01	2002Oct8
PASS		
698	r400vgt_dma_index_rectangle_list_01	2002Oct8
PASS		
699	r400vgt_dma_index_tri_fan_01	2002Oct8
PASS		
700	r400vgt_dma_index_tri_list_01	2002Oct8
PASS		
701	r400vgt_dma_index_tri_strip_01	2002Oct8
PASS		
702	r400vgt_dma_index_tri_wflags_01	2002Oct8
PASS		
703	r400vgt_dma_swap_indx16_01	2002Oct8
PASS		
704	r400vgt_dma_swap_indx32_01	2002Oct8
PASS		
705	r400vgt_edgeflags_polygon_01	2002Oct8
PASS		
706	r400vgt_edgeflags_quad_list_01	2002Oct8
PASS		
707	r400vgt_edgeflags_quad_strip_01	2002Oct8
PASS		
708	r400vgt_edgeflags_triangle_fan_01	2002Oct8
PASS		
709	r400vgt_edgeflags_triangle_list_01	2002Oct8
PASS		
710	r400vgt_edgeflags_triangle_strip_01	2002Oct8
PASS		
711	r400vgt_edgeflags_triangle_wflags_01	2002Oct8
PASS		
712	r400vgt_event_handling_01	2002Sep26
PASS		
713	r400vgt_ext2int_index_line_list_01	2002Oct8
PASS		
714	r400vgt_ext2int_index_line_loop_01	2002Oct8
PASS		
715	r400vgt_ext2int_index_line_strip_01	2002Oct8
PASS		
716	r400vgt_ext2int_index_points_01	2002Oct8

PASS		
717	r400vgt_ext2int_index_polygon_01	2002Oct8
PASS		
718	r400vgt_ext2int_index_quad_list_01	2002Oct8
PASS		
719	r400vgt_ext2int_index_quad_strip_01	2002Oct8
PASS		
720	r400vgt_ext2int_index_rectangle_list_01	2002Oct8
PASS		
721	r400vgt_ext2int_index_triangle_fan_01	2002Oct8
PASS		
722	r400vgt_ext2int_index_triangle_list_01	2002Oct8
PASS		
723	r400vgt_ext2int_index_triangle_strip_01	2002Oct8
PASS		
724	r400vgt_ext2int_index_triangle_wflags_01	2002Oct8
PASS		
725	r400vgt_hos_auto_index_line_list_01	2002Oct8
PASS		
726	r400vgt_hos_auto_index_quad_list_01	2002Oct8
PASS		
727	r400vgt_hos_auto_index_triangle_list_01	2002Oct8
PASS		
728	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Oct8
PASS		
729	r400vgt_hos_LPatch_01	2002Oct8
PASS		
730	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Oct8
PASS		
731	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Oct8
PASS		
732	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Oct8
PASS		
733	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Oct8
PASS		
734	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Oct8
PASS		
735	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Oct8
PASS		
736	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Oct8
PASS		
737	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Oct8
PASS		
738	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Oct8
PASS		
739	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Oct8
PASS		
740	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Oct8

PASS		
741	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Oct8
PASS		
742	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Oct8
PASS		
743	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Oct8
PASS		
744	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Oct8
PASS		
745	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Oct8
PASS		
746	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Oct8
PASS		
747	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Oct8
PASS		
748	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Oct8
PASS		
749	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Oct8
PASS		
750	r400vgt_hos_PNT_edge_detection_01	2002Oct8
PASS		
751	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Oct8
PASS		
752	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Oct8
PASS		
753	r400vgt_hos_RPatch_cp_02	2002Oct8
PASS		
754	r400vgt_hos_RPatch_lp_02	2002Oct8
PASS		
755	r400vgt_hos_simple_linear_PNT_discrete_01	2002Oct8
PASS		
756	r400vgt_hos_TPatch_01	2002Oct8
PASS		
757	r400vgt_hos_TPatch_02	2002Oct8
PASS		
758	r400vgt_immed_index_line_list_01	2002Oct8
PASS		
759	r400vgt_immed_index_line_loop_01	2002Oct8
PASS		
760	r400vgt_immed_index_line_strip_01	2002Oct8
PASS		
761	r400vgt_immed_index_points_01	2002Oct8
PASS		
762	r400vgt_immed_index_polygon_01	2002Oct8
PASS		
763	r400vgt_immed_index_primitives_01	2002Oct8
PASS		
764	r400vgt_immed_index_quad_list_01	2002Oct8

PASS		
765	r400vgt_immed_index_quad_strip_01	2002Oct8
PASS		
766	r400vgt_immed_index_rectangle_list_01	2002Oct8
PASS		
767	r400vgt_immed_index_tri_fan_01	2002Oct8
PASS		
768	r400vgt_immed_index_tri_list_01	2002Oct8
PASS		
769	r400vgt_immed_index_tri_strip_01	2002Oct8
PASS		
770	r400vgt_immed_index_tri_wflags_01	2002Oct8
PASS		
771	r400vgt_index_dealloc_line_list_01	2002Oct8
PASS		
772	r400vgt_index_dealloc_points_01	2002Oct8
PASS		
773	r400vgt_index_dealloc_triangle_list_01	2002Oct8
PASS		
774	r400vgt_index_min_max_01	2002Oct8
PASS		
775	r400vgt_index_min_max_02	2002Oct8
PASS		
776	r400vgt_index_offset_01	2002Oct8
PASS		
777	r400vgt_index_offset_02	2002Oct8
PASS		
778	r400vgt_index_offset_03	2002Oct8
PASS		
779	r400vgt_index_offset_04	2002Oct8
PASS		
780	r400vgt_index_offset_05	2002Oct8
PASS		
781	r400vgt_index_offset_06	2002Oct8
PASS		
782	r400vgt_index_offset_07	2002Oct8
PASS		
783	r400vgt_index_offset_08	2002Oct8
PASS		
784	r400vgt_index_size_01	2002Oct8
PASS		
785	r400vgt_index_source_switch_01	2002Oct8
PASS		
786	r400vgt_line_list_01	2002Oct8
PASS		
787	r400vgt_line_list_02	2002Oct8
PASS		
788	r400vgt_line_loop_01	2002Oct8

PASS		
789	r400vgt_line_loop_02	2002Oct8
PASS		
790	r400vgt_line_strip_01	2002Oct8
PASS		
791	r400vgt_line_strip_02	2002Oct8
PASS		
792	r400vgt_multi_context_01	2002Oct8
PASS		
793	r400vgt_multi_context_02	2002Oct8
PASS		
794	r400vgt_multi_context_03	2002Oct8
PASS		
795	r400vgt_multi_context_04	2002Oct8
PASS		
796	r400vgt_multi_context_05	2002Oct8
PASS		
797	r400vgt_multi_context_06	2002Oct8
PASS		
798	r400vgt_multi_context_07	2002Oct8
PASS		
799	r400vgt_multi_context_08	2002Oct8
PASS		
800	r400vgt_multi_context_09	2002Oct8
PASS		
801	r400vgt_multi_context_10	2002Oct8
PASS		
802	r400vgt_multi_context_11	2002Oct8
PASS		
803	r400vgt_multi_context_12	2002Oct8
PASS		
804	r400vgt_multi_prim_reset_index_all_01	2002Oct8
PASS		
805	r400vgt_multi_prim_reset_index_all_02	2002Oct8
PASS		
806	r400vgt_pass_thru_all_prims_01	2002Oct8
PASS		
807	r400vgt_point_list_01	2002Oct8
PASS		
808	r400vgt_point_list_02	2002Oct8
PASS		
809	r400vgt_polygon_01	2002Oct8
PASS		
810	r400vgt_polygon_02	2002Oct8
PASS		
811	r400vgt_provoking_vtx_all_01	2002Oct8
PASS		
812	r400vgt_provoking_vtx_edgeflags_all_01	2002Oct8

PASS			
813	r400vgt_provoking_vtx_polygon_01		2002Oct8
PASS			
814	r400vgt_provoking_vtx_quad_list_01		2002Oct8
PASS			
815	r400vgt_provoking_vtx_quad_strip_01		2002Oct8
PASS			
816	r400vgt_provoking_vtx_tri_fan_01		2002Oct8
PASS			
817	r400vgt_provoking_vtx_tri_strip_01		2002Oct8
PASS			
818	r400vgt_quad_list_01		2002Oct8
PASS			
819	r400vgt_quad_list_02		2002Oct8
PASS			
820	r400vgt_quad_strip_01		2002Oct8
PASS			
821	r400vgt_quad_strip_02		2002Oct8
PASS			
822	r400vgt_rectangle_list_01		2002Oct8
PASS			
823	r400vgt_rectangle_list_02		2002Oct8
PASS			
824	r400vgt_reuse_depth_line_list_01		2002Oct8
PASS			
825	r400vgt_reuse_depth_point_list_01		2002Oct8
PASS			
826	r400vgt_reuse_depth_triangle_list_01		2002Oct8
PASS			
827	r400vgt_reuse_index_line_list_01		2002Oct8
PASS			
828	r400vgt_reuse_index_point_list_01		2002Oct8
PASS			
829	r400vgt_reuse_index_triangle_list_01		2002Oct8
PASS			
830	r400vgt_reuse_index_triangle_list_02		2002Oct8
PASS			
831	r400vgt_reuse_index_triangle_list_03		2002Oct8
PASS			
832	r400vgt_suppress_eop_01	0:0:1	2002Oct8
PASS			
833	r400vgt_suppress_eop_02	0:0:1	2002Oct8
PASS			
834	r400vgt_suppress_eop_03	0:0:1	2002Oct8
PASS			
835	r400vgt_suppress_eop_04	0:0:1	2002Oct8
PASS			
836	r400vgt_suppress_eop_05	0:0:1	2002Sep26

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PASS
837 r400vgt_triangle_fan_01          0:0:1  2002Oct8
PASS
838 r400vgt_triangle_fan_02          0:0:1  2002Oct8
PASS
839 r400vgt_triangle_list_01         0:0:1  2002Oct8
PASS
840 r400vgt_triangle_list_02         0:0:1  2002Oct8
PASS
841 r400vgt_triangle_strip_01        0:0:1  2002Oct8
PASS
842 r400vgt_triangle_strip_02        0:0:1  2002Oct8
PASS
843 r400vgt_triangle_wflags_01       0:0:1  2002Oct8
PASS
844 r400vgt_triangle_wflags_02       0:0:1  2002Oct8
PASS
845 r400vgt_viz_query_01             0:0:1  2002Oct8
PASS
846 r400vgt_vtx_vector_packing_01    0:0:1  2002Oct8
PASS
847 r400vte_coverage_01              0:0:1  2002Oct8
PASS
848 r400vte_simple_point_01          0:0:1  2002Oct8
PASS
849 r400vte_simple_triangle_01       0:0:1  2002Oct8
PASS
850 r400vte_w0_fmt_01                0:0:1  2002Oct8
PASS
851 r400vte_w0_fmt_03                0:0:1  2002Oct8
PASS
852 r400vte_xy_fmt_01                0:0:1  2002Oct8
PASS
853 r400vte_xy_fmt_mk_01_bug         0:0:1  2002Oct8
PASS
854 r400vte_xyz_scale_01             0:0:1  2002Oct8
PASS
855 r400vte_xyz_scale_02             0:0:1  2002Oct8
PASS
856 r400vte_z_fmt_01                 0:0:1  2002Oct8
PASS

```

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=====
Tests_Run_count = 856 Pass_count = 850 Fail_count = 1 Incomplete_count = 5
=====

```

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++
++++      Date: Tue Sep 24 10:30:41 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
++++      CP          11          11          11          100
++++      RBBM        1           1           1           100
++++      VGT         170         170         140          82
++++      SC          275         275         245          89
++++      CL          115         115         115          100
++++      VTE         10          10          10           100
++++      SPECIALTY   6           6           5            83
++++      SU          85          85          85           100
++++      SANITY      2           2           2           100
+++++
++++      TOTAL        675         675         614          90
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep 18 10:01:54 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             161         161         156           96
              SC              244         244         241           98
              CL              110         110         110           100
              VTE              9          9           9           100
              SPECIALTY         6          6           6           100
              SU              85          85          85           100
              SANITY            2          2           2           100
+++++
              TOTAL            629         629         621           98
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	cp_dma_simple		2002Sep18
PASS			
2	cp_e2blit_brush_mt_ropcc		2002Sep18
PASS			
3	cp_e2_hostdata_blt_pntr_8888		2002Sep18
PASS			
4	cp_e2_one_blit		2002Sep18
PASS			
5	cp_e2_one_line		2002Sep18
PASS			
6	cp_e2paint_565		2002Sep18
PASS			
7	cp_e2paint_multi		2002Sep18
PASS			
8	cp_e2polyscanlines_brush		2002Sep18
PASS			
9	cp_e2polyscanlines_brush_mt		2002Sep18
PASS			
10	cp_e2smalltext_jcl		2002Sep18
PASS			
11	cp_simple_triangle		2002Sep18
PASS			
12	primlib_1st_tri_june15		2002Sep18
PASS			
13	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep18
PASS			
14	primlib_gouraud_triangles_2_draw_passes		2002Sep18
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351	r400sc_tri_walk_start_vertex_03	2002Sep18
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352	r400sc_tri_walk_start_vertex_04	2002Sep18
PASS		
353	r400sc_tri_walk_start_vertex_05	2002Sep18
PASS		
354	r400sc_tri_walk_start_vertex_06	2002Sep18
PASS		
355	r400sc_tri_walk_start_vertex_07	2002Sep18
PASS		
356	r400sc_tri_walk_start_vertex_08	2002Sep18

PASS		
357	r400sc_tri_walk_start_vertex_09	2002Sep18
PASS		
358	r400sc_tri_walk_start_vertex_10	2002Sep18
PASS		
359	r400sc_tri_walk_start_vertex_11	2002Sep18
PASS		
360	r400sc_tri_walk_start_vertex_12	2002Sep18
PASS		
361	r400sc_tri_walk_start_vertex_13	2002Sep18
PASS		
362	r400sc_tri_walk_start_vertex_14	2002Sep18
PASS		
363	r400sc_tri_walk_start_vertex_15	2002Sep18
PASS		
364	r400sc_tri_walk_start_vertex_16	2002Sep18
PASS		
365	r400sc_window_offset_01	2002Sep18
PASS		
366	r400sc_window_offset_fc_01	2002Sep18
PASS		
367	r400sc_zbuffer_line_list_01	2002Sep18
PASS		
368	r400sc_zbuffer_point_list_01	2002Sep18
PASS		
369	r400sc_zbuffer_rectangle_list_01	2002Sep18
PASS		
370	r400sc_zbuffer_rectangle_list_02	2002Sep18
PASS		
371	r400sc_zbuffer_rectangle_list_fc_02	2002Sep18
PASS		
372	r400sc_zbuffer_triangle_list_01	2002Sep18
PASS		
373	r400su_4tri_text_offscreen_01	2002Sep18
PASS		
374	r400su_baryc_test_01	2002Sep18
PASS		
375	r400su_baryc_test_02	2002Sep18
PASS		
376	r400su_baryc_test_03	2002Sep18
PASS		
377	r400su_baryc_test_04	2002Sep18
PASS		
378	r400su_clip_baryc_test_01	2002Sep18
PASS		
379	r400su_clip_baryc_test_02	2002Sep18
PASS		
380	r400su_clip_edgeflag_polymode_01	2002Sep18

PASS		
381	r400su_clipline_01	2002Sep18
PASS		
382	r400su_clippoint_01	2002Sep18
PASS		
383	r400su_clip_pointsize_test_01	2002Sep18
PASS		
384	r400su_clip_pointttest_01	2002Sep18
PASS		
385	r400su_clip_pointttest_02	2002Sep18
PASS		
386	r400su_clip_pointttest_03	2002Sep18
PASS		
387	r400su_clip_pointttest_04	2002Sep18
PASS		
388	r400su_clip_polymode_test_01	2002Sep18
PASS		
389	r400su_clipvertexsortingfunctional_01	2002Sep18
PASS		
390	r400su_cullingfunctional_01	2002Sep18
PASS		
391	r400su_degentri_test_01	2002Sep18
PASS		
392	r400su_degentri_test_02	2002Sep18
PASS		
393	r400su_degentri_test_03	2002Sep18
PASS		
394	r400su_edge_flag_01	2002Sep18
PASS		
395	r400su_edge_flag_02	2002Sep18
PASS		
396	r400su_edgeflags_triangle_01	2002Sep18
PASS		
397	r400su_edgeflags_triangle_02	2002Sep18
PASS		
398	r400su_geom_sort_01	2002Sep18
PASS		
399	r400su_line_end_cap_functional_01	2002Sep18
PASS		
400	r400su_line_end_cap_width_functional_02	2002Sep18
PASS		
401	r400su_line_test_01	2002Sep18
PASS		
402	r400su_line_test_02	2002Sep18
PASS		
403	r400su_longstrip_01	2002Sep18
PASS		
404	r400su_multi_context_01	2002Sep18

PASS		
405	r400su_multi_prim_01	2002Sep18
PASS		
406	r400su_multi_prim_02	2002Sep18
PASS		
407	r400su_parallel_orientation_all_01	2002Sep18
PASS		
408	r400su_parallel_orientation_all_02	2002Sep18
PASS		
409	r400su_pc_management_01	2002Sep18
PASS		
410	r400su_pc_management_02	2002Sep18
PASS		
411	r400su_pc_management_03	2002Sep18
PASS		
412	r400su_pointsizepresent_01	2002Sep18
PASS		
413	r400su_pointsizepresent_02	2002Sep18
PASS		
414	r400su_point_sprite_01	2002Sep18
PASS		
415	r400su_point_sprite_02	2002Sep18
PASS		
416	r400su_point_sprite_03	2002Sep18
PASS		
417	r400su_point_sprite_04	2002Sep18
PASS		
418	r400su_point_sprite_05	2002Sep18
PASS		
419	r400su_point_sprite_06	2002Sep18
PASS		
420	r400su_point_sprite_07	2002Sep18
PASS		
421	r400su_point_sprite_08	2002Sep18
PASS		
422	r400su_point_wl6_h1_functional_01	2002Sep18
PASS		
423	r400su_point_wl_h16_functional_01	2002Sep18
PASS		
424	r400su_polymodeculling_01	2002Sep18
PASS		
425	r400su_polymodefunctional_01	2002Sep18
PASS		
426	r400su_polymode_lines_degen_triangle_01	2002Sep18
PASS		
427	r400su_polymode_lines_degen_triangle_02	2002Sep18
PASS		
428	r400su_polymode_lines_zero_area_triangle_01	2002Sep18

PASS		
429	r400su_polymode_lines_zero_area_triangle_02	2002Sep18
PASS		
430	r400su_polymode_points_degen_triangle_01	2002Sep18
PASS		
431	r400su_polymode_points_degen_triangle_02	2002Sep18
PASS		
432	r400su_polymode_points_zero_area_triangle_01	2002Sep18
PASS		
433	r400su_polymode_points_zero_area_triangle_02	2002Sep18
PASS		
434	r400su_polymode_rectangle_01	2002Sep18
PASS		
435	r400su_polymode_zero_area_triangle_01	2002Sep18
PASS		
436	r400su_polymode_zero_area_triangle_02	2002Sep18
PASS		
437	r400su_polymode_zero_area_triangle_03	2002Sep18
PASS		
438	r400su_polymode_zero_area_triangle_04	2002Sep18
PASS		
439	r400su_provoking_vtx_edgeflags_triangle_01	2002Sep18
PASS		
440	r400su_provoking_vtx_edgeflags_triangle_02	2002Sep18
PASS		
441	r400su_provoking_vtx_edgeflags_triangle_03	2002Sep18
PASS		
442	r400su_provoking_vtx_edgeflags_triangle_04	2002Sep18
PASS		
443	r400su_provoking_vtx_line_01	2002Sep18
PASS		
444	r400su_provoking_vtx_point_01	2002Sep18
PASS		
445	r400su_provoking_vtx_polymode_rectangle_01	2002Sep18
PASS		
446	r400su_provoking_vtx_rectangle_01	2002Sep18
PASS		
447	r400su_provoking_vtx_triangle_01	2002Sep18
PASS		
448	r400su_rand_line_01	2002Sep18
PASS		
449	r400su_rand_point_01	2002Sep18
PASS		
450	r400su_rand_tri_01	2002Sep18
PASS		
451	r400su_rectangle_list_01	2002Sep18
PASS		
452	r400su_sliver_01	2002Sep18

PASS		
453	r400su_triarea_test_01	2002Sep18
PASS		
454	r400su_triarea_test_02	2002Sep18
PASS		
455	r400su_triarea_test_03	2002Sep18
PASS		
456	r400su_triarea_test_04	2002Sep18
PASS		
457	r400su_vertexsortingfunctional_01	2002Sep18
PASS		
458	r400vgt_auto_index_line_list_01	2002Sep18
PASS		
459	r400vgt_auto_index_line_loop_01	2002Sep18
PASS		
460	r400vgt_auto_index_line_strip_01	2002Sep18
PASS		
461	r400vgt_auto_index_points_01	2002Sep18
PASS		
462	r400vgt_auto_index_polygon_01	2002Sep18
PASS		
463	r400vgt_auto_index_primitives_01	2002Sep18
PASS		
464	r400vgt_auto_index_quad_list_01	2002Sep18
PASS		
465	r400vgt_auto_index_quad_strip_01	2002Sep18
PASS		
466	r400vgt_auto_index_rectangle_list_01	2002Sep18
PASS		
467	r400vgt_auto_index_tri_fan_01	2002Sep18
PASS		
468	r400vgt_auto_index_tri_list_01	2002Sep18
PASS		
469	r400vgt_auto_index_tri_strip_01	2002Sep18
PASS		
470	r400vgt_auto_index_tri_wflags_01	2002Sep18
PASS		
471	r400vgt_dma_engine_01	2002Sep18
PASS		
472	r400vgt_dma_engine_02	2002Sep18
PASS		
473	r400vgt_dma_engine_03	2002Sep18
PASS		
474	r400vgt_dma_engine_04	2002Sep18
PASS		
475	r400vgt_dma_engine_05	2002Sep18
PASS		
476	r400vgt_dma_engine_06	2002Sep18

PASS		
477	r400vgt_dma_engine_07	2002Sep18
PASS		
478	r400vgt_dma_engine_08	2002Sep18
PASS		
479	r400vgt_dma_engine_09	2002Sep18
PASS		
480	r400vgt_dma_engine_10	2002Sep18
PASS		
481	r400vgt_dma_index_line_list_01	2002Sep18
PASS		
482	r400vgt_dma_index_line_loop_01	2002Sep18
PASS		
483	r400vgt_dma_index_line_strip_01	2002Sep18
PASS		
484	r400vgt_dma_index_points_01	2002Sep18
PASS		
485	r400vgt_dma_index_polygon_01	2002Sep18
PASS		
486	r400vgt_dma_index_primitives_01	2002Sep18
PASS		
487	r400vgt_dma_index_quad_list_01	2002Sep18
PASS		
488	r400vgt_dma_index_quad_strip_01	2002Sep18
PASS		
489	r400vgt_dma_index_rectangle_list_01	2002Sep18
PASS		
490	r400vgt_dma_index_tri_fan_01	2002Sep18
PASS		
491	r400vgt_dma_index_tri_list_01	2002Sep18
PASS		
492	r400vgt_dma_index_tri_strip_01	2002Sep18
PASS		
493	r400vgt_dma_index_tri_wflags_01	2002Sep18
PASS		
494	r400vgt_dma_swap_idx16_01	2002Sep18
PASS		
495	r400vgt_dma_swap_idx32_01	2002Sep18
PASS		
496	r400vgt_edgeflags_polygon_01	2002Sep18
PASS		
497	r400vgt_edgeflags_quad_list_01	2002Sep18
PASS		
498	r400vgt_edgeflags_quad_strip_01	2002Sep18
PASS		
499	r400vgt_edgeflags_triangle_fan_01	2002Sep18
PASS		
500	r400vgt_edgeflags_triangle_list_01	2002Sep18

PASS		
501	r400vgt_edgeflags_triangle_strip_01	2002Sep18
PASS		
502	r400vgt_edgeflags_triangle_wflags_01	2002Sep18
PASS		
503	r400vgt_ext2int_index_line_list_01	2002Sep18
PASS		
504	r400vgt_ext2int_index_line_loop_01	2002Sep18
PASS		
505	r400vgt_ext2int_index_line_strip_01	2002Sep18
PASS		
506	r400vgt_ext2int_index_points_01	2002Sep18
PASS		
507	r400vgt_ext2int_index_polygon_01	2002Sep18
PASS		
508	r400vgt_ext2int_index_quad_list_01	2002Sep18
PASS		
509	r400vgt_ext2int_index_quad_strip_01	2002Sep18
PASS		
510	r400vgt_ext2int_index_triangle_fan_01	2002Sep18
PASS		
511	r400vgt_ext2int_index_triangle_list_01	2002Sep18
PASS		
512	r400vgt_ext2int_index_triangle_strip_01	2002Sep18
PASS		
513	r400vgt_hos_auto_index_line_list_01	2002Sep18
PASS		
514	r400vgt_hos_auto_index_quad_list_01	2002Sep18
PASS		
515	r400vgt_hos_auto_index_triangle_list_01	2002Sep18
PASS		
516	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep18
PASS		
517	r400vgt_hos_LPatch_01	2002Sep18
PASS		
518	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep18
PASS		
519	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep18
PASS		
520	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep18
PASS		
521	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep18
PASS		
522	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep18
PASS		
523	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep18
PASS		
524	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep18

PASS		
525	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep18
PASS		
526	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep18
PASS		
527	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep18
PASS		
528	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep18
PASS		
529	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep18
PASS		
530	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep18
PASS		
531	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep18
PASS		
532	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep18
PASS		
533	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep18
PASS		
534	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep18
PASS		
535	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep18
PASS		
536	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep18
PASS		
537	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep18
PASS		
538	r400vgt_hos_PNT_edge_detection_01	2002Sep18
PASS		
539	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep18
PASS		
540	r400vgt_hos_PNTQL_cp_qn_cont_stress_01	2002Sep18
INCOMPLETE		
541	r400vgt_hos_RPatch_cp_02	2002Sep18
PASS		
542	r400vgt_hos_RPatch_lp_02	2002Sep18
PASS		
543	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep13
INCOMPLETE		
544	r400vgt_hos_TPatch_01	2002Sep13
INCOMPLETE		
545	r400vgt_hos_TPatch_02	2002Sep13
INCOMPLETE		
546	r400vgt_immed_index_line_list_01	2002Sep18
PASS		
547	r400vgt_immed_index_line_loop_01	2002Sep18
PASS		
548	r400vgt_immed_index_line_strip_01	2002Sep18

PASS		
549	r400vgt_immed_index_points_01	2002Sep18
PASS		
550	r400vgt_immed_index_polygon_01	2002Sep18
PASS		
551	r400vgt_immed_index_primitives_01	2002Sep18
PASS		
552	r400vgt_immed_index_quad_list_01	2002Sep18
PASS		
553	r400vgt_immed_index_quad_strip_01	2002Sep18
PASS		
554	r400vgt_immed_index_rectangle_list_01	2002Sep18
PASS		
555	r400vgt_immed_index_tri_fan_01	2002Sep18
PASS		
556	r400vgt_immed_index_tri_list_01	2002Sep18
PASS		
557	r400vgt_immed_index_tri_strip_01	2002Sep18
PASS		
558	r400vgt_immed_index_tri_wflags_01	2002Sep18
PASS		
559	r400vgt_index_dealloc_line_list_01	2002Sep18
PASS		
560	r400vgt_index_dealloc_points_01	2002Sep18
PASS		
561	r400vgt_index_dealloc_triangle_list_01	2002Sep18
PASS		
562	r400vgt_index_min_max_01	2002Sep18
PASS		
563	r400vgt_index_min_max_02	2002Sep18
PASS		
564	r400vgt_index_offset_01	2002Sep18
PASS		
565	r400vgt_index_offset_02	2002Sep18
PASS		
566	r400vgt_index_offset_03	2002Sep18
PASS		
567	r400vgt_index_size_01	2002Sep18
PASS		
568	r400vgt_index_source_switch_01	2002Sep18
PASS		
569	r400vgt_line_list_01	2002Sep18
PASS		
570	r400vgt_line_list_02	2002Sep18
PASS		
571	r400vgt_line_loop_01	2002Sep18
PASS		
572	r400vgt_line_loop_02	2002Sep18

PASS		
573	r400vgt_line_strip_01	2002Sep18
PASS		
574	r400vgt_line_strip_02	2002Sep18
PASS		
575	r400vgt_multi_context_01	2002Sep18
PASS		
576	r400vgt_multi_context_02	2002Sep18
PASS		
577	r400vgt_multi_context_03	2002Sep18
PASS		
578	r400vgt_multi_context_04	2002Sep18
PASS		
579	r400vgt_multi_context_05	2002Sep18
PASS		
580	r400vgt_multi_context_06	2002Sep18
PASS		
581	r400vgt_multi_context_07	2002Sep18
PASS		
582	r400vgt_multi_context_08	2002Sep18
PASS		
583	r400vgt_multi_context_09	2002Sep18
PASS		
584	r400vgt_multi_context_10	2002Sep18
PASS		
585	r400vgt_multi_context_11	2002Sep18
PASS		
586	r400vgt_multi_context_12	2002Sep18
PASS		
587	r400vgt_multi_prim_reset_index_all_01	2002Sep18
FAIL	VgtPaCcGen_VgtSide.cmp VgtPaClipP_VgtSide.cmp	
588	r400vgt_pass_thru_all_prims_01	2002Sep18
PASS		
589	r400vgt_point_list_01	2002Sep18
PASS		
590	r400vgt_polygon_01	2002Sep18
PASS		
591	r400vgt_provoking_vtx_all_01	2002Sep18
PASS		
592	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep18
PASS		
593	r400vgt_provoking_vtx_polygon_01	2002Sep18
PASS		
594	r400vgt_provoking_vtx_quad_list_01	2002Sep18
PASS		
595	r400vgt_provoking_vtx_quad_strip_01	2002Sep18
PASS		
596	r400vgt_provoking_vtx_tri_fan_01	2002Sep18

PASS		
597	r400vgt_provoking_vtx_tri_strip_01	2002Sep18
PASS		
598	r400vgt_quad_list_01	2002Sep18
PASS		
599	r400vgt_quad_strip_01	2002Sep18
PASS		
600	r400vgt_rectangle_list_01	2002Sep18
PASS		
601	r400vgt_reuse_depth_line_list_01	2002Sep18
PASS		
602	r400vgt_reuse_depth_point_list_01	2002Sep18
PASS		
603	r400vgt_reuse_depth_triangle_list_01	2002Sep18
PASS		
604	r400vgt_reuse_index_line_list_01	2002Sep18
PASS		
605	r400vgt_reuse_index_point_list_01	2002Sep18
PASS		
606	r400vgt_reuse_index_triangle_list_01	2002Sep18
PASS		
607	r400vgt_reuse_index_triangle_list_02	2002Sep18
PASS		
608	r400vgt_reuse_index_triangle_list_03	2002Sep18
PASS		
609	r400vgt_suppress_eop_01	2002Sep18
PASS		
610	r400vgt_suppress_eop_02	2002Sep18
PASS		
611	r400vgt_suppress_eop_03	2002Sep18
PASS		
612	r400vgt_suppress_eop_04	2002Sep18
PASS		
613	r400vgt_triangle_fan_01	2002Sep18
PASS		
614	r400vgt_triangle_list_01	2002Sep18
PASS		
615	r400vgt_triangle_strip_01	2002Sep18
PASS		
616	r400vgt_triangle_wflags_01	2002Sep18
PASS		
617	r400vgt_viz_query_01	2002Sep18
PASS		
618	r400vgt_vtx_vector_packing_01	2002Sep18
PASS		
619	r400vte_coverage_01	2002Sep18
PASS		
620	r400vte_simple_point_01	2002Sep18

PASS
621 r400vte_simple_triangle_01 2002Sep18
PASS
622 r400vte_w0_fmt_01 2002Sep18
PASS
623 r400vte_w0_fmt_03 2002Sep18
PASS
624 r400vte_xy_fmt_01 2002Sep18
PASS
625 r400vte_xyz_scale_01 2002Sep18
PASS
626 r400vte_xyz_scale_02 2002Sep18
PASS
627 r400vte_z_fmt_01 2002Sep18
PASS

=====
Tests_Run_count = 627 Pass_count = 621 Fail_count = 1 Incomplete_count = 5
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Mon Aug 26  3:47:07 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141          100
              SC           156          156          154           98
              CL           81           81           81          100
              VTE           9           9            9          100
SPECIALTY    SU           79           79           79          100
              SANITY       2            2            1           50
+++++
              TOTAL        474          474          471           99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++
++++                        Date: Mon Oct 14 22:49:41 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
+++++
+++++      CP      50      50      50      100
+++++      RBBM     1      1      1      100
+++++      VGT     183     183     150     81
+++++      SC      320     320     301     94
+++++      CL      189     189     186     98
+++++      VTE     10      10      10      100
+++++      SPECIALTY 6      6      6      100
+++++      PARAMETER 62     62     62     100
+++++      SU      107     107     107     100
+++++      SANITY   2      2      2      100
+++++
+++++      TOTAL    930     930     875     94
+++++
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep 19  8:59:37 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              11          11          11          100
              RBBM             1           1           1           100
              VGT             162         162         161           99
              SC              269         269         269          100
              CL              115         115         115          100
              VTE              10          10          10          100
              SPECIALTY         6           6           6           100
              SU               85          85          85          100
              SANITY            2           2           2           100
+++++
              TOTAL            661         661         660           99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Aug16
INCOMPLETE			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug16
INCOMPLETE			
3	primlib_gouraud_triangles_2_draw_passes		2002Aug16
INCOMPLETE			
4	primlib_parameterized_simple_triangle		2002Aug16
INCOMPLETE			
5	primlib_template_simple_triangle		2002Aug16
INCOMPLETE			
6	primlib_tex_tri		2002Aug16
INCOMPLETE			
7	r400cl_clip_10_verts_01		2002Aug16
INCOMPLETE			
8	r400cl_clip_disable_01		2002Aug16
INCOMPLETE			
9	r400cl_clip_space_dx_ogl_01		2002Aug16
INCOMPLETE			
10	r400cl_clip_ucp_6bits_01		2002Aug16
INCOMPLETE			
11	r400cl_cull_only_ena_01		2002Aug16
INCOMPLETE			
12	r400cl_edgeflags_01		2002Aug16
INCOMPLETE			
13	r400cl_edgeflags_frustum_bottom_01		2002Aug16
INCOMPLETE			
14	r400cl_edgeflags_frustum_far_01		2002Aug16
INCOMPLETE			
15	r400cl_edgeflags_frustum_left_01		2002Aug16
INCOMPLETE			
16	r400cl_edgeflags_frustum_near_01		2002Aug16
INCOMPLETE			
17	r400cl_edgeflags_frustum_right_01		2002Aug16
INCOMPLETE			
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326 r400vgt_edgeflags_quad_strip_01	2002Aug16
INCOMPLETE	
327 r400vgt_edgeflags_triangle_fan_01	2002Aug16
INCOMPLETE	
328 r400vgt_edgeflags_triangle_list_01	2002Aug16
INCOMPLETE	
329 r400vgt_edgeflags_triangle_strip_01	2002Aug16
INCOMPLETE	
330 r400vgt_edgeflags_triangle_wflags_01	2002Aug16
INCOMPLETE	
331 r400vgt_ext2int_index_line_list_01	2002Aug16
INCOMPLETE	
332 r400vgt_ext2int_index_line_loop_01	2002Aug16

INCOMPLETE	
333 r400vgt_ext2int_index_line_strip_01	2002Aug16
INCOMPLETE	
334 r400vgt_ext2int_index_points_01	2002Aug16
INCOMPLETE	
335 r400vgt_ext2int_index_polygon_01	2002Aug16
INCOMPLETE	
336 r400vgt_ext2int_index_quad_list_01	2002Aug16
INCOMPLETE	
337 r400vgt_ext2int_index_quad_strip_01	2002Aug16
INCOMPLETE	
338 r400vgt_ext2int_index_triangle_fan_01	2002Aug16
INCOMPLETE	
339 r400vgt_ext2int_index_triangle_list_01	2002Aug16
INCOMPLETE	
340 r400vgt_ext2int_index_triangle_strip_01	2002Aug16
INCOMPLETE	
341 r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug16
INCOMPLETE	
342 r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug16
INCOMPLETE	
343 r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug16
INCOMPLETE	
344 r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug16
INCOMPLETE	
345 r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug16
INCOMPLETE	
346 r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug16
INCOMPLETE	
347 r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug16
INCOMPLETE	
348 r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug16
INCOMPLETE	
349 r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug16
INCOMPLETE	
350 r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug16
INCOMPLETE	
351 r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug16
INCOMPLETE	
352 r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug16
INCOMPLETE	
353 r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug16
INCOMPLETE	
354 r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug16
INCOMPLETE	
355 r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug16
INCOMPLETE	
356 r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug16

INCOMPLETE	
357 r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug16
INCOMPLETE	
358 r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug16
INCOMPLETE	
359 r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug16
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360 r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug16
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361 r400vgt_hos_PNT_edge_detection_01	2002Aug16
INCOMPLETE	
362 r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug16
INCOMPLETE	
363 r400vgt_hos_auto_index_line_list_01	2002Aug16
INCOMPLETE	
364 r400vgt_hos_auto_index_quad_list_01	2002Aug16
INCOMPLETE	
365 r400vgt_hos_auto_index_triangle_list_01	2002Aug16
INCOMPLETE	
366 r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug16
INCOMPLETE	
367 r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug16
INCOMPLETE	
368 r400vgt_immed_index_line_list_01	2002Aug16
INCOMPLETE	
369 r400vgt_immed_index_line_loop_01	2002Aug16
INCOMPLETE	
370 r400vgt_immed_index_line_strip_01	2002Aug16
INCOMPLETE	
371 r400vgt_immed_index_points_01	2002Aug16
INCOMPLETE	
372 r400vgt_immed_index_polygon_01	2002Aug16
INCOMPLETE	
373 r400vgt_immed_index_primitives_01	2002Aug16
INCOMPLETE	
374 r400vgt_immed_index_quad_list_01	2002Aug16
INCOMPLETE	
375 r400vgt_immed_index_quad_strip_01	2002Aug16
INCOMPLETE	
376 r400vgt_immed_index_rectangle_list_01	2002Aug16
INCOMPLETE	
377 r400vgt_immed_index_tri_fan_01	2002Aug16
INCOMPLETE	
378 r400vgt_immed_index_tri_list_01	2002Aug16
INCOMPLETE	
379 r400vgt_immed_index_tri_strip_01	2002Aug16
INCOMPLETE	
380 r400vgt_immed_index_tri_wflags_01	2002Aug16

INCOMPLETE		
381	r400vgt_index_dealloc_line_list_01	2002Aug16
INCOMPLETE		
382	r400vgt_index_dealloc_points_01	2002Aug16
INCOMPLETE		
383	r400vgt_index_dealloc_triangle_list_01	2002Aug16
INCOMPLETE		
384	r400vgt_index_min_max_01	2002Aug16
INCOMPLETE		
385	r400vgt_index_min_max_02	2002Aug16
INCOMPLETE		
386	r400vgt_index_offset_01	2002Aug16
INCOMPLETE		
387	r400vgt_index_offset_02	2002Aug16
INCOMPLETE		
388	r400vgt_index_offset_03	2002Aug16
INCOMPLETE		
389	r400vgt_index_size_01	2002Aug16
INCOMPLETE		
390	r400vgt_index_source_switch_01	2002Aug16
INCOMPLETE		
391	r400vgt_line_list_01	2002Aug16
INCOMPLETE		
392	r400vgt_line_loop_01	2002Aug16
INCOMPLETE		
393	r400vgt_line_strip_01	2002Aug16
INCOMPLETE		
394	r400vgt_pass_thru_all_prims_01	2002Aug16
INCOMPLETE		
395	r400vgt_point_list_01	2002Aug16
INCOMPLETE		
396	r400vgt_polygon_01	2002Aug16
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397	r400vgt_provoking_vtx_all_01	2002Aug16
INCOMPLETE		
398	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug16
INCOMPLETE		
399	r400vgt_quad_list_01	2002Aug16
INCOMPLETE		
400	r400vgt_quad_strip_01	2002Aug16
INCOMPLETE		
401	r400vgt_rectangle_list_01	2002Aug16
INCOMPLETE		
402	r400vgt_reuse_depth_line_list_01	2002Aug16
INCOMPLETE		
403	r400vgt_reuse_depth_point_list_01	2002Aug16
INCOMPLETE		
404	r400vgt_reuse_depth_triangle_list_01	2002Aug16

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INCOMPLETE
405  r400vgt_reuse_index_line_list_01          2002Aug16
INCOMPLETE
406  r400vgt_reuse_index_point_list_01        2002Aug16
INCOMPLETE
407  r400vgt_reuse_index_triangle_list_01     2002Aug16
INCOMPLETE
408  r400vgt_reuse_index_triangle_list_02     2002Aug16
INCOMPLETE
409  r400vgt_reuse_index_triangle_list_03     2002Aug16
INCOMPLETE
410  r400vgt_triangle_fan_01                  2002Aug16
INCOMPLETE
411  r400vgt_triangle_list_01                 2002Aug16
INCOMPLETE
412  r400vgt_triangle_strip_01                2002Aug16
INCOMPLETE
413  r400vgt_triangle_wflags_01              2002Aug16
INCOMPLETE
414  r400vgt_vtx_vector_packing_01            2002Aug16
INCOMPLETE
415  r400vte_coverage_01                      2002Aug16
INCOMPLETE
416  r400vte_simple_point_01                  2002Aug16
INCOMPLETE
417  r400vte_simple_triangle_01              2002Aug16
INCOMPLETE
418  r400vte_w0_fmt_01                        2002Aug16
INCOMPLETE
419  r400vte_w0_fmt_03                        2002Aug16
INCOMPLETE
420  r400vte_xy_fmt_01                        2002Aug16
INCOMPLETE
421  r400vte_xyz_scale_01                     2002Aug16
INCOMPLETE
422  r400vte_xyz_scale_02                     2002Aug16
INCOMPLETE
423  r400vte_z_fmt_01                          2002Aug16
INCOMPLETE

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=====
Tests_Run_count = 423  Pass_count = 0  Fail_count = 0  Incomplete_count = 423
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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Oct 16 22:06:01 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP          50          50          50          100
              RBBM        1           1           1           100
              VGT         186         186         186          100
              SC          326         326         318           97
              CL          189         189         188           99
              VTE         10          10          10           100
              SPECIALTY    6           6           6           100
              PARAMETER    62          62          62           100
              SU          111         111         111           100
              SANITY       2           2           2           100
+++++
              TOTAL        943         943         934           99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Oct 12 21:40:41 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              50          50          50          100
              RBBM             1           1           1           100
              VGT             183         183         183          100
              SC              320         320         312           97
              CL              189         189         188           99
              VTE              10          10          10           100
              SPECIALTY         6           6           6           100
              PARAMETER        62          62          62           100
              SU              107         107         107           100
              SANITY            2           2           2           100
+++++
              TOTAL            930         930         921           99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_1st_tri_june15		2002Aug14
	PASS		
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Aug14
	PASS		
3	primlib_gouraud_triangles_2_draw_passes		2002Aug14
	PASS		
4	primlib_parameterized_simple_triangle		2002Aug14
	PASS		
5	primlib_template_simple_triangle		2002Aug14
	PASS		
6	primlib_tex_tri		2002Aug14
	PASS		
7	r400cl_clip_10_verts_01		2002Aug14
	INCOMPLETE		
8	r400cl_clip_disable_01		2002Aug14
	INCOMPLETE		
9	r400cl_clip_space_dx_ogl_01		2002Aug14
	INCOMPLETE		
10	r400cl_clip_ucp_6bits_01		2002Aug14
	INCOMPLETE		
11	r400cl_cull_only_ena_01		2002Aug14
	INCOMPLETE		
12	r400cl_edgeflags_01		2002Aug14
	INCOMPLETE		
13	r400cl_edgeflags_frustum_bottom_01		2002Aug14
	INCOMPLETE		
14	r400cl_edgeflags_frustum_far_01		2002Aug14
	INCOMPLETE		
15	r400cl_edgeflags_frustum_left_01		2002Aug14
	INCOMPLETE		
16	r400cl_edgeflags_frustum_near_01		2002Aug14
	INCOMPLETE		
17	r400cl_edgeflags_frustum_right_01		2002Aug14
	INCOMPLETE		
18	r400cl_edgeflags_frustum_top_01		2002Aug14
	INCOMPLETE		
19	r400cl_edgeflags_gband_01		2002Aug14
	INCOMPLETE		
20	r400cl_edgeflags_gband_bottom_01		2002Aug14

INCOMPLETE		
21	r400cl_edgeflags_gband_left_01	2002Aug14
INCOMPLETE		
22	r400cl_edgeflags_gband_right_01	2002Aug14
INCOMPLETE		
23	r400cl_edgeflags_gband_top_01	2002Aug14
INCOMPLETE		
24	r400cl_frustum_01	2002Aug14
INCOMPLETE		
25	r400cl_frustum_02	2002Aug14
INCOMPLETE		
26	r400cl_frustum_03	2002Aug14
INCOMPLETE		
27	r400cl_frustum_04	2002Aug14
INCOMPLETE		
28	r400cl_frustum_05	2002Aug14
INCOMPLETE		
29	r400cl_frustum_06	2002Aug14
INCOMPLETE		
30	r400cl_frustum_07	2002Aug14
INCOMPLETE		
31	r400cl_frustum_08	2002Aug14
INCOMPLETE		
32	r400cl_frustum_09	2002Aug14
INCOMPLETE		
33	r400cl_frustum_10	2002Aug14
INCOMPLETE		
34	r400cl_frustum_11	2002Aug14
INCOMPLETE		
35	r400cl_frustum_12	2002Aug14
INCOMPLETE		
36	r400cl_frustum_13	2002Aug14
INCOMPLETE		
37	r400cl_frustum_14	2002Aug14
INCOMPLETE		
38	r400cl_frustum_15	2002Aug14
INCOMPLETE		
39	r400cl_frustum_16	2002Aug14
INCOMPLETE		
40	r400cl_frustum_17	2002Aug14
INCOMPLETE		
41	r400cl_frustum_18	2002Aug14
INCOMPLETE		
42	r400cl_frustum_19	2002Aug14
INCOMPLETE		
43	r400cl_frustum_20	2002Aug14
INCOMPLETE		
44	r400cl_frustum_21	2002Aug14

INCOMPLETE		
45	r400cl_frustum_22	2002Aug14
INCOMPLETE		
46	r400cl_frustum_23	2002Aug14
INCOMPLETE		
47	r400cl_frustum_24	2002Aug14
INCOMPLETE		
48	r400cl_frustum_25	2002Aug14
INCOMPLETE		
49	r400cl_frustum_26	2002Aug14
INCOMPLETE		
50	r400cl_frustum_27	2002Aug14
INCOMPLETE		
51	r400cl_frustum_28	2002Aug14
INCOMPLETE		
52	r400cl_frustum_lines_01	2002Aug14
INCOMPLETE		
53	r400cl_frustum_lines_02	2002Aug14
INCOMPLETE		
54	r400cl_frustum_lines_03	2002Aug14
INCOMPLETE		
55	r400cl_frustum_lines_04	2002Aug14
INCOMPLETE		
56	r400cl_frustum_lines_05	2002Aug14
INCOMPLETE		
57	r400cl_frustum_lines_06	2002Aug14
INCOMPLETE		
58	r400cl_frustum_lines_07	2002Aug14
INCOMPLETE		
59	r400cl_frustum_lines_08	2002Aug14
INCOMPLETE		
60	r400cl_frustum_lines_09	2002Aug14
INCOMPLETE		
61	r400cl_frustum_lines_10	2002Aug14
INCOMPLETE		
62	r400cl_frustum_lines_11	2002Aug14
INCOMPLETE		
63	r400cl_frustum_lines_12	2002Aug14
INCOMPLETE		
64	r400cl_frustum_lines_13	2002Aug14
INCOMPLETE		
65	r400cl_frustum_lines_14	2002Aug14
INCOMPLETE		
66	r400cl_frustum_lines_15	2002Aug14
INCOMPLETE		
67	r400cl_frustum_lines_16	2002Aug14
INCOMPLETE		
68	r400cl_frustum_lines_17	2002Aug14

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69	r400cl_frustum_lines_18	2002Aug14
INCOMPLETE		
70	r400cl_frustum_lines_19	2002Aug14
INCOMPLETE		
71	r400cl_frustum_lines_20	2002Aug14
INCOMPLETE		
72	r400cl_frustum_lines_21	2002Aug14
INCOMPLETE		
73	r400cl_frustum_lines_22	2002Aug14
INCOMPLETE		
74	r400cl_frustum_lines_23	2002Aug14
INCOMPLETE		
75	r400cl_frustum_lines_24	2002Aug14
INCOMPLETE		
76	r400cl_gband_04	2002Aug14
INCOMPLETE		
77	r400cl_gband_tcl_01	2002Aug14
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78	r400cl_simple_triangle_01	2002Aug14
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79	r400cl_ucp_combos_01	2002Aug14
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80	r400cl_ucp_combos_02	2002Aug14
INCOMPLETE		
81	r400cl_ucp_combos_03	2002Aug14
INCOMPLETE		
82	r400cl_ucp_combos_04	2002Aug14
INCOMPLETE		
83	r400cl_vertex_reuse_clip_01	2002Aug14
INCOMPLETE		
84	r400cl_vtx_kill_01	2002Aug14
INCOMPLETE		
85	r400cl_vtx_kill_02	2002Aug14
INCOMPLETE		
86	r400cl_w_eq_0	2002Aug14
INCOMPLETE		
87	r400sanity_persp_projection_01	2002Aug14
PASS		
88	r400sanity_vfd_texture_sample_01	2002Aug14
PASS		
89	r400sc_bres_cntl_01	2002Aug15
INCOMPLETE		
90	r400sc_clip_rect_01	2002Aug14
INCOMPLETE		
91	r400sc_clip_rect_02	2002Aug14
INCOMPLETE		
92	r400sc_clip_rect_03	2002Aug14

INCOMPLETE		
93	r400sc_clip_rect_04	2002Aug14
INCOMPLETE		
94	r400sc_diamond_exit_01	2002Aug14
INCOMPLETE		
95	r400sc_diamond_exit_02	2002Aug14
INCOMPLETE		
96	r400sc_diamond_exit_03	2002Aug14
INCOMPLETE		
97	r400sc_diamond_exit_04	2002Aug14
INCOMPLETE		
98	r400sc_diamond_exit_05	2002Aug14
INCOMPLETE		
99	r400sc_jss_1x2_01	2002Aug14
INCOMPLETE		
100	r400sc_jss_1x2_02	2002Aug14
INCOMPLETE		
101	r400sc_jss_1x3_01	2002Aug14
INCOMPLETE		
102	r400sc_jss_1x3_02	2002Aug14
INCOMPLETE		
103	r400sc_jss_1x4_01	2002Aug14
INCOMPLETE		
104	r400sc_jss_1x4_02	2002Aug14
INCOMPLETE		
105	r400sc_jss_2x1_01	2002Aug14
INCOMPLETE		
106	r400sc_jss_2x1_02	2002Aug14
INCOMPLETE		
107	r400sc_jss_2x2_01	2002Aug14
INCOMPLETE		
108	r400sc_jss_2x2_02	2002Aug14
INCOMPLETE		
109	r400sc_jss_2x3_01	2002Aug14
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110	r400sc_jss_2x3_02	2002Aug14
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111	r400sc_jss_2x4_01	2002Aug14
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112	r400sc_jss_2x4_02	2002Aug14
INCOMPLETE		
113	r400sc_jss_3x1_01	2002Aug14
INCOMPLETE		
114	r400sc_jss_3x1_02	2002Aug14
INCOMPLETE		
115	r400sc_jss_3x2_01	2002Aug14
INCOMPLETE		
116	r400sc_jss_3x2_02	2002Aug14

INCOMPLETE		
117	r400sc_jss_3x3_01	2002Aug14
INCOMPLETE		
118	r400sc_jss_3x3_02	2002Aug14
INCOMPLETE		
119	r400sc_jss_3x4_01	2002Aug14
INCOMPLETE		
120	r400sc_jss_3x4_02	2002Aug14
INCOMPLETE		
121	r400sc_jss_3x4_03	2002Aug14
INCOMPLETE		
122	r400sc_jss_4x1_01	2002Aug14
INCOMPLETE		
123	r400sc_jss_4x1_02	2002Aug14
INCOMPLETE		
124	r400sc_jss_4x2_01	2002Aug14
INCOMPLETE		
125	r400sc_jss_4x2_02	2002Aug14
INCOMPLETE		
126	r400sc_jss_4x3_01	2002Aug14
INCOMPLETE		
127	r400sc_jss_4x3_02	2002Aug14
INCOMPLETE		
128	r400sc_jss_4x4_01	2002Aug14
INCOMPLETE		
129	r400sc_jss_4x4_02	2002Aug14
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130	r400sc_jss_4x4_aa_mask_01	2002Aug14
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131	r400sc_jss_4x4_aa_mask_02	2002Aug14
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132	r400sc_jss_4x4_max_dist_01	2002Aug14
INCOMPLETE		
133	r400sc_line_dx10_eq_0_01	2002Aug14
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134	r400sc_line_dx10_ge_0_01	2002Aug14
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135	r400sc_line_dx10_lt_0_01	2002Aug14
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136	r400sc_line_dy10_eq_0_01	2002Aug14
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137	r400sc_line_dy10_ge_0_01	2002Aug14
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138	r400sc_line_dy10_lt_0_01	2002Aug14
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139	r400sc_line_jss_3x4_01	2002Aug14
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140	r400sc_line_msaa_8_01	2002Aug14

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141	r400sc_line_stipple_01	2002Aug14
INCOMPLETE		
142	r400sc_line_stipple_02	2002Aug14
INCOMPLETE		
143	r400sc_line_stipple_03	2002Aug14
INCOMPLETE		
144	r400sc_line_stipple_04	2002Aug14
INCOMPLETE		
145	r400sc_line_stipple_05	2002Aug14
INCOMPLETE		
146	r400sc_line_stipple_06	2002Aug14
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147	r400sc_line_stipple_07	2002Aug14
INCOMPLETE		
148	r400sc_line_stipple_08	2002Aug14
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149	r400sc_line_stipple_09	2002Aug14
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150	r400sc_line_strip_stipple_01	2002Aug14
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154	r400sc_msa_8_01	2002Aug14
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155	r400sc_msa_8_aa_mask_01	2002Aug14
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156	r400sc_msa_8_aa_mask_02	2002Aug14
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157	r400sc_null_triangles_01	2002Aug14
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158	r400sc_point_jss_3x4_01	2002Aug14
INCOMPLETE		
159	r400sc_point_list_01	2002Aug14
INCOMPLETE		
160	r400sc_point_list_02	2002Aug14
INCOMPLETE		
161	r400sc_point_list_03	2002Aug14
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162	r400sc_point_list_04	2002Aug14
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163	r400sc_point_list_05	2002Aug14
INCOMPLETE		
164	r400sc_point_list_06	2002Aug14

INCOMPLETE		
165	r400sc_point_list_07	2002Aug14
INCOMPLETE		
166	r400sc_point_list_08	2002Aug14
INCOMPLETE		
167	r400sc_point_list_09	2002Aug14
INCOMPLETE		
168	r400sc_point_msa_8_01	2002Aug14
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169	r400sc_poly_offset_01	2002Aug14
INCOMPLETE		
170	r400sc_poly_offset_02	2002Aug14
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171	r400sc_polygon_stipple_01	2002Aug14
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172	r400sc_prsp_byc_intrp_ref_pix_01	2002Aug14
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173	r400sc_prsp_byc_intrp_ref_pix_02	2002Aug14
INCOMPLETE		
174	r400sc_prsp_byc_intrp_ref_pix_03	2002Aug14
INCOMPLETE		
175	r400sc_prsp_byc_intrp_ref_pix_04	2002Aug14
INCOMPLETE		
176	r400sc_prsp_byc_intrp_ref_pix_05	2002Aug14
INCOMPLETE		
177	r400sc_prsp_byc_intrp_ref_pix_06	2002Aug14
INCOMPLETE		
178	r400sc_prsp_byc_intrp_ref_pix_07	2002Aug14
INCOMPLETE		
179	r400sc_prsp_byc_intrp_ref_pix_08	2002Aug14
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180	r400sc_raster_fill_rule_01	2002Aug14
INCOMPLETE		
181	r400sc_raster_fill_rule_02	2002Aug14
INCOMPLETE		
182	r400sc_raster_fill_rule_03	2002Aug14
INCOMPLETE		
183	r400sc_raster_fill_rule_04	2002Aug14
INCOMPLETE		
184	r400sc_raster_fill_rule_05	2002Aug14
INCOMPLETE		
185	r400sc_rectangle_list_01	2002Aug14
INCOMPLETE		
186	r400sc_rectangle_list_02	2002Aug14
INCOMPLETE		
187	r400sc_rectangle_list_03	2002Aug14
INCOMPLETE		
188	r400sc_rectangle_list_04	2002Aug14

INCOMPLETE		
189	r400sc_rectangle_list_05	2002Aug14
INCOMPLETE		
190	r400sc_rectangle_list_06	2002Aug14
INCOMPLETE		
191	r400sc_rectangle_list_07	2002Aug14
INCOMPLETE		
192	r400sc_rectangle_list_08	2002Aug14
INCOMPLETE		
193	r400sc_scissor_rect_01	2002Aug14
INCOMPLETE		
194	r400sc_scissor_rect_02	2002Aug14
INCOMPLETE		
195	r400sc_scissor_rect_03	2002Aug14
INCOMPLETE		
196	r400sc_scissor_rect_04	2002Aug14
INCOMPLETE		
197	r400sc_scissor_rect_05	2002Aug14
INCOMPLETE		
198	r400sc_set_state_01	2002Aug14
INCOMPLETE		
199	r400sc_tri_walk_start_vertex_01	2002Aug14
INCOMPLETE		
200	r400sc_tri_walk_start_vertex_02	2002Aug14
INCOMPLETE		
201	r400sc_tri_walk_start_vertex_03	2002Aug14
INCOMPLETE		
202	r400sc_tri_walk_start_vertex_04	2002Aug14
INCOMPLETE		
203	r400sc_tri_walk_start_vertex_05	2002Aug14
INCOMPLETE		
204	r400sc_tri_walk_start_vertex_06	2002Aug14
INCOMPLETE		
205	r400sc_tri_walk_start_vertex_07	2002Aug14
INCOMPLETE		
206	r400sc_tri_walk_start_vertex_08	2002Aug14
INCOMPLETE		
207	r400sc_tri_walk_start_vertex_09	2002Aug14
INCOMPLETE		
208	r400sc_tri_walk_start_vertex_10	2002Aug14
INCOMPLETE		
209	r400sc_tri_walk_start_vertex_11	2002Aug14
INCOMPLETE		
210	r400sc_tri_walk_start_vertex_12	2002Aug14
INCOMPLETE		
211	r400sc_tri_walk_start_vertex_13	2002Aug14
INCOMPLETE		
212	r400sc_tri_walk_start_vertex_14	2002Aug14

INCOMPLETE	
213 r400sc_tri_walk_start_vertex_15	2002Aug14
INCOMPLETE	
214 r400sc_tri_walk_start_vertex_16	2002Aug14
INCOMPLETE	
215 r400sc_triangle_stipple_01	2002Aug14
INCOMPLETE	
216 r400sc_window_offset_01	2002Aug14
INCOMPLETE	
217 r400sc_zbuffer_line_list_01	2002Aug14
INCOMPLETE	
218 r400sc_zbuffer_point_list_01	2002Aug14
INCOMPLETE	
219 r400sc_zbuffer_rectangle_list_01	2002Aug14
INCOMPLETE	
220 r400sc_zbuffer_rectangle_list_02	2002Aug14
INCOMPLETE	
221 r400sc_zbuffer_triangle_list_01	2002Aug14
INCOMPLETE	
222 r400su_4tri_text_offscreen_01	2002Aug14
PASS	
223 r400su_baryc_test_01	2002Aug14
PASS	
224 r400su_baryc_test_02	2002Aug14
PASS	
225 r400su_baryc_test_03	2002Aug14
PASS	
226 r400su_baryc_test_04	2002Aug14
PASS	
227 r400su_clip_edgeflag_polymode_01	2002Aug14
PASS	
228 r400su_clip_pointsize_test_01	2002Aug14
PASS	
229 r400su_clip_pointtest_01	2002Aug14
PASS	
230 r400su_clip_pointtest_02	2002Aug14
PASS	
231 r400su_clip_pointtest_03	2002Aug14
PASS	
232 r400su_clip_pointtest_04	2002Aug14
PASS	
233 r400su_clip_polymode_test_01	2002Aug14
PASS	
234 r400su_clipline_01	2002Aug14
PASS	
235 r400su_clippoint_01	2002Aug14
PASS	
236 r400su_clipvertexsortingfunctional_01	2002Aug14

PASS		
237	r400su_cullingfunctional_01	2002Aug14
PASS		
238	r400su_degentri_test_01	2002Aug14
PASS		
239	r400su_degentri_test_02	2002Aug14
PASS		
240	r400su_degentri_test_03	2002Aug14
PASS		
241	r400su_edge_flag_01	2002Aug14
PASS		
242	r400su_edge_flag_02	2002Aug14
PASS		
243	r400su_edgeflags_triangle_01	2002Aug14
PASS		
244	r400su_edgeflags_triangle_02	2002Aug14
PASS		
245	r400su_geom_sort_01	2002Aug14
PASS		
246	r400su_line_end_cap_functional_01	2002Aug14
PASS		
247	r400su_line_end_cap_width_functional_02	2002Aug14
PASS		
248	r400su_line_test_01	2002Aug14
PASS		
249	r400su_line_test_02	2002Aug14
PASS		
250	r400su_longstrip_01	2002Aug14
PASS		
251	r400su_multi_prim_01	2002Aug14
PASS		
252	r400su_parallel_orientation_all_01	2002Aug14
PASS		
253	r400su_parallel_orientation_all_02	2002Aug14
PASS		
254	r400su_point_sprite_01	2002Aug14
PASS		
255	r400su_point_sprite_02	2002Aug14
PASS		
256	r400su_point_sprite_03	2002Aug14
PASS		
257	r400su_point_sprite_04	2002Aug14
PASS		
258	r400su_point_sprite_05	2002Aug14
PASS		
259	r400su_point_sprite_06	2002Aug14
PASS		
260	r400su_point_wl6_hl_functional_01	2002Aug14

PASS		
261	r400su_point_wl_h16_functional_01	2002Aug14
PASS		
262	r400su_pointsizepresent_01	2002Aug14
PASS		
263	r400su_pointsizepresent_02	2002Aug14
PASS		
264	r400su_polymode_rectangle_01	2002Aug14
PASS		
265	r400su_polymodeculling_01	2002Aug14
PASS		
266	r400su_polymodefunctional_01	2002Aug14
PASS		
267	r400su_provoking_vtx_edgeflags_triangle_01	2002Aug14
PASS		
268	r400su_provoking_vtx_edgeflags_triangle_02	2002Aug14
PASS		
269	r400su_provoking_vtx_edgeflags_triangle_03	2002Aug14
PASS		
270	r400su_provoking_vtx_edgeflags_triangle_04	2002Aug14
PASS		
271	r400su_provoking_vtx_line_01	2002Aug14
PASS		
272	r400su_provoking_vtx_point_01	2002Aug14
PASS		
273	r400su_provoking_vtx_polymode_rectangle_01	2002Aug14
PASS		
274	r400su_provoking_vtx_rectangle_01	2002Aug14
PASS		
275	r400su_provoking_vtx_triangle_01	2002Aug14
PASS		
276	r400su_rand_line_01	2002Aug14
PASS		
277	r400su_rand_point_01	2002Aug14
PASS		
278	r400su_rand_tri_01	2002Aug14
PASS		
279	r400su_rectangle_list_01	2002Aug14
PASS		
280	r400su_sliver_01	2002Aug14
PASS		
281	r400su_triarea_test_01	2002Aug14
PASS		
282	r400su_triarea_test_02	2002Aug14
PASS		
283	r400su_triarea_test_03	2002Aug14
PASS		
284	r400su_triarea_test_04	2002Aug14

PASS		
285	r400su_vertexsortingfunctional_01	2002Aug14
PASS		
286	r400vgt_auto_index_line_list_01	2002Aug14
INCOMPLETE		
287	r400vgt_auto_index_line_loop_01	2002Aug14
INCOMPLETE		
288	r400vgt_auto_index_line_strip_01	2002Aug14
INCOMPLETE		
289	r400vgt_auto_index_points_01	2002Aug14
INCOMPLETE		
290	r400vgt_auto_index_polygon_01	2002Aug14
INCOMPLETE		
291	r400vgt_auto_index_primitives_01	2002Aug14
PASS		
292	r400vgt_auto_index_quad_list_01	2002Aug14
PASS		
293	r400vgt_auto_index_quad_strip_01	2002Aug14
PASS		
294	r400vgt_auto_index_rectangle_list_01	2002Aug14
PASS		
295	r400vgt_auto_index_tri_fan_01	2002Aug14
PASS		
296	r400vgt_auto_index_tri_list_01	2002Aug14
PASS		
297	r400vgt_auto_index_tri_strip_01	2002Aug14
PASS		
298	r400vgt_auto_index_tri_wflags_01	2002Aug14
PASS		
299	r400vgt_dma_engine_01	2002Aug14
PASS		
300	r400vgt_dma_engine_02	2002Aug14
PASS		
301	r400vgt_dma_engine_03	2002Aug14
PASS		
302	r400vgt_dma_engine_04	2002Aug14
PASS		
303	r400vgt_dma_engine_05	2002Aug14
PASS		
304	r400vgt_dma_engine_06	2002Aug14
PASS		
305	r400vgt_dma_engine_07	2002Aug14
PASS		
306	r400vgt_dma_engine_08	2002Aug14
PASS		
307	r400vgt_dma_engine_09	2002Aug14
PASS		
308	r400vgt_dma_engine_10	2002Aug14

PASS		
309	r400vgt_dma_index_line_list_01	2002Aug14
PASS		
310	r400vgt_dma_index_line_loop_01	2002Aug14
PASS		
311	r400vgt_dma_index_line_strip_01	2002Aug14
PASS		
312	r400vgt_dma_index_points_01	2002Aug14
PASS		
313	r400vgt_dma_index_polygon_01	2002Aug14
PASS		
314	r400vgt_dma_index_primitives_01	2002Aug14
PASS		
315	r400vgt_dma_index_quad_list_01	2002Aug14
PASS		
316	r400vgt_dma_index_quad_strip_01	2002Aug14
PASS		
317	r400vgt_dma_index_rectangle_list_01	2002Aug14
PASS		
318	r400vgt_dma_index_tri_fan_01	2002Aug14
PASS		
319	r400vgt_dma_index_tri_list_01	2002Aug14
PASS		
320	r400vgt_dma_index_tri_strip_01	2002Aug14
PASS		
321	r400vgt_dma_index_tri_wflags_01	2002Aug14
PASS		
322	r400vgt_dma_swap_indx16_01	2002Aug14
PASS		
323	r400vgt_dma_swap_indx32_01	2002Aug14
PASS		
324	r400vgt_edgeflags_polygon_01	2002Aug14
PASS		
325	r400vgt_edgeflags_quad_list_01	2002Aug14
PASS		
326	r400vgt_edgeflags_quad_strip_01	2002Aug14
PASS		
327	r400vgt_edgeflags_triangle_fan_01	2002Aug14
PASS		
328	r400vgt_edgeflags_triangle_list_01	2002Aug14
PASS		
329	r400vgt_edgeflags_triangle_strip_01	2002Aug14
PASS		
330	r400vgt_edgeflags_triangle_wflags_01	2002Aug14
PASS		
331	r400vgt_ext2int_index_line_list_01	2002Aug14
PASS		
332	r400vgt_ext2int_index_line_loop_01	2002Aug14

PASS		
333	r400vgt_ext2int_index_line_strip_01	2002Aug14
PASS		
334	r400vgt_ext2int_index_points_01	2002Aug14
PASS		
335	r400vgt_ext2int_index_polygon_01	2002Aug14
PASS		
336	r400vgt_ext2int_index_quad_list_01	2002Aug14
PASS		
337	r400vgt_ext2int_index_quad_strip_01	2002Aug14
PASS		
338	r400vgt_ext2int_index_triangle_fan_01	2002Aug14
PASS		
339	r400vgt_ext2int_index_triangle_list_01	2002Aug14
PASS		
340	r400vgt_ext2int_index_triangle_strip_01	2002Aug14
PASS		
341	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Aug14
PASS		
342	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Aug14
PASS		
343	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Aug14
PASS		
344	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Aug14
PASS		
345	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Aug14
PASS		
346	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Aug14
PASS		
347	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Aug14
PASS		
348	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Aug14
PASS		
349	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Aug14
PASS		
350	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Aug14
PASS		
351	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Aug14
PASS		
352	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Aug14
PASS		
353	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Aug14
PASS		
354	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Aug14
PASS		
355	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Aug14
PASS		
356	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Aug14

PASS		
357	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Aug14
PASS		
358	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Aug14
PASS		
359	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Aug14
PASS		
360	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Aug14
PASS		
361	r400vgt_hos_PNT_edge_detection_01	2002Aug14
PASS		
362	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Aug14
PASS		
363	r400vgt_hos_auto_index_line_list_01	2002Aug14
PASS		
364	r400vgt_hos_auto_index_quad_list_01	2002Aug14
PASS		
365	r400vgt_hos_auto_index_triangle_list_01	2002Aug14
PASS		
366	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Aug14
PASS		
367	r400vgt_hos_simple_linear_PNT_discrete_01	2002Aug14
PASS		
368	r400vgt_immed_index_line_list_01	2002Aug14
PASS		
369	r400vgt_immed_index_line_loop_01	2002Aug14
PASS		
370	r400vgt_immed_index_line_strip_01	2002Aug14
PASS		
371	r400vgt_immed_index_points_01	2002Aug14
PASS		
372	r400vgt_immed_index_polygon_01	2002Aug14
PASS		
373	r400vgt_immed_index_primitives_01	2002Aug14
PASS		
374	r400vgt_immed_index_quad_list_01	2002Aug14
PASS		
375	r400vgt_immed_index_quad_strip_01	2002Aug14
PASS		
376	r400vgt_immed_index_rectangle_list_01	2002Aug14
PASS		
377	r400vgt_immed_index_tri_fan_01	2002Aug14
PASS		
378	r400vgt_immed_index_tri_list_01	2002Aug14
PASS		
379	r400vgt_immed_index_tri_strip_01	2002Aug14
PASS		
380	r400vgt_immed_index_tri_wflags_01	2002Aug14

PASS		
381	r400vgt_index_dealloc_line_list_01	2002Aug14
PASS		
382	r400vgt_index_dealloc_points_01	2002Aug14
PASS		
383	r400vgt_index_dealloc_triangle_list_01	2002Aug14
PASS		
384	r400vgt_index_min_max_01	2002Aug14
PASS		
385	r400vgt_index_min_max_02	2002Aug14
PASS		
386	r400vgt_index_offset_01	2002Aug14
PASS		
387	r400vgt_index_offset_02	2002Aug14
PASS		
388	r400vgt_index_offset_03	2002Aug14
PASS		
389	r400vgt_index_size_01	2002Aug14
PASS		
390	r400vgt_index_source_switch_01	2002Aug14
PASS		
391	r400vgt_line_list_01	2002Aug14
PASS		
392	r400vgt_line_loop_01	2002Aug14
PASS		
393	r400vgt_line_strip_01	2002Aug14
PASS		
394	r400vgt_pass_thru_all_prims_01	2002Aug14
PASS		
395	r400vgt_point_list_01	2002Aug14
PASS		
396	r400vgt_polygon_01	2002Aug14
PASS		
397	r400vgt_provoking_vtx_all_01	2002Aug14
PASS		
398	r400vgt_provoking_vtx_edgeflags_all_01	2002Aug14
PASS		
399	r400vgt_quad_list_01	2002Aug14
PASS		
400	r400vgt_quad_strip_01	2002Aug14
PASS		
401	r400vgt_rectangle_list_01	2002Aug14
PASS		
402	r400vgt_reuse_depth_line_list_01	2002Aug14
PASS		
403	r400vgt_reuse_depth_point_list_01	2002Aug14
PASS		
404	r400vgt_reuse_depth_triangle_list_01	2002Aug14

```

PASS
405  r400vgt_reuse_index_line_list_01          2002Aug14
PASS
406  r400vgt_reuse_index_point_list_01        2002Aug14
PASS
407  r400vgt_reuse_index_triangle_list_01     2002Aug14
PASS
408  r400vgt_reuse_index_triangle_list_02     2002Aug14
PASS
409  r400vgt_reuse_index_triangle_list_03     2002Aug14
PASS
410  r400vgt_triangle_fan_01                  2002Aug14
PASS
411  r400vgt_triangle_list_01                 2002Aug14
PASS
412  r400vgt_triangle_strip_01                2002Aug14
PASS
413  r400vgt_triangle_wflags_01              2002Aug14
PASS
414  r400vgt_vtx_vector_packing_01           2002Aug14
PASS
415  r400vte_coverage_01                      2002Aug14
PASS
416  r400vte_simple_point_01                  2002Aug14
PASS
417  r400vte_simple_triangle_01              2002Aug14
PASS
418  r400vte_w0_fmt_01                        2002Aug14
PASS
419  r400vte_w0_fmt_03                        2002Aug14
PASS
420  r400vte_xy_fmt_01                        2002Aug14
PASS
421  r400vte_xyz_scale_01                     2002Aug14
PASS
422  r400vte_xyz_scale_02                     2002Aug14
PASS
423  r400vte_z_fmt_01                         2002Aug14
PASS

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=====
Tests_Run_count = 423  Pass_count = 205  Fail_count = 0  Incomplete_count = 218
=====

```

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sat Aug 31  3:20:36 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          141          141          141          100
              SC           157          157          156           99
              CL           108          108          108          100
              VTE           9           9           9           100
SPECIALTY    SU           81           81           81          100
              SANITY       2           2           2           100
+++++
              TOTAL        504          504          503           99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	primlib_lst_tri_june15		2002Sep7
PASS			
2	primlib_gouraud_tri_1_xyz_vb_1_rgb_vb		2002Sep7
PASS			
3	primlib_gouraud_triangles_2_draw_passes		2002Sep7
PASS			
4	primlib_parameterized_simple_triangle		2002Sep7
PASS			
5	primlib_template_simple_triangle		2002Sep7
PASS			
6	primlib_tex_tri		2002Sep7
PASS			
7	r400cl_clip_10_verts_01		2002Sep7
PASS			
8	r400cl_clip_disable_01		2002Sep7
PASS			
9	r400cl_clip_space_dx_ogl_01		2002Sep7
PASS			
10	r400cl_clip_ucp_6bits_01		2002Sep7
PASS			
11	r400cl_cull_only_ena_01		2002Sep7
PASS			
12	r400cl_edgeflags_01		2002Sep7
PASS			
13	r400cl_edgeflags_frustum_bottom_01		2002Sep7
PASS			
14	r400cl_edgeflags_frustum_far_01		2002Sep7
PASS			
15	r400cl_edgeflags_frustum_left_01		2002Sep7
PASS			
16	r400cl_edgeflags_frustum_near_01		2002Sep7
PASS			
17	r400cl_edgeflags_frustum_right_01		2002Sep7
PASS			
18	r400cl_edgeflags_frustum_top_01		2002Sep7
PASS			
19	r400cl_edgeflags_gband_01		2002Sep7
PASS			
20	r400cl_edgeflags_gband_bottom_01		2002Sep7

PASS		
21	r400cl_edgeflags_gband_left_01	2002Sep7
PASS		
22	r400cl_edgeflags_gband_right_01	2002Sep7
PASS		
23	r400cl_edgeflags_gband_top_01	2002Sep7
PASS		
24	r400cl_frustum_01	2002Sep7
PASS		
25	r400cl_frustum_02	2002Sep7
PASS		
26	r400cl_frustum_03	2002Sep7
PASS		
27	r400cl_frustum_04	2002Sep7
PASS		
28	r400cl_frustum_05	2002Sep7
PASS		
29	r400cl_frustum_06	2002Sep7
PASS		
30	r400cl_frustum_07	2002Sep7
PASS		
31	r400cl_frustum_08	2002Sep7
PASS		
32	r400cl_frustum_09	2002Sep7
PASS		
33	r400cl_frustum_10	2002Sep7
PASS		
34	r400cl_frustum_11	2002Sep7
PASS		
35	r400cl_frustum_12	2002Sep7
PASS		
36	r400cl_frustum_13	2002Sep7
PASS		
37	r400cl_frustum_14	2002Sep7
PASS		
38	r400cl_frustum_15	2002Sep7
PASS		
39	r400cl_frustum_16	2002Sep7
PASS		
40	r400cl_frustum_17	2002Sep7
PASS		
41	r400cl_frustum_18	2002Sep7
PASS		
42	r400cl_frustum_19	2002Sep7
PASS		
43	r400cl_frustum_20	2002Sep7
PASS		
44	r400cl_frustum_21	2002Sep7

PASS		
45	r400cl_frustum_22	2002Sep7
PASS		
46	r400cl_frustum_23	2002Sep7
PASS		
47	r400cl_frustum_24	2002Sep7
PASS		
48	r400cl_frustum_25	2002Sep7
PASS		
49	r400cl_frustum_26	2002Sep7
PASS		
50	r400cl_frustum_27	2002Sep7
PASS		
51	r400cl_frustum_28	2002Sep7
PASS		
52	r400cl_frustum_lines_01	2002Sep7
PASS		
53	r400cl_frustum_lines_02	2002Sep7
PASS		
54	r400cl_frustum_lines_03	2002Sep7
PASS		
55	r400cl_frustum_lines_04	2002Sep7
PASS		
56	r400cl_frustum_lines_05	2002Sep7
PASS		
57	r400cl_frustum_lines_06	2002Sep7
PASS		
58	r400cl_frustum_lines_07	2002Sep7
PASS		
59	r400cl_frustum_lines_08	2002Sep7
PASS		
60	r400cl_frustum_lines_09	2002Sep7
PASS		
61	r400cl_frustum_lines_10	2002Sep7
PASS		
62	r400cl_frustum_lines_11	2002Sep7
PASS		
63	r400cl_frustum_lines_12	2002Sep7
PASS		
64	r400cl_frustum_lines_13	2002Sep7
PASS		
65	r400cl_frustum_lines_14	2002Sep7
PASS		
66	r400cl_frustum_lines_15	2002Sep7
PASS		
67	r400cl_frustum_lines_16	2002Sep7
PASS		
68	r400cl_frustum_lines_17	2002Sep7

PASS		
69	r400cl_frustum_lines_18	2002Sep7
PASS		
70	r400cl_frustum_lines_19	2002Sep7
PASS		
71	r400cl_frustum_lines_20	2002Sep7
PASS		
72	r400cl_frustum_lines_21	2002Sep7
PASS		
73	r400cl_frustum_lines_22	2002Sep7
PASS		
74	r400cl_frustum_lines_23	2002Sep7
PASS		
75	r400cl_frustum_lines_24	2002Sep7
PASS		
76	r400cl_gband_04	2002Sep7
PASS		
77	r400cl_gband_tcl_01	2002Sep7
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116	r400sanity_vfd_texture_sample_01	2002Sep7

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416	r400vgt_ext2int_index_triangle_fan_01	2002Sep7
PASS		
417	r400vgt_ext2int_index_triangle_list_01	2002Sep7
PASS		
418	r400vgt_ext2int_index_triangle_strip_01	2002Sep7
PASS		
419	r400vgt_hos_PNL_cp_ln_cont_no_projection_01	2002Sep7
PASS		
420	r400vgt_hos_PNL_lp_ln_cont_no_projection_01	2002Sep7
PASS		
421	r400vgt_hos_PNQ_cp_qn_cont_light_texture_01	2002Sep7
PASS		
422	r400vgt_hos_PNQ_cp_qn_cont_light_texture_02	2002Sep7
PASS		
423	r400vgt_hos_PNQ_cp_qn_cont_no_projection_01	2002Sep7
PASS		
424	r400vgt_hos_PNQ_lp_cont_no_projection_01	2002Sep7
PASS		
425	r400vgt_hos_PNT_cont_cp_qn_complex_01	2002Sep7
PASS		
426	r400vgt_hos_PNT_cont_cp_qn_precision_01	2002Sep7
PASS		
427	r400vgt_hos_PNT_cont_cp_qn_precision_02	2002Sep7
PASS		
428	r400vgt_hos_PNT_cp_qn_cont_light_texture_01	2002Sep7

PASS		
429	r400vgt_hos_PNT_cp_qn_cont_light_texture_02	2002Sep7
PASS		
430	r400vgt_hos_PNT_cp_qn_cont_light_texture_03	2002Sep7
PASS		
431	r400vgt_hos_PNT_cp_qn_cont_moving_normals_01	2002Sep7
PASS		
432	r400vgt_hos_PNT_cp_qn_cont_no_projection_01	2002Sep7
PASS		
433	r400vgt_hos_PNT_cp_qn_disc_14_04_lit_tex_proj_01	2002Sep7
PASS		
434	r400vgt_hos_PNT_disc_cp_qn_complex_01	2002Sep7
PASS		
435	r400vgt_hos_PNT_disc_cp_qn_light_texture_01	2002Sep7
PASS		
436	r400vgt_hos_PNT_disc_cp_qn_no_projection_01	2002Sep7
PASS		
437	r400vgt_hos_PNT_disc_cp_qn_precision_01	2002Sep7
PASS		
438	r400vgt_hos_PNT_disc_cp_qn_precision_02	2002Sep7
PASS		
439	r400vgt_hos_PNT_edge_detection_01	2002Sep7
PASS		
440	r400vgt_hos_PNT_lp_cont_no_projection_01	2002Sep7
PASS		
441	r400vgt_hos_RPatch_cp_02	2002Sep7
PASS		
442	r400vgt_hos_RPatch_lp_02	2002Sep7
PASS		
443	r400vgt_hos_auto_index_line_list_01	2002Sep7
PASS		
444	r400vgt_hos_auto_index_quad_list_01	2002Sep7
PASS		
445	r400vgt_hos_auto_index_triangle_list_01	2002Sep7
PASS		
446	r400vgt_hos_cubic_pos_pnt_discrete_01	2002Sep7
PASS		
447	r400vgt_hos_simple_linear_PNT_discrete_01	2002Sep7
PASS		
448	r400vgt_immed_index_line_list_01	2002Sep7
PASS		
449	r400vgt_immed_index_line_loop_01	2002Sep7
PASS		
450	r400vgt_immed_index_line_strip_01	2002Sep7
PASS		
451	r400vgt_immed_index_points_01	2002Sep7
PASS		
452	r400vgt_immed_index_polygon_01	2002Sep7

PASS		
453	r400vgt_immed_index_primitives_01	2002Sep7
PASS		
454	r400vgt_immed_index_quad_list_01	2002Sep7
PASS		
455	r400vgt_immed_index_quad_strip_01	2002Sep7
PASS		
456	r400vgt_immed_index_rectangle_list_01	2002Sep7
PASS		
457	r400vgt_immed_index_tri_fan_01	2002Sep7
PASS		
458	r400vgt_immed_index_tri_list_01	2002Sep7
PASS		
459	r400vgt_immed_index_tri_strip_01	2002Sep7
PASS		
460	r400vgt_immed_index_tri_wflags_01	2002Sep7
PASS		
461	r400vgt_index_dealloc_line_list_01	2002Sep7
PASS		
462	r400vgt_index_dealloc_points_01	2002Sep7
PASS		
463	r400vgt_index_dealloc_triangle_list_01	2002Sep7
PASS		
464	r400vgt_index_min_max_01	2002Sep7
PASS		
465	r400vgt_index_min_max_02	2002Sep7
PASS		
466	r400vgt_index_offset_01	2002Sep7
PASS		
467	r400vgt_index_offset_02	2002Sep7
PASS		
468	r400vgt_index_offset_03	2002Sep7
PASS		
469	r400vgt_index_size_01	2002Sep7
PASS		
470	r400vgt_index_source_switch_01	2002Sep7
PASS		
471	r400vgt_line_list_01	2002Sep7
PASS		
472	r400vgt_line_loop_01	2002Sep7
PASS		
473	r400vgt_line_strip_01	2002Sep7
PASS		
474	r400vgt_multi_context_01	2002Sep7
PASS		
475	r400vgt_multi_context_02	2002Sep7
PASS		
476	r400vgt_multi_context_03	2002Sep7

PASS		
477	r400vgt_multi_context_04	2002Sep7
PASS		
478	r400vgt_multi_context_05	2002Sep7
PASS		
479	r400vgt_multi_context_06	2002Sep7
PASS		
480	r400vgt_multi_context_07	2002Sep7
PASS		
481	r400vgt_multi_context_08	2002Sep7
PASS		
482	r400vgt_multi_context_09	2002Sep7
PASS		
483	r400vgt_multi_context_10	2002Sep7
PASS		
484	r400vgt_multi_context_11	2002Sep7
PASS		
485	r400vgt_multi_context_12	2002Sep7
PASS		
486	r400vgt_pass_thru_all_prims_01	2002Sep7
PASS		
487	r400vgt_point_list_01	2002Sep7
PASS		
488	r400vgt_polygon_01	2002Sep7
PASS		
489	r400vgt_provoking_vtx_all_01	2002Sep7
PASS		
490	r400vgt_provoking_vtx_edgeflags_all_01	2002Sep7
PASS		
491	r400vgt_quad_list_01	2002Sep7
PASS		
492	r400vgt_quad_strip_01	2002Sep7
PASS		
493	r400vgt_rectangle_list_01	2002Sep7
PASS		
494	r400vgt_reuse_depth_line_list_01	2002Sep7
PASS		
495	r400vgt_reuse_depth_point_list_01	2002Sep7
PASS		
496	r400vgt_reuse_depth_triangle_list_01	2002Sep7
PASS		
497	r400vgt_reuse_index_line_list_01	2002Sep7
PASS		
498	r400vgt_reuse_index_point_list_01	2002Sep7
PASS		
499	r400vgt_reuse_index_triangle_list_01	2002Sep7
PASS		
500	r400vgt_reuse_index_triangle_list_02	2002Sep7

PASS
501 r400vgt_reuse_index_triangle_list_03 2002Sep7
PASS
502 r400vgt_suppress_eop_01 2002Sep7
PASS
503 r400vgt_suppress_eop_02 2002Sep7
PASS
504 r400vgt_suppress_eop_03 2002Sep7
PASS
505 r400vgt_suppress_eop_04 2002Sep7
PASS
506 r400vgt_triangle_fan_01 2002Sep7
PASS
507 r400vgt_triangle_list_01 2002Sep7
PASS
508 r400vgt_triangle_strip_01 2002Sep7
PASS
509 r400vgt_triangle_wflags_01 2002Sep7
PASS
510 r400vgt_vtx_vector_packing_01 2002Sep7
PASS
511 r400vte_coverage_01 2002Sep7
PASS
512 r400vte_simple_point_01 2002Sep7
PASS
513 r400vte_simple_triangle_01 2002Sep7
PASS
514 r400vte_w0_fmt_01 2002Sep7
PASS
515 r400vte_w0_fmt_03 2002Sep7
PASS
516 r400vte_xy_fmt_01 2002Sep7
PASS
517 r400vte_xyz_scale_01 2002Sep7
PASS
518 r400vte_xyz_scale_02 2002Sep7
PASS
519 r400vte_z_fmt_01 2002Sep7
PASS

=====
Tests_Run_count = 519 Pass_count = 518 Fail_count = 0 Incomplete_count = 1
=====

```

+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Sun Oct  6 21:23:51 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              CP              50          50          50          100
              RBBM             1           1           1           100
              VGT             180         180         180          100
              SC              300         300         300          100
              CL              119         119         118           99
              VTE              10          10          10          100
              SPECIALTY         6           6           6           100
              PARAMETER        62          62          62          100
              SU               97          97          97          100
              SANITY            2           2           2           100
+++++
              TOTAL            827         827         826           99
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Wed Aug 21  9:53:25 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          59          45
              SC           133          133          133          100
              CL           80           80           80           100
              VTE           9            9            0            0
SPECIALTY    SPECIALTY    6            6            0            0
              SU           64           64            0            0
              SANITY       2            2            0            0
+++++
              TOTAL        423          423          272          64
+++++

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+++++
++++      Regression Summary for vgt UNIT Simulations in Orlando      +++++
++++                               Date: Tue Aug 20 21:03:48 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          129          129          0          0
              SC           133          133          0          0
              CL           80           80           0          0
              VTE          9            9            0          0
SPECIALTY    SPECIALTY    6            6            0          0
              SU           64          64           0          0
              SANITY       2            2            0          0
+++++
              TOTAL        423          423          0          0
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep1
	PASS		
0	2d_indr_buffer_b		2002Sep1
	PASS		
0	2d_indr_buffer_c		2002Sep1
	PASS		
0	2d_indr_buffer_d		2002Sep1
	PASS		
0	2d_indr_buffer_e		2002Sep1
	PASS		
0	2d_indr_buffer_f		2002Sep1
	PASS		
0	2d_indr_buffer_g		2002Sep1
	PASS		
0	3d_draw_indx_2		2002Sep1
	PASS		
0	3d_draw_indx_2_b		2002Sep1
	PASS		
0	bitblt_multi		2002Sep1
	PASS		
0	brush_32x1mono		2002Sep1
	PASS		
0	brush_8x8color_16		2002Sep1
	PASS		
0	brush_8x8color_32		2002Sep1
	PASS		
0	brush_8x8color_64		2002Sep1
	PASS		
0	brush_8x8color_dst_0_1		2002Sep1
	PASS		
0	brush_8x8mono		2002Sep1
	PASS		
0	brush_color_mono_stress		2002Sep1
	PASS		
0	brush_flag		2002Sep1
	PASS		
0	brush_type0		2002Sep1
	PASS		
0	brush_type1		2002Sep1

PASS		
0	brush_type10	2002Sep1
PASS		
0	brush_type13	2002Sep1
PASS		
0	brush_type14	2002Sep1
PASS		
0	brush_type6	2002Sep1
PASS		
0	brush_type7	2002Sep1
PASS		
0	brush_type_other	2002Sep1
PASS		
0	brush_types_13_14	2002Sep1
PASS		
0	clear_const_wr_en_on_me_init	2002Sep1
PASS		
0	clear_const_wr_en_with_lcc	2002Sep1
PASS		
0	cond_write_mem_poll_mem	2002Sep1
PASS		
0	cond_write_mem_poll_mem2	2002Sep1
PASS		
0	cond_write_mem_poll_mem3	2002Sep1
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep1
PASS		
0	cond_write_mem_poll_reg	2002Sep1
PASS		
0	cond_write_mem_poll_reg2	2002Sep1
PASS		
0	cond_write_mem_poll_reg3	2002Sep1
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep1
PASS		
0	cp_bios_scratch	2002Sep1
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep1

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep1
PASS		
0	cp_ib_base000_bufszXXX	2002Sep1
PASS		
0	cp_ib_base001_bufszXXX	2002Sep1
PASS		
0	cp_ib_base010_bufszXXX	2002Sep1
PASS		
0	cp_ib_base011_bufszXXX	2002Sep1
PASS		
0	cp_ib_base100_bufszXXX	2002Sep1
PASS		
0	cp_ib_base101_bufszXXX	2002Sep1
PASS		
0	cp_ib_base110_bufszXXX	2002Sep1
PASS		
0	cp_ib_base111_bufszXXX	2002Sep1
PASS		
0	cp_interrupt	2002Sep1
PASS		
0	cp_interrupt_rt_events_16	2002Sep1
PASS		
0	cp_iq2_alignment_000	2002Sep1
PASS		
0	cp_iq2_alignment_001	2002Sep1
PASS		
0	cp_iq2_alignment_010	2002Sep1
PASS		
0	cp_iq2_alignment_011	2002Sep1
PASS		
0	cp_iq2_alignment_100	2002Sep1
PASS		
0	cp_iq2_alignment_101	2002Sep1
PASS		
0	cp_iq2_alignment_110	2002Sep1
PASS		
0	cp_iq2_alignment_111	2002Sep1
PASS		
0	cp_iq_alignment_000	2002Sep1
PASS		
0	cp_iq_alignment_001	2002Sep1
PASS		
0	cp_iq_alignment_010	2002Sep1
PASS		
0	cp_iq_alignment_011	2002Sep1

PASS		
0	cp_iq_alignment_100	2002Sep1
PASS		
0	cp_iq_alignment_101	2002Sep1
PASS		
0	cp_iq_alignment_110	2002Sep1
PASS		
0	cp_iq_alignment_111	2002Sep1
PASS		
0	cp_max_size_packet	2002Sep1
PASS		
74	cp_max_size_packet_random	2002Sep1
FAIL	rbbmWr.cmp	
0	cp_nrt_rt_protection_mode	2002Sep1
PASS		
0	cp_output_stress	2002Sep1
PASS		
0	cp_protection_mode	2002Sep1
PASS		
0	cp_rb_alignment_000	2002Sep1
PASS		
0	cp_rb_alignment_001	2002Sep1
PASS		
0	cp_rb_alignment_010	2002Sep1
PASS		
0	cp_rb_alignment_011	2002Sep1
PASS		
0	cp_rb_alignment_100	2002Sep1
PASS		
0	cp_rb_alignment_101	2002Sep1
PASS		
0	cp_rb_alignment_110	2002Sep1
PASS		
0	cp_rb_alignment_111	2002Sep1
PASS		
0	cp_rb_wptrXXX_base000	2002Sep1
PASS		
0	cp_rb_wptrXXX_base001	2002Sep1
PASS		
0	cp_rb_wptrXXX_base010	2002Sep1
PASS		
0	cp_rb_wptrXXX_base011	2002Sep1
PASS		
0	cp_rb_wptrXXX_base100	2002Sep1
PASS		
0	cp_rb_wptrXXX_base101	2002Sep1
PASS		
0	cp_rb_wptrXXX_base110	2002Sep1

PASS		
0	cp_rb_wptrXXX_base11	2002Sep1
PASS		
0	cp_register_reads	2002Sep1
PASS		
0	cp_reg_rw_test	2002Sep1
PASS		
0	cp_rt_protection_mode	2002Sep1
PASS		
0	cp_scratch	2002Sep1
PASS		
0	cp_scratch_multi	2002Sep1
PASS		
0	dma_cp_sync	2002Sep1
PASS		
0	dma_dscrstr_chckn_bit	2002Sep1
PASS		
0	dma_dscrstr_flush1	2002Sep1
PASS		
0	dma_dscrstr_flush2	2002Sep1
PASS		
0	dma_dscrstr_flush3	2002Sep1
PASS		
0	dma_m2m_src00_dstXX	2002Sep1
PASS		
0	dma_m2m_src01_dstXX	2002Sep1
PASS		
0	dma_m2m_src10_dstXX	2002Sep1
PASS		
0	dma_m2m_src11_dstXX	2002Sep1
PASS		
0	dma_mem2mem	2002Sep1
PASS		
0	dma_mem2mem_abort	2002Sep1
PASS		
0	dma_mem2mem_alignments	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep1

PASS		
0	dma_mem2mem_byte_alignment6	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep1
PASS		
0	dma_mem2mem_pio_abort	2002Sep1
PASS		
0	dma_mem2reg	2002Sep1
PASS		
0	dma_mem2reg_abort	2002Sep1
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep1
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep1
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep1
PASS		
0	dma_mem2reg_sync	2002Sep1
PASS		
0	dma_r2r_m2m_r2r	2002Sep1
PASS		
0	dma_r2r_m2r_r2r	2002Sep1
PASS		
0	dma_r2r_r2m_r2r	2002Sep1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep1
PASS		
0	dma_reg2mem	2002Sep1
PASS		
0	dma_reg2mem_abort	2002Sep1
PASS		
0	dma_reg2reg	2002Sep1
PASS		
0	dma_reg2reg_2dw	2002Sep1
PASS		
0	dma_reg2reg_daic	2002Sep1
PASS		
0	dma_reg2reg_mult	2002Sep1
PASS		
0	dma_reg2reg_mult2	2002Sep1
PASS		
0	dma_reg2reg_mult3	2002Sep1
PASS		
0	dma_reg2reg_saic	2002Sep1

PASS		
0	dma_reg2reg_sdaic	2002Sep1
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep1
PASS		
0	dma_reg2reg_stress	2002Sep1
PASS		
0	draw_indx_a	2002Sep1
PASS		
0	draw_indx_b	2002Sep1
PASS		
0	draw_indx_c	2002Sep1
PASS		
0	draw_indx_indr1_a	2002Sep1
PASS		
0	draw_indx_indr1_b	2002Sep1
PASS		
0	draw_indx_indr1_short_a	2002Sep1
PASS		
0	draw_indx_indr1_short_b	2002Sep1
PASS		
0	draw_indx_indr1_stress	2002Sep1
PASS		
0	draw_indx_indr2_a	2002Sep1
PASS		
0	draw_indx_indr2_b	2002Sep1
PASS		
0	draw_indx_indr2_short_a	2002Sep1
PASS		
0	draw_indx_indr2_short_b	2002Sep1
PASS		
0	draw_indx_indr2_stress	2002Sep1
PASS		
0	draw_indx_ring1	2002Sep1
PASS		
0	draw_indx_ring2	2002Sep1
PASS		
0	draw_indx_ring_short1	2002Sep1
PASS		
0	draw_indx_ring_short2	2002Sep1
PASS		
0	draw_indx_ring_stress	2002Sep1
PASS		
0	draw_indx_short_a	2002Sep1
PASS		
0	draw_indx_short_b	2002Sep1
PASS		
0	draw_indx_short_c	2002Sep1

PASS		
0	draw_indx_stress_a	2002Sep1
PASS		
0	draw_indx_stress_b	2002Sep1
PASS		
0	draw_indx_stress_c	2002Sep1
PASS		
168	event_timestamp_write	2002Sep1
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep1
PASS		
0	ft_indr1_july22	2002Sep1
PASS		
0	ft_indr2_july22	2002Sep1
PASS		
0	ft_july22	2002Sep1
PASS		
0	ft_july22_random	2002Sep1
PASS		
0	header_dump	2002Sep1
PASS		
0	header_dump_rt	2002Sep1
PASS		
0	header_dump_small	2002Sep1
PASS		
0	hostdata_blt	2002Sep1
PASS		
0	im_load_a	2002Sep1
PASS		
0	im_load_b	2002Sep1
PASS		
0	im_load_cid_mismatch	2002Sep1
PASS		
0	im_load_immediate	2002Sep1
PASS		
0	im_load_immediate_nrt_me	2002Sep1
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep1
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep1
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep1
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep1
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep1
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep1

PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep1
PASS		
0	im_load_indr1_a	2002Sep1
PASS		
0	im_load_indr1_b	2002Sep1
PASS		
0	im_load_indr2_a	2002Sep1
PASS		
0	im_load_indr2_b	2002Sep1
PASS		
0	im_load_invalidate	2002Sep1
PASS		
0	im_load_match	2002Sep1
PASS		
0	im_load_match2	2002Sep1
PASS		
0	im_load_nrt_me	2002Sep1
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep1
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep1
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep1
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep1
PASS		
0	im_load_pfmatch_disable	2002Sep1
PASS		
0	im_load_ring_a	2002Sep1
PASS		
0	im_load_ring_b	2002Sep1
PASS		
0	im_load_rt	2002Sep1
PASS		
0	im_load_rt2	2002Sep1
PASS		
0	im_load_rt3	2002Sep1
PASS		
0	im_load_rt4	2002Sep1
PASS		
0	im_load_sz_mismatch	2002Sep1
PASS		
0	incremental_update_const	2002Sep1
PASS		
0	incremental_update_instr	2002Sep1
PASS		
0	incremental_update_state	2002Sep1

PASS		
0	indirect_buffer_a	2002Sep1
PASS		
0	indirect_buffer_b	2002Sep1
PASS		
0	indirect_buffer_c	2002Sep1
PASS		
0	indirect_buffer_d	2002Sep1
PASS		
0	indirect_buffer_e	2002Sep1
PASS		
0	indirect_buffer_f	2002Sep1
PASS		
0	indirect_buffer_g	2002Sep1
PASS		
0	indirect_buffer_h	2002Sep1
PASS		
0	indirect_buffer_i	2002Sep1
PASS		
0	indirect_buffer_j	2002Sep1
PASS		
0	indirect_buffer_k	2002Sep1
PASS		
0	indirect_buffer_l	2002Sep1
PASS		
0	indirect_buffer_mp_a	2002Sep1
PASS		
0	indirect_buffer_mp_b	2002Sep1
PASS		
0	indirect_buffer_mp_c	2002Sep1
PASS		
0	indirect_buffer_mp_d	2002Sep1
PASS		
0	indirect_buffer_mp_e	2002Sep1
PASS		
0	indirect_buffer_mp_f	2002Sep1
PASS		
0	invalidate_on_me_init	2002Sep1
PASS		
0	invalidate_state_constants	2002Sep1
PASS		
0	invalidate_state_shaders	2002Sep1
PASS		
0	invalidate_state_shaders2	2002Sep1
PASS		
0	invalidate_state_sublks	2002Sep1
PASS		
0	invalidate_state_sublks2	2002Sep1

PASS		
0	invalid_opcode	2002Sep1
PASS		
0	iq1_iq2_mult	2002Sep1
PASS		
0	iq1_iq2_push_wrap	2002Sep1
PASS		
0	iq2_csq_full	2002Sep1
PASS		
0	IQ2_Mult	2002Sep1
PASS		
0	IQ2_Wrap_Legacy	2002Sep1
PASS		
0	IQ_Wrap	2002Sep1
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep1
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep1
PASS		
0	ld_cnst_cntx_match	2002Sep1
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep1
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep1
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep1
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep1
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep1
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep1

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep1
PASS		
0	ld_cnst_cntx_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep1
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep1
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep1
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep1
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep1
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep1
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep1
PASS		
0	load_constant_context_rt	2002Sep1
PASS		
0	load_palette	2002Sep1
PASS		
0	lp_2d3d	2002Sep1
PASS		
0	mem_write	2002Sep1
PASS		
0	mem_write_rt	2002Sep1
PASS		
0	mpeg_index	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep1

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep1
PASS		
0	nop_eating_disabled	2002Sep1
PASS		
0	paint_multi	2002Sep1
PASS		
0	pitch_clip	2002Sep1
PASS		
0	ply_nextscan	2002Sep1
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep1
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep1
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep1
PASS		
0	rbbm_cp_queued_rdwr	2002Sep1
PASS		
0	rbbm_soft_reset	2002Sep1
PASS		
301	rbbm_WU_2D_idle_01	2002Sep1
FAIL		
302	rbbm_WU_2D_idle_02	2002Sep1
FAIL		
303	rbbm_WU_2D_idle_03	2002Sep1
FAIL		
304	rbbm_WU_2D_idle_04	2002Sep1
FAIL		
0	rbbm_WU_2D_idle_05	2002Sep1
PASS		
306	rbbm_WU_2D_idle_06	2002Sep1
FAIL		
307	rbbm_WU_2D_idle_07	2002Sep1
FAIL		
308	rbbm_WU_2D_idle_08	2002Sep1

FAIL		
309	rbbm_WU_2D_idle_09	2002Sep1
FAIL		
310	rbbm_WU_2D_idle_clean_01	2002Sep1
FAIL		
311	rbbm_WU_2D_idle_clean_02	2002Sep1
FAIL		
312	rbbm_WU_2D_idle_clean_03	2002Sep1
FAIL		
313	rbbm_WU_2D_idle_clean_04	2002Sep1
FAIL		
0	rbbm_WU_2D_idle_clean_05	2002Sep1
PASS		
315	rbbm_WU_2D_idle_clean_06	2002Sep1
FAIL		
316	rbbm_WU_2D_idle_clean_07	2002Sep1
FAIL		
317	rbbm_WU_2D_idle_clean_08	2002Sep1
FAIL		
318	rbbm_WU_2D_idle_clean_09	2002Sep1
FAIL		
319	rbbm_WU_2D_idle_clean_10	2002Sep1
FAIL		
320	rbbm_WU_2D_idle_clean_11	2002Sep1
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Sep1
PASS		
322	rbbm_WU_3D_idle_01	2002Sep1
FAIL		
323	rbbm_WU_3D_idle_02	2002Sep1
FAIL		
324	rbbm_WU_3D_idle_03	2002Sep1
FAIL		
325	rbbm_WU_3D_idle_04	2002Sep1
FAIL		
0	rbbm_WU_3D_idle_05	2002Sep1
PASS		
327	rbbm_WU_3D_idle_06	2002Sep1
FAIL		
328	rbbm_WU_3D_idle_07	2002Sep1
FAIL		
329	rbbm_WU_3D_idle_08	2002Sep1
FAIL		
330	rbbm_WU_3D_idle_09	2002Sep1
FAIL		
331	rbbm_WU_3D_idle_clean_01	2002Sep1
FAIL		
332	rbbm_WU_3D_idle_clean_02	2002Sep1

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**FAIL**
333  rbbm_WU_3D_idle_clean_03                2002Sep1
**FAIL**
334  rbbm_WU_3D_idle_clean_04                2002Sep1
**FAIL**
0    rbbm_WU_3D_idle_clean_05                2002Sep1
PASS
336  rbbm_WU_3D_idle_clean_06                2002Sep1
**FAIL**
337  rbbm_WU_3D_idle_clean_07                2002Sep1
**FAIL**
338  rbbm_WU_3D_idle_clean_08                2002Sep1
**FAIL**
339  rbbm_WU_3D_idle_clean_09                2002Sep1
**FAIL**
340  rbbm_WU_3D_idle_clean_10                2002Sep1
**FAIL**
341  rbbm_WU_3D_idle_clean_11                2002Sep1
**FAIL**
0    rbbm_WU_3D_idle_clean_12                2002Sep1
PASS
343  rbbm_WU_BIF_RBBM_agp_flush_off          2002Sep1
**FAIL**
344  rbbm_WU_BOTH_GRP update_pending_off     2002Sep1
**FAIL**
0    rbbm_WU_cmdfifo_entries                 2002Sep1
PASS
0    rbbm_WU_CP_dma_idle                     2002Sep1
PASS
347  rbbm_WU_CRTC_D1MODE_vline_fe           2002Sep1
**FAIL**
348  rbbm_WU_CRTC_D1MODE_vline_on           2002Sep1
**FAIL**
349  rbbm_WU_CRTC_D1MODE_vline_re           2002Sep1
**FAIL**
350  rbbm_WU_CRTC_D2MODE_vline_fe           2002Sep1
**FAIL**
351  rbbm_WU_CRTC_D2MODE_vline_on           2002Sep1
**FAIL**
352  rbbm_WU_CRTC_D2MODE_vline_re           2002Sep1
**FAIL**
353  rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep1
**FAIL**
354  rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep1
**FAIL**
355  rbbm_WU_idct_semaphore                 2002Sep1
**FAIL**
356  rbbm_WU_MH_hdp_clean                   2002Sep1

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**FAIL**
357  rbbm_WU_OV_FLIP_D1OVL_update_pending_off      2002Sep1
**FAIL**
358  rbbm_WU_OV_FLIP_D2OVL_update_pending_off      2002Sep1
**FAIL**
359  rbbm_WU_pad_ext_sig_rising_edge                2002Sep1
**FAIL**
0    rbbm_WUrt_BIF_RBBM_agp_flush_off              2002Sep1
PASS
0    rbbm_WUrt_BOTH_GRP_H_update_pending_off        2002Sep1
PASS
0    rbbm_WUrt_CP_dma_idle                          2002Sep1
PASS
0    rbbm_WUrt_CRTC_D1MODE_vline_fe                 2002Sep1
PASS
0    rbbm_WUrt_CRTC_D1MODE_vline_on                 2002Sep1
PASS
0    rbbm_WUrt_CRTC_D1MODE_vline_re                 2002Sep1
PASS
0    rbbm_WUrt_CRTC_D2MODE_vline_fe                 2002Sep1
PASS
0    rbbm_WUrt_CRTC_D2MODE_vline_on                 2002Sep1
PASS
0    rbbm_WUrt_CRTC_D2MODE_vline_re                 2002Sep1
PASS
0    rbbm_WUrt_CRTC_PFLIP_D1GRP_H_update_pending_off 2002Sep1
PASS
0    rbbm_WUrt_CRTC_PFLIP_D2GRP_H_update_pending_off 2002Sep1
PASS
0    rbbm_WUrt_MH_hdp_clean                          2002Sep1
PASS
0    rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off      2002Sep1
PASS
0    rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off      2002Sep1
PASS
0    rbbm_WUrt_pad_ext_sig_rising_edge                2002Sep1
PASS
0    rbbm_WUrt_RT_idle_01                          2002Sep1
PASS
0    rbbm_WUrt_RT_idle_02                          2002Sep1
PASS
0    rbbm_WUrt_RT_idle_03                          2002Sep1
PASS
0    rbbm_WUrt_RT_idle_04                          2002Sep1
PASS
0    rbbm_WUrt_RT_idle_clean_01                     2002Sep1
PASS
0    rbbm_WUrt_RT_idle_clean_02                     2002Sep1

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PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep1
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep1
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep1
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep1
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep1
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep1
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep1
PASS		
388	rbbm_WU_VIPH0_dma_idle	2002Sep1
FAIL		
389	rbbm_WU_VIPH1_dma_idle	2002Sep1
FAIL		
390	rbbm_WU_VIPH2_dma_idle	2002Sep1
FAIL		
391	rbbm_WU_VIPH3_dma_idle	2002Sep1
FAIL		
0	RB_BUFSZ_0	2002Sep1
PASS		
0	RB_BUFSZ_1	2002Sep1
PASS		
0	RB_Wrap	2002Sep1
PASS		
0	reg_to_mem	2002Sep1
PASS		
0	reg_to_mem_rt	2002Sep1
PASS		
0	rop_cfd_wmd	2002Sep1
PASS		
0	rstr_ordr	2002Sep1
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Sep1
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep1
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep1

PASS		
0	rtee_discretes_falling_04_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep1

PASS		
0	rtee_discretes_falling_28_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep1

PASS		
0	rtee_discretes_invert_20_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep1
PASS		
0	rtee_discretes_level_00_events_16	2002Sep1
PASS		
0	rtee_discretes_level_01_events_16	2002Sep1
PASS		
0	rtee_discretes_level_02_events_16	2002Sep1
PASS		
0	rtee_discretes_level_03_events_16	2002Sep1
PASS		
0	rtee_discretes_level_04_events_16	2002Sep1
PASS		
0	rtee_discretes_level_05_events_16	2002Sep1
PASS		
0	rtee_discretes_level_06_events_16	2002Sep1
PASS		
0	rtee_discretes_level_07_events_16	2002Sep1
PASS		
0	rtee_discretes_level_08_events_16	2002Sep1
PASS		
0	rtee_discretes_level_09_events_16	2002Sep1
PASS		
0	rtee_discretes_level_10_events_16	2002Sep1
PASS		
0	rtee_discretes_level_11_events_16	2002Sep1

PASS		
0	rtee_discretes_level_12_events_16	2002Sep1
PASS		
0	rtee_discretes_level_13_events_16	2002Sep1
PASS		
0	rtee_discretes_level_14_events_16	2002Sep1
PASS		
0	rtee_discretes_level_15_events_16	2002Sep1
PASS		
0	rtee_discretes_level_16_events_16	2002Sep1
PASS		
0	rtee_discretes_level_17_events_16	2002Sep1
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0	rtee_discretes_level_18_events_16	2002Sep1
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0	rtee_discretes_level_19_events_16	2002Sep1
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0	rtee_discretes_level_20_events_16	2002Sep1
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0	rtee_discretes_level_25_events_16	2002Sep1
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0	rtee_discretes_level_26_events_16	2002Sep1
PASS		
0	rtee_discretes_level_27_events_16	2002Sep1
PASS		
0	rtee_discretes_level_28_events_16	2002Sep1
PASS		
0	rtee_discretes_level_29_events_16	2002Sep1
PASS		
0	rtee_discretes_level_30_events_16	2002Sep1
PASS		
0	rtee_discretes_level_31_events_16	2002Sep1
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0	rtee_discretes_manual_disarm_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep1

PASS		
0	rtee_discretes_rising_03_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep1
PASS		
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PASS		
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PASS		
0	rtee_discretes_rising_12_events_16	2002Sep1
PASS		
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PASS		
0	rtee_discretes_rising_14_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep1
PASS		
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PASS		
0	rtee_discretes_rising_19_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep1

PASS		
0	rtee_discretes_rising_27_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep1
PASS		
530	rtee_display1_comp_events_16	2002Sep1
FAIL	rbbmWr.cmp	
531	rtee_display2_comp_events_16	2002Sep1
FAIL	rbbmWr.cmp	
0	rtee_mem_linear_search_events_2	2002Sep1
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep1
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep1
PASS		
0	rtee_prefetch_parse_nops	2002Sep1
PASS		
0	rtee_reg_linear_search_events_2	2002Sep1
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep1

PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep1
PASS		
555	rtee_vip_comp_events_16	2002Sep1
FAIL	rbbmWr.cmp	
0	rt_invalid_opcode	2002Sep1
PASS		
0	scratch_mem_read	2002Sep1
PASS		
0	scratch_mem_read2	2002Sep1
PASS		
0	set_constant	2002Sep1
PASS		
0	set_constant2	2002Sep1
PASS		
0	set_constant_alu_max1	2002Sep1
PASS		
0	set_constant_alu_max2	2002Sep1
PASS		
0	set_constant_bool_max1	2002Sep1
PASS		
0	set_constant_bool_max2	2002Sep1
PASS		
0	set_constant_indr1_short	2002Sep1
PASS		
0	set_constant_indr1_short2	2002Sep1
PASS		
0	set_constant_indr1_write_enable	2002Sep1
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep1
PASS		
0	set_constant_indr2_short	2002Sep1
PASS		
0	set_constant_indr2_short2	2002Sep1
PASS		
0	set_constant_indr2_write_enable	2002Sep1
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep1

PASS		
0	set_constant_loop_max1	2002Sep1
PASS		
0	set_constant_loop_max2	2002Sep1
PASS		
0	set_constant_ring_short	2002Sep1
PASS		
0	set_constant_ring_short2	2002Sep1
PASS		
0	set_constant_ring_write_enable	2002Sep1
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep1
PASS		
0	set_constant_short	2002Sep1
PASS		
0	set_constant_short2	2002Sep1
PASS		
0	set_constant_tex_max1	2002Sep1
PASS		
0	set_constant_tex_max2	2002Sep1
PASS		
0	set_constant_write_enable	2002Sep1
PASS		
0	set_constant_write_enable_lcc	2002Sep1
PASS		
0	set_constant_write_enable_lcc2	2002Sep1
PASS		
0	set_scissors	2002Sep1
PASS		
0	set_state_rt_a	2002Sep1
PASS		
0	set_state_rt_b	2002Sep1
PASS		
0	set_state_rt_c	2002Sep1
PASS		
0	set_state_rt_d	2002Sep1
PASS		
0	set_st_indrl_match	2002Sep1
PASS		
0	set_st_indrl_mismatch	2002Sep1
PASS		
0	set_st_indrl_mismatch2	2002Sep1
PASS		
0	set_st_indrl_mismatch3	2002Sep1
PASS		
0	set_st_indrl_mismatch4	2002Sep1
PASS		
0	set_st_indrl_ps	2002Sep1

PASS		
0	set_st_indr1_ps_only	2002Sep1
PASS		
0	set_st_indr1_sublk0	2002Sep1
PASS		
0	set_st_indr1_sublk1	2002Sep1
PASS		
0	set_st_indr1_sublk1_only	2002Sep1
PASS		
0	set_st_indr1_sublk2	2002Sep1
PASS		
0	set_st_indr1_sublk2_only	2002Sep1
PASS		
0	set_st_indr1_sublk3	2002Sep1
PASS		
0	set_st_indr1_sublk3_only	2002Sep1
PASS		
0	set_st_indr1_sublk4	2002Sep1
PASS		
0	set_st_indr1_sublk4_only	2002Sep1
PASS		
0	set_st_indr1_sublk5	2002Sep1
PASS		
0	set_st_indr1_sublk5_only	2002Sep1
PASS		
0	set_st_indr1_sublk6	2002Sep1
PASS		
0	set_st_indr1_sublk6_only	2002Sep1
PASS		
0	set_st_indr1_sublk7	2002Sep1
PASS		
0	set_st_indr1_sublk7_only	2002Sep1
PASS		
0	set_st_indr1_vs	2002Sep1
PASS		
0	set_st_indr1_vs_only	2002Sep1
PASS		
0	set_st_indr2_match	2002Sep1
PASS		
0	set_st_indr2_mismatch	2002Sep1
PASS		
0	set_st_indr2_mismatch2	2002Sep1
PASS		
0	set_st_indr2_mismatch3	2002Sep1
PASS		
0	set_st_indr2_mismatch4	2002Sep1
PASS		
0	set_st_indr2_ps	2002Sep1

PASS		
0	set_st_indr2_ps_only	2002Sep1
PASS		
0	set_st_indr2_sublk0	2002Sep1
PASS		
0	set_st_indr2_sublk1	2002Sep1
PASS		
0	set_st_indr2_sublk1_only	2002Sep1
PASS		
0	set_st_indr2_sublk2	2002Sep1
PASS		
0	set_st_indr2_sublk2_only	2002Sep1
PASS		
0	set_st_indr2_sublk3	2002Sep1
PASS		
0	set_st_indr2_sublk3_only	2002Sep1
PASS		
0	set_st_indr2_sublk4	2002Sep1
PASS		
0	set_st_indr2_sublk4_only	2002Sep1
PASS		
0	set_st_indr2_sublk5	2002Sep1
PASS		
0	set_st_indr2_sublk5_only	2002Sep1
PASS		
0	set_st_indr2_sublk6	2002Sep1
PASS		
0	set_st_indr2_sublk6_only	2002Sep1
PASS		
0	set_st_indr2_sublk7	2002Sep1
PASS		
0	set_st_indr2_sublk7_only	2002Sep1
PASS		
0	set_st_indr2_vs	2002Sep1
PASS		
0	set_st_indr2_vs_only	2002Sep1
PASS		
0	set_st_pfmatch_disabled	2002Sep1
PASS		
0	set_st_ring_match	2002Sep1
PASS		
0	set_st_ring_mismatch	2002Sep1
PASS		
0	set_st_ring_mismatch2	2002Sep1
PASS		
0	set_st_ring_mismatch3	2002Sep1
PASS		
0	set_st_ring_mismatch4	2002Sep1

PASS		
0	set_st_ring_ps	2002Sep1
PASS		
0	set_st_ring_ps_only	2002Sep1
PASS		
0	set_st_ring_sublk0	2002Sep1
PASS		
0	set_st_ring_sublk1	2002Sep1
PASS		
0	set_st_ring_sublk1_only	2002Sep1
PASS		
0	set_st_ring_sublk2	2002Sep1
PASS		
0	set_st_ring_sublk2_only	2002Sep1
PASS		
0	set_st_ring_sublk3	2002Sep1
PASS		
0	set_st_ring_sublk3_only	2002Sep1
PASS		
0	set_st_ring_sublk4	2002Sep1
PASS		
0	set_st_ring_sublk4_only	2002Sep1
PASS		
0	set_st_ring_sublk5	2002Sep1
PASS		
0	set_st_ring_sublk5_only	2002Sep1
PASS		
0	set_st_ring_sublk6	2002Sep1
PASS		
0	set_st_ring_sublk6_only	2002Sep1
PASS		
0	set_st_ring_sublk7	2002Sep1
PASS		
0	set_st_ring_sublk7_only	2002Sep1
PASS		
0	set_st_ring_vs	2002Sep1
PASS		
0	set_st_ring_vs_only	2002Sep1
PASS		
0	set_st_stress_a	2002Sep1
PASS		
0	set_st_stress_b	2002Sep1
PASS		
0	set_st_stress_c	2002Sep1
PASS		
0	set_st_stress_d	2002Sep1
PASS		
0	set_st_stress_e	2002Sep1

PASS		
0	set_st_stress_f	2002Sep1
PASS		
0	Small_Text_BM	2002Sep1
PASS		
0	smoke_nrt	2002Sep1
PASS		
0	src_clip	2002Sep1
PASS		
0	srf_cohr	2002Sep1
PASS		
0	trans_bitblt	2002Sep1
PASS		
0	type0_2long_pckts	2002Sep1
PASS		
0	type0_3indr_buffs	2002Sep1
PASS		
0	type0_4pckts	2002Sep1
PASS		
0	type0_incr_updt_cnst	2002Sep1
PASS		
0	type0_incr_updt_instrmem	2002Sep1
PASS		
0	type0_incr_updt_st	2002Sep1
PASS		
0	type0_incr_updt_stress	2002Sep1
PASS		
0	type0_ind1_ind2_a	2002Sep1
PASS		
0	type0_ind1_ind2_b	2002Sep1
PASS		
0	type0_indr_buff	2002Sep1
PASS		
0	Type0_IQ2_BM	2002Sep1
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep1
PASS		
0	Type0_IQ_BM	2002Sep1
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep1
PASS		
0	Type0_PQ_BM	2002Sep1
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep1
PASS		
0	type1_4pckts	2002Sep1
PASS		
0	type1_indr_buff	2002Sep1

PASS		
0	type1_pckt	2002Sep1
PASS		
0	Type3_Bitblt	2002Sep1
PASS		
0	Type3_Load_Palette	2002Sep1
PASS		
0	Type3_NOP	2002Sep1
PASS		
697	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
698	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep1
PASS		
0	uram_wrt_ucode	2002Sep1
PASS		
0	viz_query_a	2002Sep1
PASS		
0	viz_query_draw_indx	2002Sep1
PASS		
0	viz_query_draw_indx_indr1	2002Sep1
PASS		
0	viz_query_draw_indx_indr2	2002Sep1
PASS		
0	viz_query_draw_indx_multipass	2002Sep1
PASS		
0	viz_query_draw_indx_ring	2002Sep1
PASS		
0	viz_query_indr1	2002Sep1
PASS		
0	viz_query_indr1_simple	2002Sep1
PASS		
0	viz_query_indr2	2002Sep1
PASS		
0	viz_query_indr2_simple	2002Sep1
PASS		
0	viz_query_ring	2002Sep1
PASS		
0	viz_query_ring_simple	2002Sep1
PASS		
0	wait_for_idle	2002Sep1
PASS		
0	wait_reg_mem_poll_mem0	2002Sep1
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Sep1
PASS		
0	wait_reg_mem_poll_mem1	2002Sep1

```
PASS
0   wait_reg_mem_poll_mem2           2002Sep1
PASS
0   wait_reg_mem_poll_mem3           2002Sep1
PASS
0   wait_reg_mem_poll_reg0           2002Sep1
PASS
0   wait_reg_mem_poll_reg1           2002Sep1
PASS
0   wait_reg_mem_poll_reg2           2002Sep1
PASS
0   wait_reg_mem_poll_reg3           2002Sep1
PASS
```

```
=====
Tests_Run_count = 722  Pass_count = 660  Fail_count = 62  Incomplete_count = 0
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Sep 21 16:04:09 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             641         641         607         94
              RBBM           109         109         107         98
+++++
              TOTAL           750         750         714         95
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buffer_a		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
2	2d_indr_buffer_b		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
3	2d_indr_buffer_c		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
4	2d_indr_buffer_d		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
5	2d_indr_buffer_e		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
6	2d_indr_buffer_f		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
7	2d_indr_buffer_g		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
8	2d_indr_buffer_random		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep17
PASS			
0	3d_draw_indx_2_b		2002Sep17
PASS			
11	bitblt_multi		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
12	brush_32x1mono		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
13	brush_8x8color_16		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
14	brush_8x8color_32		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
15	brush_8x8color_64		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
16	brush_8x8color_dst_0_1		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
17	brush_8x8mono		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
18	brush_color_mono_stress		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
19	brush_flag		2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp		
20	brush_type0		2002Sep17

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
21  brush_type1                                2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
22  brush_type10                               2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
23  brush_type13                               2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
24  brush_type14                               2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
25  brush_type6                                2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
26  brush_type7                                2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
27  brush_type_other                           2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
28  brush_types_13_14                          2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   clear_const_wr_en_on_me_init                2002Sep17
PASS
0   clear_const_wr_en_with_lcc                  2002Sep17
PASS
0   cond_write_mem_poll_mem                     2002Sep17
PASS
0   cond_write_mem_poll_mem2                    2002Sep17
PASS
0   cond_write_mem_poll_mem3                    2002Sep17
PASS
0   cond_write_mem_poll_mem_rt                  2002Sep17
PASS
0   cond_write_mem_poll_reg                     2002Sep17
PASS
0   cond_write_mem_poll_reg2                    2002Sep17
PASS
0   cond_write_mem_poll_reg3                    2002Sep17
PASS
0   cp_acpi_recovery_soft_reset                 2002Sep17
PASS
0   cp_bios_scratch                             2002Sep17
PASS
0   cp_ib2_base000_bufszXXX                     2002Sep17
PASS
0   cp_ib2_base001_bufszXXX                     2002Sep17
PASS
0   cp_ib2_base010_bufszXXX                     2002Sep17
PASS
0   cp_ib2_base011_bufszXXX                     2002Sep17
PASS
0   cp_ib2_base100_bufszXXX                     2002Sep17

```

PASS		
0	cp_ib2_base101_bufszXXX	2002Sep17
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep17
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep17
PASS		
0	cp_ib_base000_bufszXXX	2002Sep17
PASS		
0	cp_ib_base001_bufszXXX	2002Sep17
PASS		
0	cp_ib_base010_bufszXXX	2002Sep17
PASS		
0	cp_ib_base011_bufszXXX	2002Sep17
PASS		
0	cp_ib_base100_bufszXXX	2002Sep17
PASS		
0	cp_ib_base101_bufszXXX	2002Sep17
PASS		
0	cp_ib_base110_bufszXXX	2002Sep17
PASS		
0	cp_ib_base111_bufszXXX	2002Sep17
PASS		
0	cp_interrupt	2002Sep17
PASS		
0	cp_interrupt_rt_events_16	2002Sep17
PASS		
0	cp_iq2_alignment_000	2002Sep17
PASS		
0	cp_iq2_alignment_001	2002Sep17
PASS		
0	cp_iq2_alignment_010	2002Sep17
PASS		
0	cp_iq2_alignment_011	2002Sep17
PASS		
0	cp_iq2_alignment_100	2002Sep17
PASS		
0	cp_iq2_alignment_101	2002Sep17
PASS		
0	cp_iq2_alignment_110	2002Sep17
PASS		
0	cp_iq2_alignment_111	2002Sep17
PASS		
0	cp_iq_alignment_000	2002Sep17
PASS		
0	cp_iq_alignment_001	2002Sep17
PASS		
0	cp_iq_alignment_010	2002Sep17

PASS		
0	cp_iq_alignment_011	2002Sep17
PASS		
0	cp_iq_alignment_100	2002Sep17
PASS		
0	cp_iq_alignment_101	2002Sep17
PASS		
0	cp_iq_alignment_110	2002Sep17
PASS		
0	cp_iq_alignment_111	2002Sep17
PASS		
0	cp_max_size_packet	2002Sep17
PASS		
0	cp_max_size_packet_random	2002Sep17
PASS		
0	cp_nrt_rt_protection_mode	2002Sep17
PASS		
0	cp_output_stress	2002Sep17
PASS		
0	cp_output_stress_random	2002Sep17
PASS		
0	cp_protection_mode	2002Sep17
PASS		
0	cp_rb_alignment_000	2002Sep17
PASS		
0	cp_rb_alignment_001	2002Sep17
PASS		
0	cp_rb_alignment_010	2002Sep17
PASS		
0	cp_rb_alignment_011	2002Sep17
PASS		
0	cp_rb_alignment_100	2002Sep17
PASS		
0	cp_rb_alignment_101	2002Sep17
PASS		
0	cp_rb_alignment_110	2002Sep17
PASS		
0	cp_rb_alignment_111	2002Sep17
PASS		
0	cp_rb_wptrXXX_base000	2002Sep17
PASS		
0	cp_rb_wptrXXX_base001	2002Sep17
PASS		
0	cp_rb_wptrXXX_base010	2002Sep17
PASS		
0	cp_rb_wptrXXX_base011	2002Sep17
PASS		
0	cp_rb_wptrXXX_base100	2002Sep17

PASS		
0	cp_rb_wptrXXX_base101	2002Sep17
PASS		
0	cp_rb_wptrXXX_base110	2002Sep17
PASS		
0	cp_rb_wptrXXX_base111	2002Sep17
PASS		
0	cp_register_reads	2002Sep17
PASS		
0	cp_reg_rw_test	2002Sep17
PASS		
0	cp_rt_protection_mode	2002Sep17
PASS		
0	cp_scratch	2002Sep17
PASS		
0	cp_scratch_multi	2002Sep17
PASS		
0	dma_cp_sync	2002Sep17
PASS		
0	dma_dscrstr_chckn_bit	2002Sep17
PASS		
0	dma_dscrstr_flush1	2002Sep17
PASS		
0	dma_dscrstr_flush2	2002Sep17
PASS		
0	dma_dscrstr_flush3	2002Sep17
PASS		
0	dma_m2m_src00_dstXX	2002Sep17
PASS		
0	dma_m2m_src01_dstXX	2002Sep17
PASS		
0	dma_m2m_src10_dstXX	2002Sep17
PASS		
0	dma_m2m_src11_dstXX	2002Sep17
PASS		
0	dma_mem2mem	2002Sep17
PASS		
0	dma_mem2mem_abort	2002Sep17
PASS		
0	dma_mem2mem_alignments	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep17

PASS		
0	dma_mem2mem_byte_alignment4	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep17
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep17
PASS		
0	dma_mem2mem_pio_abort	2002Sep17
PASS		
0	dma_mem2mem_random	2002Sep17
PASS		
0	dma_mem2reg	2002Sep17
PASS		
0	dma_mem2reg_abort	2002Sep17
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep17
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep17
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep17
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep17
PASS		
0	dma_mem2reg_sync	2002Sep17
PASS		
0	dma_r2r_m2m_r2r	2002Sep17
PASS		
0	dma_r2r_m2r_r2r	2002Sep17
PASS		
0	dma_r2r_r2m_r2r	2002Sep17
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep17
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep17
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep17
PASS		
0	dma_reg2mem	2002Sep17
PASS		
0	dma_reg2mem_abort	2002Sep17
PASS		
0	dma_reg2reg	2002Sep17
PASS		
0	dma_reg2reg_2dw	2002Sep17
PASS		
0	dma_reg2reg_daic	2002Sep17

PASS		
0	dma_reg2reg_mult	2002Sep17
PASS		
0	dma_reg2reg_mult2	2002Sep17
PASS		
0	dma_reg2reg_mult3	2002Sep17
PASS		
0	dma_reg2reg_saic	2002Sep17
PASS		
0	dma_reg2reg_sdaic	2002Sep17
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep17
PASS		
0	dma_reg2reg_stress	2002Sep17
PASS		
0	draw_indx_a	2002Sep17
PASS		
0	draw_indx_b	2002Sep17
PASS		
0	draw_indx_c	2002Sep17
PASS		
0	draw_indx_indr1_a	2002Sep17
PASS		
0	draw_indx_indr1_b	2002Sep17
PASS		
0	draw_indx_indr1_short_a	2002Sep17
PASS		
0	draw_indx_indr1_short_b	2002Sep17
PASS		
0	draw_indx_indr1_stress	2002Sep17
PASS		
0	draw_indx_indr2_a	2002Sep17
PASS		
0	draw_indx_indr2_b	2002Sep17
PASS		
0	draw_indx_indr2_short_a	2002Sep17
PASS		
0	draw_indx_indr2_short_b	2002Sep17
PASS		
0	draw_indx_indr2_stress	2002Sep17
PASS		
0	draw_indx_ring1	2002Sep17
PASS		
0	draw_indx_ring2	2002Sep17
PASS		
0	draw_indx_ring_short1	2002Sep17
PASS		
0	draw_indx_ring_short2	2002Sep17

PASS		
0	draw_indx_ring_stress	2002Sep17
PASS		
0	draw_indx_short_a	2002Sep17
PASS		
0	draw_indx_short_b	2002Sep17
PASS		
0	draw_indx_short_c	2002Sep17
PASS		
0	draw_indx_stress_a	2002Sep17
PASS		
0	draw_indx_stress_b	2002Sep17
PASS		
0	draw_indx_stress_c	2002Sep17
PASS		
172	event_timestamp_write	2002Sep17
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep17
PASS		
0	ft_indr1_july22	2002Sep17
PASS		
0	ft_indr2_july22	2002Sep17
PASS		
0	ft_july22	2002Sep17
PASS		
0	ft_july22_random	2002Sep17
PASS		
0	header_dump	2002Sep17
PASS		
0	header_dump_rt	2002Sep17
PASS		
0	header_dump_small	2002Sep17
PASS		
181	hostdata_blt	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep17
PASS		
0	im_load_b	2002Sep17
PASS		
0	im_load_cid_mismatch	2002Sep17
PASS		
0	im_load_immediate	2002Sep17
PASS		
0	im_load_immediate_nrt_me	2002Sep17
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep17
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep17

PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep17
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep17
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep17
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep17
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep17
PASS		
0	im_load_indr1_a	2002Sep17
PASS		
0	im_load_indr1_b	2002Sep17
PASS		
0	im_load_indr2_a	2002Sep17
PASS		
0	im_load_indr2_b	2002Sep17
PASS		
0	im_load_invalidate	2002Sep17
PASS		
0	im_load_match	2002Sep17
PASS		
0	im_load_match2	2002Sep17
PASS		
0	im_load_nrt_me	2002Sep17
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep17
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep17
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep17
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep17
PASS		
0	im_load_pfmacth_disable	2002Sep17
PASS		
0	im_load_ring_a	2002Sep17
PASS		
0	im_load_ring_b	2002Sep17
PASS		
0	im_load_rt	2002Sep17
PASS		
0	im_load_rt2	2002Sep17
PASS		
0	im_load_rt3	2002Sep17
PASS		
0	im_load_rt4	2002Sep17

PASS		
0	im_load_sz_mismatch	2002Sep17
PASS		
0	incremental_update_const	2002Sep17
PASS		
0	incremental_update_instr	2002Sep17
PASS		
0	incremental_update_state	2002Sep17
PASS		
0	indirect_buffer_a	2002Sep17
PASS		
0	indirect_buffer_b	2002Sep17
PASS		
0	indirect_buffer_c	2002Sep17
PASS		
0	indirect_buffer_d	2002Sep17
PASS		
221	indirect_buffer_e	2002Sep17
FAIL	rbbmWr.cmp	
0	indirect_buffer_f	2002Sep17
PASS		
0	indirect_buffer_g	2002Sep17
PASS		
0	indirect_buffer_h	2002Sep17
PASS		
0	indirect_buffer_i	2002Sep17
PASS		
0	indirect_buffer_j	2002Sep17
PASS		
0	indirect_buffer_k	2002Sep17
PASS		
0	indirect_buffer_l	2002Sep17
PASS		
0	indirect_buffer_mp_a	2002Sep17
PASS		
0	indirect_buffer_mp_b	2002Sep17
PASS		
0	indirect_buffer_mp_c	2002Sep17
PASS		
0	indirect_buffer_mp_d	2002Sep17
PASS		
0	indirect_buffer_mp_e	2002Sep17
PASS		
0	indirect_buffer_mp_f	2002Sep17
PASS		
0	invalidate_on_me_init	2002Sep17
PASS		
0	invalidate_state_constants	2002Sep17

PASS		
0	invalidate_state_shaders	2002Sep17
PASS		
0	invalidate_state_shaders2	2002Sep17
PASS		
0	invalidate_state_sublks	2002Sep17
PASS		
0	invalidate_state_sublks2	2002Sep17
PASS		
0	invalid_opcode	2002Sep17
PASS		
0	iq1_iq2_mult	2002Sep17
PASS		
0	iq1_iq2_push_wrap	2002Sep17
PASS		
0	iq2_csq_full	2002Sep17
PASS		
0	IQ2_Mult	2002Sep17
PASS		
0	IQ2_Wrap_Legacy	2002Sep17
PASS		
0	IQ_Wrap	2002Sep17
PASS		
248	Isync_Control	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
249	Isync_Control_2D_3D	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
250	Isync_Control_3D_2D	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	ld_cnst_cntx_coherency_cntrl	2002Sep17
PASS		
0	ld_cnst_cntx_coherency_cntrl2	2002Sep17
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep17
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep17
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep17
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep17
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep17
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep17
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep17
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep17

PASS		
0	ld_cnst_cntx_match	2002Sep17
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep17
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep17
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep17
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep17
PASS		
0	ld_cnst_cntx_pfmatch_disable	2002Sep17
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep17
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep17
PASS		
0	ld_cnst_cntx_replicate8	2002Sep17
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep17
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep17
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep17
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep17
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep17
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep17
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep17
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep17
PASS		
0	load_constant_context_rt	2002Sep17
PASS		
0	load_palette	2002Sep17
PASS		
280	lp_2d3d	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	2002Sep17
PASS		
0	mem_write_rt	2002Sep17
PASS		
0	mpeg_index	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep17

PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep17
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep17
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep17
PASS		
0	nop_eating_disabled	2002Sep17
PASS		
300	paint_multi	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
301	paint_packet	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
302	paint_packet_gui_control	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
303	pitch_clip	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
304	ply_nextscan	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_cp_non_queued_rdwr_01	2002Sep17
PASS		
306	rbbm_cp_non_queued_rdwr_02	2002Sep17
FAIL	cp_rbbm.cmp	
307	rbbm_cp_queued_holds_nonqueued	2002Sep17
FAIL	cp_rbbm.cmp	
308	rbbm_cp_queued_rdwr	2002Sep17

```

**FAIL**      cp_rbbm.cmp
0  rbbm_isync_cpscratch_idlegui      2002Sep17
PASS
0  rbbm_isync_wait_idlegui          2002Sep17
PASS
0  rbbm_soft_reset                  2002Sep17
PASS
312 rbbm_WU_2D_idle_01              2002Sep17
**FAIL**
313 rbbm_WU_2D_idle_02              2002Sep17
**FAIL**
314 rbbm_WU_2D_idle_03              2002Sep17
**FAIL**
315 rbbm_WU_2D_idle_04              2002Sep17
**FAIL**
0  rbbm_WU_2D_idle_05              2002Sep17
PASS
317 rbbm_WU_2D_idle_06              2002Sep17
**FAIL**
318 rbbm_WU_2D_idle_07              2002Sep17
**FAIL**
319 rbbm_WU_2D_idle_08              2002Sep17
**FAIL**
320 rbbm_WU_2D_idle_09              2002Sep17
**FAIL**
321 rbbm_WU_2D_idle_clean_01        2002Sep17
**FAIL**
322 rbbm_WU_2D_idle_clean_02        2002Sep17
**FAIL**
323 rbbm_WU_2D_idle_clean_03        2002Sep17
**FAIL**
324 rbbm_WU_2D_idle_clean_04        2002Sep17
**FAIL**
0  rbbm_WU_2D_idle_clean_05        2002Sep17
PASS
326 rbbm_WU_2D_idle_clean_06        2002Sep17
**FAIL**
327 rbbm_WU_2D_idle_clean_07        2002Sep17
**FAIL**
328 rbbm_WU_2D_idle_clean_08        2002Sep17
**FAIL**
329 rbbm_WU_2D_idle_clean_09        2002Sep17
**FAIL**
330 rbbm_WU_2D_idle_clean_10        2002Sep17
**FAIL**
331 rbbm_WU_2D_idle_clean_11        2002Sep17
**FAIL**
0  rbbm_WU_2D_idle_clean_12        2002Sep17

```

PASS		
333	rbbm_WU_3D_idle_01	2002Sep17
FAIL		
334	rbbm_WU_3D_idle_02	2002Sep17
FAIL		
335	rbbm_WU_3D_idle_03	2002Sep17
FAIL		
336	rbbm_WU_3D_idle_04	2002Sep17
FAIL		
0	rbbm_WU_3D_idle_05	2002Sep17
PASS		
338	rbbm_WU_3D_idle_06	2002Sep17
FAIL		
339	rbbm_WU_3D_idle_07	2002Sep17
FAIL		
340	rbbm_WU_3D_idle_08	2002Sep17
FAIL		
341	rbbm_WU_3D_idle_09	2002Sep17
FAIL		
342	rbbm_WU_3D_idle_clean_01	2002Sep17
FAIL		
343	rbbm_WU_3D_idle_clean_02	2002Sep17
FAIL		
344	rbbm_WU_3D_idle_clean_03	2002Sep17
FAIL		
345	rbbm_WU_3D_idle_clean_04	2002Sep17
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Sep17
PASS		
347	rbbm_WU_3D_idle_clean_06	2002Sep17
FAIL		
348	rbbm_WU_3D_idle_clean_07	2002Sep17
FAIL		
349	rbbm_WU_3D_idle_clean_08	2002Sep17
FAIL		
350	rbbm_WU_3D_idle_clean_09	2002Sep17
FAIL		
351	rbbm_WU_3D_idle_clean_10	2002Sep17
FAIL		
352	rbbm_WU_3D_idle_clean_11	2002Sep17
FAIL		
0	rbbm_WU_3D_idle_clean_12	2002Sep17
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep17
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep17
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep17

PASS		
0	rbbm_WU_CP_dma_idle	2002Sep17
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep17
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep17
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep17
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep17
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep17
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep17
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep17
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep17
PASS		
0	rbbm_WU_idct_semaphore	2002Sep17
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep17
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep17
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep17
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep17
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep17
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep17
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep17
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep17

PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep17
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep17
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep17
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep17
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep17
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep17
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep17
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep17
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep17
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep17
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep17
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep17
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep17
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep17
PASS		
0	RB_BUFSZ_0	2002Sep17
PASS		
0	RB_BUFSZ_1	2002Sep17

PASS		
0	RB_Wrap	2002Sep17
PASS		
0	reg_to_mem	2002Sep17
PASS		
0	reg_to_mem_rt	2002Sep17
PASS		
408	rop_cfd_wmd	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
409	rstr_ordr	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rtee_arm_wait_idle_trigger_events_16	2002Sep17
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep17
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep17

PASS		
0	rtee_discretes_falling_17_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep17
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep17

PASS		
0	rtee_discretes_invert_09_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep17
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep17
PASS		
0	rtee_discretes_level_00_events_16	2002Sep17

PASS		
0	rtee_discretes_level_01_events_16	2002Sep17
PASS		
0	rtee_discretes_level_02_events_16	2002Sep17
PASS		
0	rtee_discretes_level_03_events_16	2002Sep17
PASS		
0	rtee_discretes_level_04_events_16	2002Sep17
PASS		
0	rtee_discretes_level_05_events_16	2002Sep17
PASS		
0	rtee_discretes_level_06_events_16	2002Sep17
PASS		
0	rtee_discretes_level_07_events_16	2002Sep17
PASS		
0	rtee_discretes_level_08_events_16	2002Sep17
PASS		
0	rtee_discretes_level_09_events_16	2002Sep17
PASS		
0	rtee_discretes_level_10_events_16	2002Sep17
PASS		
0	rtee_discretes_level_11_events_16	2002Sep17
PASS		
0	rtee_discretes_level_12_events_16	2002Sep17
PASS		
0	rtee_discretes_level_13_events_16	2002Sep17
PASS		
0	rtee_discretes_level_14_events_16	2002Sep17
PASS		
0	rtee_discretes_level_15_events_16	2002Sep17
PASS		
0	rtee_discretes_level_16_events_16	2002Sep17
PASS		
0	rtee_discretes_level_17_events_16	2002Sep17
PASS		
0	rtee_discretes_level_18_events_16	2002Sep17
PASS		
0	rtee_discretes_level_19_events_16	2002Sep17
PASS		
0	rtee_discretes_level_20_events_16	2002Sep17
PASS		
0	rtee_discretes_level_21_events_16	2002Sep17
PASS		
0	rtee_discretes_level_22_events_16	2002Sep17
PASS		
0	rtee_discretes_level_23_events_16	2002Sep17
PASS		
0	rtee_discretes_level_24_events_16	2002Sep17

PASS		
0	rtee_discretes_level_25_events_16	2002Sep17
PASS		
0	rtee_discretes_level_26_events_16	2002Sep17
PASS		
0	rtee_discretes_level_27_events_16	2002Sep17
PASS		
0	rtee_discretes_level_28_events_16	2002Sep17
PASS		
0	rtee_discretes_level_29_events_16	2002Sep17
PASS		
0	rtee_discretes_level_30_events_16	2002Sep17
PASS		
0	rtee_discretes_level_31_events_16	2002Sep17
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep17

PASS		
0	rtee_discretes_rising_16_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep17
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep17
PASS		
0	rtee_display1_comp_events_16	2002Sep17
PASS		
0	rtee_display2_comp_events_16	2002Sep17
PASS		
0	rtee_mem_linear_search_events_2	2002Sep17
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep17

PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep17
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep17
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep17
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep17
PASS		
0	rtee_prefetch_parse_nops	2002Sep17
PASS		
0	rtee_reg_linear_search_events_2	2002Sep17
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep17
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep17
PASS		
0	rtee_vip_comp_events_16	2002Sep17
PASS		
0	rt_invalid_opcode	2002Sep17
PASS		
0	scratch_mem_read	2002Sep17
PASS		
0	scratch_mem_read2	2002Sep17
PASS		
0	set_constant	2002Sep17
PASS		
0	set_constant2	2002Sep17

PASS		
0	set_constant_alu_max1	2002Sep17
PASS		
0	set_constant_alu_max2	2002Sep17
PASS		
0	set_constant_bool_max1	2002Sep17
PASS		
0	set_constant_bool_max2	2002Sep17
PASS		
0	set_constant_indr1_short	2002Sep17
PASS		
0	set_constant_indr1_short2	2002Sep17
PASS		
0	set_constant_indr1_write_enable	2002Sep17
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep17
PASS		
0	set_constant_indr2_short	2002Sep17
PASS		
0	set_constant_indr2_short2	2002Sep17
PASS		
0	set_constant_indr2_write_enable	2002Sep17
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep17
PASS		
0	set_constant_loop_max1	2002Sep17
PASS		
0	set_constant_loop_max2	2002Sep17
PASS		
0	set_constant_ring_short	2002Sep17
PASS		
0	set_constant_ring_short2	2002Sep17
PASS		
0	set_constant_ring_write_enable	2002Sep17
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep17
PASS		
0	set_constant_short	2002Sep17
PASS		
0	set_constant_short2	2002Sep17
PASS		
0	set_constant_tex_max1	2002Sep17
PASS		
0	set_constant_tex_max2	2002Sep17
PASS		
0	set_constant_write_enable	2002Sep17
PASS		
0	set_constant_write_enable_lcc	2002Sep17

PASS		
0	set_constant_write_enable_lcc2	2002Sep17
PASS		
598	set_scissors	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
599	set_state_rt_a	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
600	set_state_rt_b	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
601	set_state_rt_c	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
602	set_state_rt_d	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	set_st_indrl_match	2002Sep17
PASS		
0	set_st_indrl_mismatch	2002Sep17
PASS		
0	set_st_indrl_mismatch2	2002Sep17
PASS		
0	set_st_indrl_mismatch3	2002Sep17
PASS		
0	set_st_indrl_mismatch4	2002Sep17
PASS		
0	set_st_indrl_ps	2002Sep17
PASS		
0	set_st_indrl_ps_only	2002Sep17
PASS		
0	set_st_indrl_sublk0	2002Sep17
PASS		
0	set_st_indrl_sublk1	2002Sep17
PASS		
0	set_st_indrl_sublk1_only	2002Sep17
PASS		
0	set_st_indrl_sublk2	2002Sep17
PASS		
0	set_st_indrl_sublk2_only	2002Sep17
PASS		
0	set_st_indrl_sublk3	2002Sep17
PASS		
0	set_st_indrl_sublk3_only	2002Sep17
PASS		
0	set_st_indrl_sublk4	2002Sep17
PASS		
0	set_st_indrl_sublk4_only	2002Sep17
PASS		
0	set_st_indrl_sublk5	2002Sep17
PASS		
0	set_st_indrl_sublk5_only	2002Sep17

PASS		
0	set_st_indr1_sublk6	2002Sep17
PASS		
0	set_st_indr1_sublk6_only	2002Sep17
PASS		
0	set_st_indr1_sublk7	2002Sep17
PASS		
0	set_st_indr1_sublk7_only	2002Sep17
PASS		
0	set_st_indr1_vs	2002Sep17
PASS		
0	set_st_indr1_vs_only	2002Sep17
PASS		
0	set_st_indr2_match	2002Sep17
PASS		
0	set_st_indr2_mismatch	2002Sep17
PASS		
0	set_st_indr2_mismatch2	2002Sep17
PASS		
0	set_st_indr2_mismatch3	2002Sep17
PASS		
0	set_st_indr2_mismatch4	2002Sep17
PASS		
0	set_st_indr2_ps	2002Sep17
PASS		
0	set_st_indr2_ps_only	2002Sep17
PASS		
0	set_st_indr2_sublk0	2002Sep17
PASS		
0	set_st_indr2_sublk1	2002Sep17
PASS		
0	set_st_indr2_sublk1_only	2002Sep17
PASS		
0	set_st_indr2_sublk2	2002Sep17
PASS		
0	set_st_indr2_sublk2_only	2002Sep17
PASS		
0	set_st_indr2_sublk3	2002Sep17
PASS		
0	set_st_indr2_sublk3_only	2002Sep17
PASS		
0	set_st_indr2_sublk4	2002Sep17
PASS		
0	set_st_indr2_sublk4_only	2002Sep17
PASS		
0	set_st_indr2_sublk5	2002Sep17
PASS		
0	set_st_indr2_sublk5_only	2002Sep17

PASS		
0	set_st_indr2_sublk6	2002Sep17
PASS		
0	set_st_indr2_sublk6_only	2002Sep17
PASS		
0	set_st_indr2_sublk7	2002Sep17
PASS		
0	set_st_indr2_sublk7_only	2002Sep17
PASS		
0	set_st_indr2_vs	2002Sep17
PASS		
0	set_st_indr2_vs_only	2002Sep17
PASS		
0	set_st_pfmatch_disabled	2002Sep17
PASS		
0	set_st_ring_match	2002Sep17
PASS		
0	set_st_ring_mismatch	2002Sep17
PASS		
0	set_st_ring_mismatch2	2002Sep17
PASS		
0	set_st_ring_mismatch3	2002Sep17
PASS		
0	set_st_ring_mismatch4	2002Sep17
PASS		
0	set_st_ring_ps	2002Sep17
PASS		
0	set_st_ring_ps_only	2002Sep17
PASS		
0	set_st_ring_sublk0	2002Sep17
PASS		
0	set_st_ring_sublk1	2002Sep17
PASS		
0	set_st_ring_sublk1_only	2002Sep17
PASS		
0	set_st_ring_sublk2	2002Sep17
PASS		
0	set_st_ring_sublk2_only	2002Sep17
PASS		
0	set_st_ring_sublk3	2002Sep17
PASS		
0	set_st_ring_sublk3_only	2002Sep17
PASS		
0	set_st_ring_sublk4	2002Sep17
PASS		
0	set_st_ring_sublk4_only	2002Sep17
PASS		
0	set_st_ring_sublk5	2002Sep17

PASS		
0	set_st_ring_sublk5_only	2002Sep17
PASS		
0	set_st_ring_sublk6	2002Sep17
PASS		
0	set_st_ring_sublk6_only	2002Sep17
PASS		
0	set_st_ring_sublk7	2002Sep17
PASS		
0	set_st_ring_sublk7_only	2002Sep17
PASS		
0	set_st_ring_vs	2002Sep17
PASS		
0	set_st_ring_vs_only	2002Sep17
PASS		
0	set_st_stress_a	2002Sep17
PASS		
0	set_st_stress_b	2002Sep17
PASS		
0	set_st_stress_c	2002Sep17
PASS		
0	set_st_stress_d	2002Sep17
PASS		
0	set_st_stress_e	2002Sep17
PASS		
0	set_st_stress_f	2002Sep17
PASS		
682	Small_Text_BM	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep17
PASS		
684	src_clip	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
685	srf_cohr	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
686	trans_bitblt	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	type0_2long_pckts	2002Sep17
PASS		
0	type0_3indr_buffs	2002Sep17
PASS		
0	type0_4pckts	2002Sep17
PASS		
0	type0_incr_updt_cnst	2002Sep17
PASS		
0	type0_incr_updt_instrmem	2002Sep17
PASS		
0	type0_incr_updt_st	2002Sep17

PASS		
0	type0_incr_updt_stress	2002Sep17
PASS		
0	type0_ind1_ind2_a	2002Sep17
PASS		
0	type0_ind1_ind2_b	2002Sep17
PASS		
0	type0_indr_buff	2002Sep17
PASS		
0	Type0_IQ2_BM	2002Sep17
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep17
PASS		
0	Type0_IQ_BM	2002Sep17
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep17
PASS		
0	Type0_PQ_BM	2002Sep17
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep17
PASS		
0	type1_4pckts	2002Sep17
PASS		
0	type1_indr_buff	2002Sep17
PASS		
0	type1_pckt	2002Sep17
PASS		
706	Type3_Bitblt	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	Type3_Load_Palette	2002Sep17
PASS		
0	Type3_NOP	2002Sep17
PASS		
709	Type3_Polyline	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
710	Type3_Polyline_Large	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
711	type_random	2002Sep17
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep17
PASS		
0	uram_wrt_ucode	2002Sep17
PASS		
714	viz_query_a	2002Sep17
FAIL	rbbmWr.cmp	
715	viz_query_draw_indx	2002Sep17
FAIL	rbbmWr.cmp	
716	viz_query_draw_indx_indr1	2002Sep17

```

**FAIL**      rbbmWr.cmp
717 viz_query_draw_indx_indr2      2002Sep17
**FAIL**      rbbmWr.cmp
718 viz_query_draw_indx_multipass  2002Sep17
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
719 viz_query_draw_indx_ring      2002Sep17
**FAIL**      rbbmWr.cmp
720 viz_query_indr1                2002Sep17
**FAIL**      rbbmWr.cmp
0 viz_query_indr1_simple           2002Sep17
PASS
722 viz_query_indr2                2002Sep17
**FAIL**      rbbmWr.cmp
0 viz_query_indr2_simple           2002Sep17
PASS
724 viz_query_ring                 2002Sep17
**FAIL**      rbbmWr.cmp
0 viz_query_ring_simple           2002Sep17
PASS
0 wait_for_idle                    2002Sep17
PASS
0 wait_reg_mem_poll_mem0           2002Sep17
PASS
0 wait_reg_mem_poll_mem0_rt        2002Sep17
PASS
0 wait_reg_mem_poll_mem1           2002Sep17
PASS
0 wait_reg_mem_poll_mem2           2002Sep17
PASS
0 wait_reg_mem_poll_mem3           2002Sep17
PASS
0 wait_reg_mem_poll_reg0           2002Sep17
PASS
0 wait_reg_mem_poll_reg1           2002Sep17
PASS
0 wait_reg_mem_poll_reg2           2002Sep17
PASS
0 wait_reg_mem_poll_reg3           2002Sep17
PASS

```

```

=====
Tests_Run_count = 735 Pass_count = 634 Fail_count = 101 Incomplete_count = 0
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buffer_a		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
2	2d_indr_buffer_b		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
3	2d_indr_buffer_c		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
4	2d_indr_buffer_d		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
5	2d_indr_buffer_e		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
6	2d_indr_buffer_f		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
7	2d_indr_buffer_g		2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp		
8	2d_indr_buffer_random		2002Sep19
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep19
	PASS		
0	3d_draw_indx_2_b		2002Sep19
	PASS		
0	IQ2_Mult		2002Sep19
	PASS		
0	IQ2_Wrap_Legacy		2002Sep19
	PASS		
0	IQ_Wrap		2002Sep19
	PASS		
0	Isync_Control		2002Sep19
	PASS		
0	Isync_Control_2D_3D		2002Sep19
	PASS		
0	Isync_Control_3D_2D		2002Sep19
	PASS		
0	RB_BUFSZ_0		2002Sep19
	PASS		
0	RB_BUFSZ_1		2002Sep19
	PASS		
0	RB_Wrap		2002Sep19
	PASS		
0	Small_Text_BM		2002Sep19

PASS		
0	Type0_IQ2_BM	2002Sep19
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep19
PASS		
0	Type0_IQ_BM	2002Sep19
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep19
PASS		
0	Type0_PQ_BM	2002Sep19
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep19
PASS		
0	Type3_Bitblt	2002Sep19
PASS		
0	Type3_Load_Palette	2002Sep19
PASS		
0	Type3_NOP	2002Sep19
PASS		
0	Type3_Polyline	2002Sep19
PASS		
0	Type3_Polyline_Large	2002Sep19
PASS		
0	bitblt_multi	2002Sep19
PASS		
0	brush_32x1mono	2002Sep19
PASS		
0	brush_8x8color_16	2002Sep19
PASS		
0	brush_8x8color_32	2002Sep19
PASS		
0	brush_8x8color_64	2002Sep19
PASS		
0	brush_8x8color_dst_0_1	2002Sep19
PASS		
0	brush_8x8mono	2002Sep19
PASS		
0	brush_color_mono_stress	2002Sep19
PASS		
0	brush_flag	2002Sep19
PASS		
41	brush_type0	2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp	
42	brush_type1	2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp	
43	brush_type10	2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp	
44	brush_type13	2002Sep19

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
45  brush_type14                                2002Sep19
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
46  brush_type6                                  2002Sep19
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
47  brush_type7                                  2002Sep19
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
48  brush_type_other                             2002Sep19
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   brush_types_13_14                           2002Sep19
PASS
0   clear_const_wr_en_on_me_init                 2002Sep19
PASS
0   clear_const_wr_en_with_lcc                  2002Sep19
PASS
0   cond_write_mem_poll_mem                     2002Sep19
PASS
0   cond_write_mem_poll_mem2                    2002Sep19
PASS
0   cond_write_mem_poll_mem3                    2002Sep19
PASS
0   cond_write_mem_poll_mem_rt                  2002Sep19
PASS
0   cond_write_mem_poll_reg                     2002Sep19
PASS
0   cond_write_mem_poll_reg2                    2002Sep19
PASS
0   cond_write_mem_poll_reg3                    2002Sep19
PASS
0   cp_acpi_recovery_soft_reset                 2002Sep19
PASS
0   cp_bios_scratch                             2002Sep19
PASS
0   cp_ib2_base000_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base001_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base010_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base011_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base100_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base101_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base110_bufszXXX                     2002Sep19
PASS
0   cp_ib2_base111_bufszXXX                     2002Sep19

```

PASS		
0	cp_ib_base000_bufszXXX	2002Sep19
PASS		
0	cp_ib_base001_bufszXXX	2002Sep19
PASS		
0	cp_ib_base010_bufszXXX	2002Sep19
PASS		
0	cp_ib_base011_bufszXXX	2002Sep19
PASS		
0	cp_ib_base100_bufszXXX	2002Sep19
PASS		
0	cp_ib_base101_bufszXXX	2002Sep19
PASS		
0	cp_ib_base110_bufszXXX	2002Sep19
PASS		
0	cp_ib_base111_bufszXXX	2002Sep19
PASS		
0	cp_interrupt	2002Sep19
PASS		
0	cp_interrupt_rt_events_16	2002Sep19
PASS		
0	cp_iq2_alignment_000	2002Sep19
PASS		
0	cp_iq2_alignment_001	2002Sep19
PASS		
0	cp_iq2_alignment_010	2002Sep19
PASS		
0	cp_iq2_alignment_011	2002Sep19
PASS		
0	cp_iq2_alignment_100	2002Sep19
PASS		
0	cp_iq2_alignment_101	2002Sep19
PASS		
0	cp_iq2_alignment_110	2002Sep19
PASS		
0	cp_iq2_alignment_111	2002Sep19
PASS		
0	cp_iq_alignment_000	2002Sep19
PASS		
0	cp_iq_alignment_001	2002Sep19
PASS		
0	cp_iq_alignment_010	2002Sep19
PASS		
0	cp_iq_alignment_011	2002Sep19
PASS		
0	cp_iq_alignment_100	2002Sep19
PASS		
0	cp_iq_alignment_101	2002Sep19

PASS		
0	cp_iq_alignment_110	2002Sep19
PASS		
0	cp_iq_alignment_111	2002Sep19
PASS		
0	cp_max_size_packet	2002Sep19
PASS		
96	cp_max_size_packet_random	2002Sep19
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep19
PASS		
0	cp_output_stress	2002Sep19
PASS		
0	cp_output_stress_random	2002Sep19
PASS		
0	cp_protection_mode	2002Sep19
PASS		
0	cp_rb_alignment_000	2002Sep19
PASS		
0	cp_rb_alignment_001	2002Sep19
PASS		
0	cp_rb_alignment_010	2002Sep19
PASS		
0	cp_rb_alignment_011	2002Sep19
PASS		
0	cp_rb_alignment_100	2002Sep19
PASS		
0	cp_rb_alignment_101	2002Sep19
PASS		
0	cp_rb_alignment_110	2002Sep19
PASS		
0	cp_rb_alignment_111	2002Sep19
PASS		
0	cp_rb_wptrXXX_base000	2002Sep19
PASS		
0	cp_rb_wptrXXX_base001	2002Sep19
PASS		
0	cp_rb_wptrXXX_base010	2002Sep19
PASS		
0	cp_rb_wptrXXX_base011	2002Sep19
PASS		
0	cp_rb_wptrXXX_base100	2002Sep19
PASS		
0	cp_rb_wptrXXX_base101	2002Sep19
PASS		
0	cp_rb_wptrXXX_base110	2002Sep19
PASS		
0	cp_rb_wptrXXX_base111	2002Sep19

PASS		
117	cp_reg_rw_test	2002Sep19
FAIL		
0	cp_register_reads	2002Sep19
PASS		
0	cp_rt_protection_mode	2002Sep19
PASS		
0	cp_scratch	2002Sep19
PASS		
0	cp_scratch_multi	2002Sep19
PASS		
0	dma_cp_sync	2002Sep19
PASS		
0	dma_dscrstr_chckn_bit	2002Sep19
PASS		
0	dma_dscrstr_flush1	2002Sep19
PASS		
0	dma_dscrstr_flush2	2002Sep19
PASS		
0	dma_dscrstr_flush3	2002Sep19
PASS		
0	dma_m2m_src00_dstXX	2002Sep19
PASS		
0	dma_m2m_src01_dstXX	2002Sep19
PASS		
0	dma_m2m_src10_dstXX	2002Sep19
PASS		
0	dma_m2m_src11_dstXX	2002Sep19
PASS		
0	dma_mem2mem	2002Sep19
PASS		
0	dma_mem2mem_abort	2002Sep19
PASS		
0	dma_mem2mem_alignments	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep19
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep19

PASS		
0	dma_mem2mem_byte_alignment7	2002Sep19
PASS		
0	dma_mem2mem_pio_abort	2002Sep19
PASS		
0	dma_mem2mem_random	2002Sep19
PASS		
0	dma_mem2reg	2002Sep19
PASS		
0	dma_mem2reg_abort	2002Sep19
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep19
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep19
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep19
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep19
PASS		
0	dma_mem2reg_sync	2002Sep19
PASS		
0	dma_r2r_m2m_r2r	2002Sep19
PASS		
0	dma_r2r_m2r_r2r	2002Sep19
PASS		
0	dma_r2r_r2m_r2r	2002Sep19
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep19
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep19
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep19
PASS		
0	dma_reg2mem	2002Sep19
PASS		
0	dma_reg2mem_abort	2002Sep19
PASS		
0	dma_reg2reg	2002Sep19
PASS		
0	dma_reg2reg_2dw	2002Sep19
PASS		
0	dma_reg2reg_daic	2002Sep19
PASS		
0	dma_reg2reg_mult	2002Sep19
PASS		
0	dma_reg2reg_mult2	2002Sep19
PASS		
0	dma_reg2reg_mult3	2002Sep19

PASS		
0	dma_reg2reg_saic	2002Sep19
PASS		
0	dma_reg2reg_sdaic	2002Sep19
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep19
PASS		
0	dma_reg2reg_stress	2002Sep19
PASS		
0	draw_indx_a	2002Sep19
PASS		
0	draw_indx_b	2002Sep19
PASS		
0	draw_indx_c	2002Sep19
PASS		
0	draw_indx_indr1_a	2002Sep19
PASS		
0	draw_indx_indr1_b	2002Sep19
PASS		
0	draw_indx_indr1_short_a	2002Sep19
PASS		
0	draw_indx_indr1_short_b	2002Sep19
PASS		
0	draw_indx_indr1_stress	2002Sep19
PASS		
0	draw_indx_indr2_a	2002Sep19
PASS		
0	draw_indx_indr2_b	2002Sep19
PASS		
0	draw_indx_indr2_short_a	2002Sep19
PASS		
0	draw_indx_indr2_short_b	2002Sep19
PASS		
0	draw_indx_indr2_stress	2002Sep19
PASS		
0	draw_indx_ring1	2002Sep19
PASS		
0	draw_indx_ring2	2002Sep19
PASS		
0	draw_indx_ring_short1	2002Sep19
PASS		
0	draw_indx_ring_short2	2002Sep19
PASS		
0	draw_indx_ring_stress	2002Sep19
PASS		
0	draw_indx_short_a	2002Sep19
PASS		
0	draw_indx_short_b	2002Sep19

PASS		
0	draw_indx_short_c	2002Sep19
PASS		
0	draw_indx_stress_a	2002Sep19
PASS		
0	draw_indx_stress_b	2002Sep19
PASS		
0	draw_indx_stress_c	2002Sep19
PASS		
0	event_timestamp_write	2002Sep19
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep19
PASS		
0	ft_indr1_july22	2002Sep19
PASS		
0	ft_indr2_july22	2002Sep19
PASS		
0	ft_july22	2002Sep19
PASS		
198	ft_july22_random	2002Sep19
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	header_dump	2002Sep19
PASS		
0	header_dump_rt	2002Sep19
PASS		
0	header_dump_small	2002Sep19
PASS		
0	hostdata_blt	2002Sep19
PASS		
0	im_load_a	2002Sep19
PASS		
0	im_load_b	2002Sep19
PASS		
0	im_load_cid_mismatch	2002Sep19
PASS		
0	im_load_immediate	2002Sep19
PASS		
0	im_load_immediate_nrt_me	2002Sep19
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep19
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep19
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep19
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep19
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep19

PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep19
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep19
PASS		
0	im_load_indr1_a	2002Sep19
PASS		
0	im_load_indr1_b	2002Sep19
PASS		
0	im_load_indr2_a	2002Sep19
PASS		
0	im_load_indr2_b	2002Sep19
PASS		
0	im_load_invalidate	2002Sep19
PASS		
0	im_load_match	2002Sep19
PASS		
0	im_load_match2	2002Sep19
PASS		
0	im_load_nrt_me	2002Sep19
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep19
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep19
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep19
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep19
PASS		
0	im_load_pfmacth_disable	2002Sep19
PASS		
0	im_load_ring_a	2002Sep19
PASS		
0	im_load_ring_b	2002Sep19
PASS		
0	im_load_rt	2002Sep19
PASS		
0	im_load_rt2	2002Sep19
PASS		
0	im_load_rt3	2002Sep19
PASS		
0	im_load_rt4	2002Sep19
PASS		
0	im_load_sz_mismatch	2002Sep19
PASS		
0	incremental_update_const	2002Sep19
PASS		
236	incremental_update_instr	2002Sep19

```

**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_state      2002Sep19
PASS
0  indirect_buffer_a             2002Sep19
PASS
0  indirect_buffer_b             2002Sep19
PASS
0  indirect_buffer_c             2002Sep19
PASS
0  indirect_buffer_d             2002Sep19
PASS
0  indirect_buffer_e             2002Sep19
PASS
0  indirect_buffer_f             2002Sep19
PASS
0  indirect_buffer_g             2002Sep19
PASS
0  indirect_buffer_h             2002Sep19
PASS
0  indirect_buffer_i             2002Sep19
PASS
0  indirect_buffer_j             2002Sep19
PASS
0  indirect_buffer_k             2002Sep19
PASS
0  indirect_buffer_l             2002Sep19
PASS
0  indirect_buffer_mp_a          2002Sep19
PASS
0  indirect_buffer_mp_b          2002Sep19
PASS
0  indirect_buffer_mp_c          2002Sep19
PASS
0  indirect_buffer_mp_d          2002Sep19
PASS
0  indirect_buffer_mp_e          2002Sep19
PASS
0  indirect_buffer_mp_f          2002Sep19
PASS
0  invalid_opcode                2002Sep19
PASS
0  invalidate_on_me_init         2002Sep19
PASS
0  invalidate_state_constants    2002Sep19
PASS
0  invalidate_state_shaders      2002Sep19
PASS
0  invalidate_state_shaders2     2002Sep19

```

PASS		
0	invalidate_state_sublks	2002Sep19
PASS		
0	invalidate_state_sublks2	2002Sep19
PASS		
0	iq1_iq2_mult	2002Sep19
PASS		
0	iq1_iq2_push_wrap	2002Sep19
PASS		
0	iq2_csq_full	2002Sep19
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep19
PASS		
267	ld_cnst_cntx_coherency_cntrl2	2002Sep19
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Sep19
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep19
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep19
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep19
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep19
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep19
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep19
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep19
PASS		
0	ld_cnst_cntx_match	2002Sep19
PASS		
277	ld_cnst_cntx_match_valid_preserved	2002Sep19
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_mismatch_alutex	2002Sep19
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep19
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep19
PASS		
0	ld_cnst_cntx_pfmatch_disable	2002Sep19
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep19
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep19
PASS		
0	ld_cnst_cntx_replicate8	2002Sep19

PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep19
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep19
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep19
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep19
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep19
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep19
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep19
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep19
PASS		
0	load_constant_context_rt	2002Sep19
PASS		
0	load_palette	2002Sep19
PASS		
0	lp_2d3d	2002Sep19
PASS		
0	mem_write	2002Sep19
PASS		
0	mem_write_rt	2002Sep19
PASS		
0	mpeg_index	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep19
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep19
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep19
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep19

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep19
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep19
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep19
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep19
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep19
PASS		
0	nop_eating_disabled	2002Sep19
PASS		
0	paint_multi	2002Sep19
PASS		
0	paint_packet	2002Sep19
PASS		
0	paint_packet_gui_control	2002Sep19
PASS		
0	pitch_clip	2002Sep19
PASS		
0	ply_nextscan	2002Sep19
PASS		
320	rbbm_WU_2D_idle_01	2002Sep19
FAIL		
321	rbbm_WU_2D_idle_02	2002Sep19
FAIL		
322	rbbm_WU_2D_idle_03	2002Sep19
FAIL		
323	rbbm_WU_2D_idle_04	2002Sep19
FAIL		
0	rbbm_WU_2D_idle_05	2002Sep19
PASS		
325	rbbm_WU_2D_idle_06	2002Sep19
FAIL		
326	rbbm_WU_2D_idle_07	2002Sep19
FAIL		
327	rbbm_WU_2D_idle_08	2002Sep19
FAIL		
328	rbbm_WU_2D_idle_09	2002Sep19
FAIL		
329	rbbm_WU_2D_idle_clean_01	2002Sep19
FAIL		
330	rbbm_WU_2D_idle_clean_02	2002Sep19
FAIL		
331	rbbm_WU_2D_idle_clean_03	2002Sep19
FAIL		
332	rbbm_WU_2D_idle_clean_04	2002Sep19

FAIL		
0	rbbm_WU_2D_idle_clean_05	2002Sep19
	PASS	
334	rbbm_WU_2D_idle_clean_06	2002Sep19
FAIL		
335	rbbm_WU_2D_idle_clean_07	2002Sep19
FAIL		
336	rbbm_WU_2D_idle_clean_08	2002Sep19
FAIL		
337	rbbm_WU_2D_idle_clean_09	2002Sep19
FAIL		
338	rbbm_WU_2D_idle_clean_10	2002Sep19
FAIL		
339	rbbm_WU_2D_idle_clean_11	2002Sep19
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Sep19
	PASS	
341	rbbm_WU_3D_idle_01	2002Sep19
FAIL		
342	rbbm_WU_3D_idle_02	2002Sep19
FAIL		
343	rbbm_WU_3D_idle_03	2002Sep19
FAIL		
344	rbbm_WU_3D_idle_04	2002Sep19
FAIL		
0	rbbm_WU_3D_idle_05	2002Sep19
	PASS	
346	rbbm_WU_3D_idle_06	2002Sep19
FAIL		
347	rbbm_WU_3D_idle_07	2002Sep19
FAIL		
348	rbbm_WU_3D_idle_08	2002Sep19
FAIL		
349	rbbm_WU_3D_idle_09	2002Sep19
FAIL		
350	rbbm_WU_3D_idle_clean_01	2002Sep19
FAIL		
351	rbbm_WU_3D_idle_clean_02	2002Sep19
FAIL		
352	rbbm_WU_3D_idle_clean_03	2002Sep19
FAIL		
353	rbbm_WU_3D_idle_clean_04	2002Sep19
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Sep19
	PASS	
355	rbbm_WU_3D_idle_clean_06	2002Sep19
FAIL		
356	rbbm_WU_3D_idle_clean_07	2002Sep19

```

**FAIL**
357  rbbm_WU_3D_idle_clean_08                2002Sep19
**FAIL**
358  rbbm_WU_3D_idle_clean_09                2002Sep19
**FAIL**
359  rbbm_WU_3D_idle_clean_10                2002Sep19
**FAIL**
360  rbbm_WU_3D_idle_clean_11                2002Sep19
**FAIL**
0    rbbm_WU_3D_idle_clean_12                2002Sep19
PASS
0    rbbm_WU_BIF_RBBM_agp_flush_off          2002Sep19
PASS
0    rbbm_WU_BOTH_GRP update_pending_off     2002Sep19
PASS
0    rbbm_WU_CP_dma_idle                     2002Sep19
PASS
0    rbbm_WU_CRTC_D1MODE_vline_fe           2002Sep19
PASS
0    rbbm_WU_CRTC_D1MODE_vline_on           2002Sep19
PASS
0    rbbm_WU_CRTC_D1MODE_vline_re           2002Sep19
PASS
0    rbbm_WU_CRTC_D2MODE_vline_fe           2002Sep19
PASS
0    rbbm_WU_CRTC_D2MODE_vline_on           2002Sep19
PASS
0    rbbm_WU_CRTC_D2MODE_vline_re           2002Sep19
PASS
0    rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep19
PASS
0    rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep19
PASS
0    rbbm_WU_MH_hdp_clean                   2002Sep19
PASS
0    rbbm_WU_OV_FLIP_D1OVL_update_pending_off 2002Sep19
PASS
0    rbbm_WU_OV_FLIP_D2OVL_update_pending_off 2002Sep19
PASS
0    rbbm_WU_VIPH0_dma_idle                 2002Sep19
PASS
0    rbbm_WU_VIPH1_dma_idle                 2002Sep19
PASS
0    rbbm_WU_VIPH2_dma_idle                 2002Sep19
PASS
0    rbbm_WU_VIPH3_dma_idle                 2002Sep19
PASS
0    rbbm_WU_cmdfifo_entries                2002Sep19

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PASS		
0	rbbm_WU_idct_semaphore	2002Sep19
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep19
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep19
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep19
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRP update_pending_off	2002Sep19
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP update_pending_off	2002Sep19
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep19
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep19
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep19
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep19

PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep19
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep19
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep19
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep19
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep19
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep19
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep19
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep19
PASS		
413	rbbm_cp_queued_holds_nonqueued	2002Sep19
FAIL	cp_rbbm.cmp	
414	rbbm_cp_queued_rdwr	2002Sep19
FAIL	cp_rbbm.cmp	
0	rbbm_isync_cpscratch_idlegui	2002Sep19
PASS		
0	rbbm_isync_wait_idlegui	2002Sep19
PASS		
0	rbbm_soft_reset	2002Sep19
PASS		
0	reg_to_mem	2002Sep19
PASS		
0	reg_to_mem_rt	2002Sep19
PASS		
0	rop_cfd_wmd	2002Sep19
PASS		
0	rstr_ordr	2002Sep19
PASS		
0	rt_invalid_opcode	2002Sep19
PASS		
423	rtee_arm_wait_idle_trigger_events_16	2002Sep19
FAIL		
0	rtee_discretes_arm_rearm_events_32	2002Sep19
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep19

PASS		
0	rtee_discretes_falling_04_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep19

PASS		
0	rtee_discretes_falling_28_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep19
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep19

PASS		
0	rtee_discretes_invert_20_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep19
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep19
PASS		
0	rtee_discretes_level_00_events_16	2002Sep19
PASS		
0	rtee_discretes_level_01_events_16	2002Sep19
PASS		
0	rtee_discretes_level_02_events_16	2002Sep19
PASS		
0	rtee_discretes_level_03_events_16	2002Sep19
PASS		
0	rtee_discretes_level_04_events_16	2002Sep19
PASS		
0	rtee_discretes_level_05_events_16	2002Sep19
PASS		
0	rtee_discretes_level_06_events_16	2002Sep19
PASS		
0	rtee_discretes_level_07_events_16	2002Sep19
PASS		
0	rtee_discretes_level_08_events_16	2002Sep19
PASS		
0	rtee_discretes_level_09_events_16	2002Sep19
PASS		
0	rtee_discretes_level_10_events_16	2002Sep19
PASS		
0	rtee_discretes_level_11_events_16	2002Sep19

PASS		
0	rtee_discretes_level_12_events_16	2002Sep19
PASS		
0	rtee_discretes_level_13_events_16	2002Sep19
PASS		
0	rtee_discretes_level_14_events_16	2002Sep19
PASS		
0	rtee_discretes_level_15_events_16	2002Sep19
PASS		
0	rtee_discretes_level_16_events_16	2002Sep19
PASS		
0	rtee_discretes_level_17_events_16	2002Sep19
PASS		
0	rtee_discretes_level_18_events_16	2002Sep19
PASS		
0	rtee_discretes_level_19_events_16	2002Sep19
PASS		
0	rtee_discretes_level_20_events_16	2002Sep19
PASS		
0	rtee_discretes_level_21_events_16	2002Sep19
PASS		
0	rtee_discretes_level_22_events_16	2002Sep19
PASS		
0	rtee_discretes_level_23_events_16	2002Sep19
PASS		
0	rtee_discretes_level_24_events_16	2002Sep19
PASS		
0	rtee_discretes_level_25_events_16	2002Sep19
PASS		
0	rtee_discretes_level_26_events_16	2002Sep19
PASS		
0	rtee_discretes_level_27_events_16	2002Sep19
PASS		
0	rtee_discretes_level_28_events_16	2002Sep19
PASS		
0	rtee_discretes_level_29_events_16	2002Sep19
PASS		
0	rtee_discretes_level_30_events_16	2002Sep19
PASS		
0	rtee_discretes_level_31_events_16	2002Sep19
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep19

PASS		
0	rtee_discretes_rising_03_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep19

PASS		
0	rtee_discretes_rising_27_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep19
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep19
PASS		
0	rtee_display1_comp_events_16	2002Sep19
PASS		
0	rtee_display2_comp_events_16	2002Sep19
PASS		
0	rtee_mem_linear_search_events_2	2002Sep19
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep19
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep19
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep19
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep19
PASS		
0	rtee_prefetch_parse_nops	2002Sep19
PASS		
0	rtee_reg_linear_search_events_2	2002Sep19
PASS		
571	rtee_reg_poll_comp_000_events_16	2002Sep19
FAIL		
572	rtee_reg_poll_comp_001_events_16	2002Sep19

```

**FAIL**
573 rtee_reg_poll_comp_010_events_16          2002Sep19
**FAIL**
574 rtee_reg_poll_comp_011_events_16          2002Sep19
**FAIL**
575 rtee_reg_poll_comp_100_events_16          2002Sep19
**FAIL**
576 rtee_reg_poll_comp_101_events_16          2002Sep19
**FAIL**
577 rtee_reg_poll_comp_110_events_16          2002Sep19
**FAIL**
578 rtee_reg_poll_comp_111_events_16          2002Sep19
**FAIL**
579 rtee_reg_poll_comp_queued_nonqueued_events_16 2002Sep19
**FAIL**
0      rtee_vip_comp_events_16                 2002Sep19
PASS
0      scratch_mem_read                       2002Sep19
PASS
0      scratch_mem_read2                     2002Sep19
PASS
0      set_constant                           2002Sep19
PASS
0      set_constant2                          2002Sep19
PASS
0      set_constant_alu_max1                  2002Sep19
PASS
0      set_constant_alu_max2                  2002Sep19
PASS
0      set_constant_bool_max1                 2002Sep19
PASS
0      set_constant_bool_max2                 2002Sep19
PASS
0      set_constant_indr1_short                2002Sep19
PASS
0      set_constant_indr1_short2              2002Sep19
PASS
0      set_constant_indr1_write_enable         2002Sep19
PASS
0      set_constant_indr1_write_enable_lcc     2002Sep19
PASS
0      set_constant_indr2_short                2002Sep19
PASS
0      set_constant_indr2_short2              2002Sep19
PASS
0      set_constant_indr2_write_enable         2002Sep19
PASS
0      set_constant_indr2_write_enable_lcc     2002Sep19

```

PASS		
0	set_constant_loop_max1	2002Sep19
PASS		
0	set_constant_loop_max2	2002Sep19
PASS		
0	set_constant_ring_short	2002Sep19
PASS		
0	set_constant_ring_short2	2002Sep19
PASS		
0	set_constant_ring_write_enable	2002Sep19
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep19
PASS		
0	set_constant_short	2002Sep19
PASS		
0	set_constant_short2	2002Sep19
PASS		
0	set_constant_tex_max1	2002Sep19
PASS		
0	set_constant_tex_max2	2002Sep19
PASS		
0	set_constant_write_enable	2002Sep19
PASS		
0	set_constant_write_enable_lcc	2002Sep19
PASS		
0	set_constant_write_enable_lcc2	2002Sep19
PASS		
0	set_scissors	2002Sep19
PASS		
0	set_st_indrl_match	2002Sep19
PASS		
0	set_st_indrl_mismatch	2002Sep19
PASS		
0	set_st_indrl_mismatch2	2002Sep19
PASS		
0	set_st_indrl_mismatch3	2002Sep19
PASS		
0	set_st_indrl_mismatch4	2002Sep19
PASS		
0	set_st_indrl_ps	2002Sep19
PASS		
0	set_st_indrl_ps_only	2002Sep19
PASS		
0	set_st_indrl_sublk0	2002Sep19
PASS		
0	set_st_indrl_sublk1	2002Sep19
PASS		
0	set_st_indrl_sublk1_only	2002Sep19

PASS		
0	set_st_indr1_sublk2	2002Sep19
PASS		
0	set_st_indr1_sublk2_only	2002Sep19
PASS		
0	set_st_indr1_sublk3	2002Sep19
PASS		
0	set_st_indr1_sublk3_only	2002Sep19
PASS		
0	set_st_indr1_sublk4	2002Sep19
PASS		
0	set_st_indr1_sublk4_only	2002Sep19
PASS		
0	set_st_indr1_sublk5	2002Sep19
PASS		
0	set_st_indr1_sublk5_only	2002Sep19
PASS		
0	set_st_indr1_sublk6	2002Sep19
PASS		
0	set_st_indr1_sublk6_only	2002Sep19
PASS		
0	set_st_indr1_sublk7	2002Sep19
PASS		
0	set_st_indr1_sublk7_only	2002Sep19
PASS		
0	set_st_indr1_vs	2002Sep19
PASS		
0	set_st_indr1_vs_only	2002Sep19
PASS		
0	set_st_indr2_match	2002Sep19
PASS		
0	set_st_indr2_mismatch	2002Sep19
PASS		
0	set_st_indr2_mismatch2	2002Sep19
PASS		
0	set_st_indr2_mismatch3	2002Sep19
PASS		
0	set_st_indr2_mismatch4	2002Sep19
PASS		
0	set_st_indr2_ps	2002Sep19
PASS		
0	set_st_indr2_ps_only	2002Sep19
PASS		
0	set_st_indr2_sublk0	2002Sep19
PASS		
0	set_st_indr2_sublk1	2002Sep19
PASS		
0	set_st_indr2_sublk1_only	2002Sep19

PASS		
0	set_st_indr2_sublk2	2002Sep19
PASS		
0	set_st_indr2_sublk2_only	2002Sep19
PASS		
0	set_st_indr2_sublk3	2002Sep19
PASS		
0	set_st_indr2_sublk3_only	2002Sep19
PASS		
0	set_st_indr2_sublk4	2002Sep19
PASS		
0	set_st_indr2_sublk4_only	2002Sep19
PASS		
0	set_st_indr2_sublk5	2002Sep19
PASS		
0	set_st_indr2_sublk5_only	2002Sep19
PASS		
0	set_st_indr2_sublk6	2002Sep19
PASS		
0	set_st_indr2_sublk6_only	2002Sep19
PASS		
0	set_st_indr2_sublk7	2002Sep19
PASS		
0	set_st_indr2_sublk7_only	2002Sep19
PASS		
0	set_st_indr2_vs	2002Sep19
PASS		
0	set_st_indr2_vs_only	2002Sep19
PASS		
0	set_st_pfmatch_disabled	2002Sep19
PASS		
0	set_st_ring_match	2002Sep19
PASS		
0	set_st_ring_mismatch	2002Sep19
PASS		
0	set_st_ring_mismatch2	2002Sep19
PASS		
0	set_st_ring_mismatch3	2002Sep19
PASS		
0	set_st_ring_mismatch4	2002Sep19
PASS		
0	set_st_ring_ps	2002Sep19
PASS		
0	set_st_ring_ps_only	2002Sep19
PASS		
0	set_st_ring_sublk0	2002Sep19
PASS		
0	set_st_ring_sublk1	2002Sep19

PASS		
0	set_st_ring_subblk1_only	2002Sep19
PASS		
0	set_st_ring_subblk2	2002Sep19
PASS		
0	set_st_ring_subblk2_only	2002Sep19
PASS		
0	set_st_ring_subblk3	2002Sep19
PASS		
0	set_st_ring_subblk3_only	2002Sep19
PASS		
0	set_st_ring_subblk4	2002Sep19
PASS		
0	set_st_ring_subblk4_only	2002Sep19
PASS		
0	set_st_ring_subblk5	2002Sep19
PASS		
0	set_st_ring_subblk5_only	2002Sep19
PASS		
0	set_st_ring_subblk6	2002Sep19
PASS		
0	set_st_ring_subblk6_only	2002Sep19
PASS		
0	set_st_ring_subblk7	2002Sep19
PASS		
0	set_st_ring_subblk7_only	2002Sep19
PASS		
0	set_st_ring_vs	2002Sep19
PASS		
0	set_st_ring_vs_only	2002Sep19
PASS		
0	set_st_stress_a	2002Sep19
PASS		
685	set_st_stress_b	2002Sep19
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Sep19
PASS		
0	set_st_stress_d	2002Sep19
PASS		
0	set_st_stress_e	2002Sep19
PASS		
0	set_st_stress_f	2002Sep19
PASS		
690	set_state_rt_a	2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp	
691	set_state_rt_b	2002Sep19
FAIL	cp_rbbm.cmp rbbmWr.cmp	
692	set_state_rt_c	2002Sep19

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
693  set_state_rt_d                                2002Sep19
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    smoke_nrt                                     2002Sep19
PASS
695  src_clip                                       2002Sep19
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    srf_cohr                                       2002Sep19
PASS
0    trans_bitblt                                    2002Sep19
PASS
0    type0_2long_pckts                              2002Sep19
PASS
0    type0_3indr_buffs                              2002Sep19
PASS
0    type0_4pckts                                    2002Sep19
PASS
0    type0_incr_updt_cnst                           2002Sep19
PASS
0    type0_incr_updt_instrmem                       2002Sep19
PASS
0    type0_incr_updt_st                             2002Sep19
PASS
0    type0_incr_updt_stress                         2002Sep19
PASS
0    type0_ind1_ind2_a                              2002Sep19
PASS
0    type0_ind1_ind2_b                              2002Sep19
PASS
0    type0_indr_buff                                2002Sep19
PASS
0    type1_4pckts                                    2002Sep19
PASS
0    type1_indr_buff                                2002Sep19
PASS
0    type1_pckt                                      2002Sep19
PASS
711  type_random                                    2002Sep19
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
712  uram_wrt_rt_ucode                              2002Sep19
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
713  uram_wrt_ucode                                  2002Sep19
**FAIL**          rbbmWr.cmp
714  viz_query_a                                     2002Sep19
**FAIL**          rbbmWr.cmp
715  viz_query_draw_indx                            2002Sep19
**FAIL**          rbbmWr.cmp
716  viz_query_draw_indx_indr1                     2002Sep19

```

```

**FAIL**      rbbmWr.cmp
717 viz_query_draw_indx_indr2      2002Sep19
**FAIL**      rbbmWr.cmp
718 viz_query_draw_indx_multipass  2002Sep19
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
719 viz_query_draw_indx_ring      2002Sep19
**FAIL**      rbbmWr.cmp
720 viz_query_indr1                2002Sep19
**FAIL**      rbbmWr.cmp
0 viz_query_indr1_simple           2002Sep19
PASS
722 viz_query_indr2                2002Sep19
**FAIL**      rbbmWr.cmp
0 viz_query_indr2_simple           2002Sep19
PASS
724 viz_query_ring                 2002Sep19
**FAIL**      rbbmWr.cmp
0 viz_query_ring_simple            2002Sep19
PASS
0 wait_for_idle                     2002Sep19
PASS
727 wait_reg_mem_poll_mem0         2002Sep19
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_mem0_rt        2002Sep19
PASS
729 wait_reg_mem_poll_mem1         2002Sep19
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
730 wait_reg_mem_poll_mem2         2002Sep19
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
731 wait_reg_mem_poll_mem3         2002Sep19
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_reg0           2002Sep19
PASS
0 wait_reg_mem_poll_reg1           2002Sep19
PASS
0 wait_reg_mem_poll_reg2           2002Sep19
PASS
0 wait_reg_mem_poll_reg3           2002Sep19
PASS

```

```

=====
Tests_Run_count = 735 Pass_count = 643 Fail_count = 92 Incomplete_count = 0
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep26
PASS			
0	2d_indr_buffer_b		2002Sep26
PASS			
0	2d_indr_buffer_c		2002Sep26
PASS			
0	2d_indr_buffer_d		2002Sep26
PASS			
0	2d_indr_buffer_e		2002Sep26
PASS			
0	2d_indr_buffer_f		2002Sep26
PASS			
0	2d_indr_buffer_g		2002Sep26
PASS			
8	2d_indr_buffer_random		2002Sep26
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep26
PASS			
0	3d_draw_indx_2_b		2002Sep26
PASS			
0	bitblt_multi		2002Sep26
PASS			
0	brush_32x1mono		2002Sep26
PASS			
0	brush_8x8color_16		2002Sep26
PASS			
0	brush_8x8color_32		2002Sep26
PASS			
0	brush_8x8color_64		2002Sep26
PASS			
0	brush_8x8color_dst_0_1		2002Sep26
PASS			
0	brush_8x8mono		2002Sep26
PASS			
0	brush_color_mono_stress		2002Sep26
PASS			
0	brush_flag		2002Sep26
PASS			
0	brush_type0		2002Sep26

PASS		
0	brush_type1	2002Sep26
PASS		
0	brush_type10	2002Sep26
PASS		
0	brush_type13	2002Sep26
PASS		
0	brush_type14	2002Sep26
PASS		
0	brush_type6	2002Sep26
PASS		
0	brush_type7	2002Sep26
PASS		
0	brush_type_other	2002Sep26
PASS		
0	brush_types_13_14	2002Sep26
PASS		
0	clear_const_wr_en_on_me_init	2002Sep26
PASS		
0	clear_const_wr_en_with_lcc	2002Sep26
PASS		
0	cond_write_mem_poll_mem	2002Sep26
PASS		
0	cond_write_mem_poll_mem2	2002Sep26
PASS		
0	cond_write_mem_poll_mem3	2002Sep26
PASS		
34	cond_write_mem_poll_mem_rt	2002Sep26
FAIL	rbbmWr.cmp	
0	cond_write_mem_poll_reg	2002Sep26
PASS		
0	cond_write_mem_poll_reg2	2002Sep26
PASS		
0	cond_write_mem_poll_reg3	2002Sep26
PASS		
38	cp_acpi_recovery_soft_reset	2002Sep26
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	cp_bios_scratch	2002Sep26
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep26
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep26
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep26
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep26
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep26

PASS		
0	cp_ib2_base101_bufszXXX	2002Sep26
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep26
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep26
PASS		
0	cp_ib_base000_bufszXXX	2002Sep26
PASS		
0	cp_ib_base001_bufszXXX	2002Sep26
PASS		
0	cp_ib_base010_bufszXXX	2002Sep26
PASS		
0	cp_ib_base011_bufszXXX	2002Sep26
PASS		
0	cp_ib_base100_bufszXXX	2002Sep26
PASS		
0	cp_ib_base101_bufszXXX	2002Sep26
PASS		
0	cp_ib_base110_bufszXXX	2002Sep26
PASS		
0	cp_ib_base111_bufszXXX	2002Sep26
PASS		
0	cp_interrupt	2002Sep26
PASS		
57	cp_interrupt_rt_events_16	2002Sep26
FAIL	rbbmWr.cmp	
0	cp_iq2_alignment_000	2002Sep26
PASS		
0	cp_iq2_alignment_001	2002Sep26
PASS		
0	cp_iq2_alignment_010	2002Sep26
PASS		
0	cp_iq2_alignment_011	2002Sep26
PASS		
0	cp_iq2_alignment_100	2002Sep26
PASS		
0	cp_iq2_alignment_101	2002Sep26
PASS		
0	cp_iq2_alignment_110	2002Sep26
PASS		
0	cp_iq2_alignment_111	2002Sep26
PASS		
0	cp_iq_alignment_000	2002Sep26
PASS		
0	cp_iq_alignment_001	2002Sep26
PASS		
0	cp_iq_alignment_010	2002Sep26

PASS		
0	cp_iq_alignment_011	2002Sep26
PASS		
0	cp_iq_alignment_100	2002Sep26
PASS		
0	cp_iq_alignment_101	2002Sep26
PASS		
0	cp_iq_alignment_110	2002Sep26
PASS		
0	cp_iq_alignment_111	2002Sep26
PASS		
0	cp_max_size_packet	2002Sep26
PASS		
0	cp_max_size_packet_random	2002Sep26
PASS		
0	cp_nrt_rt_protection_mode	2002Sep26
PASS		
0	cp_output_stress	2002Sep26
PASS		
78	cp_output_stress_random	2002Sep26
FAIL	cp_mh_req.cmp	
0	cp_protection_mode	2002Sep26
PASS		
0	cp_rb_alignment_000	2002Sep26
PASS		
0	cp_rb_alignment_001	2002Sep26
PASS		
0	cp_rb_alignment_010	2002Sep26
PASS		
0	cp_rb_alignment_011	2002Sep26
PASS		
0	cp_rb_alignment_100	2002Sep26
PASS		
0	cp_rb_alignment_101	2002Sep26
PASS		
0	cp_rb_alignment_110	2002Sep26
PASS		
0	cp_rb_alignment_111	2002Sep26
PASS		
88	cp_rb_wptrXXX_base000	2002Sep26
FAIL	rbbmWr.cmp	
89	cp_rb_wptrXXX_base001	2002Sep26
FAIL	rbbmWr.cmp	
90	cp_rb_wptrXXX_base010	2002Sep26
FAIL	rbbmWr.cmp	
91	cp_rb_wptrXXX_base011	2002Sep26
FAIL	rbbmWr.cmp	
92	cp_rb_wptrXXX_base100	2002Sep26

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**FAIL**      rbbmWr.cmp
93  cp_rb_wptrXXX_base101      2002Sep26
**FAIL**      rbbmWr.cmp
94  cp_rb_wptrXXX_base110      2002Sep26
**FAIL**      rbbmWr.cmp
95  cp_rb_wptrXXX_base111      2002Sep26
**FAIL**      rbbmWr.cmp
0   cp_register_reads          2002Sep26
PASS
0   cp_reg_rw_test             2002Sep26
PASS
0   cp_rt_protection_mode      2002Sep26
PASS
0   cp_scratch                 2002Sep26
PASS
100 cp_scratch_multi           2002Sep26
**FAIL**      cp_mh_req.cmp
0   csf_reg_rd_default         2002Sep26
PASS
102 csf_reg_rd_only           2002Sep26
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp
0   csf_reg_wr_rd             2002Sep26
PASS
0   dma_cp_sync               2002Sep26
PASS
0   dma_dscrstr_chckn_bit      2002Sep26
PASS
0   dma_dscrstr_flush1        2002Sep26
PASS
0   dma_dscrstr_flush2        2002Sep26
PASS
0   dma_dscrstr_flush3        2002Sep26
PASS
0   dma_m2m_src00_dstXX       2002Sep26
PASS
0   dma_m2m_src01_dstXX       2002Sep26
PASS
0   dma_m2m_src10_dstXX       2002Sep26
PASS
0   dma_m2m_src11_dstXX       2002Sep26
PASS
0   dma_mem2mem               2002Sep26
PASS
0   dma_mem2mem_abort         2002Sep26
PASS
0   dma_mem2mem_alignments    2002Sep26
PASS
0   dma_mem2mem_byte_alignment0 2002Sep26

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PASS		
0	dma_mem2mem_byte_alignment1	2002Sep26
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep26
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep26
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep26
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep26
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep26
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep26
PASS		
124	dma_mem2mem_pio_abort	2002Sep26
FAIL		
0	dma_mem2mem_random	2002Sep26
PASS		
0	dma_mem2reg	2002Sep26
PASS		
127	dma_mem2reg_abort	2002Sep26
FAIL	rbbmWr.cmp	
0	dma_mem2reg_dtaq_stress	2002Sep26
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep26
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep26
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep26
PASS		
0	dma_mem2reg_sync	2002Sep26
PASS		
0	dma_r2r_m2m_r2r	2002Sep26
PASS		
0	dma_r2r_m2r_r2r	2002Sep26
PASS		
0	dma_r2r_r2m_r2r	2002Sep26
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep26
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep26
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep26
PASS		
0	dma_reg2mem	2002Sep26
PASS		
0	dma_reg2mem_abort	2002Sep26

PASS		
0	dma_reg2reg	2002Sep26
PASS		
0	dma_reg2reg_2dw	2002Sep26
PASS		
0	dma_reg2reg_daic	2002Sep26
PASS		
0	dma_reg2reg_mult	2002Sep26
PASS		
0	dma_reg2reg_mult2	2002Sep26
PASS		
0	dma_reg2reg_mult3	2002Sep26
PASS		
0	dma_reg2reg_saic	2002Sep26
PASS		
0	dma_reg2reg_sdaic	2002Sep26
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep26
PASS		
0	dma_reg2reg_stress	2002Sep26
PASS		
0	draw_indx_a	2002Sep26
PASS		
0	draw_indx_b	2002Sep26
PASS		
0	draw_indx_c	2002Sep26
PASS		
0	draw_indx_indr1_a	2002Sep26
PASS		
0	draw_indx_indr1_b	2002Sep26
PASS		
0	draw_indx_indr1_short_a	2002Sep26
PASS		
0	draw_indx_indr1_short_b	2002Sep26
PASS		
0	draw_indx_indr1_stress	2002Sep26
PASS		
0	draw_indx_indr2_a	2002Sep26
PASS		
0	draw_indx_indr2_b	2002Sep26
PASS		
0	draw_indx_indr2_short_a	2002Sep26
PASS		
0	draw_indx_indr2_short_b	2002Sep26
PASS		
0	draw_indx_indr2_stress	2002Sep26
PASS		
0	draw_indx_ring1	2002Sep26

PASS		
0	draw_indx_ring2	2002Sep26
PASS		
0	draw_indx_ring_short1	2002Sep26
PASS		
0	draw_indx_ring_short2	2002Sep26
PASS		
0	draw_indx_ring_stress	2002Sep26
PASS		
0	draw_indx_short_a	2002Sep26
PASS		
0	draw_indx_short_b	2002Sep26
PASS		
0	draw_indx_short_c	2002Sep26
PASS		
0	draw_indx_stress_a	2002Sep26
PASS		
0	draw_indx_stress_b	2002Sep26
PASS		
0	draw_indx_stress_c	2002Sep26
PASS		
175	event_timestamp_write	2002Sep26
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep26
PASS		
0	fix2flt_reg	2002Sep26
PASS		
0	ft_indr1_july22	2002Sep26
PASS		
0	ft_indr2_july22	2002Sep26
PASS		
0	ft_july22	2002Sep26
PASS		
181	ft_july22_random	2002Sep26
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	header_dump	2002Sep26
PASS		
183	header_dump_rt	2002Sep26
FAIL	rbbmWr.cmp	
0	header_dump_small	2002Sep26
PASS		
0	hostdata_blt	2002Sep26
PASS		
0	im_load_a	2002Sep26
PASS		
0	im_load_b	2002Sep26
PASS		
0	im_load_cid_mismatch	2002Sep26

PASS		
189	im_load_immediate	2002Sep26
FAIL	rbbmWr.cmp	
0	im_load_immediate_nrt_me	2002Sep26
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep26
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep26
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep26
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep26
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep26
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep26
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep26
PASS		
0	im_load_indr1_a	2002Sep26
PASS		
0	im_load_indr1_b	2002Sep26
PASS		
0	im_load_indr2_a	2002Sep26
PASS		
0	im_load_indr2_b	2002Sep26
PASS		
0	im_load_invalidate	2002Sep26
PASS		
0	im_load_match	2002Sep26
PASS		
0	im_load_match2	2002Sep26
PASS		
0	im_load_nrt_me	2002Sep26
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep26
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep26
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep26
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep26
PASS		
0	im_load_pfmatch_disable	2002Sep26
PASS		
0	im_load_ring_a	2002Sep26
PASS		
0	im_load_ring_b	2002Sep26

PASS		
213	im_load_rt	2002Sep26
FAIL	rbbmWr.cmp	
214	im_load_rt2	2002Sep26
FAIL	rbbmWr.cmp	
215	im_load_rt3	2002Sep26
FAIL	rbbmWr.cmp	
216	im_load_rt4	2002Sep26
FAIL	rbbmWr.cmp	
0	im_load_sz_mismatch	2002Sep26
PASS		
0	incremental_update_const	2002Sep26
PASS		
0	incremental_update_instr	2002Sep26
PASS		
0	incremental_update_state	2002Sep26
PASS		
0	indirect_buffer_a	2002Sep26
PASS		
0	indirect_buffer_b	2002Sep26
PASS		
0	indirect_buffer_c	2002Sep26
PASS		
0	indirect_buffer_d	2002Sep26
PASS		
0	indirect_buffer_e	2002Sep26
PASS		
0	indirect_buffer_f	2002Sep26
PASS		
0	indirect_buffer_g	2002Sep26
PASS		
0	indirect_buffer_h	2002Sep26
PASS		
0	indirect_buffer_i	2002Sep26
PASS		
0	indirect_buffer_j	2002Sep26
PASS		
0	indirect_buffer_k	2002Sep26
PASS		
0	indirect_buffer_l	2002Sep26
PASS		
0	indirect_buffer_mp_a	2002Sep26
PASS		
0	indirect_buffer_mp_b	2002Sep26
PASS		
0	indirect_buffer_mp_c	2002Sep26
PASS		
0	indirect_buffer_mp_d	2002Sep26

PASS		
0	indirect_buffer_mp_e	2002Sep26
PASS		
0	indirect_buffer_mp_f	2002Sep26
PASS		
0	invalidate_on_me_init	2002Sep26
PASS		
0	invalidate_state_constants	2002Sep26
PASS		
0	invalidate_state_shaders	2002Sep26
PASS		
0	invalidate_state_shaders2	2002Sep26
PASS		
0	invalidate_state_sublks	2002Sep26
PASS		
0	invalidate_state_sublks2	2002Sep26
PASS		
0	invalid_opcode	2002Sep26
PASS		
0	iq1_iq2_mult	2002Sep26
PASS		
0	iq1_iq2_push_wrap	2002Sep26
PASS		
0	iq2_csq_full	2002Sep26
PASS		
0	IQ2_Mult	2002Sep26
PASS		
0	IQ2_Wrap_Legacy	2002Sep26
PASS		
0	IQ_Wrap	2002Sep26
PASS		
0	Isync_Control	2002Sep26
PASS		
0	Isync_Control_2D_3D	2002Sep26
PASS		
0	Isync_Control_3D_2D	2002Sep26
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep26
PASS		
256	ld_cnst_cntx_coherency_cntr2	2002Sep26
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Sep26
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep26
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep26
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep26

PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep26
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep26
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep26
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep26
PASS		
0	ld_cnst_cntx_match	2002Sep26
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep26
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep26
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep26
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep26
PASS		
0	ld_cnst_cntx_pfmatch_disable	2002Sep26
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep26
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep26
PASS		
0	ld_cnst_cntx_replicate8	2002Sep26
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep26
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep26
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep26
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep26
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep26
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep26
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep26
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep26
PASS		
282	load_constant_context_rt	2002Sep26
FAIL	rbbmWr.cmp	
0	load_palette	2002Sep26
PASS		
0	lp_2d3d	2002Sep26

```

PASS
0 mem_write 2002Sep26
PASS
286 mem_write_rt 2002Sep26
**FAIL** rbbmWr.cmp
0 mpeg_index 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_0 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_1 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_2 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_3 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_4 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_5 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_6 2002Sep26
PASS
0 mpeg_index_multi_context_max_context_7 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_1 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_2 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_3 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_4 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_5 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_6 2002Sep26
PASS
0 mpeg_index_multi_context_rt_enabled_max_context_7 2002Sep26
PASS
0 nop_eating_disabled 2002Sep26
PASS
304 nrt_rt_interaction1 2002Sep26
**FAIL** rbbmWr.cmp
0 paint_multi 2002Sep26
PASS
0 paint_packet 2002Sep26
PASS
0 paint_packet_gui_control 2002Sep26
PASS
0 pfp_reg_reads1 2002Sep26

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PASS		
0	pfp_reg_reads2	2002Sep26
PASS		
0	pitch_clip	2002Sep26
PASS		
0	ply_nextscan	2002Sep26
PASS		
0	rbbm_clk_active_deassert_min_max	2002Sep26
PASS		
313	rbbm_clk_active_each_pipe	2002Sep26
FAIL	rbbmWr.cmp	
0	rbbm_clk_active_for_read_error	2002Sep26
PASS		
0	rbbm_clk_active_for_read_request	2002Sep26
PASS		
0	rbbm_clk_active_on_reset	2002Sep26
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep26
PASS		
318	rbbm_cp_non_queued_rdwr_02	2002Sep26
FAIL	cp_rbbm.cmp	
319	rbbm_cp_queued_holds_nonqueued	2002Sep26
FAIL	cp_rbbm.cmp	
320	rbbm_cp_queued_rdwr	2002Sep26
FAIL	cp_rbbm.cmp	
0	rbbm_int_ack	2002Sep26
PASS		
322	rbbm_int_each_client	2002Sep26
FAIL		
0	rbbm_int_nrt_gfxpipe_idle	2002Sep26
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep26
PASS		
0	rbbm_int_rderr	2002Sep26
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep26
PASS		
327	rbbm_isync_cpscratch_idlegui	2002Sep26
FAIL		
0	rbbm_isync_wait_idlegui	2002Sep26
PASS		
0	rbbm_soft_reset	2002Sep26
PASS		
0	rbbm_WU_2D_idle_01	2002Sep26
PASS		
0	rbbm_WU_2D_idle_02	2002Sep26
PASS		
0	rbbm_WU_2D_idle_03	2002Sep26

PASS		
0	rbbm_WU_2D_idle_04	2002Sep26
PASS		
0	rbbm_WU_2D_idle_05	2002Sep26
PASS		
0	rbbm_WU_2D_idle_06	2002Sep26
PASS		
0	rbbm_WU_2D_idle_07	2002Sep26
PASS		
0	rbbm_WU_2D_idle_08	2002Sep26
PASS		
0	rbbm_WU_2D_idle_09	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep26
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep26
PASS		
0	rbbm_WU_3D_idle_01	2002Sep26
PASS		
0	rbbm_WU_3D_idle_02	2002Sep26
PASS		
0	rbbm_WU_3D_idle_03	2002Sep26
PASS		
0	rbbm_WU_3D_idle_04	2002Sep26
PASS		
0	rbbm_WU_3D_idle_05	2002Sep26
PASS		
0	rbbm_WU_3D_idle_06	2002Sep26

PASS		
0	rbbm_WU_3D_idle_07	2002Sep26
PASS		
0	rbbm_WU_3D_idle_08	2002Sep26
PASS		
0	rbbm_WU_3D_idle_09	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep26
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep26
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep26
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep26
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep26
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep26
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep26
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep26
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep26
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep26
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep26

PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep26
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep26
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep26
PASS		
0	rbbm_WU_idct_semaphore	2002Sep26
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep26
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep26
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep26
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep26
PASS		
389	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep26
FAIL		
390	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep26
FAIL		
391	rbbm_WUrt_CP_dma_idle	2002Sep26
FAIL	cp_rbbm.cmp	
392	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep26
FAIL		
393	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep26
FAIL		
394	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep26
FAIL		
395	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep26
FAIL		
396	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep26
FAIL		
397	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep26
FAIL		
398	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep26
FAIL		
399	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep26
FAIL		
400	rbbm_WUrt_MH_hdp_clean	2002Sep26
FAIL		
401	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep26
FAIL		
402	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep26
FAIL		
403	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep26
FAIL		
0	rbbm_WUrt_RT_idle_01	2002Sep26

PASS		
405	rbbm_WUrt_RT_idle_02	2002Sep26
FAIL		
406	rbbm_WUrt_RT_idle_03	2002Sep26
FAIL		
407	rbbm_WUrt_RT_idle_04	2002Sep26
FAIL		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep26
PASS		
409	rbbm_WUrt_RT_idle_clean_02	2002Sep26
FAIL		
410	rbbm_WUrt_RT_idle_clean_03	2002Sep26
FAIL		
411	rbbm_WUrt_RT_idle_clean_04	2002Sep26
FAIL		
412	rbbm_WUrt_RT_idle_clean_05	2002Sep26
FAIL		
413	rbbm_WUrt_VIPH0_dma_idle	2002Sep26
FAIL		
414	rbbm_WUrt_VIPH1_dma_idle	2002Sep26
FAIL		
415	rbbm_WUrt_VIPH2_dma_idle	2002Sep26
FAIL		
416	rbbm_WUrt_VIPH3_dma_idle	2002Sep26
FAIL		
0	rbbm_WU_VIPH0_dma_idle	2002Sep26
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep26
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep26
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep26
PASS		
0	RB_BUFSZ_0	2002Sep26
PASS		
0	RB_BUFSZ_1	2002Sep26
PASS		
0	RB_Wrap	2002Sep26
PASS		
0	reg_to_mem	2002Sep26
PASS		
425	reg_to_mem_rt	2002Sep26
FAIL	rbbmWr.cmp	
0	rop_cfd_wmd	2002Sep26
PASS		
427	roq_reg_reads	2002Sep26
FAIL	cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	rstr_ordr	2002Sep26

PASS
429 rtee_arm_wait_idle_trigger_events_16 2002Sep26
FAIL
0 rtee_discretes_arm_rearm_events_32 2002Sep26
PASS
0 rtee_discretes_falling_00_events_16 2002Sep26
PASS
0 rtee_discretes_falling_01_events_16 2002Sep26
PASS
0 rtee_discretes_falling_02_events_16 2002Sep26
PASS
0 rtee_discretes_falling_03_events_16 2002Sep26
PASS
0 rtee_discretes_falling_04_events_16 2002Sep26
PASS
0 rtee_discretes_falling_05_events_16 2002Sep26
PASS
0 rtee_discretes_falling_06_events_16 2002Sep26
PASS
0 rtee_discretes_falling_07_events_16 2002Sep26
PASS
0 rtee_discretes_falling_08_events_16 2002Sep26
PASS
0 rtee_discretes_falling_09_events_16 2002Sep26
PASS
0 rtee_discretes_falling_10_events_16 2002Sep26
PASS
0 rtee_discretes_falling_11_events_16 2002Sep26
PASS
0 rtee_discretes_falling_12_events_16 2002Sep26
PASS
0 rtee_discretes_falling_13_events_16 2002Sep26
PASS
0 rtee_discretes_falling_14_events_16 2002Sep26
PASS
0 rtee_discretes_falling_15_events_16 2002Sep26
PASS
0 rtee_discretes_falling_16_events_16 2002Sep26
PASS
0 rtee_discretes_falling_17_events_16 2002Sep26
PASS
0 rtee_discretes_falling_18_events_16 2002Sep26
PASS
0 rtee_discretes_falling_19_events_16 2002Sep26
PASS
0 rtee_discretes_falling_20_events_16 2002Sep26
PASS
0 rtee_discretes_falling_21_events_16 2002Sep26

PASS		
0	rtee_discretes_falling_22_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep26
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep26

PASS		
0	rtee_discretes_invert_14_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep26
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep26
PASS		
0	rtee_discretes_level_00_events_16	2002Sep26
PASS		
0	rtee_discretes_level_01_events_16	2002Sep26
PASS		
0	rtee_discretes_level_02_events_16	2002Sep26
PASS		
0	rtee_discretes_level_03_events_16	2002Sep26
PASS		
0	rtee_discretes_level_04_events_16	2002Sep26
PASS		
0	rtee_discretes_level_05_events_16	2002Sep26

PASS		
0	rtee_discretes_level_06_events_16	2002Sep26
PASS		
0	rtee_discretes_level_07_events_16	2002Sep26
PASS		
0	rtee_discretes_level_08_events_16	2002Sep26
PASS		
0	rtee_discretes_level_09_events_16	2002Sep26
PASS		
0	rtee_discretes_level_10_events_16	2002Sep26
PASS		
0	rtee_discretes_level_11_events_16	2002Sep26
PASS		
0	rtee_discretes_level_12_events_16	2002Sep26
PASS		
0	rtee_discretes_level_13_events_16	2002Sep26
PASS		
0	rtee_discretes_level_14_events_16	2002Sep26
PASS		
0	rtee_discretes_level_15_events_16	2002Sep26
PASS		
0	rtee_discretes_level_16_events_16	2002Sep26
PASS		
0	rtee_discretes_level_17_events_16	2002Sep26
PASS		
0	rtee_discretes_level_18_events_16	2002Sep26
PASS		
0	rtee_discretes_level_19_events_16	2002Sep26
PASS		
0	rtee_discretes_level_20_events_16	2002Sep26
PASS		
0	rtee_discretes_level_21_events_16	2002Sep26
PASS		
0	rtee_discretes_level_22_events_16	2002Sep26
PASS		
0	rtee_discretes_level_23_events_16	2002Sep26
PASS		
0	rtee_discretes_level_24_events_16	2002Sep26
PASS		
0	rtee_discretes_level_25_events_16	2002Sep26
PASS		
0	rtee_discretes_level_26_events_16	2002Sep26
PASS		
0	rtee_discretes_level_27_events_16	2002Sep26
PASS		
0	rtee_discretes_level_28_events_16	2002Sep26
PASS		
0	rtee_discretes_level_29_events_16	2002Sep26

PASS		
0	rtee_discretes_level_30_events_16	2002Sep26
PASS		
0	rtee_discretes_level_31_events_16	2002Sep26
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep26

PASS		
0	rtee_discretes_rising_21_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep26
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep26
PASS		
0	rtee_display1_comp_events_16	2002Sep26
PASS		
0	rtee_display2_comp_events_16	2002Sep26
PASS		
0	rtee_mem_linear_search_events_2	2002Sep26
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep26
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep26

PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep26
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep26
PASS		
575	rtee_prefetch_parse_nops	2002Sep26
FAIL	rbbmWr.cmp	
0	rtee_reg_linear_search_events_2	2002Sep26
PASS		
577	rtee_reg_poll_comp_000_events_16	2002Sep26
FAIL		
578	rtee_reg_poll_comp_001_events_16	2002Sep26
FAIL		
579	rtee_reg_poll_comp_010_events_16	2002Sep26
FAIL		
580	rtee_reg_poll_comp_011_events_16	2002Sep26
FAIL		
581	rtee_reg_poll_comp_100_events_16	2002Sep26
FAIL		
582	rtee_reg_poll_comp_101_events_16	2002Sep26
FAIL		
583	rtee_reg_poll_comp_110_events_16	2002Sep26
FAIL		
584	rtee_reg_poll_comp_111_events_16	2002Sep26
FAIL		
585	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep26
FAIL	cp_rbbm.cmp	
0	rtee_vip_comp_events_16	2002Sep26
PASS		
587	rt_invalid_opcode	2002Sep26
FAIL	rbbmWr.cmp	
0	scratch_mem_read	2002Sep26
PASS		
0	scratch_mem_read2	2002Sep26
PASS		
0	set_constant	2002Sep26
PASS		
0	set_constant2	2002Sep26
PASS		
0	set_constant_alu_max1	2002Sep26
PASS		
0	set_constant_alu_max2	2002Sep26
PASS		
0	set_constant_bool_max1	2002Sep26
PASS		
0	set_constant_bool_max2	2002Sep26
PASS		
0	set_constant_indr1_short	2002Sep26

PASS		
0	set_constant_indr1_short2	2002Sep26
PASS		
0	set_constant_indr1_write_enable	2002Sep26
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep26
PASS		
0	set_constant_indr2_short	2002Sep26
PASS		
0	set_constant_indr2_short2	2002Sep26
PASS		
0	set_constant_indr2_write_enable	2002Sep26
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep26
PASS		
0	set_constant_loop_max1	2002Sep26
PASS		
0	set_constant_loop_max2	2002Sep26
PASS		
0	set_constant_ring_short	2002Sep26
PASS		
0	set_constant_ring_short2	2002Sep26
PASS		
0	set_constant_ring_write_enable	2002Sep26
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep26
PASS		
0	set_constant_short	2002Sep26
PASS		
0	set_constant_short2	2002Sep26
PASS		
0	set_constant_tex_max1	2002Sep26
PASS		
0	set_constant_tex_max2	2002Sep26
PASS		
0	set_constant_write_enable	2002Sep26
PASS		
0	set_constant_write_enable_lcc	2002Sep26
PASS		
0	set_constant_write_enable_lcc2	2002Sep26
PASS		
0	set_scissors	2002Sep26
PASS		
618	set_state_rt_a	2002Sep26
FAIL	rbbmWr.cmp	
619	set_state_rt_b	2002Sep26
FAIL	rbbmWr.cmp	
620	set_state_rt_c	2002Sep26

```

**FAIL**      rbbmWr.cmp
621 set_state_rt_d                                2002Sep26
**FAIL**      rbbmWr.cmp
0 set_st_indrl_match                              2002Sep26
PASS
0 set_st_indrl_mismatch                          2002Sep26
PASS
0 set_st_indrl_mismatch2                         2002Sep26
PASS
0 set_st_indrl_mismatch3                        2002Sep26
PASS
0 set_st_indrl_mismatch4                        2002Sep26
PASS
0 set_st_indrl_ps                                2002Sep26
PASS
0 set_st_indrl_ps_only                          2002Sep26
PASS
0 set_st_indrl_sublk0                           2002Sep26
PASS
0 set_st_indrl_sublk1                           2002Sep26
PASS
0 set_st_indrl_sublk1_only                      2002Sep26
PASS
0 set_st_indrl_sublk2                           2002Sep26
PASS
0 set_st_indrl_sublk2_only                     2002Sep26
PASS
0 set_st_indrl_sublk3                           2002Sep26
PASS
0 set_st_indrl_sublk3_only                     2002Sep26
PASS
0 set_st_indrl_sublk4                           2002Sep26
PASS
0 set_st_indrl_sublk4_only                     2002Sep26
PASS
0 set_st_indrl_sublk5                           2002Sep26
PASS
0 set_st_indrl_sublk5_only                     2002Sep26
PASS
0 set_st_indrl_sublk6                           2002Sep26
PASS
0 set_st_indrl_sublk6_only                     2002Sep26
PASS
0 set_st_indrl_sublk7                           2002Sep26
PASS
0 set_st_indrl_sublk7_only                     2002Sep26
PASS
0 set_st_indrl_vs                                2002Sep26

```

PASS		
0	set_st_indr1_vs_only	2002Sep26
PASS		
0	set_st_indr2_match	2002Sep26
PASS		
0	set_st_indr2_mismatch	2002Sep26
PASS		
0	set_st_indr2_mismatch2	2002Sep26
PASS		
0	set_st_indr2_mismatch3	2002Sep26
PASS		
0	set_st_indr2_mismatch4	2002Sep26
PASS		
0	set_st_indr2_ps	2002Sep26
PASS		
0	set_st_indr2_ps_only	2002Sep26
PASS		
0	set_st_indr2_sublk0	2002Sep26
PASS		
0	set_st_indr2_sublk1	2002Sep26
PASS		
0	set_st_indr2_sublk1_only	2002Sep26
PASS		
0	set_st_indr2_sublk2	2002Sep26
PASS		
0	set_st_indr2_sublk2_only	2002Sep26
PASS		
0	set_st_indr2_sublk3	2002Sep26
PASS		
0	set_st_indr2_sublk3_only	2002Sep26
PASS		
0	set_st_indr2_sublk4	2002Sep26
PASS		
0	set_st_indr2_sublk4_only	2002Sep26
PASS		
0	set_st_indr2_sublk5	2002Sep26
PASS		
0	set_st_indr2_sublk5_only	2002Sep26
PASS		
0	set_st_indr2_sublk6	2002Sep26
PASS		
0	set_st_indr2_sublk6_only	2002Sep26
PASS		
0	set_st_indr2_sublk7	2002Sep26
PASS		
0	set_st_indr2_sublk7_only	2002Sep26
PASS		
0	set_st_indr2_vs	2002Sep26

PASS		
0	set_st_indr2_vs_only	2002Sep26
PASS		
0	set_st_pfmatch_disabled	2002Sep26
PASS		
0	set_st_ring_match	2002Sep26
PASS		
0	set_st_ring_mismatch	2002Sep26
PASS		
0	set_st_ring_mismatch2	2002Sep26
PASS		
0	set_st_ring_mismatch3	2002Sep26
PASS		
0	set_st_ring_mismatch4	2002Sep26
PASS		
0	set_st_ring_ps	2002Sep26
PASS		
0	set_st_ring_ps_only	2002Sep26
PASS		
0	set_st_ring_subblk0	2002Sep26
PASS		
0	set_st_ring_subblk1	2002Sep26
PASS		
0	set_st_ring_subblk1_only	2002Sep26
PASS		
0	set_st_ring_subblk2	2002Sep26
PASS		
0	set_st_ring_subblk2_only	2002Sep26
PASS		
0	set_st_ring_subblk3	2002Sep26
PASS		
0	set_st_ring_subblk3_only	2002Sep26
PASS		
0	set_st_ring_subblk4	2002Sep26
PASS		
0	set_st_ring_subblk4_only	2002Sep26
PASS		
0	set_st_ring_subblk5	2002Sep26
PASS		
0	set_st_ring_subblk5_only	2002Sep26
PASS		
0	set_st_ring_subblk6	2002Sep26
PASS		
0	set_st_ring_subblk6_only	2002Sep26
PASS		
0	set_st_ring_subblk7	2002Sep26
PASS		
0	set_st_ring_subblk7_only	2002Sep26

PASS		
0	set_st_ring_vs	2002Sep26
PASS		
0	set_st_ring_vs_only	2002Sep26
PASS		
0	set_st_stress_a	2002Sep26
PASS		
0	set_st_stress_b	2002Sep26
PASS		
0	set_st_stress_c	2002Sep26
PASS		
0	set_st_stress_d	2002Sep26
PASS		
0	set_st_stress_e	2002Sep26
PASS		
0	set_st_stress_f	2002Sep26
PASS		
0	Small_Text_BM	2002Sep26
PASS		
0	smoke_nrt	2002Sep26
PASS		
0	src_clip	2002Sep26
PASS		
0	srf_cohr	2002Sep26
PASS		
0	trans_bitblt	2002Sep26
PASS		
706	type0_2long_pckts	2002Sep26
FAIL	cp_mh_req.cmp	
0	type0_3indr_buffs	2002Sep26
PASS		
0	type0_4pckts	2002Sep26
PASS		
0	type0_incr_updt_cnst	2002Sep26
PASS		
0	type0_incr_updt_instrmem	2002Sep26
PASS		
0	type0_incr_updt_st	2002Sep26
PASS		
0	type0_incr_updt_stress	2002Sep26
PASS		
0	type0_ind1_ind2_a	2002Sep26
PASS		
0	type0_ind1_ind2_b	2002Sep26
PASS		
0	type0_indr_buff	2002Sep26
PASS		
0	Type0_IQ2_BM	2002Sep26

PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep26
PASS		
0	Type0_IQ_BM	2002Sep26
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep26
PASS		
0	Type0_PQ_BM	2002Sep26
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep26
PASS		
0	type1_4pckts	2002Sep26
PASS		
0	type1_indr_buff	2002Sep26
PASS		
0	type1_pckt	2002Sep26
PASS		
0	Type3_Bitblt	2002Sep26
PASS		
0	Type3_Load_Palette	2002Sep26
PASS		
0	Type3_NOP	2002Sep26
PASS		
0	Type3_Polyline	2002Sep26
PASS		
0	Type3_Polyline_Large	2002Sep26
PASS		
730	type_random	2002Sep26
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
731	uram_wrt_rt_ucode	2002Sep26
FAIL	rbbmWr.cmp	
732	uram_wrt_ucode	2002Sep26
FAIL	rbbmWr.cmp	
733	viz_query_a	2002Sep26
FAIL	rbbmWr.cmp	
734	viz_query_draw_indx	2002Sep26
FAIL	rbbmWr.cmp	
735	viz_query_draw_indx_indr1	2002Sep26
FAIL	rbbmWr.cmp	
736	viz_query_draw_indx_indr2	2002Sep26
FAIL	rbbmWr.cmp	
737	viz_query_draw_indx_multipass	2002Sep26
FAIL	cp_rbbm.cmp rbbmWr.cmp	
738	viz_query_draw_indx_ring	2002Sep26
FAIL	rbbmWr.cmp	
739	viz_query_indr1	2002Sep26
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep26

```

PASS
741 viz_query_indr2 2002Sep26
**FAIL** rbbmWr.cmp
0 viz_query_indr2_simple 2002Sep26
PASS
743 viz_query_ring 2002Sep26
**FAIL** rbbmWr.cmp
0 viz_query_ring_simple 2002Sep26
PASS
0 wait_for_idle 2002Sep26
PASS
746 wait_reg_mem_poll_mem0 2002Sep26
**FAIL** cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
747 wait_reg_mem_poll_mem0_rt 2002Sep26
**FAIL** rbbmWr.cmp
748 wait_reg_mem_poll_mem1 2002Sep26
**FAIL** cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
749 wait_reg_mem_poll_mem2 2002Sep26
**FAIL** cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
750 wait_reg_mem_poll_mem3 2002Sep26
**FAIL** cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_reg0 2002Sep26
PASS
0 wait_reg_mem_poll_reg1 2002Sep26
PASS
0 wait_reg_mem_poll_reg2 2002Sep26
PASS
0 wait_reg_mem_poll_reg3 2002Sep26
PASS

```

```

=====
Tests_Run_count = 754 Pass_count = 657 Fail_count = 97 Incomplete_count = 0
=====

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Aug 13 21:54:18 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU              0          0          0          0
              CP             564         564         520         92
              RBBM           21          21          21         100
+++++
              TOTAL           585         585         541         92
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep 12  8:13:18 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          635        635        584         91
              RBBM        98         98         75          76
+++++
              TOTAL        733        733        659         89
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Sep  7  8:01:15 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          634         634         624         98
              RBBM        98          98          70          71
+++++
              TOTAL        732         732         694         94
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Oct  1 23:36:33 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          672        672        653         97
              RBBM        113        113        113        100
+++++
              TOTAL        785        785        766         97
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep23
	PASS		
0	2d_indr_buffer_b		2002Sep23
	PASS		
0	2d_indr_buffer_c		2002Sep23
	PASS		
0	2d_indr_buffer_d		2002Sep23
	PASS		
0	2d_indr_buffer_e		2002Sep23
	PASS		
0	2d_indr_buffer_f		2002Sep23
	PASS		
0	2d_indr_buffer_g		2002Sep23
	PASS		
8	2d_indr_buffer_random		2002Sep23
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep23
	PASS		
0	3d_draw_indx_2_b		2002Sep23
	PASS		
0	IQ2_Mult		2002Sep23
	PASS		
0	IQ2_Wrap_Legacy		2002Sep23
	PASS		
0	IQ_Wrap		2002Sep23
	PASS		
0	Isync_Control		2002Sep23
	PASS		
0	Isync_Control_2D_3D		2002Sep23
	PASS		
0	Isync_Control_3D_2D		2002Sep23
	PASS		
0	RB_BUFSZ_0		2002Sep23
	PASS		
0	RB_BUFSZ_1		2002Sep23
	PASS		
0	RB_Wrap		2002Sep23
	PASS		
0	Small_Text_BM		2002Sep23

PASS		
0	Type0_IQ2_BM	2002Sep23
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep23
PASS		
0	Type0_IQ_BM	2002Sep23
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep23
PASS		
0	Type0_PQ_BM	2002Sep23
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep23
PASS		
0	Type3_Bitblt	2002Sep23
PASS		
0	Type3_Load_Palette	2002Sep23
PASS		
0	Type3_NOP	2002Sep23
PASS		
0	Type3_Polyline	2002Sep23
PASS		
0	Type3_Polyline_Large	2002Sep23
PASS		
0	bitblt_multi	2002Sep23
PASS		
0	brush_32x1mono	2002Sep23
PASS		
0	brush_8x8color_16	2002Sep23
PASS		
0	brush_8x8color_32	2002Sep23
PASS		
0	brush_8x8color_64	2002Sep23
PASS		
0	brush_8x8color_dst_0_1	2002Sep23
PASS		
0	brush_8x8mono	2002Sep23
PASS		
0	brush_color_mono_stress	2002Sep23
PASS		
0	brush_flag	2002Sep23
PASS		
0	brush_type0	2002Sep23
PASS		
0	brush_type1	2002Sep23
PASS		
0	brush_type10	2002Sep23
PASS		
0	brush_type13	2002Sep23

PASS		
0	brush_type14	2002Sep23
PASS		
0	brush_type6	2002Sep23
PASS		
0	brush_type7	2002Sep23
PASS		
0	brush_type_other	2002Sep23
PASS		
0	brush_types_13_14	2002Sep23
PASS		
0	clear_const_wr_en_on_me_init	2002Sep23
PASS		
0	clear_const_wr_en_with_lcc	2002Sep23
PASS		
0	cond_write_mem_poll_mem	2002Sep23
PASS		
0	cond_write_mem_poll_mem2	2002Sep23
PASS		
0	cond_write_mem_poll_mem3	2002Sep23
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep23
PASS		
0	cond_write_mem_poll_reg	2002Sep23
PASS		
0	cond_write_mem_poll_reg2	2002Sep23
PASS		
0	cond_write_mem_poll_reg3	2002Sep23
PASS		
59	cp_acpi_recovery_soft_reset	2002Sep23
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	cp_bios_scratch	2002Sep23
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep23
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep23

PASS		
0	cp_ib_base000_bufszXXX	2002Sep23
PASS		
0	cp_ib_base001_bufszXXX	2002Sep23
PASS		
0	cp_ib_base010_bufszXXX	2002Sep23
PASS		
0	cp_ib_base011_bufszXXX	2002Sep23
PASS		
0	cp_ib_base100_bufszXXX	2002Sep23
PASS		
0	cp_ib_base101_bufszXXX	2002Sep23
PASS		
0	cp_ib_base110_bufszXXX	2002Sep23
PASS		
0	cp_ib_base111_bufszXXX	2002Sep23
PASS		
0	cp_interrupt	2002Sep23
PASS		
0	cp_interrupt_rt_events_16	2002Sep23
PASS		
0	cp_iq2_alignment_000	2002Sep23
PASS		
0	cp_iq2_alignment_001	2002Sep23
PASS		
0	cp_iq2_alignment_010	2002Sep23
PASS		
0	cp_iq2_alignment_011	2002Sep23
PASS		
0	cp_iq2_alignment_100	2002Sep23
PASS		
0	cp_iq2_alignment_101	2002Sep23
PASS		
0	cp_iq2_alignment_110	2002Sep23
PASS		
0	cp_iq2_alignment_111	2002Sep23
PASS		
0	cp_iq_alignment_000	2002Sep23
PASS		
0	cp_iq_alignment_001	2002Sep23
PASS		
0	cp_iq_alignment_010	2002Sep23
PASS		
0	cp_iq_alignment_011	2002Sep23
PASS		
0	cp_iq_alignment_100	2002Sep23
PASS		
0	cp_iq_alignment_101	2002Sep23

PASS		
0	cp_iq_alignment_110	2002Sep23
PASS		
0	cp_iq_alignment_111	2002Sep23
PASS		
0	cp_max_size_packet	2002Sep23
PASS		
96	cp_max_size_packet_random	2002Sep23
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep23
PASS		
0	cp_output_stress	2002Sep23
PASS		
0	cp_output_stress_random	2002Sep23
PASS		
0	cp_protection_mode	2002Sep23
PASS		
0	cp_rb_alignment_000	2002Sep23
PASS		
0	cp_rb_alignment_001	2002Sep23
PASS		
0	cp_rb_alignment_010	2002Sep23
PASS		
0	cp_rb_alignment_011	2002Sep23
PASS		
0	cp_rb_alignment_100	2002Sep23
PASS		
0	cp_rb_alignment_101	2002Sep23
PASS		
0	cp_rb_alignment_110	2002Sep23
PASS		
0	cp_rb_alignment_111	2002Sep23
PASS		
0	cp_rb_wptrXXX_base000	2002Sep23
PASS		
0	cp_rb_wptrXXX_base001	2002Sep23
PASS		
0	cp_rb_wptrXXX_base010	2002Sep23
PASS		
0	cp_rb_wptrXXX_base011	2002Sep23
PASS		
0	cp_rb_wptrXXX_base100	2002Sep23
PASS		
0	cp_rb_wptrXXX_base101	2002Sep23
PASS		
0	cp_rb_wptrXXX_base110	2002Sep23
PASS		
0	cp_rb_wptrXXX_base111	2002Sep23

PASS		
0	cp_reg_rw_test	2002Sep23
PASS		
0	cp_register_reads	2002Sep23
PASS		
0	cp_rt_protection_mode	2002Sep23
PASS		
0	cp_scratch	2002Sep23
PASS		
0	cp_scratch_multi	2002Sep23
PASS		
0	csf_reg_rd_default	2002Sep23
PASS		
0	csf_reg_wr_rd	2002Sep23
PASS		
0	dma_cp_sync	2002Sep23
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep23
PASS		
0	dma_dscrstr_flush1	2002Sep23
PASS		
0	dma_dscrstr_flush2	2002Sep23
PASS		
0	dma_dscrstr_flush3	2002Sep23
PASS		
0	dma_m2m_src00_dstXX	2002Sep23
PASS		
0	dma_m2m_src01_dstXX	2002Sep23
PASS		
0	dma_m2m_src10_dstXX	2002Sep23
PASS		
0	dma_m2m_src11_dstXX	2002Sep23
PASS		
0	dma_mem2mem	2002Sep23
PASS		
0	dma_mem2mem_abort	2002Sep23
PASS		
0	dma_mem2mem_alignments	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep23

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep23
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep23
PASS		
0	dma_mem2mem_pio_abort	2002Sep23
PASS		
0	dma_mem2mem_random	2002Sep23
PASS		
0	dma_mem2reg	2002Sep23
PASS		
0	dma_mem2reg_abort	2002Sep23
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep23
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep23
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep23
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep23
PASS		
0	dma_mem2reg_sync	2002Sep23
PASS		
0	dma_r2r_m2m_r2r	2002Sep23
PASS		
0	dma_r2r_m2r_r2r	2002Sep23
PASS		
0	dma_r2r_r2m_r2r	2002Sep23
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep23
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep23
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep23
PASS		
0	dma_reg2mem	2002Sep23
PASS		
0	dma_reg2mem_abort	2002Sep23
PASS		
0	dma_reg2reg	2002Sep23
PASS		
0	dma_reg2reg_2dw	2002Sep23
PASS		
0	dma_reg2reg_daic	2002Sep23
PASS		
0	dma_reg2reg_mult	2002Sep23

PASS		
0	dma_reg2reg_mult2	2002Sep23
PASS		
0	dma_reg2reg_mult3	2002Sep23
PASS		
0	dma_reg2reg_saic	2002Sep23
PASS		
0	dma_reg2reg_sdaic	2002Sep23
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep23
PASS		
0	dma_reg2reg_stress	2002Sep23
PASS		
0	draw_indx_a	2002Sep23
PASS		
0	draw_indx_b	2002Sep23
PASS		
0	draw_indx_c	2002Sep23
PASS		
0	draw_indx_indr1_a	2002Sep23
PASS		
0	draw_indx_indr1_b	2002Sep23
PASS		
0	draw_indx_indr1_short_a	2002Sep23
PASS		
0	draw_indx_indr1_short_b	2002Sep23
PASS		
0	draw_indx_indr1_stress	2002Sep23
PASS		
0	draw_indx_indr2_a	2002Sep23
PASS		
0	draw_indx_indr2_b	2002Sep23
PASS		
0	draw_indx_indr2_short_a	2002Sep23
PASS		
0	draw_indx_indr2_short_b	2002Sep23
PASS		
0	draw_indx_indr2_stress	2002Sep23
PASS		
0	draw_indx_ring1	2002Sep23
PASS		
0	draw_indx_ring2	2002Sep23
PASS		
0	draw_indx_ring_short1	2002Sep23
PASS		
0	draw_indx_ring_short2	2002Sep23
PASS		
0	draw_indx_ring_stress	2002Sep23

PASS		
0	draw_indx_short_a	2002Sep23
PASS		
0	draw_indx_short_b	2002Sep23
PASS		
0	draw_indx_short_c	2002Sep23
PASS		
0	draw_indx_stress_a	2002Sep23
PASS		
0	draw_indx_stress_b	2002Sep23
PASS		
0	draw_indx_stress_c	2002Sep23
PASS		
0	event_timestamp_write	2002Sep23
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep23
PASS		
0	ft_indr1_july22	2002Sep23
PASS		
0	ft_indr2_july22	2002Sep23
PASS		
0	ft_july22	2002Sep23
PASS		
0	ft_july22_random	2002Sep23
PASS		
0	header_dump	2002Sep23
PASS		
0	header_dump_rt	2002Sep23
PASS		
0	header_dump_small	2002Sep23
PASS		
0	hostdata_blt	2002Sep23
PASS		
0	im_load_a	2002Sep23
PASS		
0	im_load_b	2002Sep23
PASS		
0	im_load_cid_mismatch	2002Sep23
PASS		
0	im_load_immediate	2002Sep23
PASS		
0	im_load_immediate_nrt_me	2002Sep23
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep23
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep23
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep23

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep23
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep23
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep23
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep23
PASS		
0	im_load_indr1_a	2002Sep23
PASS		
0	im_load_indr1_b	2002Sep23
PASS		
0	im_load_indr2_a	2002Sep23
PASS		
0	im_load_indr2_b	2002Sep23
PASS		
0	im_load_invalidate	2002Sep23
PASS		
0	im_load_match	2002Sep23
PASS		
0	im_load_match2	2002Sep23
PASS		
0	im_load_nrt_me	2002Sep23
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep23
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep23
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep23
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep23
PASS		
0	im_load_pfmatch_disable	2002Sep23
PASS		
0	im_load_ring_a	2002Sep23
PASS		
0	im_load_ring_b	2002Sep23
PASS		
0	im_load_rt	2002Sep23
PASS		
233	im_load_rt2	2002Sep23
FAIL	cp_mh_req.cmp	
234	im_load_rt3	2002Sep23
FAIL	cp_mh_req.cmp	
235	im_load_rt4	2002Sep23
FAIL	cp_mh_req.cmp	
0	im_load_sz_mismatch	2002Sep23

PASS		
0	incremental_update_const	2002Sep23
PASS		
0	incremental_update_instr	2002Sep23
PASS		
0	incremental_update_state	2002Sep23
PASS		
0	indirect_buffer_a	2002Sep23
PASS		
0	indirect_buffer_b	2002Sep23
PASS		
0	indirect_buffer_c	2002Sep23
PASS		
0	indirect_buffer_d	2002Sep23
PASS		
0	indirect_buffer_e	2002Sep23
PASS		
0	indirect_buffer_f	2002Sep23
PASS		
0	indirect_buffer_g	2002Sep23
PASS		
0	indirect_buffer_h	2002Sep23
PASS		
0	indirect_buffer_i	2002Sep23
PASS		
0	indirect_buffer_j	2002Sep23
PASS		
0	indirect_buffer_k	2002Sep23
PASS		
0	indirect_buffer_l	2002Sep23
PASS		
0	indirect_buffer_mp_a	2002Sep23
PASS		
0	indirect_buffer_mp_b	2002Sep23
PASS		
0	indirect_buffer_mp_c	2002Sep23
PASS		
0	indirect_buffer_mp_d	2002Sep23
PASS		
0	indirect_buffer_mp_e	2002Sep23
PASS		
0	indirect_buffer_mp_f	2002Sep23
PASS		
258	invalid_opcode	2002Sep23
INCOMPLETE		
0	invalidate_on_me_init	2002Sep23
PASS		
0	invalidate_state_constants	2002Sep23

PASS		
0	invalidate_state_shaders	2002Sep23
PASS		
0	invalidate_state_shaders2	2002Sep23
PASS		
0	invalidate_state_sublks	2002Sep23
PASS		
0	invalidate_state_sublks2	2002Sep23
PASS		
0	iq1_iq2_mult	2002Sep23
PASS		
0	iq1_iq2_push_wrap	2002Sep23
PASS		
0	iq2_csq_full	2002Sep23
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep23
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep23
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep23
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep23
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep23
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep23
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep23
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep23
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep23
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep23
PASS		
0	ld_cnst_cntx_match	2002Sep23
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep23
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep23
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep23
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep23
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep23
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep23

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep23
PASS		
0	ld_cnst_cntx_replicate8	2002Sep23
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep23
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep23
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep23
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep23
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep23
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep23
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep23
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep23
PASS		
0	load_constant_context_rt	2002Sep23
PASS		
0	load_palette	2002Sep23
PASS		
0	lp_2d3d	2002Sep23
PASS		
0	mem_write	2002Sep23
PASS		
0	mem_write_rt	2002Sep23
PASS		
0	mpeg_index	2002Sep23
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0	mpeg_index_multi_context_max_context_0	2002Sep23
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep23
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0	mpeg_index_multi_context_max_context_2	2002Sep23
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0	mpeg_index_multi_context_max_context_3	2002Sep23
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep23
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep23
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0	mpeg_index_multi_context_max_context_6	2002Sep23
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep23

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0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep23
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0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep23
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0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep23
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0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep23
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep23
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep23
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0	nop_eating_disabled	2002Sep23
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0	paint_multi	2002Sep23
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0	paint_packet	2002Sep23
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0	paint_packet_gui_control	2002Sep23
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0	pitch_clip	2002Sep23
PASS		
0	ply_nextscan	2002Sep23
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0	rbbm_WU_2D_idle_01	2002Sep23
PASS		
0	rbbm_WU_2D_idle_02	2002Sep23
PASS		
0	rbbm_WU_2D_idle_03	2002Sep23
PASS		
0	rbbm_WU_2D_idle_04	2002Sep23
PASS		
0	rbbm_WU_2D_idle_05	2002Sep23
PASS		
0	rbbm_WU_2D_idle_06	2002Sep23
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0	rbbm_WU_2D_idle_07	2002Sep23
PASS		
0	rbbm_WU_2D_idle_08	2002Sep23
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0	rbbm_WU_2D_idle_09	2002Sep23
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0	rbbm_WU_2D_idle_clean_01	2002Sep23
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep23

PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep23
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep23
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep23
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep23
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0	rbbm_WU_2D_idle_clean_07	2002Sep23
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0	rbbm_WU_2D_idle_clean_08	2002Sep23
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0	rbbm_WU_2D_idle_clean_09	2002Sep23
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0	rbbm_WU_2D_idle_clean_12	2002Sep23
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0	rbbm_WU_3D_idle_01	2002Sep23
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0	rbbm_WU_3D_idle_02	2002Sep23
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0	rbbm_WU_3D_idle_03	2002Sep23
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0	rbbm_WU_3D_idle_09	2002Sep23
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0	rbbm_WU_3D_idle_clean_01	2002Sep23
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep23
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep23
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep23
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep23

PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep23
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0	rbbm_WU_3D_idle_clean_07	2002Sep23
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0	rbbm_WU_3D_idle_clean_08	2002Sep23
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0	rbbm_WU_3D_idle_clean_09	2002Sep23
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0	rbbm_WU_3D_idle_clean_10	2002Sep23
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep23
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep23
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0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep23
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep23
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep23
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep23
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep23
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep23
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep23
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep23
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep23
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep23
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep23
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep23
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep23
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep23
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep23
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep23
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep23

PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep23
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep23
PASS		
0	rbbm_WU_idct_semaphore	2002Sep23
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep23
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep23
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep23
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0	rbbm_WUrt_CP_dma_idle	2002Sep23
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0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep23
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep23
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep23
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0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep23
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep23
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep23
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0	rbbm_WUrt_RT_idle_02	2002Sep23
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0	rbbm_WUrt_RT_idle_03	2002Sep23
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep23
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403	rbbm_WUrt_RT_idle_clean_01	2002Sep23
FAIL		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep23

PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep23
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0	rbbm_WUrt_RT_idle_clean_04	2002Sep23
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep23
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0	rbbm_WUrt_VIPH0_dma_idle	2002Sep23
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0	rbbm_WUrt_VIPH1_dma_idle	2002Sep23
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep23
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0	rbbm_WUrt_VIPH3_dma_idle	2002Sep23
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0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep23
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0	rbbm_clk_active_deassert_min_max	2002Sep23
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0	rbbm_clk_active_each_pipe	2002Sep23
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0	rbbm_clk_active_for_read_error	2002Sep23
PASS		
0	rbbm_clk_active_for_read_request	2002Sep23
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0	rbbm_clk_active_on_reset	2002Sep23
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0	rbbm_cp_non_queued_rdwr_01	2002Sep23
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep23
PASS		
420	rbbm_cp_queued_holds_nonqueued	2002Sep23
FAIL	cp_rbbm.cmp	
0	rbbm_cp_queued_rdwr	2002Sep23
PASS		
0	rbbm_int_ack	2002Sep23
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0	rbbm_int_each_client	2002Sep23
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0	rbbm_int_nrt_gfxpipe_idle	2002Sep23
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0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep23
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0	rbbm_int_rderr	2002Sep23
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0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep23
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0	rbbm_isync_cpscratch_idlegui	2002Sep23

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0	rbbm_isync_wait_idlegui	2002Sep23
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0	rbbm_soft_reset	2002Sep23
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0	reg_to_mem	2002Sep23
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0	reg_to_mem_rt	2002Sep23
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0	rop_cfd_wmd	2002Sep23
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0	rstr_ordr	2002Sep23
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0	rt_invalid_opcode	2002Sep23
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0	rtee_arm_wait_idle_trigger_events_16	2002Sep23
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0	rtee_discretes_arm_rearm_events_32	2002Sep23
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0	rtee_discretes_falling_01_events_16	2002Sep23
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0	rtee_discretes_falling_02_events_16	2002Sep23
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0	rtee_discretes_falling_14_events_16	2002Sep23

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0	rtee_discretes_falling_31_events_16	2002Sep23
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0	rtee_discretes_invert_00_events_16	2002Sep23
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0	rtee_discretes_invert_01_events_16	2002Sep23
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0	rtee_discretes_invert_02_events_16	2002Sep23
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0	rtee_discretes_invert_03_events_16	2002Sep23
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0	rtee_discretes_level_31_events_16	2002Sep23
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0	rtee_discretes_manual_disarm_events_16	2002Sep23
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0	rtee_discretes_rising_31_events_16	2002Sep23
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0	rtee_display1_comp_events_16	2002Sep23
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0	rtee_display2_comp_events_16	2002Sep23
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0	rtee_mem_linear_search_events_2	2002Sep23
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0	rtee_mem_poll_comp_000_events_16	2002Sep23
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0	rtee_mem_poll_comp_001_events_16	2002Sep23
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0	rtee_mem_poll_comp_010_events_16	2002Sep23

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0	rtee_mem_poll_comp_011_events_16	2002Sep23
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0	rtee_mem_poll_comp_100_events_16	2002Sep23
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0	rtee_mem_poll_comp_101_events_16	2002Sep23
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0	rtee_mem_poll_comp_110_events_16	2002Sep23
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0	rtee_mem_poll_comp_111_events_16	2002Sep23
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0	rtee_mem_poll_comp_masks_events_16	2002Sep23
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0	rtee_mem_poll_comp_masks_events_16_random	2002Sep23
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0	rtee_mem_poll_comp_swaps_events_16	2002Sep23
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0	rtee_mem_poll_intervals_events_16	2002Sep23
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0	rtee_prefetch_parse_nops	2002Sep23
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0	rtee_reg_linear_search_events_2	2002Sep23
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0	rtee_reg_poll_comp_000_events_16	2002Sep23
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0	rtee_reg_poll_comp_001_events_16	2002Sep23
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0	rtee_reg_poll_comp_010_events_16	2002Sep23
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0	rtee_reg_poll_comp_011_events_16	2002Sep23
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0	rtee_reg_poll_comp_100_events_16	2002Sep23
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0	rtee_reg_poll_comp_101_events_16	2002Sep23
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0	rtee_reg_poll_comp_110_events_16	2002Sep23
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0	rtee_reg_poll_comp_111_events_16	2002Sep23
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0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep23
PASS		
0	rtee_vip_comp_events_16	2002Sep23
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0	scratch_mem_read	2002Sep23
PASS		
0	scratch_mem_read2	2002Sep23
PASS		
0	set_constant	2002Sep23

PASS		
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PASS		
0	set_constant_alu_max1	2002Sep23
PASS		
0	set_constant_alu_max2	2002Sep23
PASS		
0	set_constant_bool_max1	2002Sep23
PASS		
0	set_constant_bool_max2	2002Sep23
PASS		
0	set_constant_indr1_short	2002Sep23
PASS		
0	set_constant_indr1_short2	2002Sep23
PASS		
0	set_constant_indr1_write_enable	2002Sep23
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep23
PASS		
0	set_constant_indr2_short	2002Sep23
PASS		
0	set_constant_indr2_short2	2002Sep23
PASS		
0	set_constant_indr2_write_enable	2002Sep23
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep23
PASS		
0	set_constant_loop_max1	2002Sep23
PASS		
0	set_constant_loop_max2	2002Sep23
PASS		
0	set_constant_ring_short	2002Sep23
PASS		
0	set_constant_ring_short2	2002Sep23
PASS		
0	set_constant_ring_write_enable	2002Sep23
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep23
PASS		
0	set_constant_short	2002Sep23
PASS		
0	set_constant_short2	2002Sep23
PASS		
0	set_constant_tex_max1	2002Sep23
PASS		
0	set_constant_tex_max2	2002Sep23
PASS		
0	set_constant_write_enable	2002Sep23

PASS		
0	set_constant_write_enable_lcc	2002Sep23
PASS		
0	set_constant_write_enable_lcc2	2002Sep23
PASS		
0	set_scissors	2002Sep23
PASS		
0	set_st_indrl_match	2002Sep23
PASS		
0	set_st_indrl_mismatch	2002Sep23
PASS		
0	set_st_indrl_mismatch2	2002Sep23
PASS		
0	set_st_indrl_mismatch3	2002Sep23
PASS		
0	set_st_indrl_mismatch4	2002Sep23
PASS		
0	set_st_indrl_ps	2002Sep23
PASS		
0	set_st_indrl_ps_only	2002Sep23
PASS		
0	set_st_indrl_sublk0	2002Sep23
PASS		
0	set_st_indrl_sublk1	2002Sep23
PASS		
0	set_st_indrl_sublk1_only	2002Sep23
PASS		
0	set_st_indrl_sublk2	2002Sep23
PASS		
0	set_st_indrl_sublk2_only	2002Sep23
PASS		
0	set_st_indrl_sublk3	2002Sep23
PASS		
0	set_st_indrl_sublk3_only	2002Sep23
PASS		
0	set_st_indrl_sublk4	2002Sep23
PASS		
0	set_st_indrl_sublk4_only	2002Sep23
PASS		
0	set_st_indrl_sublk5	2002Sep23
PASS		
0	set_st_indrl_sublk5_only	2002Sep23
PASS		
0	set_st_indrl_sublk6	2002Sep23
PASS		
0	set_st_indrl_sublk6_only	2002Sep23
PASS		
0	set_st_indrl_sublk7	2002Sep23

PASS		
0	set_st_indr1_sublk7_only	2002Sep23
PASS		
0	set_st_indr1_vs	2002Sep23
PASS		
0	set_st_indr1_vs_only	2002Sep23
PASS		
0	set_st_indr2_match	2002Sep23
PASS		
0	set_st_indr2_mismatch	2002Sep23
PASS		
0	set_st_indr2_mismatch2	2002Sep23
PASS		
0	set_st_indr2_mismatch3	2002Sep23
PASS		
0	set_st_indr2_mismatch4	2002Sep23
PASS		
0	set_st_indr2_ps	2002Sep23
PASS		
0	set_st_indr2_ps_only	2002Sep23
PASS		
0	set_st_indr2_sublk0	2002Sep23
PASS		
0	set_st_indr2_sublk1	2002Sep23
PASS		
0	set_st_indr2_sublk1_only	2002Sep23
PASS		
0	set_st_indr2_sublk2	2002Sep23
PASS		
0	set_st_indr2_sublk2_only	2002Sep23
PASS		
0	set_st_indr2_sublk3	2002Sep23
PASS		
0	set_st_indr2_sublk3_only	2002Sep23
PASS		
0	set_st_indr2_sublk4	2002Sep23
PASS		
0	set_st_indr2_sublk4_only	2002Sep23
PASS		
0	set_st_indr2_sublk5	2002Sep23
PASS		
0	set_st_indr2_sublk5_only	2002Sep23
PASS		
0	set_st_indr2_sublk6	2002Sep23
PASS		
0	set_st_indr2_sublk6_only	2002Sep23
PASS		
0	set_st_indr2_sublk7	2002Sep23

PASS		
0	set_st_indr2_sublk7_only	2002Sep23
PASS		
0	set_st_indr2_vs	2002Sep23
PASS		
0	set_st_indr2_vs_only	2002Sep23
PASS		
0	set_st_pfmatch_disabled	2002Sep23
PASS		
0	set_st_ring_match	2002Sep23
PASS		
0	set_st_ring_mismatch	2002Sep23
PASS		
0	set_st_ring_mismatch2	2002Sep23
PASS		
0	set_st_ring_mismatch3	2002Sep23
PASS		
0	set_st_ring_mismatch4	2002Sep23
PASS		
0	set_st_ring_ps	2002Sep23
PASS		
0	set_st_ring_ps_only	2002Sep23
PASS		
0	set_st_ring_sublk0	2002Sep23
PASS		
0	set_st_ring_sublk1	2002Sep23
PASS		
0	set_st_ring_sublk1_only	2002Sep23
PASS		
0	set_st_ring_sublk2	2002Sep23
PASS		
0	set_st_ring_sublk2_only	2002Sep23
PASS		
0	set_st_ring_sublk3	2002Sep23
PASS		
0	set_st_ring_sublk3_only	2002Sep23
PASS		
0	set_st_ring_sublk4	2002Sep23
PASS		
0	set_st_ring_sublk4_only	2002Sep23
PASS		
0	set_st_ring_sublk5	2002Sep23
PASS		
0	set_st_ring_sublk5_only	2002Sep23
PASS		
0	set_st_ring_sublk6	2002Sep23
PASS		
0	set_st_ring_sublk6_only	2002Sep23

PASS		
0	set_st_ring_sublk7	2002Sep23
PASS		
0	set_st_ring_sublk7_only	2002Sep23
PASS		
0	set_st_ring_vs	2002Sep23
PASS		
0	set_st_ring_vs_only	2002Sep23
PASS		
0	set_st_stress_a	2002Sep23
PASS		
0	set_st_stress_b	2002Sep23
PASS		
0	set_st_stress_c	2002Sep23
PASS		
0	set_st_stress_d	2002Sep23
PASS		
0	set_st_stress_e	2002Sep23
PASS		
0	set_st_stress_f	2002Sep23
PASS		
703	set_state_rt_a	2002Sep23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
704	set_state_rt_b	2002Sep23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
705	set_state_rt_c	2002Sep23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
706	set_state_rt_d	2002Sep23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep23
PASS		
0	src_clip	2002Sep23
PASS		
0	srf_cohr	2002Sep23
PASS		
0	trans_bitblt	2002Sep23
PASS		
0	type0_2long_pckts	2002Sep23
PASS		
0	type0_3indr_buffs	2002Sep23
PASS		
0	type0_4pckts	2002Sep23
PASS		
0	type0_incr_updt_cnst	2002Sep23
PASS		
0	type0_incr_updt_instrmem	2002Sep23
PASS		
0	type0_incr_updt_st	2002Sep23

PASS		
0	type0_incr_updt_stress	2002Sep23
PASS		
0	type0_ind1_ind2_a	2002Sep23
PASS		
0	type0_ind1_ind2_b	2002Sep23
PASS		
0	type0_indr_buff	2002Sep23
PASS		
0	type1_4pckts	2002Sep23
PASS		
0	type1_indr_buff	2002Sep23
PASS		
0	type1_pckt	2002Sep23
PASS		
0	type_random	2002Sep23
PASS		
725	uram_wrt_rt_ucose	2002Sep23
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
726	uram_wrt_ucose	2002Sep23
FAIL	rbbmWr.cmp	
727	viz_query_a	2002Sep23
FAIL	rbbmWr.cmp	
728	viz_query_draw_indx	2002Sep23
FAIL	rbbmWr.cmp	
729	viz_query_draw_indx_indr1	2002Sep23
FAIL	rbbmWr.cmp	
730	viz_query_draw_indx_indr2	2002Sep23
FAIL	rbbmWr.cmp	
731	viz_query_draw_indx_multipass	2002Sep23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
732	viz_query_draw_indx_ring	2002Sep23
FAIL	rbbmWr.cmp	
733	viz_query_indr1	2002Sep23
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep23
PASS		
735	viz_query_indr2	2002Sep23
FAIL	rbbmWr.cmp	
0	viz_query_indr2_simple	2002Sep23
PASS		
737	viz_query_ring	2002Sep23
FAIL	rbbmWr.cmp	
0	viz_query_ring_simple	2002Sep23
PASS		
0	wait_for_idle	2002Sep23
PASS		
0	wait_reg_mem_poll_mem0	2002Sep23

PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep23
PASS
0 wait_reg_mem_poll_mem1 2002Sep23
PASS
0 wait_reg_mem_poll_mem2 2002Sep23
PASS
0 wait_reg_mem_poll_mem3 2002Sep23
PASS
0 wait_reg_mem_poll_reg0 2002Sep23
PASS
0 wait_reg_mem_poll_reg1 2002Sep23
PASS
0 wait_reg_mem_poll_reg2 2002Sep23
PASS
0 wait_reg_mem_poll_reg3 2002Sep23
PASS

=====
Tests_Run_count = 748 Pass_count = 724 Fail_count = 23 Incomplete_count = 1
=====

=====
Regression Report For CP - Project = r400 Created on Sat Aug 24 5:26:30 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

0	2d_indr_buffer_a		2002Aug23
PASS			
0	2d_indr_buffer_b		2002Aug23
PASS			
0	2d_indr_buffer_c		2002Aug23
PASS			
0	2d_indr_buffer_d		2002Aug23
PASS			
0	2d_indr_buffer_e		2002Aug23
PASS			
0	2d_indr_buffer_f		2002Aug23
PASS			
0	2d_indr_buffer_g		2002Aug23
PASS			
0	3d_draw_indx_2		2002Aug23
PASS			
0	3d_draw_indx_2_b		2002Aug23
PASS			
0	bitblt_multi		2002Aug23
PASS			
0	brush_32x1mono		2002Aug23
PASS			
0	brush_8x8color_16		2002Aug23
PASS			
0	brush_8x8color_32		2002Aug23
PASS			
0	brush_8x8color_64		2002Aug23
PASS			
0	brush_8x8color_dst_0_1		2002Aug23
PASS			
0	brush_8x8mono		2002Aug23
PASS			
0	brush_color_mono_stress		2002Aug23
PASS			
0	brush_flag		2002Aug23
PASS			
0	brush_types_13_14		2002Aug23
PASS			
0	clear_const_wr_en_on_me_init		2002Aug23

PASS		
0	clear_const_wr_en_with_lcc	2002Aug23
PASS		
0	cond_write_mem_poll_mem	2002Aug23
PASS		
0	cond_write_mem_poll_mem2	2002Aug23
PASS		
0	cond_write_mem_poll_mem3	2002Aug23
PASS		
0	cond_write_mem_poll_mem_rt	2002Aug23
PASS		
0	cond_write_mem_poll_reg	2002Aug23
PASS		
0	cond_write_mem_poll_reg2	2002Aug23
PASS		
0	cond_write_mem_poll_reg3	2002Aug23
PASS		
0	cp_acpi_recovery_soft_reset	2002Aug23
PASS		
0	cp_bios_scratch	2002Aug23
PASS		
0	cp_ib2_base000_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug23
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug23
PASS		
0	cp_ib_base000_bufszXXX	2002Aug23
PASS		
0	cp_ib_base001_bufszXXX	2002Aug23
PASS		
0	cp_ib_base010_bufszXXX	2002Aug23
PASS		
0	cp_ib_base011_bufszXXX	2002Aug23
PASS		
0	cp_ib_base100_bufszXXX	2002Aug23
PASS		
0	cp_ib_base101_bufszXXX	2002Aug23

PASS		
0	cp_ib_base110_bufszXXX	2002Aug23
PASS		
0	cp_ib_base111_bufszXXX	2002Aug23
PASS		
0	cp_interrupt	2002Aug23
PASS		
0	cp_interrupt_rt_events_16	2002Aug23
PASS		
0	cp_iq2_alignment_000	2002Aug23
PASS		
0	cp_iq2_alignment_001	2002Aug23
PASS		
0	cp_iq2_alignment_010	2002Aug23
PASS		
0	cp_iq2_alignment_011	2002Aug23
PASS		
0	cp_iq2_alignment_100	2002Aug23
PASS		
0	cp_iq2_alignment_101	2002Aug23
PASS		
0	cp_iq2_alignment_110	2002Aug23
PASS		
0	cp_iq2_alignment_111	2002Aug23
PASS		
0	cp_iq_alignment_000	2002Aug23
PASS		
0	cp_iq_alignment_001	2002Aug23
PASS		
0	cp_iq_alignment_010	2002Aug23
PASS		
0	cp_iq_alignment_011	2002Aug23
PASS		
0	cp_iq_alignment_100	2002Aug23
PASS		
0	cp_iq_alignment_101	2002Aug23
PASS		
0	cp_iq_alignment_110	2002Aug23
PASS		
0	cp_iq_alignment_111	2002Aug23
PASS		
0	cp_max_size_packet	2002Aug23
PASS		
0	cp_nrt_rt_protection_mode	2002Aug23
PASS		
0	cp_output_stress	2002Aug23
PASS		
0	cp_protection_mode	2002Aug23

PASS		
0	cp_rb_alignment_000	2002Aug23
PASS		
0	cp_rb_alignment_001	2002Aug23
PASS		
0	cp_rb_alignment_010	2002Aug23
PASS		
0	cp_rb_alignment_011	2002Aug23
PASS		
0	cp_rb_alignment_100	2002Aug23
PASS		
0	cp_rb_alignment_101	2002Aug23
PASS		
0	cp_rb_alignment_110	2002Aug23
PASS		
0	cp_rb_alignment_111	2002Aug23
PASS		
0	cp_rb_wptrXXX_base000	2002Aug23
PASS		
0	cp_rb_wptrXXX_base001	2002Aug23
PASS		
0	cp_rb_wptrXXX_base010	2002Aug23
PASS		
0	cp_rb_wptrXXX_base011	2002Aug23
PASS		
0	cp_rb_wptrXXX_base100	2002Aug23
PASS		
0	cp_rb_wptrXXX_base101	2002Aug23
PASS		
0	cp_rb_wptrXXX_base110	2002Aug23
PASS		
0	cp_rb_wptrXXX_base111	2002Aug23
PASS		
0	cp_register_reads	2002Aug23
PASS		
0	cp_reg_rw_test	2002Aug23
PASS		
0	cp_rt_protection_mode	2002Aug23
PASS		
0	cp_scratch	2002Aug23
PASS		
0	cp_scratch_multi	2002Aug23
PASS		
0	dma_cp_sync	2002Aug23
PASS		
0	dma_dscrstr_chckn_bit	2002Aug23
PASS		
0	dma_dscrstr_flush1	2002Aug23

PASS		
0	dma_dscrstr_flush2	2002Aug23
PASS		
0	dma_dscrstr_flush3	2002Aug23
PASS		
0	dma_m2m_src00_dstXX	2002Aug23
PASS		
0	dma_m2m_src01_dstXX	2002Aug23
PASS		
0	dma_m2m_src10_dstXX	2002Aug23
PASS		
0	dma_m2m_src11_dstXX	2002Aug23
PASS		
0	dma_mem2mem	2002Aug23
PASS		
0	dma_mem2mem_abort	2002Aug23
PASS		
0	dma_mem2mem_alignments	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug23
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug23
PASS		
0	dma_mem2mem_pio_abort	2002Aug23
PASS		
0	dma_mem2reg	2002Aug23
PASS		
0	dma_mem2reg_abort	2002Aug23
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug23
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug23
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug23
PASS		
0	dma_mem2reg_sync	2002Aug23

PASS		
0	dma_r2r_m2m_r2r	2002Aug23
PASS		
0	dma_r2r_m2r_r2r	2002Aug23
PASS		
0	dma_r2r_r2m_r2r	2002Aug23
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug23
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug23
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug23
PASS		
0	dma_reg2mem	2002Aug23
PASS		
0	dma_reg2mem_abort	2002Aug23
PASS		
0	dma_reg2reg	2002Aug23
PASS		
0	dma_reg2reg_2dw	2002Aug23
PASS		
0	dma_reg2reg_daic	2002Aug23
PASS		
0	dma_reg2reg_mult	2002Aug23
PASS		
0	dma_reg2reg_mult2	2002Aug23
PASS		
0	dma_reg2reg_mult3	2002Aug23
PASS		
0	dma_reg2reg_saic	2002Aug23
PASS		
0	dma_reg2reg_sdaic	2002Aug23
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug23
PASS		
0	dma_reg2reg_stress	2002Aug23
PASS		
0	draw_indx_a	2002Aug23
PASS		
0	draw_indx_b	2002Aug23
PASS		
0	draw_indx_c	2002Aug23
PASS		
0	draw_indx_indr1_a	2002Aug23
PASS		
0	draw_indx_indr1_b	2002Aug23
PASS		
0	draw_indx_indr1_short_a	2002Aug23

PASS		
0	draw_indx_indr1_short_b	2002Aug23
PASS		
0	draw_indx_indr1_stress	2002Aug23
PASS		
0	draw_indx_indr2_a	2002Aug23
PASS		
0	draw_indx_indr2_b	2002Aug23
PASS		
0	draw_indx_indr2_short_a	2002Aug23
PASS		
0	draw_indx_indr2_short_b	2002Aug23
PASS		
0	draw_indx_indr2_stress	2002Aug23
PASS		
0	draw_indx_ring1	2002Aug23
PASS		
0	draw_indx_ring2	2002Aug23
PASS		
0	draw_indx_ring_short1	2002Aug23
PASS		
0	draw_indx_ring_short2	2002Aug23
PASS		
0	draw_indx_ring_stress	2002Aug23
PASS		
0	draw_indx_short_a	2002Aug23
PASS		
0	draw_indx_short_b	2002Aug23
PASS		
0	draw_indx_short_c	2002Aug23
PASS		
0	draw_indx_stress_a	2002Aug23
PASS		
0	draw_indx_stress_b	2002Aug23
PASS		
0	draw_indx_stress_c	2002Aug23
PASS		
0	event_timestamp_write	2002Aug23
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug23
PASS		
0	ft_indr1_july22	2002Aug23
PASS		
0	ft_indr2_july22	2002Aug23
PASS		
0	ft_july22	2002Aug23
PASS		
0	header_dump	2002Aug23

PASS		
0	header_dump_rt	2002Aug23
PASS		
0	header_dump_small	2002Aug23
PASS		
0	hostdata_blt	2002Aug23
PASS		
0	im_load_a	2002Aug23
PASS		
0	im_load_b	2002Aug23
PASS		
0	im_load_cid_mismatch	2002Aug23
PASS		
0	im_load_immediate	2002Aug23
PASS		
0	im_load_immediate_nrt_me	2002Aug23
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug23
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug23
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug23
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug23
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug23
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug23
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug23
PASS		
0	im_load_indr1_a	2002Aug23
PASS		
0	im_load_indr1_b	2002Aug23
PASS		
0	im_load_indr2_a	2002Aug23
PASS		
0	im_load_indr2_b	2002Aug23
PASS		
0	im_load_invalidate	2002Aug23
PASS		
0	im_load_match	2002Aug23
PASS		
0	im_load_match2	2002Aug23
PASS		
0	im_load_nrt_me	2002Aug23
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug23

PASS		
0	im_load_nrt_me_ps_wrap	2002Aug23
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug23
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug23
PASS		
0	im_load_pfmatch_disable	2002Aug23
PASS		
0	im_load_ring_a	2002Aug23
PASS		
0	im_load_ring_b	2002Aug23
PASS		
0	im_load_rt	2002Aug23
PASS		
0	im_load_rt2	2002Aug23
PASS		
0	im_load_rt3	2002Aug23
PASS		
0	im_load_rt4	2002Aug23
PASS		
0	im_load_sz_mismatch	2002Aug23
PASS		
0	incremental_update_const	2002Aug23
PASS		
0	incremental_update_instr	2002Aug23
PASS		
0	incremental_update_state	2002Aug23
PASS		
0	indirect_buffer_a	2002Aug23
PASS		
0	indirect_buffer_b	2002Aug23
PASS		
0	indirect_buffer_c	2002Aug23
PASS		
0	indirect_buffer_d	2002Aug23
PASS		
0	indirect_buffer_e	2002Aug23
PASS		
0	indirect_buffer_f	2002Aug23
PASS		
0	indirect_buffer_g	2002Aug23
PASS		
0	indirect_buffer_h	2002Aug23
PASS		
0	indirect_buffer_i	2002Aug23
PASS		
0	indirect_buffer_j	2002Aug23

PASS		
0	indirect_buffer_k	2002Aug23
PASS		
0	indirect_buffer_l	2002Aug23
PASS		
0	indirect_buffer_mp_a	2002Aug23
PASS		
0	indirect_buffer_mp_b	2002Aug23
PASS		
0	indirect_buffer_mp_c	2002Aug23
PASS		
0	indirect_buffer_mp_d	2002Aug23
PASS		
0	indirect_buffer_mp_e	2002Aug23
PASS		
0	indirect_buffer_mp_f	2002Aug23
PASS		
0	invalidate_on_me_init	2002Aug23
PASS		
0	invalidate_state_constants	2002Aug23
PASS		
0	invalidate_state_shaders	2002Aug23
PASS		
0	invalidate_state_shaders2	2002Aug23
PASS		
0	invalidate_state_sublks	2002Aug23
PASS		
0	invalidate_state_sublks2	2002Aug23
PASS		
0	invalid_opcode	2002Aug23
PASS		
0	iq1_iq2_mult	2002Aug23
PASS		
0	iq1_iq2_push_wrap	2002Aug23
PASS		
0	iq2_csq_full	2002Aug23
PASS		
0	IQ2_Mult	2002Aug23
PASS		
0	IQ2_Wrap_Legacy	2002Aug23
PASS		
0	IQ_Wrap	2002Aug23
PASS		
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0	ld_cnst_cntx_indr1_simple	2002Aug23
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0	ld_cnst_cntx_indr2_replicate	2002Aug23
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0	ld_cnst_cntx_indr2_replicate8	2002Aug23
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0	ld_cnst_cntx_indr2_simple	2002Aug23
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0	ld_cnst_cntx_mismatch_alutex	2002Aug23
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0	ld_cnst_cntx_mismatch_base	2002Aug23
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0	ld_cnst_cntx_mismatch_force	2002Aug23
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0	ld_cnst_cntx_pfmatch_disable	2002Aug23
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0	ld_cnst_cntx_replicate4_alu	2002Aug23
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0	ld_cnst_cntx_replicate4_tex	2002Aug23
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0	ld_cnst_cntx_replicate8	2002Aug23
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0	ld_cnst_cntx_replicate_alu	2002Aug23
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0	ld_cnst_cntx_replicate_tex	2002Aug23
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0	ld_cnst_cntx_ring_replicate	2002Aug23
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0	ld_cnst_cntx_ring_replicate8	2002Aug23
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0	ld_cnst_cntx_ring_simple	2002Aug23
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0	ld_cnst_cntx_simple_tex	2002Aug23

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0	load_palette	2002Aug23
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0	mpeg_index_multi_context_max_context_5	2002Aug23
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0	mpeg_index_multi_context_max_context_6	2002Aug23
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0	mpeg_index_multi_context_max_context_7	2002Aug23
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0	rbbm_WU_cmdfifo_entries	2002Aug23
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0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug23
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0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug23
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0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug23
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0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug23
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug23
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0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug23
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0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug23
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0	rbbm_WU_MH_hdp_clean	2002Aug23
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0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug23
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0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug23
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0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Aug23
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0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Aug23
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0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Aug23
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0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Aug23
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0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Aug23
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0	rtee_mem_poll_comp_000_events_16	2002Aug23
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0	rtee_mem_poll_comp_001_events_16	2002Aug23
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0	rtee_mem_poll_comp_010_events_16	2002Aug23
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0	rtee_mem_poll_comp_011_events_16	2002Aug23
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0	rtee_mem_poll_comp_110_events_16	2002Aug23

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0	rtee_mem_poll_comp_masks_events_16	2002Aug23
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0	rtee_mem_poll_comp_swaps_events_16	2002Aug23
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0	rtee_mem_poll_intervals_events_16	2002Aug23
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0	scratch_mem_read2	2002Aug23
PASS		
0	set_constant	2002Aug23
PASS		
0	set_constant2	2002Aug23
PASS		
0	set_constant_alu_max1	2002Aug23
PASS		
0	set_constant_alu_max2	2002Aug23
PASS		
0	set_constant_bool_max1	2002Aug23

PASS		
0	set_constant_bool_max2	2002Aug23
PASS		
0	set_constant_indr1_short	2002Aug23
PASS		
0	set_constant_indr1_short2	2002Aug23
PASS		
0	set_constant_indr1_write_enable	2002Aug23
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug23
PASS		
0	set_constant_indr2_short	2002Aug23
PASS		
0	set_constant_indr2_short2	2002Aug23
PASS		
0	set_constant_indr2_write_enable	2002Aug23
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug23
PASS		
0	set_constant_loop_max1	2002Aug23
PASS		
0	set_constant_loop_max2	2002Aug23
PASS		
0	set_constant_ring_short	2002Aug23
PASS		
0	set_constant_ring_short2	2002Aug23
PASS		
0	set_constant_ring_write_enable	2002Aug23
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug23
PASS		
0	set_constant_short	2002Aug23
PASS		
0	set_constant_short2	2002Aug23
PASS		
0	set_constant_tex_max1	2002Aug23
PASS		
0	set_constant_tex_max2	2002Aug23
PASS		
0	set_constant_write_enable	2002Aug23
PASS		
0	set_constant_write_enable_lcc	2002Aug23
PASS		
0	set_constant_write_enable_lcc2	2002Aug23
PASS		
0	set_scissors	2002Aug23
PASS		
0	set_state_rt_a	2002Aug23

PASS		
0	set_state_rt_b	2002Aug23
PASS		
0	set_state_rt_c	2002Aug23
PASS		
0	set_state_rt_d	2002Aug23
PASS		
0	set_st_indrl_match	2002Aug23
PASS		
0	set_st_indrl_mismatch	2002Aug23
PASS		
0	set_st_indrl_mismatch2	2002Aug23
PASS		
0	set_st_indrl_mismatch3	2002Aug23
PASS		
0	set_st_indrl_mismatch4	2002Aug23
PASS		
0	set_st_indrl_ps	2002Aug23
PASS		
0	set_st_indrl_ps_only	2002Aug23
PASS		
0	set_st_indrl_sublk0	2002Aug23
PASS		
0	set_st_indrl_sublk1	2002Aug23
PASS		
0	set_st_indrl_sublk1_only	2002Aug23
PASS		
0	set_st_indrl_sublk2	2002Aug23
PASS		
0	set_st_indrl_sublk2_only	2002Aug23
PASS		
0	set_st_indrl_sublk3	2002Aug23
PASS		
0	set_st_indrl_sublk3_only	2002Aug23
PASS		
0	set_st_indrl_sublk4	2002Aug23
PASS		
0	set_st_indrl_sublk4_only	2002Aug23
PASS		
0	set_st_indrl_sublk5	2002Aug23
PASS		
0	set_st_indrl_sublk5_only	2002Aug23
PASS		
0	set_st_indrl_sublk6	2002Aug23
PASS		
0	set_st_indrl_sublk6_only	2002Aug23
PASS		
0	set_st_indrl_sublk7	2002Aug23

PASS		
0	set_st_indr1_sublk7_only	2002Aug23
PASS		
0	set_st_indr1_vs	2002Aug23
PASS		
0	set_st_indr1_vs_only	2002Aug23
PASS		
0	set_st_indr2_match	2002Aug23
PASS		
0	set_st_indr2_mismatch	2002Aug23
PASS		
0	set_st_indr2_mismatch2	2002Aug23
PASS		
0	set_st_indr2_mismatch3	2002Aug23
PASS		
0	set_st_indr2_mismatch4	2002Aug23
PASS		
0	set_st_indr2_ps	2002Aug23
PASS		
0	set_st_indr2_ps_only	2002Aug23
PASS		
0	set_st_indr2_sublk0	2002Aug23
PASS		
0	set_st_indr2_sublk1	2002Aug23
PASS		
0	set_st_indr2_sublk1_only	2002Aug23
PASS		
0	set_st_indr2_sublk2	2002Aug23
PASS		
0	set_st_indr2_sublk2_only	2002Aug23
PASS		
0	set_st_indr2_sublk3	2002Aug23
PASS		
0	set_st_indr2_sublk3_only	2002Aug23
PASS		
0	set_st_indr2_sublk4	2002Aug23
PASS		
0	set_st_indr2_sublk4_only	2002Aug23
PASS		
0	set_st_indr2_sublk5	2002Aug23
PASS		
0	set_st_indr2_sublk5_only	2002Aug23
PASS		
0	set_st_indr2_sublk6	2002Aug23
PASS		
0	set_st_indr2_sublk6_only	2002Aug23
PASS		
0	set_st_indr2_sublk7	2002Aug23

PASS		
0	set_st_indr2_sublk7_only	2002Aug23
PASS		
0	set_st_indr2_vs	2002Aug23
PASS		
0	set_st_indr2_vs_only	2002Aug23
PASS		
0	set_st_pfmatch_disabled	2002Aug23
PASS		
0	set_st_ring_match	2002Aug23
PASS		
0	set_st_ring_mismatch	2002Aug23
PASS		
0	set_st_ring_mismatch2	2002Aug23
PASS		
0	set_st_ring_mismatch3	2002Aug23
PASS		
0	set_st_ring_mismatch4	2002Aug23
PASS		
0	set_st_ring_ps	2002Aug23
PASS		
0	set_st_ring_ps_only	2002Aug23
PASS		
0	set_st_ring_sublk0	2002Aug23
PASS		
0	set_st_ring_sublk1	2002Aug23
PASS		
0	set_st_ring_sublk1_only	2002Aug23
PASS		
0	set_st_ring_sublk2	2002Aug23
PASS		
0	set_st_ring_sublk2_only	2002Aug23
PASS		
0	set_st_ring_sublk3	2002Aug23
PASS		
0	set_st_ring_sublk3_only	2002Aug23
PASS		
0	set_st_ring_sublk4	2002Aug23
PASS		
0	set_st_ring_sublk4_only	2002Aug23
PASS		
0	set_st_ring_sublk5	2002Aug23
PASS		
0	set_st_ring_sublk5_only	2002Aug23
PASS		
0	set_st_ring_sublk6	2002Aug23
PASS		
0	set_st_ring_sublk6_only	2002Aug23

PASS		
0	set_st_ring_sublk7	2002Aug23
PASS		
0	set_st_ring_sublk7_only	2002Aug23
PASS		
0	set_st_ring_vs	2002Aug23
PASS		
0	set_st_ring_vs_only	2002Aug23
PASS		
0	set_st_stress_a	2002Aug23
PASS		
0	set_st_stress_b	2002Aug23
PASS		
0	set_st_stress_c	2002Aug23
PASS		
0	set_st_stress_d	2002Aug23
PASS		
0	set_st_stress_e	2002Aug23
PASS		
0	set_st_stress_f	2002Aug23
PASS		
0	Small_Text_EM	2002Aug23
PASS		
0	smoke_nrt	2002Aug23
PASS		
0	src_clip	2002Aug23
PASS		
0	srf_cohr	2002Aug23
PASS		
0	trans_bitblt	2002Aug23
PASS		
0	type0_2long_pckts	2002Aug23
PASS		
0	type0_3indr_buffs	2002Aug23
PASS		
0	type0_4pckts	2002Aug23
PASS		
0	type0_incr_updt_cnst	2002Aug23
PASS		
0	type0_incr_updt_instrmem	2002Aug23
PASS		
0	type0_incr_updt_st	2002Aug23
PASS		
0	type0_incr_updt_stress	2002Aug23
PASS		
0	type0_ind1_ind2_a	2002Aug23
PASS		
0	type0_ind1_ind2_b	2002Aug23

PASS		
0	type0_indr_buff	2002Aug23
PASS		
0	Type0_IQ2_BM	2002Aug23
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug23
PASS		
0	Type0_IQ_BM	2002Aug23
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug23
PASS		
0	Type0_PQ_BM	2002Aug23
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug23
PASS		
0	typel_4pckts	2002Aug23
PASS		
0	typel_indr_buff	2002Aug23
PASS		
0	typel_pckt	2002Aug23
PASS		
0	Type3_Bitblt	2002Aug23
PASS		
0	Type3_Load_Palette	2002Aug23
PASS		
0	Type3_NOP	2002Aug23
PASS		
682	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
683	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
684	uram_wrt_rt_ucode	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_ucode	2002Aug23
PASS		
0	viz_query_a	2002Aug23
PASS		
687	viz_query_draw_indx	2002Aug23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
688	viz_query_draw_indx_indr1	2002Aug23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
689	viz_query_draw_indx_indr2	2002Aug23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
690	viz_query_draw_indx_multipass	2002Aug23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
691	viz_query_draw_indx_ring	2002Aug23
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_indr1	2002Aug23

```
PASS
0 viz_query_indr1_simple 2002Aug23
PASS
0 viz_query_indr2 2002Aug23
PASS
0 viz_query_indr2_simple 2002Aug23
PASS
0 viz_query_ring 2002Aug23
PASS
0 viz_query_ring_simple 2002Aug23
PASS
0 wait_for_idle 2002Aug23
PASS
0 wait_reg_mem_poll_mem0 2002Aug23
PASS
0 wait_reg_mem_poll_mem0_rt 2002Aug23
PASS
0 wait_reg_mem_poll_mem1 2002Aug23
PASS
0 wait_reg_mem_poll_mem2 2002Aug23
PASS
0 wait_reg_mem_poll_mem3 2002Aug23
PASS
0 wait_reg_mem_poll_reg0 2002Aug23
PASS
0 wait_reg_mem_poll_reg1 2002Aug23
PASS
0 wait_reg_mem_poll_reg2 2002Aug23
PASS
0 wait_reg_mem_poll_reg3 2002Aug23
PASS
=====
Tests_Run_count = 707 Pass_count = 699 Fail_count = 8 Incomplete_count = 0
=====
```

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Regression Report For CP - Project = r400 Created on Fri Aug 16 5:29:34 2002
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No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug15
PASS			
0	2d_indr_buffer_b		2002Aug15
PASS			
0	2d_indr_buffer_c		2002Aug15
PASS			
0	2d_indr_buffer_d		2002Aug15
PASS			
0	2d_indr_buffer_e		2002Aug15
PASS			
0	2d_indr_buffer_f		2002Aug15
PASS			
0	2d_indr_buffer_g		2002Aug15
PASS			
0	3d_draw_indx_2		2002Aug15
PASS			
0	3d_draw_indx_2_b		2002Aug15
PASS			
0	bitblt_multi		2002Aug15
PASS			
0	brush_flag		2002Aug15
PASS			
0	cond_write_mem_poll_mem		2002Aug15
PASS			
0	cond_write_mem_poll_mem2		2002Aug15
PASS			
0	cond_write_mem_poll_mem3		2002Aug15
PASS			
0	cond_write_mem_poll_mem_rt		2002Aug15
PASS			
0	cond_write_mem_poll_reg		2002Aug15
PASS			
0	cond_write_mem_poll_reg2		2002Aug15
PASS			
0	cond_write_mem_poll_reg3		2002Aug15
PASS			
0	cp_acpi_recovery_soft_reset		2002Aug15
PASS			
0	cp_bios_scratch		2002Aug15

PASS		
0	cp_ib2_base000_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug15
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug15
PASS		
0	cp_ib_base000_bufszXXX	2002Aug15
PASS		
0	cp_ib_base001_bufszXXX	2002Aug15
PASS		
0	cp_ib_base010_bufszXXX	2002Aug15
PASS		
0	cp_ib_base011_bufszXXX	2002Aug15
PASS		
0	cp_ib_base100_bufszXXX	2002Aug15
PASS		
0	cp_ib_base101_bufszXXX	2002Aug15
PASS		
0	cp_ib_base110_bufszXXX	2002Aug15
PASS		
0	cp_ib_base111_bufszXXX	2002Aug15
PASS		
0	cp_interrupt	2002Aug15
PASS		
0	cp_interrupt_rt_events_16	2002Aug15
PASS		
0	cp_iq2_alignment_000	2002Aug15
PASS		
0	cp_iq2_alignment_001	2002Aug15
PASS		
0	cp_iq2_alignment_010	2002Aug15
PASS		
0	cp_iq2_alignment_011	2002Aug15
PASS		
0	cp_iq2_alignment_100	2002Aug15
PASS		
0	cp_iq2_alignment_101	2002Aug15

PASS		
0	cp_iq2_alignment_110	2002Aug15
PASS		
0	cp_iq2_alignment_111	2002Aug15
PASS		
0	cp_iq_alignment_000	2002Aug15
PASS		
0	cp_iq_alignment_001	2002Aug15
PASS		
0	cp_iq_alignment_010	2002Aug15
PASS		
0	cp_iq_alignment_011	2002Aug15
PASS		
0	cp_iq_alignment_100	2002Aug15
PASS		
0	cp_iq_alignment_101	2002Aug15
PASS		
0	cp_iq_alignment_110	2002Aug15
PASS		
0	cp_iq_alignment_111	2002Aug15
PASS		
0	cp_max_size_packet	2002Aug15
PASS		
0	cp_nrt_rt_protection_mode	2002Aug15
PASS		
0	cp_output_stress	2002Aug15
PASS		
0	cp_protection_mode	2002Aug15
PASS		
0	cp_rb_alignment_000	2002Aug15
PASS		
0	cp_rb_alignment_001	2002Aug15
PASS		
0	cp_rb_alignment_010	2002Aug15
PASS		
0	cp_rb_alignment_011	2002Aug15
PASS		
0	cp_rb_alignment_100	2002Aug15
PASS		
0	cp_rb_alignment_101	2002Aug15
PASS		
0	cp_rb_alignment_110	2002Aug15
PASS		
0	cp_rb_alignment_111	2002Aug15
PASS		
0	cp_register_reads	2002Aug15
PASS		
0	cp_reg_rw_test	2002Aug15

PASS		
0	cp_rt_protection_mode	2002Aug15
PASS		
0	cp_scratch	2002Aug15
PASS		
0	cp_scratch_multi	2002Aug15
PASS		
0	dma_cp_sync	2002Aug15
PASS		
0	dma_dscrstr_chckn_bit	2002Aug15
PASS		
0	dma_dscrstr_flush1	2002Aug15
PASS		
0	dma_dscrstr_flush2	2002Aug15
PASS		
0	dma_dscrstr_flush3	2002Aug15
PASS		
0	dma_m2m_src00_dstXX	2002Aug15
PASS		
0	dma_m2m_src01_dstXX	2002Aug15
PASS		
0	dma_m2m_src10_dstXX	2002Aug15
PASS		
0	dma_m2m_src11_dstXX	2002Aug15
PASS		
0	dma_mem2mem	2002Aug15
PASS		
0	dma_mem2mem_abort	2002Aug15
PASS		
0	dma_mem2mem_alignments	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug15
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug15
PASS		
0	dma_mem2mem_pio_abort	2002Aug15

PASS		
0	dma_mem2reg	2002Aug15
PASS		
0	dma_mem2reg_abort	2002Aug15
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug15
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug15
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug15
PASS		
0	dma_mem2reg_sync	2002Aug15
PASS		
0	dma_r2r_m2m_r2r	2002Aug15
PASS		
0	dma_r2r_m2r_r2r	2002Aug15
PASS		
0	dma_r2r_r2m_r2r	2002Aug15
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug15
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug15
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug15
PASS		
0	dma_reg2mem	2002Aug15
PASS		
0	dma_reg2mem_abort	2002Aug15
PASS		
0	dma_reg2reg	2002Aug15
PASS		
0	dma_reg2reg_2dw	2002Aug15
PASS		
0	dma_reg2reg_daic	2002Aug15
PASS		
0	dma_reg2reg_mult	2002Aug15
PASS		
0	dma_reg2reg_mult2	2002Aug15
PASS		
0	dma_reg2reg_mult3	2002Aug15
PASS		
0	dma_reg2reg_saic	2002Aug15
PASS		
0	dma_reg2reg_sdaic	2002Aug15
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug15
PASS		
0	dma_reg2reg_stress	2002Aug15

PASS		
0	draw_indx_a	2002Aug15
PASS		
0	draw_indx_b	2002Aug15
PASS		
0	draw_indx_c	2002Aug15
PASS		
0	draw_indx_indr1_a	2002Aug15
PASS		
0	draw_indx_indr1_b	2002Aug15
PASS		
0	draw_indx_indr1_short_a	2002Aug15
PASS		
0	draw_indx_indr1_short_b	2002Aug15
PASS		
0	draw_indx_indr1_stress	2002Aug15
PASS		
0	draw_indx_indr2_a	2002Aug15
PASS		
0	draw_indx_indr2_b	2002Aug15
PASS		
0	draw_indx_indr2_short_a	2002Aug15
PASS		
0	draw_indx_indr2_short_b	2002Aug15
PASS		
0	draw_indx_indr2_stress	2002Aug15
PASS		
0	draw_indx_ring1	2002Aug15
PASS		
0	draw_indx_ring2	2002Aug15
PASS		
0	draw_indx_ring_short1	2002Aug15
PASS		
0	draw_indx_ring_short2	2002Aug15
PASS		
0	draw_indx_ring_stress	2002Aug15
PASS		
0	draw_indx_short_a	2002Aug15
PASS		
0	draw_indx_short_b	2002Aug15
PASS		
0	draw_indx_short_c	2002Aug15
PASS		
0	draw_indx_stress_a	2002Aug15
PASS		
0	draw_indx_stress_b	2002Aug15
PASS		
0	draw_indx_stress_c	2002Aug15

PASS		
0	event_timestamp_write	2002Aug15
PASS		
142	event_timestamp_write_multicontext_maxcontext_7	2002Aug15
FAIL		
0	ft_indr1_july22	2002Aug15
PASS		
0	ft_indr2_july22	2002Aug15
PASS		
0	ft_july22	2002Aug15
PASS		
0	header_dump	2002Aug15
PASS		
0	header_dump_rt	2002Aug15
PASS		
0	header_dump_small	2002Aug15
PASS		
0	hostdata_blt	2002Aug15
PASS		
0	im_load	2002Aug12
PASS		
0	im_load_a	2002Aug15
PASS		
0	im_load_b	2002Aug15
PASS		
0	im_load_cid_mismatch	2002Aug15
PASS		
0	im_load_immediate	2002Aug15
PASS		
0	im_load_immediate_nrt_me	2002Aug15
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug15
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug15
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug15
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug15
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug15
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug15
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug15
PASS		
0	im_load_indr1_a	2002Aug15
PASS		
0	im_load_indr1_b	2002Aug15

PASS		
0	im_load_indr2_a	2002Aug15
PASS		
0	im_load_indr2_b	2002Aug15
PASS		
0	im_load_invalidate	2002Aug15
PASS		
0	im_load_match	2002Aug15
PASS		
0	im_load_match2	2002Aug15
PASS		
0	im_load_nrt_me	2002Aug15
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug15
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug15
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug15
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug15
PASS		
0	im_load_pfmacth_disable	2002Aug15
PASS		
0	im_load_ring_a	2002Aug15
PASS		
0	im_load_ring_b	2002Aug15
PASS		
0	im_load_rt	2002Aug15
PASS		
0	im_load_rt2	2002Aug15
PASS		
0	im_load_rt3	2002Aug15
PASS		
0	im_load_rt4	2002Aug15
PASS		
0	im_load_sz_mismatch	2002Aug15
PASS		
0	incremental_update_const	2002Aug15
PASS		
0	incremental_update_instr	2002Aug15
PASS		
0	incremental_update_state	2002Aug15
PASS		
0	indirect_buffer_a	2002Aug15
PASS		
0	indirect_buffer_b	2002Aug15
PASS		
0	indirect_buffer_c	2002Aug15

PASS		
0	indirect_buffer_d	2002Aug15
PASS		
0	indirect_buffer_e	2002Aug15
PASS		
0	indirect_buffer_f	2002Aug15
PASS		
0	indirect_buffer_g	2002Aug15
PASS		
0	indirect_buffer_h	2002Aug15
PASS		
0	indirect_buffer_i	2002Aug15
PASS		
0	indirect_buffer_j	2002Aug15
PASS		
0	indirect_buffer_k	2002Aug15
PASS		
0	indirect_buffer_l	2002Aug15
PASS		
0	indirect_buffer_mp_a	2002Aug15
PASS		
0	indirect_buffer_mp_b	2002Aug15
PASS		
0	indirect_buffer_mp_c	2002Aug15
PASS		
0	indirect_buffer_mp_d	2002Aug15
PASS		
0	indirect_buffer_mp_e	2002Aug15
PASS		
0	indirect_buffer_mp_f	2002Aug15
PASS		
0	invalidate_on_me_init	2002Aug15
PASS		
0	invalidate_state_constants	2002Aug15
PASS		
0	invalidate_state_shaders	2002Aug15
PASS		
0	invalidate_state_shaders2	2002Aug15
PASS		
0	invalidate_state_sublks	2002Aug15
PASS		
0	invalidate_state_sublks2	2002Aug15
PASS		
210	invalid_opcode	2002Aug15
FAIL	cp_rbbm.cmp rbbmWr.cmp	
211	iq1_iq2_mult	2002Aug15
INCOMPLETE		
212	iq1_iq2_push_wrap	2002Aug15

INCOMPLETE	
0 iq2_csq_full	2002Aug15
PASS	
0 IQ2_Mult	2002Aug15
PASS	
0 IQ2_Wrap_Legacy	2002Aug15
PASS	
0 IQ_Wrap	2002Aug15
PASS	
0 ld_cnst_cntx_indr1_replicate	2002Aug15
PASS	
0 ld_cnst_cntx_indr1_replicate4	2002Aug15
PASS	
0 ld_cnst_cntx_indr1_replicate8	2002Aug15
PASS	
0 ld_cnst_cntx_indr1_simple	2002Aug15
PASS	
0 ld_cnst_cntx_indr2_replicate	2002Aug15
PASS	
0 ld_cnst_cntx_indr2_replicate4	2002Aug15
PASS	
0 ld_cnst_cntx_indr2_replicate8	2002Aug15
PASS	
0 ld_cnst_cntx_indr2_simple	2002Aug15
PASS	
0 ld_cnst_cntx_match	2002Aug15
PASS	
0 ld_cnst_cntx_match_valid_preserved	2002Aug15
PASS	
0 ld_cnst_cntx_mismatch_alutex	2002Aug15
PASS	
0 ld_cnst_cntx_mismatch_base	2002Aug15
PASS	
0 ld_cnst_cntx_mismatch_force	2002Aug15
PASS	
0 ld_cnst_cntx_pfmatch_disable	2002Aug15
PASS	
0 ld_cnst_cntx_replicate4_alu	2002Aug15
PASS	
0 ld_cnst_cntx_replicate4_tex	2002Aug15
PASS	
0 ld_cnst_cntx_replicate8	2002Aug15
PASS	
0 ld_cnst_cntx_replicate_alu	2002Aug15
PASS	
0 ld_cnst_cntx_replicate_tex	2002Aug15
PASS	
0 ld_cnst_cntx_ring_replicate	2002Aug15

PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug15
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug15
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug15
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug15
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug15
PASS		
0	load_constant_context_rt	2002Aug15
PASS		
0	load_palette	2002Aug15
PASS		
0	mem_write	2002Aug15
PASS		
0	mem_write_rt	2002Aug15
PASS		
0	mpeg_index	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug15
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug15

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug15
PASS		
0	nop_eating_disabled	2002Aug15
PASS		
0	paint_multi	2002Aug15
PASS		
0	pitch_clip	2002Aug15
PASS		
0	rbbm_soft_reset	2002Aug15
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug15
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Aug15
PASS		
0	rbbm_WU_CP_dma_idle	2002Aug15
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug15
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug15
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug15
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug15
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug15
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug15
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP update_pending_off	2002Aug15
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP update_pending_off	2002Aug15
PASS		
0	rbbm_WU_idct_semaphore	2002Aug15
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug15
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug15
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug15
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug15
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Aug15
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Aug15
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Aug15

PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Aug15
PASS		
0	RB_BUFSZ_0	2002Aug15
PASS		
0	RB_BUFSZ_1	2002Aug15
PASS		
0	RB_Wrap	2002Aug15
PASS		
0	reg_to_mem	2002Aug15
PASS		
0	reg_to_mem_rt	2002Aug15
PASS		
0	rop_cfd_wmd	2002Aug15
PASS		
0	rstr_ordr	2002Aug15
PASS		
0	rtee_discretes_falling_00_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug15

PASS		
0	rtee_discretes_falling_16_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug15
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug15

PASS		
0	rtee_discretes_invert_08_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_11_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug15
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0	rtee_discretes_invert_23_events_16	2002Aug15
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0	rtee_discretes_invert_24_events_16	2002Aug15
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0	rtee_discretes_invert_25_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug15
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug15

PASS		
0	rtee_discretes_level_00_events_16	2002Aug15
PASS		
0	rtee_discretes_level_01_events_16	2002Aug15
PASS		
0	rtee_discretes_level_02_events_16	2002Aug15
PASS		
0	rtee_discretes_level_03_events_16	2002Aug15
PASS		
0	rtee_discretes_level_04_events_16	2002Aug15
PASS		
0	rtee_discretes_level_05_events_16	2002Aug15
PASS		
0	rtee_discretes_level_06_events_16	2002Aug15
PASS		
0	rtee_discretes_level_07_events_16	2002Aug15
PASS		
0	rtee_discretes_level_08_events_16	2002Aug15
PASS		
0	rtee_discretes_level_09_events_16	2002Aug15
PASS		
0	rtee_discretes_level_10_events_16	2002Aug15
PASS		
0	rtee_discretes_level_11_events_16	2002Aug15
PASS		
0	rtee_discretes_level_12_events_16	2002Aug15
PASS		
0	rtee_discretes_level_13_events_16	2002Aug15
PASS		
0	rtee_discretes_level_14_events_16	2002Aug15
PASS		
0	rtee_discretes_level_15_events_16	2002Aug15
PASS		
0	rtee_discretes_level_16_events_16	2002Aug15
PASS		
0	rtee_discretes_level_17_events_16	2002Aug15
PASS		
0	rtee_discretes_level_18_events_16	2002Aug15
PASS		
0	rtee_discretes_level_19_events_16	2002Aug15
PASS		
0	rtee_discretes_level_20_events_16	2002Aug15
PASS		
0	rtee_discretes_level_21_events_16	2002Aug15
PASS		
0	rtee_discretes_level_22_events_16	2002Aug15
PASS		
0	rtee_discretes_level_23_events_16	2002Aug15

PASS		
0	rtee_discretes_level_24_events_16	2002Aug15
PASS		
0	rtee_discretes_level_25_events_16	2002Aug15
PASS		
0	rtee_discretes_level_26_events_16	2002Aug15
PASS		
0	rtee_discretes_level_27_events_16	2002Aug15
PASS		
0	rtee_discretes_level_28_events_16	2002Aug15
PASS		
0	rtee_discretes_level_29_events_16	2002Aug15
PASS		
0	rtee_discretes_level_30_events_16	2002Aug15
PASS		
0	rtee_discretes_level_31_events_16	2002Aug15
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug15

PASS		
0	rtee_discretes_rising_15_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug15
PASS		
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PASS		
0	rtee_discretes_rising_24_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug15
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug15
PASS		
0	rtee_display1_comp_events_16	2002Aug15
PASS		
0	rtee_display2_comp_events_16	2002Aug15
PASS		
0	rtee_mem_linear_search_events_2	2002Aug15
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug15

PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug15
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug15
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug15
PASS		
0	rtee_prefetch_parse_nops	2002Aug15
PASS		
0	rtee_reg_linear_search_events_2	2002Aug15
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug15
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug15
PASS		
0	rtee_vip_comp_events_16	2002Aug15
PASS		
448	rt_invalid_opcode	2002Aug15
FAIL	rbbmWr.cmp	
0	scratch_mem_read	2002Aug15
PASS		
0	scratch_mem_read2	2002Aug15
PASS		
0	set_constant	2002Aug15
PASS		
0	set_constant2	2002Aug15

PASS		
0	set_constant_indr1_short	2002Aug15
PASS		
0	set_constant_indr1_short2	2002Aug15
PASS		
0	set_constant_indr1_write_enable	2002Aug15
PASS		
0	set_constant_indr2_short	2002Aug15
PASS		
0	set_constant_indr2_short2	2002Aug15
PASS		
0	set_constant_indr2_write_enable	2002Aug15
PASS		
0	set_constant_ring_short	2002Aug15
PASS		
0	set_constant_ring_short2	2002Aug15
PASS		
0	set_constant_ring_write_enable	2002Aug15
PASS		
0	set_constant_short	2002Aug15
PASS		
0	set_constant_short2	2002Aug15
PASS		
0	set_constant_write_enable	2002Aug15
PASS		
0	set_scissors	2002Aug15
PASS		
0	set_state_rt_a	2002Aug15
PASS		
467	set_state_rt_b	2002Aug15
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
468	set_state_rt_c	2002Aug15
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
469	set_state_rt_d	2002Aug15
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_indr1_match	2002Aug15
PASS		
0	set_st_indr1_mismatch	2002Aug15
PASS		
0	set_st_indr1_mismatch2	2002Aug15
PASS		
0	set_st_indr1_mismatch3	2002Aug15
PASS		
0	set_st_indr1_mismatch4	2002Aug15
PASS		
0	set_st_indr1_ps	2002Aug15
PASS		
0	set_st_indr1_ps_only	2002Aug15

PASS
477 set_st_indr1_sublk0 2002Aug15
FAIL rbbmWr.cmp
0 set_st_indr1_sublk1 2002Aug15
PASS
0 set_st_indr1_sublk1_only 2002Aug15
PASS
0 set_st_indr1_sublk2 2002Aug15
PASS
0 set_st_indr1_sublk2_only 2002Aug15
PASS
0 set_st_indr1_sublk3 2002Aug15
PASS
0 set_st_indr1_sublk3_only 2002Aug15
PASS
0 set_st_indr1_sublk4 2002Aug15
PASS
0 set_st_indr1_sublk4_only 2002Aug15
PASS
0 set_st_indr1_sublk5 2002Aug15
PASS
0 set_st_indr1_sublk5_only 2002Aug15
PASS
0 set_st_indr1_sublk6 2002Aug15
PASS
0 set_st_indr1_sublk6_only 2002Aug15
PASS
0 set_st_indr1_sublk7 2002Aug15
PASS
0 set_st_indr1_sublk7_only 2002Aug15
PASS
0 set_st_indr1_vs 2002Aug15
PASS
0 set_st_indr1_vs_only 2002Aug15
PASS
0 set_st_indr2_match 2002Aug15
PASS
0 set_st_indr2_mismatch 2002Aug15
PASS
0 set_st_indr2_mismatch2 2002Aug15
PASS
0 set_st_indr2_mismatch3 2002Aug15
PASS
0 set_st_indr2_mismatch4 2002Aug15
PASS
0 set_st_indr2_ps 2002Aug15
PASS
0 set_st_indr2_ps_only 2002Aug15

PASS		
501	set_st_indr2_sublk0	2002Aug15
FAIL	rbbmWr.cmp	
0	set_st_indr2_sublk1	2002Aug15
PASS		
0	set_st_indr2_sublk1_only	2002Aug15
PASS		
0	set_st_indr2_sublk2	2002Aug15
PASS		
0	set_st_indr2_sublk2_only	2002Aug15
PASS		
0	set_st_indr2_sublk3	2002Aug15
PASS		
0	set_st_indr2_sublk3_only	2002Aug15
PASS		
0	set_st_indr2_sublk4	2002Aug15
PASS		
0	set_st_indr2_sublk4_only	2002Aug15
PASS		
0	set_st_indr2_sublk5	2002Aug15
PASS		
0	set_st_indr2_sublk5_only	2002Aug15
PASS		
0	set_st_indr2_sublk6	2002Aug15
PASS		
0	set_st_indr2_sublk6_only	2002Aug15
PASS		
0	set_st_indr2_sublk7	2002Aug15
PASS		
0	set_st_indr2_sublk7_only	2002Aug15
PASS		
0	set_st_indr2_vs	2002Aug15
PASS		
0	set_st_indr2_vs_only	2002Aug15
PASS		
0	set_st_pfmacth_disabled	2002Aug15
PASS		
0	set_st_ring_match	2002Aug15
PASS		
0	set_st_ring_mismatch	2002Aug15
PASS		
0	set_st_ring_mismatch2	2002Aug15
PASS		
0	set_st_ring_mismatch3	2002Aug15
PASS		
0	set_st_ring_mismatch4	2002Aug15
PASS		
0	set_st_ring_ps	2002Aug15

PASS		
0	set_st_ring_ps_only	2002Aug15
PASS		
526	set_st_ring_sublk0	2002Aug15
FAIL	rbbmWr.cmp	
0	set_st_ring_sublk1	2002Aug15
PASS		
0	set_st_ring_sublk1_only	2002Aug15
PASS		
0	set_st_ring_sublk2	2002Aug15
PASS		
0	set_st_ring_sublk2_only	2002Aug15
PASS		
0	set_st_ring_sublk3	2002Aug15
PASS		
0	set_st_ring_sublk3_only	2002Aug15
PASS		
0	set_st_ring_sublk4	2002Aug15
PASS		
0	set_st_ring_sublk4_only	2002Aug15
PASS		
0	set_st_ring_sublk5	2002Aug15
PASS		
0	set_st_ring_sublk5_only	2002Aug15
PASS		
0	set_st_ring_sublk6	2002Aug15
PASS		
0	set_st_ring_sublk6_only	2002Aug15
PASS		
0	set_st_ring_sublk7	2002Aug15
PASS		
0	set_st_ring_sublk7_only	2002Aug15
PASS		
0	set_st_ring_vs	2002Aug15
PASS		
0	set_st_ring_vs_only	2002Aug15
PASS		
0	set_st_stress_a	2002Aug15
PASS		
544	set_st_stress_b	2002Aug15
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug15
PASS		
0	set_st_stress_d	2002Aug15
PASS		
0	set_st_stress_e	2002Aug15
PASS		
0	set_st_stress_f	2002Aug15

PASS		
0	smoke_nrt	2002Aug15
PASS		
0	src_clip	2002Aug15
PASS		
0	srf_cohr	2002Aug15
PASS		
0	trans_bitblt	2002Aug15
PASS		
0	type0_2long_pckts	2002Aug15
PASS		
0	type0_3indr_buffs	2002Aug15
PASS		
0	type0_4pckts	2002Aug15
PASS		
0	type0_incr_updt_cnst	2002Aug15
PASS		
0	type0_incr_updt_instrmem	2002Aug15
PASS		
0	type0_incr_updt_st	2002Aug15
PASS		
0	type0_incr_updt_stress	2002Aug15
PASS		
0	type0_ind1_ind2_a	2002Aug15
PASS		
0	type0_ind1_ind2_b	2002Aug15
PASS		
0	type0_indr_buff	2002Aug15
PASS		
0	Type0_IQ2_BM	2002Aug15
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug15
PASS		
0	Type0_IQ_BM	2002Aug15
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug15
PASS		
0	Type0_PQ_BM	2002Aug15
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug15
PASS		
0	type1_4pckts	2002Aug15
PASS		
0	type1_indr_buff	2002Aug15
PASS		
0	type1_pckt	2002Aug15
PASS		
0	Type3_Bitblt	2002Aug15

```

PASS
0   Type3_Load_Palette                2002Aug15
PASS
0   Type3_NOP                          2002Aug15
PASS
575 uram_rd_wrt                        2002Aug15
**FAIL**
576 uram_rd_wrt_rt                    2002Aug15
**FAIL**
577 uram_wrt_rt_ucode                 2002Aug15
**FAIL**          rbbmWr.cmp
0   uram_wrt_ucode                    2002Aug15
PASS
0   wait_for_idle                     2002Aug15
PASS
0   wait_reg_mem_poll_mem0            2002Aug15
PASS
0   wait_reg_mem_poll_mem0_rt         2002Aug15
PASS
582 wait_reg_mem_poll_mem1            2002Aug15
**FAIL**
583 wait_reg_mem_poll_mem2            2002Aug15
**FAIL**
0   wait_reg_mem_poll_mem3            2002Aug15
PASS
585 wait_reg_mem_poll_reg0            2002Aug15
**FAIL**          cp_rbbm.cmp
0   wait_reg_mem_poll_reg1            2002Aug15
PASS
0   wait_reg_mem_poll_reg2            2002Aug15
PASS
0   wait_reg_mem_poll_reg3            2002Aug15
PASS
=====
Tests_Run_count = 588  Pass_count = 570 Fail_count = 16  Incomplete_count = 2
=====

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Sep 17  6:42:13 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU              0          0          0          0
              CP             639         639         575         89
              RBBM           98          98          59          60
+++++
              TOTAL           737         737         634         86
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Fri Sep 20 11:36:47 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          639        639        585         91
              RBBM        105        105        103         98
+++++
              TOTAL        744        744        688         92
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct5
	PASS		
0	2d_indr_buffer_b		2002Oct5
	PASS		
0	2d_indr_buffer_c		2002Oct5
	PASS		
0	2d_indr_buffer_d		2002Oct5
	PASS		
0	2d_indr_buffer_e		2002Oct5
	PASS		
0	2d_indr_buffer_f		2002Oct5
	PASS		
0	2d_indr_buffer_g		2002Oct5
	PASS		
0	2d_indr_buffer_random		2002Oct5
	PASS		
9	2d_indr_buffer_random2		2002Sep29
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Oct5
	PASS		
0	3d_draw_indx_2_b		2002Oct5
	PASS		
0	IQ2_Mult		2002Oct5
	PASS		
0	IQ2_Wrap_Legacy		2002Oct5
	PASS		
0	IQ_Wrap		2002Oct5
	PASS		
0	Isync_Control		2002Oct5
	PASS		
0	Isync_Control_2D_3D		2002Oct5
	PASS		
0	Isync_Control_3D_2D		2002Oct5
	PASS		
0	RB_BUFSZ_0		2002Oct5
	PASS		
0	RB_BUFSZ_1		2002Oct5
	PASS		
0	RB_Wrap		2002Oct5

PASS		
0	Small_Text_BM	20020ct5
PASS		
0	Type0_IQ2_BM	20020ct5
PASS		
0	Type0_IQ2_BM_IB_Packet	20020ct5
PASS		
0	Type0_IQ_BM	20020ct5
PASS		
0	Type0_IQ_BM_IB_Packet	20020ct5
PASS		
0	Type0_PQ_BM	20020ct5
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	20020ct5
PASS		
0	Type3_Bitblt	20020ct5
PASS		
0	Type3_Hostdata_blt	20020ct5
PASS		
0	Type3_Hostdata_blt_PIO	20020ct5
PASS		
0	Type3_Load_Palette	20020ct5
PASS		
0	Type3_NOP	20020ct5
PASS		
0	Type3_Polyline	20020ct5
PASS		
0	Type3_Polyline_Large	20020ct5
PASS		
0	bitblt_multi	20020ct5
PASS		
0	brush_32x1mono	20020ct5
PASS		
0	brush_8x8color_16	20020ct5
PASS		
0	brush_8x8color_32	20020ct5
PASS		
0	brush_8x8color_64	20020ct5
PASS		
0	brush_8x8color_dst_0_1	20020ct5
PASS		
0	brush_8x8mono	20020ct5
PASS		
0	brush_color_mono_stress	20020ct5
PASS		
0	brush_flag	20020ct5
PASS		
0	brush_type0	20020ct5

PASS		
0	brush_type1	2002Oct5
PASS		
0	brush_type10	2002Oct5
PASS		
0	brush_type13	2002Oct5
PASS		
0	brush_type14	2002Oct5
PASS		
0	brush_type6	2002Oct5
PASS		
0	brush_type7	2002Oct5
PASS		
0	brush_type_other	2002Oct5
PASS		
0	brush_types_13_14	2002Oct5
PASS		
0	change_queue_sizes	2002Oct5
PASS		
0	clear_const_wr_en_on_me_init	2002Oct5
PASS		
0	clear_const_wr_en_with_lcc	2002Oct5
PASS		
0	cond_write_mem_poll_mem	2002Oct5
PASS		
0	cond_write_mem_poll_mem2	2002Oct5
PASS		
0	cond_write_mem_poll_mem3	2002Oct5
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct5
PASS		
0	cond_write_mem_poll_reg	2002Oct5
PASS		
0	cond_write_mem_poll_reg2	2002Oct5
PASS		
0	cond_write_mem_poll_reg3	2002Oct5
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct5
PASS		
0	cp_bios_scratch	2002Oct5
PASS		
0	cp_high_stress	2002Oct5
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct5
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct5
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct5

PASS		
0	cp_ib2_base011_bufszXXX	20020ct5
PASS		
0	cp_ib2_base100_bufszXXX	20020ct5
PASS		
0	cp_ib2_base101_bufszXXX	20020ct5
PASS		
0	cp_ib2_base110_bufszXXX	20020ct5
PASS		
0	cp_ib2_base111_bufszXXX	20020ct5
PASS		
0	cp_ib_base000_bufszXXX	20020ct5
PASS		
0	cp_ib_base001_bufszXXX	20020ct5
PASS		
0	cp_ib_base010_bufszXXX	20020ct5
PASS		
0	cp_ib_base011_bufszXXX	20020ct5
PASS		
0	cp_ib_base100_bufszXXX	20020ct5
PASS		
0	cp_ib_base101_bufszXXX	20020ct5
PASS		
0	cp_ib_base110_bufszXXX	20020ct5
PASS		
0	cp_ib_base111_bufszXXX	20020ct5
PASS		
0	cp_interrupt	20020ct5
PASS		
0	cp_interrupt_rt_events_16	20020ct5
PASS		
0	cp_iq2_alignment_000	20020ct5
PASS		
0	cp_iq2_alignment_001	20020ct5
PASS		
0	cp_iq2_alignment_010	20020ct5
PASS		
0	cp_iq2_alignment_011	20020ct5
PASS		
0	cp_iq2_alignment_100	20020ct5
PASS		
0	cp_iq2_alignment_101	20020ct5
PASS		
0	cp_iq2_alignment_110	20020ct5
PASS		
0	cp_iq2_alignment_111	20020ct5
PASS		
0	cp_iq_alignment_000	20020ct5

PASS		
0	cp_iq_alignment_001	20020ct5
PASS		
0	cp_iq_alignment_010	20020ct5
PASS		
0	cp_iq_alignment_011	20020ct5
PASS		
0	cp_iq_alignment_100	20020ct5
PASS		
0	cp_iq_alignment_101	20020ct5
PASS		
0	cp_iq_alignment_110	20020ct5
PASS		
0	cp_iq_alignment_111	20020ct5
PASS		
0	cp_max_size_packet	20020ct5
PASS		
0	cp_max_size_packet_random	20020ct5
PASS		
0	cp_nrt_rt_protection_mode	20020ct5
PASS		
0	cp_output_stress	20020ct5
PASS		
0	cp_output_stress_random	20020ct5
PASS		
0	cp_protection_mode	20020ct5
PASS		
0	cp_rb_alignment_000	20020ct5
PASS		
0	cp_rb_alignment_001	20020ct5
PASS		
0	cp_rb_alignment_010	20020ct5
PASS		
0	cp_rb_alignment_011	20020ct5
PASS		
0	cp_rb_alignment_100	20020ct5
PASS		
0	cp_rb_alignment_101	20020ct5
PASS		
0	cp_rb_alignment_110	20020ct5
PASS		
0	cp_rb_alignment_111	20020ct5
PASS		
0	cp_rb_wptrXXX_base000	20020ct5
PASS		
0	cp_rb_wptrXXX_base001	20020ct5
PASS		
0	cp_rb_wptrXXX_base010	20020ct5

PASS		
0	cp_rb_wptrXXX_base011	20020ct5
PASS		
0	cp_rb_wptrXXX_base100	20020ct5
PASS		
0	cp_rb_wptrXXX_base101	20020ct5
PASS		
0	cp_rb_wptrXXX_base110	20020ct5
PASS		
0	cp_rb_wptrXXX_base111	20020ct5
PASS		
0	cp_reg_rw_test	20020ct5
PASS		
0	cp_register_reads	20020ct5
PASS		
0	cp_rt_protection_mode	20020ct5
PASS		
0	cp_scratch	20020ct5
PASS		
0	cp_scratch_multi	20020ct5
PASS		
0	csf_reg_rd_default	20020ct5
PASS		
128	csf_reg_rd_only	20020ct5
FAIL		
0	csf_reg_wr_rd	20020ct5
PASS		
0	dma_cp_sync	20020ct5
PASS		
0	dma_dscrstr_chckn_bit	20020ct5
PASS		
0	dma_dscrstr_flush1	20020ct5
PASS		
0	dma_dscrstr_flush2	20020ct5
PASS		
0	dma_dscrstr_flush3	20020ct5
PASS		
0	dma_m2m_src00_dstXX	20020ct5
PASS		
0	dma_m2m_src01_dstXX	20020ct5
PASS		
0	dma_m2m_src10_dstXX	20020ct5
PASS		
0	dma_m2m_src11_dstXX	20020ct5
PASS		
0	dma_mem2mem	20020ct5
PASS		
0	dma_mem2mem_abort	20020ct5

PASS		
0	dma_mem2mem_alignments	20020ct5
PASS		
0	dma_mem2mem_byte_alignment0	20020ct5
PASS		
0	dma_mem2mem_byte_alignment1	20020ct5
PASS		
0	dma_mem2mem_byte_alignment2	20020ct5
PASS		
0	dma_mem2mem_byte_alignment3	20020ct5
PASS		
0	dma_mem2mem_byte_alignment4	20020ct5
PASS		
0	dma_mem2mem_byte_alignment5	20020ct5
PASS		
0	dma_mem2mem_byte_alignment6	20020ct5
PASS		
0	dma_mem2mem_byte_alignment7	20020ct5
PASS		
0	dma_mem2mem_large	20020ct5
PASS		
0	dma_mem2mem_pio_abort	20020ct5
PASS		
0	dma_mem2mem_random	20020ct5
PASS		
0	dma_mem2reg	20020ct5
PASS		
0	dma_mem2reg_abort	20020ct5
PASS		
0	dma_mem2reg_dtaq_stress	20020ct5
PASS		
0	dma_mem2reg_dtaq_stress_random	20020ct5
PASS		
0	dma_mem2reg_pio_stressbm	20020ct5
PASS		
0	dma_mem2reg_pio_stresspio	20020ct5
PASS		
0	dma_mem2reg_sync	20020ct5
PASS		
0	dma_r2r_m2m_r2r	20020ct5
PASS		
0	dma_r2r_m2r_r2r	20020ct5
PASS		
0	dma_r2r_r2m_r2r	20020ct5
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	20020ct5
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	20020ct5

PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct5
PASS		
0	dma_reg2mem	2002Oct5
PASS		
0	dma_reg2mem_abort	2002Oct5
PASS		
0	dma_reg2reg	2002Oct5
PASS		
0	dma_reg2reg_2dw	2002Oct5
PASS		
0	dma_reg2reg_daic	2002Oct5
PASS		
0	dma_reg2reg_mult	2002Oct5
PASS		
0	dma_reg2reg_mult2	2002Oct5
PASS		
0	dma_reg2reg_mult3	2002Oct5
PASS		
0	dma_reg2reg_saic	2002Oct5
PASS		
0	dma_reg2reg_sdaic	2002Oct5
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct5
PASS		
0	dma_reg2reg_stress	2002Oct5
PASS		
0	draw_indx_a	2002Oct5
PASS		
0	draw_indx_b	2002Oct5
PASS		
0	draw_indx_c	2002Oct5
PASS		
0	draw_indx_indr1_a	2002Oct5
PASS		
0	draw_indx_indr1_b	2002Oct5
PASS		
0	draw_indx_indr1_short_a	2002Oct5
PASS		
0	draw_indx_indr1_short_b	2002Oct5
PASS		
0	draw_indx_indr1_stress	2002Oct5
PASS		
0	draw_indx_indr2_a	2002Oct5
PASS		
0	draw_indx_indr2_b	2002Oct5
PASS		
0	draw_indx_indr2_short_a	2002Oct5

PASS		
0	draw_indx_indr2_short_b	20020ct5
PASS		
0	draw_indx_indr2_stress	20020ct5
PASS		
0	draw_indx_random_a	20020ct5
PASS		
0	draw_indx_random_b	20020ct5
PASS		
0	draw_indx_random_c	20020ct5
PASS		
0	draw_indx_ring1	20020ct5
PASS		
0	draw_indx_ring2	20020ct5
PASS		
0	draw_indx_ring_short1	20020ct5
PASS		
0	draw_indx_ring_short2	20020ct5
PASS		
0	draw_indx_ring_stress	20020ct5
PASS		
0	draw_indx_short_a	20020ct5
PASS		
0	draw_indx_short_b	20020ct5
PASS		
0	draw_indx_short_c	20020ct5
PASS		
0	draw_indx_stress_a	20020ct5
PASS		
0	draw_indx_stress_b	20020ct5
PASS		
0	draw_indx_stress_c	20020ct5
PASS		
0	event_timestamp_write	20020ct5
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	20020ct5
PASS		
0	fix2flt_reg	20020ct5
PASS		
0	ft_indr1_july22	20020ct5
PASS		
0	ft_indr2_july22	20020ct5
PASS		
0	ft_july22	20020ct5
PASS		
0	ft_july22_random	20020ct5
PASS		
0	header_dump	20020ct5

PASS		
0	header_dump_rt	2002Oct5
PASS		
0	header_dump_small	2002Oct5
PASS		
0	hostdata_blt	2002Oct5
PASS		
0	im_load_a	2002Oct5
PASS		
0	im_load_b	2002Oct5
PASS		
0	im_load_cid_mismatch	2002Oct5
PASS		
0	im_load_immediate	2002Oct5
PASS		
0	im_load_immediate_nrt_me	2002Oct5
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct5
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct5
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct5
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct5
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct5
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct5
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct5
PASS		
0	im_load_indr1_a	2002Oct5
PASS		
0	im_load_indr1_b	2002Oct5
PASS		
0	im_load_indr2_a	2002Oct5
PASS		
0	im_load_indr2_b	2002Oct5
PASS		
0	im_load_invalidate	2002Oct5
PASS		
0	im_load_match	2002Oct5
PASS		
0	im_load_match2	2002Oct5
PASS		
0	im_load_nrt_me	2002Oct5
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct5

PASS		
0	im_load_nrt_me_ps_wrap	2002Oct5
PASS		
0	im_load_nrt_me_vs_nowrap	2002Oct5
PASS		
0	im_load_nrt_me_vs_wrap	2002Oct5
PASS		
0	im_load_pfmatch_disable	2002Oct5
PASS		
241	im_load_random_a	2002Oct5
FAIL	mh_cp_rd.cmp	
242	im_load_random_b	2002Oct5
FAIL	mh_cp_rd.cmp	
243	im_load_random_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Oct5
PASS		
0	im_load_ring_b	2002Oct5
PASS		
0	im_load_rt	2002Oct5
PASS		
0	im_load_rt2	2002Oct5
PASS		
0	im_load_rt3	2002Oct5
PASS		
0	im_load_rt4	2002Oct5
PASS		
0	im_load_sz_mismatch	2002Oct5
PASS		
0	incremental_update_const	2002Oct5
PASS		
0	incremental_update_instr	2002Oct5
PASS		
0	incremental_update_state	2002Oct5
PASS		
0	indirect_buffer_a	2002Oct5
PASS		
0	indirect_buffer_b	2002Oct5
PASS		
0	indirect_buffer_c	2002Oct5
PASS		
0	indirect_buffer_d	2002Oct5
PASS		
0	indirect_buffer_e	2002Oct5
PASS		
0	indirect_buffer_f	2002Oct5
PASS		
0	indirect_buffer_g	2002Oct5

PASS		
0	indirect_buffer_h	20020ct5
PASS		
0	indirect_buffer_i	20020ct5
PASS		
0	indirect_buffer_j	20020ct5
PASS		
0	indirect_buffer_k	20020ct5
PASS		
0	indirect_buffer_l	20020ct5
PASS		
0	indirect_buffer_mp_a	20020ct5
PASS		
0	indirect_buffer_mp_b	20020ct5
PASS		
0	indirect_buffer_mp_c	20020ct5
PASS		
0	indirect_buffer_mp_d	20020ct5
PASS		
0	indirect_buffer_mp_e	20020ct5
PASS		
0	indirect_buffer_mp_f	20020ct5
PASS		
272	indirect_buffer_mp_random1	20020ct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
273	indirect_buffer_mp_random2	20020ct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
274	indirect_buffer_random1	20020ct5
FAIL	mh_cp_rd.cmp	
275	indirect_buffer_random2	20020ct5
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	20020ct5
PASS		
0	invalidate_on_me_init	20020ct5
PASS		
0	invalidate_state_constants	20020ct5
PASS		
0	invalidate_state_shaders	20020ct5
PASS		
0	invalidate_state_shaders2	20020ct5
PASS		
0	invalidate_state_sublks	20020ct5
PASS		
0	invalidate_state_sublks2	20020ct5
PASS		
0	iq1_iq2_mult	20020ct5
PASS		
0	iq1_iq2_push_wrap	20020ct5

PASS		
0	iq2_csq_full	2002Oct5
PASS		
286	lcc_coherency_cntr_random	2002Sep29
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Oct5
PASS		
0	lcc_mismatch_alutex_random	2002Oct5
PASS		
0	lcc_mismatch_base_random	2002Oct5
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct5
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Oct5
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct5
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct5
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct5
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct5
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct5
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct5
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct5
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct5
PASS		
0	ld_cnst_cntx_match	2002Oct5
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct5
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct5
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct5
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct5
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct5
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct5
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct5
PASS		
0	ld_cnst_cntx_replicate8	2002Oct5

PASS		
0	ld_cnst_cntx_replicate_alu	20020ct5
PASS		
0	ld_cnst_cntx_replicate_tex	20020ct5
PASS		
0	ld_cnst_cntx_ring_replicate	20020ct5
PASS		
0	ld_cnst_cntx_ring_replicate4	20020ct5
PASS		
0	ld_cnst_cntx_ring_replicate8	20020ct5
PASS		
0	ld_cnst_cntx_ring_simple	20020ct5
PASS		
0	ld_cnst_cntx_simple_alu	20020ct5
PASS		
0	ld_cnst_cntx_simple_tex	20020ct5
PASS		
0	load_constant_context_rt	20020ct5
PASS		
0	load_palette	20020ct5
PASS		
0	lp_2d3d	20020ct5
PASS		
0	me_reg_wr_rd	20020ct5
PASS		
0	mem_write	20020ct5
PASS		
0	mem_write_rt	20020ct5
PASS		
0	mpeg_index	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_5	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_6	20020ct5
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct5

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	20020ct5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	20020ct5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	20020ct5
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0	mpeg_index_multi_context_rt_enabled_max_context_5	20020ct5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	20020ct5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	20020ct5
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0	nop_eating_disabled	20020ct5
PASS		
0	nrt_rt_interaction1	20020ct5
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0	paint_multi	20020ct5
PASS		
0	paint_packet	20020ct5
PASS		
0	paint_packet_gui_control	20020ct5
PASS		
0	pfp_reg_reads1	20020ct5
PASS		
0	pfp_reg_reads2	20020ct5
PASS		
0	pitch_clip	20020ct5
PASS		
0	ply_nextscan	20020ct5
PASS		
0	rbbm_NQWU_BIF_01	20020ct5
PASS		
0	rbbm_NQWU_BIF_02	20020ct5
PASS		
0	rbbm_NQWU_CF_01	20020ct5
PASS		
0	rbbm_NQWU_CPnq_01	20020ct5
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0	rbbm_WU_2D_idle_01	20020ct5
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0	rbbm_WU_2D_idle_02	20020ct5
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0	rbbm_WU_2D_idle_03	20020ct5
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0	rbbm_WU_2D_idle_04	20020ct5
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0	rbbm_WU_2D_idle_05	20020ct5

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0	rbbm_WU_2D_idle_clean_01	2002Oct5
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0	rbbm_WU_2D_idle_clean_02	2002Oct5
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct5
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0	rbbm_WU_2D_idle_clean_11	2002Oct5
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0	rbbm_WU_2D_idle_clean_12	2002Oct5
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0	rbbm_WU_3D_idle_clean_11	2002Oct5
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0	rbbm_WU_3D_idle_clean_12	2002Oct5
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0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct5
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0	rbbm_WU_BOTH_GRP update_pending_off	2002Oct5
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0	rbbm_WU_CP_dma_idle	2002Oct5
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0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct5
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct5
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct5
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct5
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct5
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct5
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP update_pending_off	2002Oct5
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP update_pending_off	2002Oct5

PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct5
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct5
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct5
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct5
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0	rbbm_WU_VIPH1_dma_idle	2002Oct5
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct5
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct5
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0	rbbm_WU_cmdfifo_entries	2002Oct5
PASS		
0	rbbm_WU_idct_semaphore	2002Oct5
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct5
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct5
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Oct5
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0	rbbm_WUrt_CP_dma_idle	2002Oct5
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0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct5
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct5
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct5
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0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct5
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct5
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0	rbbm_WUrt_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Oct5
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Oct5
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0	rbbm_WUrt_MH_hdp_clean	2002Oct5
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0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct5
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct5

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0	rbbm_WUrt_RT_idle_01	2002Oct5
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0	rbbm_WUrt_RT_idle_02	2002Oct5
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct5
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0	rbbm_WUrt_RT_idle_04	2002Oct5
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0	rbbm_WUrt_RT_idle_clean_01	2002Oct5
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0	rbbm_WUrt_RT_idle_clean_02	2002Oct5
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Oct5
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0	rbbm_WUrt_RT_idle_clean_04	2002Oct5
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Oct5
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0	rbbm_WUrt_VIPH0_dma_idle	2002Oct5
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0	rbbm_WUrt_VIPH1_dma_idle	2002Oct5
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0	rbbm_WUrt_VIPH2_dma_idle	2002Oct5
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0	rbbm_WUrt_VIPH3_dma_idle	2002Oct5
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0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Oct5
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0	rbbm_clk_active_deassert_min_max	2002Oct5
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0	rbbm_clk_active_each_pipe	2002Oct5
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0	rbbm_clk_active_for_read_error	2002Oct5
PASS		
0	rbbm_clk_active_for_read_request	2002Oct5
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0	rbbm_clk_active_on_reset	2002Oct5
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0	rbbm_cp_non_queued_rdwr_01	2002Oct5
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0	rbbm_cp_non_queued_rdwr_02	2002Oct5
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0	rbbm_cp_queued_holds_nonqueued	2002Oct5
PASS		
0	rbbm_cp_queued_rdwr	2002Oct5
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0	rbbm_hi_data_and_be_swap	2002Oct5

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0	rbbm_hi_data_endian_swap	20020ct5
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0	rbbm_hi_data_no_swap_for_cppush	20020ct5
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0	rbbm_hi_data_swap_for_cppush	20020ct5
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0	rbbm_int_ack	20020ct5
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0	rbbm_int_each_client	20020ct5
PASS		
0	rbbm_int_nrt_gfxpipe_idle	20020ct5
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	20020ct5
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0	rbbm_int_rderr	20020ct5
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	20020ct5
PASS		
0	rbbm_isync_cpscratch_idlegui	20020ct5
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0	rbbm_isync_wait_idlegui	20020ct5
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0	rbbm_soft_reset	20020ct5
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0	rbbm_vgt_default_req	20020ct5
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0	rbbm_vgt_max_req	20020ct5
PASS		
0	rbbm_vgt_min_req	20020ct5
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0	reg_to_mem	20020ct5
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0	reg_to_mem_rt	20020ct5
PASS		
0	ring_i1_pio_i2_bm	20020ct5
PASS		
0	ring_pio_ibs_bm_stress	20020ct5
PASS		
0	rop_cfd_wmd	20020ct5
PASS		
0	roq_reg_reads	20020ct5
PASS		
0	rstr_ordr	20020ct5
PASS		
0	rt_invalid_opcode	20020ct5
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0	rtee_arm_wait_idle_trigger_events_16	20020ct5

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0	rtee_discretes_arm_rearm_events_32	2002Oct5
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0	rtee_discretes_falling_00_events_16	2002Oct5
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0	rtee_discretes_falling_01_events_16	2002Oct5
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0	rtee_discretes_falling_02_events_16	2002Oct5
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0	rtee_discretes_falling_15_events_16	2002Oct5
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0	rtee_discretes_falling_31_events_16	2002Oct5
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0	rtee_discretes_invert_31_events_16	2002Oct5
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0	rtee_discretes_level_31_events_16	2002Oct5
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0	rtee_discretes_rising_00_events_16	2002Oct5
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0	rtee_discretes_rising_31_events_16	2002Oct5
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0	rtee_display1_comp_events_16	2002Oct5
PASS		
0	rtee_display2_comp_events_16	2002Oct5
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0	rtee_mem_linear_search_events_2	2002Oct5
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0	rtee_mem_poll_comp_000_events_16	2002Oct5
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0	rtee_mem_poll_comp_001_events_16	2002Oct5
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0	rtee_mem_poll_comp_010_events_16	2002Oct5
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0	rtee_mem_poll_comp_011_events_16	2002Oct5
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0	rtee_mem_poll_comp_100_events_16	2002Oct5
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0	rtee_mem_poll_comp_101_events_16	2002Oct5
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0	rtee_mem_poll_comp_110_events_16	2002Oct5
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0	rtee_mem_poll_comp_111_events_16	2002Oct5
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0	rtee_mem_poll_comp_masks_events_16	2002Oct5
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0	rtee_mem_poll_comp_masks_events_16_random	2002Oct5
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct5

PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct5
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct5
PASS		
0	rtee_pio	2002Oct5
PASS		
0	rtee_prefetch_parse_nops	2002Oct5
PASS		
0	rtee_reg_linear_search_events_2	2002Oct5
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Oct5
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Oct5
PASS		
0	rtee_vip_comp_events_16	2002Oct5
PASS		
0	scratch_mem_read	2002Oct5
PASS		
0	scratch_mem_read2	2002Oct5
PASS		
0	set_cnst_alu_max_random	2002Oct5
PASS		
0	set_cnst_we_lcc_random	2002Oct5
PASS		
0	set_cnst_we_random	2002Oct5
PASS		
0	set_constant	2002Oct5
PASS		
0	set_constant2	2002Oct5
PASS		
0	set_constant_alu_max1	2002Oct5
PASS		
0	set_constant_alu_max2	2002Oct5

PASS		
0	set_constant_bool_max1	20020ct5
PASS		
0	set_constant_bool_max2	20020ct5
PASS		
0	set_constant_indr1_short	20020ct5
PASS		
0	set_constant_indr1_short2	20020ct5
PASS		
0	set_constant_indr1_write_enable	20020ct5
PASS		
0	set_constant_indr1_write_enable_lcc	20020ct5
PASS		
0	set_constant_indr2_short	20020ct5
PASS		
0	set_constant_indr2_short2	20020ct5
PASS		
0	set_constant_indr2_write_enable	20020ct5
PASS		
0	set_constant_indr2_write_enable_lcc	20020ct5
PASS		
0	set_constant_loop_max1	20020ct5
PASS		
0	set_constant_loop_max2	20020ct5
PASS		
0	set_constant_ring_short	20020ct5
PASS		
0	set_constant_ring_short2	20020ct5
PASS		
0	set_constant_ring_write_enable	20020ct5
PASS		
0	set_constant_ring_write_enable_lcc	20020ct5
PASS		
0	set_constant_short	20020ct5
PASS		
0	set_constant_short2	20020ct5
PASS		
0	set_constant_tex_max1	20020ct5
PASS		
0	set_constant_tex_max2	20020ct5
PASS		
0	set_constant_write_enable	20020ct5
PASS		
0	set_constant_write_enable_lcc	20020ct5
PASS		
0	set_constant_write_enable_lcc2	20020ct5
PASS		
0	set_scissors	20020ct5

PASS		
0	set_st_indrl_match	2002Oct5
PASS		
0	set_st_indrl_mismatch	2002Oct5
PASS		
0	set_st_indrl_mismatch2	2002Oct5
PASS		
0	set_st_indrl_mismatch3	2002Oct5
PASS		
0	set_st_indrl_mismatch4	2002Oct5
PASS		
0	set_st_indrl_ps	2002Oct5
PASS		
0	set_st_indrl_ps_only	2002Oct5
PASS		
0	set_st_indrl_sublk0	2002Oct5
PASS		
0	set_st_indrl_sublk1	2002Oct5
PASS		
0	set_st_indrl_sublk1_only	2002Oct5
PASS		
0	set_st_indrl_sublk2	2002Oct5
PASS		
0	set_st_indrl_sublk2_only	2002Oct5
PASS		
0	set_st_indrl_sublk3	2002Oct5
PASS		
0	set_st_indrl_sublk3_only	2002Oct5
PASS		
0	set_st_indrl_sublk4	2002Oct5
PASS		
0	set_st_indrl_sublk4_only	2002Oct5
PASS		
0	set_st_indrl_sublk5	2002Oct5
PASS		
0	set_st_indrl_sublk5_only	2002Oct5
PASS		
0	set_st_indrl_sublk6	2002Oct5
PASS		
0	set_st_indrl_sublk6_only	2002Oct5
PASS		
0	set_st_indrl_sublk7	2002Oct5
PASS		
0	set_st_indrl_sublk7_only	2002Oct5
PASS		
0	set_st_indrl_vs	2002Oct5
PASS		
0	set_st_indrl_vs_only	2002Oct5

PASS		
0	set_st_indr2_match	2002Oct5
PASS		
0	set_st_indr2_mismatch	2002Oct5
PASS		
0	set_st_indr2_mismatch2	2002Oct5
PASS		
0	set_st_indr2_mismatch3	2002Oct5
PASS		
0	set_st_indr2_mismatch4	2002Oct5
PASS		
0	set_st_indr2_ps	2002Oct5
PASS		
0	set_st_indr2_ps_only	2002Oct5
PASS		
0	set_st_indr2_sublk0	2002Oct5
PASS		
0	set_st_indr2_sublk1	2002Oct5
PASS		
0	set_st_indr2_sublk1_only	2002Oct5
PASS		
0	set_st_indr2_sublk2	2002Oct5
PASS		
0	set_st_indr2_sublk2_only	2002Oct5
PASS		
0	set_st_indr2_sublk3	2002Oct5
PASS		
0	set_st_indr2_sublk3_only	2002Oct5
PASS		
0	set_st_indr2_sublk4	2002Oct5
PASS		
0	set_st_indr2_sublk4_only	2002Oct5
PASS		
0	set_st_indr2_sublk5	2002Oct5
PASS		
0	set_st_indr2_sublk5_only	2002Oct5
PASS		
0	set_st_indr2_sublk6	2002Oct5
PASS		
0	set_st_indr2_sublk6_only	2002Oct5
PASS		
0	set_st_indr2_sublk7	2002Oct5
PASS		
0	set_st_indr2_sublk7_only	2002Oct5
PASS		
0	set_st_indr2_vs	2002Oct5
PASS		
0	set_st_indr2_vs_only	2002Oct5

PASS		
0	set_st_pfmatch_disabled	20020ct5
PASS		
0	set_st_ring_match	20020ct5
PASS		
0	set_st_ring_mismatch	20020ct5
PASS		
0	set_st_ring_mismatch2	20020ct5
PASS		
0	set_st_ring_mismatch3	20020ct5
PASS		
0	set_st_ring_mismatch4	20020ct5
PASS		
0	set_st_ring_ps	20020ct5
PASS		
0	set_st_ring_ps_only	20020ct5
PASS		
0	set_st_ring_subblk0	20020ct5
PASS		
0	set_st_ring_subblk1	20020ct5
PASS		
0	set_st_ring_subblk1_only	20020ct5
PASS		
0	set_st_ring_subblk2	20020ct5
PASS		
0	set_st_ring_subblk2_only	20020ct5
PASS		
0	set_st_ring_subblk3	20020ct5
PASS		
0	set_st_ring_subblk3_only	20020ct5
PASS		
0	set_st_ring_subblk4	20020ct5
PASS		
0	set_st_ring_subblk4_only	20020ct5
PASS		
0	set_st_ring_subblk5	20020ct5
PASS		
0	set_st_ring_subblk5_only	20020ct5
PASS		
0	set_st_ring_subblk6	20020ct5
PASS		
0	set_st_ring_subblk6_only	20020ct5
PASS		
0	set_st_ring_subblk7	20020ct5
PASS		
0	set_st_ring_subblk7_only	20020ct5
PASS		
0	set_st_ring_vs	20020ct5

PASS		
0	set_st_ring_vs_only	2002Oct5
PASS		
0	set_st_stress_a	2002Oct5
PASS		
0	set_st_stress_b	2002Oct5
PASS		
744	set_st_stress_c	2002Oct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
745	set_st_stress_d	2002Oct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
746	set_st_stress_e	2002Oct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_f	2002Oct5
PASS		
0	set_state_rt_a	2002Oct5
PASS		
749	set_state_rt_b	2002Oct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
750	set_state_rt_c	2002Oct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
751	set_state_rt_d	2002Oct5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	smoke_nrt	2002Oct5
PASS		
0	src_clip	2002Oct5
PASS		
0	srf_cohr	2002Oct5
PASS		
0	switch_pio_bm	2002Oct5
PASS		
0	trans_bitblt	2002Oct5
PASS		
0	type0_2long_pckts	2002Oct5
PASS		
0	type0_3indr_buffs	2002Oct5
PASS		
0	type0_4pckts	2002Oct5
PASS		
0	type0_incr_updt_cnst	2002Oct5
PASS		
0	type0_incr_updt_instrmem	2002Oct5
PASS		
0	type0_incr_updt_st	2002Oct5
PASS		
0	type0_incr_updt_stress	2002Oct5
PASS		
0	type0_ind1_ind2_a	2002Oct5

PASS		
0	type0_ind1_ind2_b	20020ct5
PASS		
0	type0_indr_buff	20020ct5
PASS		
0	type1_4pckts	20020ct5
PASS		
0	type1_indr_buff	20020ct5
PASS		
0	type1_pckt	20020ct5
PASS		
0	type_random	20020ct5
PASS		
771	uram_wrt_rt_ucose	20020ct5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
772	uram_wrt_ucose	20020ct5
FAIL	rbbmWr.cmp	
0	viz_query_a	20020ct5
PASS		
0	viz_query_draw_indx	20020ct5
PASS		
0	viz_query_draw_indx_indr1	20020ct5
PASS		
0	viz_query_draw_indx_indr2	20020ct5
PASS		
0	viz_query_draw_indx_multipass	20020ct5
PASS		
0	viz_query_draw_indx_ring	20020ct5
PASS		
0	viz_query_indr1	20020ct5
PASS		
0	viz_query_indr1_simple	20020ct5
PASS		
0	viz_query_indr2	20020ct5
PASS		
0	viz_query_indr2_simple	20020ct5
PASS		
0	viz_query_ring	20020ct5
PASS		
0	viz_query_ring_simple	20020ct5
PASS		
0	wait_for_idle	20020ct5
PASS		
0	wait_reg_mem_poll_mem0	20020ct5
PASS		
0	wait_reg_mem_poll_mem0_rt	20020ct5
PASS		
0	wait_reg_mem_poll_mem1	20020ct5

```
PASS
0 wait_reg_mem_poll_mem2 2002Oct5
PASS
0 wait_reg_mem_poll_mem3 2002Oct5
PASS
0 wait_reg_mem_poll_reg0 2002Oct5
PASS
0 wait_reg_mem_poll_reg1 2002Oct5
PASS
0 wait_reg_mem_poll_reg2 2002Oct5
PASS
0 wait_reg_mem_poll_reg3 2002Oct5
PASS
```

```
=====
Tests_Run_count = 794 Pass_count = 776 Fail_count = 18 Incomplete_count = 0
=====
```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep3
PASS			
0	2d_indr_buffer_b		2002Sep3
PASS			
0	2d_indr_buffer_c		2002Sep3
PASS			
0	2d_indr_buffer_d		2002Sep3
PASS			
0	2d_indr_buffer_e		2002Sep3
PASS			
0	2d_indr_buffer_f		2002Sep3
PASS			
0	2d_indr_buffer_g		2002Sep3
PASS			
0	3d_draw_indx_2		2002Sep3
PASS			
0	3d_draw_indx_2_b		2002Sep3
PASS			
0	bitblt_multi		2002Sep3
PASS			
0	brush_32x1mono		2002Sep3
PASS			
0	brush_8x8color_16		2002Sep3
PASS			
0	brush_8x8color_32		2002Sep3
PASS			
0	brush_8x8color_64		2002Sep3
PASS			
0	brush_8x8color_dst_0_1		2002Sep3
PASS			
0	brush_8x8mono		2002Sep3
PASS			
0	brush_color_mono_stress		2002Sep3
PASS			
0	brush_flag		2002Sep3
PASS			
0	brush_type0		2002Sep3
PASS			
0	brush_type1		2002Sep3

PASS		
0	brush_type10	2002Sep3
PASS		
0	brush_type13	2002Sep3
PASS		
0	brush_type14	2002Sep3
PASS		
0	brush_type6	2002Sep3
PASS		
0	brush_type7	2002Sep3
PASS		
0	brush_type_other	2002Sep3
PASS		
0	brush_types_13_14	2002Sep3
PASS		
0	clear_const_wr_en_on_me_init	2002Sep3
PASS		
0	clear_const_wr_en_with_lcc	2002Sep3
PASS		
0	cond_write_mem_poll_mem	2002Sep3
PASS		
0	cond_write_mem_poll_mem2	2002Sep3
PASS		
0	cond_write_mem_poll_mem3	2002Sep3
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep3
PASS		
0	cond_write_mem_poll_reg	2002Sep3
PASS		
0	cond_write_mem_poll_reg2	2002Sep3
PASS		
0	cond_write_mem_poll_reg3	2002Sep3
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep3
PASS		
0	cp_bios_scratch	2002Sep3
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep3
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep3
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep3
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep3
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep3
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep3

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep3
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep3
PASS		
0	cp_ib_base000_bufszXXX	2002Sep3
PASS		
0	cp_ib_base001_bufszXXX	2002Sep3
PASS		
0	cp_ib_base010_bufszXXX	2002Sep3
PASS		
0	cp_ib_base011_bufszXXX	2002Sep3
PASS		
0	cp_ib_base100_bufszXXX	2002Sep3
PASS		
0	cp_ib_base101_bufszXXX	2002Sep3
PASS		
0	cp_ib_base110_bufszXXX	2002Sep3
PASS		
0	cp_ib_base111_bufszXXX	2002Sep3
PASS		
0	cp_interrupt	2002Sep3
PASS		
0	cp_interrupt_rt_events_16	2002Sep3
PASS		
0	cp_iq2_alignment_000	2002Sep3
PASS		
0	cp_iq2_alignment_001	2002Sep3
PASS		
0	cp_iq2_alignment_010	2002Sep3
PASS		
0	cp_iq2_alignment_011	2002Sep3
PASS		
0	cp_iq2_alignment_100	2002Sep3
PASS		
0	cp_iq2_alignment_101	2002Sep3
PASS		
0	cp_iq2_alignment_110	2002Sep3
PASS		
0	cp_iq2_alignment_111	2002Sep3
PASS		
0	cp_iq_alignment_000	2002Sep3
PASS		
0	cp_iq_alignment_001	2002Sep3
PASS		
0	cp_iq_alignment_010	2002Sep3
PASS		
0	cp_iq_alignment_011	2002Sep3

PASS		
0	cp_iq_alignment_100	2002Sep3
PASS		
0	cp_iq_alignment_101	2002Sep3
PASS		
0	cp_iq_alignment_110	2002Sep3
PASS		
0	cp_iq_alignment_111	2002Sep3
PASS		
0	cp_max_size_packet	2002Sep3
PASS		
0	cp_max_size_packet_random	2002Sep3
PASS		
0	cp_nrt_rt_protection_mode	2002Sep3
PASS		
0	cp_output_stress	2002Sep3
PASS		
0	cp_output_stress_random	2002Sep3
PASS		
0	cp_protection_mode	2002Sep3
PASS		
0	cp_rb_alignment_000	2002Sep3
PASS		
0	cp_rb_alignment_001	2002Sep3
PASS		
0	cp_rb_alignment_010	2002Sep3
PASS		
0	cp_rb_alignment_011	2002Sep3
PASS		
0	cp_rb_alignment_100	2002Sep3
PASS		
0	cp_rb_alignment_101	2002Sep3
PASS		
0	cp_rb_alignment_110	2002Sep3
PASS		
0	cp_rb_alignment_111	2002Sep3
PASS		
0	cp_rb_wptrXXX_base000	2002Sep3
PASS		
0	cp_rb_wptrXXX_base001	2002Sep3
PASS		
0	cp_rb_wptrXXX_base010	2002Sep3
PASS		
0	cp_rb_wptrXXX_base011	2002Sep3
PASS		
0	cp_rb_wptrXXX_base100	2002Sep3
PASS		
0	cp_rb_wptrXXX_base101	2002Sep3

PASS		
0	cp_rb_wptrXXX_base110	2002Sep3
PASS		
0	cp_rb_wptrXXX_base111	2002Sep3
PASS		
0	cp_register_reads	2002Sep3
PASS		
0	cp_reg_rw_test	2002Sep3
PASS		
0	cp_rt_protection_mode	2002Sep3
PASS		
0	cp_scratch	2002Sep3
PASS		
0	cp_scratch_multi	2002Sep3
PASS		
0	dma_cp_sync	2002Sep3
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep3
PASS		
0	dma_dscrstr_flush1	2002Sep3
PASS		
0	dma_dscrstr_flush2	2002Sep3
PASS		
0	dma_dscrstr_flush3	2002Sep3
PASS		
0	dma_m2m_src00_dstXX	2002Sep3
PASS		
0	dma_m2m_src01_dstXX	2002Sep3
PASS		
0	dma_m2m_src10_dstXX	2002Sep3
PASS		
0	dma_m2m_src11_dstXX	2002Sep3
PASS		
0	dma_mem2mem	2002Sep3
PASS		
0	dma_mem2mem_abort	2002Sep3
PASS		
0	dma_mem2mem_alignments	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep3

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep3
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep3
PASS		
0	dma_mem2mem_pio_abort	2002Sep3
PASS		
0	dma_mem2mem_random	2002Sep3
PASS		
0	dma_mem2reg	2002Sep3
PASS		
0	dma_mem2reg_abort	2002Sep3
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep3
PASS		
125	dma_mem2reg_dtaq_stress_random	2002Sep3
FAIL	mh_cp_rd.cmp	
0	dma_mem2reg_pio_stressbm	2002Sep3
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep3
PASS		
0	dma_mem2reg_sync	2002Sep3
PASS		
0	dma_r2r_m2m_r2r	2002Sep3
PASS		
0	dma_r2r_m2r_r2r	2002Sep3
PASS		
0	dma_r2r_r2m_r2r	2002Sep3
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep3
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep3
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep3
PASS		
0	dma_reg2mem	2002Sep3
PASS		
0	dma_reg2mem_abort	2002Sep3
PASS		
0	dma_reg2reg	2002Sep3
PASS		
0	dma_reg2reg_2dw	2002Sep3
PASS		
0	dma_reg2reg_daic	2002Sep3
PASS		
0	dma_reg2reg_mult	2002Sep3

PASS		
0	dma_reg2reg_mult2	2002Sep3
PASS		
0	dma_reg2reg_mult3	2002Sep3
PASS		
0	dma_reg2reg_saic	2002Sep3
PASS		
0	dma_reg2reg_sdaic	2002Sep3
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep3
PASS		
0	dma_reg2reg_stress	2002Sep3
PASS		
0	draw_indx_a	2002Sep3
PASS		
0	draw_indx_b	2002Sep3
PASS		
0	draw_indx_c	2002Sep3
PASS		
0	draw_indx_indr1_a	2002Sep3
PASS		
0	draw_indx_indr1_b	2002Sep3
PASS		
0	draw_indx_indr1_short_a	2002Sep3
PASS		
0	draw_indx_indr1_short_b	2002Sep3
PASS		
0	draw_indx_indr1_stress	2002Sep3
PASS		
0	draw_indx_indr2_a	2002Sep3
PASS		
0	draw_indx_indr2_b	2002Sep3
PASS		
0	draw_indx_indr2_short_a	2002Sep3
PASS		
0	draw_indx_indr2_short_b	2002Sep3
PASS		
0	draw_indx_indr2_stress	2002Sep3
PASS		
0	draw_indx_ring1	2002Sep3
PASS		
0	draw_indx_ring2	2002Sep3
PASS		
0	draw_indx_ring_short1	2002Sep3
PASS		
0	draw_indx_ring_short2	2002Sep3
PASS		
0	draw_indx_ring_stress	2002Sep3

PASS		
0	draw_indx_short_a	2002Sep3
PASS		
0	draw_indx_short_b	2002Sep3
PASS		
0	draw_indx_short_c	2002Sep3
PASS		
0	draw_indx_stress_a	2002Sep3
PASS		
0	draw_indx_stress_b	2002Sep3
PASS		
0	draw_indx_stress_c	2002Sep3
PASS		
0	event_timestamp_write	2002Sep3
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep3
PASS		
0	ft_indr1_july22	2002Sep3
PASS		
0	ft_indr2_july22	2002Sep3
PASS		
0	ft_july22	2002Sep3
PASS		
0	ft_july22_random	2002Sep3
PASS		
0	header_dump	2002Sep3
PASS		
0	header_dump_rt	2002Sep3
PASS		
0	header_dump_small	2002Sep3
PASS		
0	hostdata_blt	2002Sep3
PASS		
0	im_load_a	2002Sep3
PASS		
0	im_load_b	2002Sep3
PASS		
0	im_load_cid_mismatch	2002Sep3
PASS		
0	im_load_immediate	2002Sep3
PASS		
0	im_load_immediate_nrt_me	2002Sep3
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep3
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep3
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep3

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep3
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep3
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep3
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep3
PASS		
0	im_load_indr1_a	2002Sep3
PASS		
0	im_load_indr1_b	2002Sep3
PASS		
0	im_load_indr2_a	2002Sep3
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0	im_load_indr2_b	2002Sep3
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0	im_load_invalidate	2002Sep3
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0	im_load_match	2002Sep3
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0	im_load_match2	2002Sep3
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0	im_load_nrt_me	2002Sep3
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0	im_load_nrt_me_ps_nowrap	2002Sep3
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0	im_load_nrt_me_ps_wrap	2002Sep3
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0	im_load_nrt_me_vs_nowrap	2002Sep3
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0	im_load_nrt_me_vs_wrap	2002Sep3
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0	im_load_pfmatch_disable	2002Sep3
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0	im_load_ring_a	2002Sep3
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0	im_load_rt2	2002Sep3
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0	im_load_rt3	2002Sep3
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0	im_load_rt4	2002Sep3
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0	im_load_sz_mismatch	2002Sep3

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0	incremental_update_instr	2002Sep3
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0	incremental_update_state	2002Sep3
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0	indirect_buffer_i	2002Sep3
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0	indirect_buffer_j	2002Sep3
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0	indirect_buffer_k	2002Sep3
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0	indirect_buffer_l	2002Sep3
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0	indirect_buffer_mp_a	2002Sep3
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0	indirect_buffer_mp_f	2002Sep3
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0	invalidate_state_shaders	2002Sep3

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0	invalidate_state_sublks	2002Sep3
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0	invalidate_state_sublks2	2002Sep3
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0	invalid_opcode	2002Sep3
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0	iq1_iq2_mult	2002Sep3
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0	iq1_iq2_push_wrap	2002Sep3
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0	iq2_csq_full	2002Sep3
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0	IQ2_Mult	2002Sep3
PASS		
0	IQ2_Wrap_Legacy	2002Sep3
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0	IQ_Wrap	2002Sep3
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0	ld_cnst_cntx_coherency_cntr2	2002Sep3
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0	ld_cnst_cntx_indr1_replicate8	2002Sep3
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0	ld_cnst_cntx_indr1_simple	2002Sep3
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0	ld_cnst_cntx_indr2_replicate4	2002Sep3
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0	ld_cnst_cntx_indr2_replicate8	2002Sep3
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0	ld_cnst_cntx_indr2_simple	2002Sep3
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0	ld_cnst_cntx_replicate4_tex	2002Sep3
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0	ld_cnst_cntx_ring_replicate8	2002Sep3
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0	mpeg_index_multi_context_max_context_6	2002Sep3
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0	mpeg_index_multi_context_max_context_7	2002Sep3
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0	rbbm_cp_non_queued_rdwr_02	2002Sep3
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0	rbbm_cp_queued_holds_nonqueued	2002Sep3
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0	rbbm_cp_queued_rdwr	2002Sep3
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0	rbbm_soft_reset	2002Sep3
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0	rbbm_WU_3D_idle_clean_12	2002Sep3
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0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep3
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0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep3
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0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep3
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0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep3
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep3
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0	rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Sep3

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0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep3
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0	rbbm_WU_idct_semaphore	2002Sep3
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0	rbbm_WU_MH_hdp_clean	2002Sep3
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0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep3
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0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep3
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0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep3
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0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep3
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0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep3
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0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep3
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep3
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0	rbbm_WUrt_MH_hdp_clean	2002Sep3
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0	rbbm_WUrt_VIPH1_dma_idle	2002Sep3
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0	rbbm_WUrt_VIPH2_dma_idle	2002Sep3
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0	rbbm_WUrt_VIPH3_dma_idle	2002Sep3
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0	rbbm_WU_VIPH2_dma_idle	2002Sep3
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0	RB_BUFSZ_1	2002Sep3
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0	reg_to_mem_rt	2002Sep3
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0	rtee_discretes_rising_14_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep3

PASS		
0	rtee_discretes_rising_24_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep3
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep3
PASS		
0	rtee_display1_comp_events_16	2002Sep3
PASS		
0	rtee_display2_comp_events_16	2002Sep3
PASS		
0	rtee_mem_linear_search_events_2	2002Sep3
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep3
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep3
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep3
PASS		
0	rtee_prefetch_parse_nops	2002Sep3
PASS		
0	rtee_reg_linear_search_events_2	2002Sep3

PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep3
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep3
PASS		
0	rtee_vip_comp_events_16	2002Sep3
PASS		
0	rt_invalid_opcode	2002Sep3
PASS		
0	scratch_mem_read	2002Sep3
PASS		
0	scratch_mem_read2	2002Sep3
PASS		
0	set_constant	2002Sep3
PASS		
0	set_constant2	2002Sep3
PASS		
0	set_constant_alu_max1	2002Sep3
PASS		
0	set_constant_alu_max2	2002Sep3
PASS		
0	set_constant_bool_max1	2002Sep3
PASS		
0	set_constant_bool_max2	2002Sep3
PASS		
0	set_constant_indr1_short	2002Sep3
PASS		
0	set_constant_indr1_short2	2002Sep3
PASS		
0	set_constant_indr1_write_enable	2002Sep3
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep3
PASS		
0	set_constant_indr2_short	2002Sep3

PASS		
0	set_constant_indr2_short2	2002Sep3
PASS		
0	set_constant_indr2_write_enable	2002Sep3
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep3
PASS		
0	set_constant_loop_max1	2002Sep3
PASS		
0	set_constant_loop_max2	2002Sep3
PASS		
0	set_constant_ring_short	2002Sep3
PASS		
0	set_constant_ring_short2	2002Sep3
PASS		
0	set_constant_ring_write_enable	2002Sep3
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep3
PASS		
0	set_constant_short	2002Sep3
PASS		
0	set_constant_short2	2002Sep3
PASS		
0	set_constant_tex_max1	2002Sep3
PASS		
0	set_constant_tex_max2	2002Sep3
PASS		
0	set_constant_write_enable	2002Sep3
PASS		
0	set_constant_write_enable_lcc	2002Sep3
PASS		
0	set_constant_write_enable_lcc2	2002Sep3
PASS		
0	set_scissors	2002Sep3
PASS		
0	set_state_rt_a	2002Sep3
PASS		
0	set_state_rt_b	2002Sep3
PASS		
0	set_state_rt_c	2002Sep3
PASS		
0	set_state_rt_d	2002Sep3
PASS		
0	set_st_indr1_match	2002Sep3
PASS		
0	set_st_indr1_mismatch	2002Sep3
PASS		
0	set_st_indr1_mismatch2	2002Sep3

PASS		
0	set_st_indr1_mismatch3	2002Sep3
PASS		
0	set_st_indr1_mismatch4	2002Sep3
PASS		
0	set_st_indr1_ps	2002Sep3
PASS		
0	set_st_indr1_ps_only	2002Sep3
PASS		
0	set_st_indr1_sublk0	2002Sep3
PASS		
0	set_st_indr1_sublk1	2002Sep3
PASS		
0	set_st_indr1_sublk1_only	2002Sep3
PASS		
0	set_st_indr1_sublk2	2002Sep3
PASS		
0	set_st_indr1_sublk2_only	2002Sep3
PASS		
0	set_st_indr1_sublk3	2002Sep3
PASS		
0	set_st_indr1_sublk3_only	2002Sep3
PASS		
0	set_st_indr1_sublk4	2002Sep3
PASS		
0	set_st_indr1_sublk4_only	2002Sep3
PASS		
0	set_st_indr1_sublk5	2002Sep3
PASS		
0	set_st_indr1_sublk5_only	2002Sep3
PASS		
0	set_st_indr1_sublk6	2002Sep3
PASS		
0	set_st_indr1_sublk6_only	2002Sep3
PASS		
0	set_st_indr1_sublk7	2002Sep3
PASS		
0	set_st_indr1_sublk7_only	2002Sep3
PASS		
0	set_st_indr1_vs	2002Sep3
PASS		
0	set_st_indr1_vs_only	2002Sep3
PASS		
0	set_st_indr2_match	2002Sep3
PASS		
0	set_st_indr2_mismatch	2002Sep3
PASS		
0	set_st_indr2_mismatch2	2002Sep3

PASS		
0	set_st_indr2_mismatch3	2002Sep3
PASS		
0	set_st_indr2_mismatch4	2002Sep3
PASS		
0	set_st_indr2_ps	2002Sep3
PASS		
0	set_st_indr2_ps_only	2002Sep3
PASS		
0	set_st_indr2_sublk0	2002Sep3
PASS		
0	set_st_indr2_sublk1	2002Sep3
PASS		
0	set_st_indr2_sublk1_only	2002Sep3
PASS		
0	set_st_indr2_sublk2	2002Sep3
PASS		
0	set_st_indr2_sublk2_only	2002Sep3
PASS		
0	set_st_indr2_sublk3	2002Sep3
PASS		
0	set_st_indr2_sublk3_only	2002Sep3
PASS		
0	set_st_indr2_sublk4	2002Sep3
PASS		
0	set_st_indr2_sublk4_only	2002Sep3
PASS		
0	set_st_indr2_sublk5	2002Sep3
PASS		
0	set_st_indr2_sublk5_only	2002Sep3
PASS		
0	set_st_indr2_sublk6	2002Sep3
PASS		
0	set_st_indr2_sublk6_only	2002Sep3
PASS		
0	set_st_indr2_sublk7	2002Sep3
PASS		
0	set_st_indr2_sublk7_only	2002Sep3
PASS		
0	set_st_indr2_vs	2002Sep3
PASS		
0	set_st_indr2_vs_only	2002Sep3
PASS		
0	set_st_pfmatch_disabled	2002Sep3
PASS		
0	set_st_ring_match	2002Sep3
PASS		
0	set_st_ring_mismatch	2002Sep3

PASS		
0	set_st_ring_mismatch2	2002Sep3
PASS		
0	set_st_ring_mismatch3	2002Sep3
PASS		
0	set_st_ring_mismatch4	2002Sep3
PASS		
0	set_st_ring_ps	2002Sep3
PASS		
0	set_st_ring_ps_only	2002Sep3
PASS		
0	set_st_ring_subblk0	2002Sep3
PASS		
0	set_st_ring_subblk1	2002Sep3
PASS		
0	set_st_ring_subblk1_only	2002Sep3
PASS		
0	set_st_ring_subblk2	2002Sep3
PASS		
0	set_st_ring_subblk2_only	2002Sep3
PASS		
0	set_st_ring_subblk3	2002Sep3
PASS		
0	set_st_ring_subblk3_only	2002Sep3
PASS		
0	set_st_ring_subblk4	2002Sep3
PASS		
0	set_st_ring_subblk4_only	2002Sep3
PASS		
0	set_st_ring_subblk5	2002Sep3
PASS		
0	set_st_ring_subblk5_only	2002Sep3
PASS		
0	set_st_ring_subblk6	2002Sep3
PASS		
0	set_st_ring_subblk6_only	2002Sep3
PASS		
0	set_st_ring_subblk7	2002Sep3
PASS		
0	set_st_ring_subblk7_only	2002Sep3
PASS		
0	set_st_ring_vs	2002Sep3
PASS		
0	set_st_ring_vs_only	2002Sep3
PASS		
0	set_st_stress_a	2002Sep3
PASS		
0	set_st_stress_b	2002Sep3

PASS		
0	set_st_stress_c	2002Sep3
PASS		
0	set_st_stress_d	2002Sep3
PASS		
0	set_st_stress_e	2002Sep3
PASS		
0	set_st_stress_f	2002Sep3
PASS		
0	Small_Text_BM	2002Sep3
PASS		
0	smoke_nrt	2002Sep3
PASS		
0	src_clip	2002Sep3
PASS		
0	srf_cohr	2002Sep3
PASS		
0	trans_bitblt	2002Sep3
PASS		
0	type0_2long_pckts	2002Sep3
PASS		
0	type0_3indr_buffs	2002Sep3
PASS		
0	type0_4pckts	2002Sep3
PASS		
0	type0_incr_updt_cnst	2002Sep3
PASS		
0	type0_incr_updt_instrmem	2002Sep3
PASS		
0	type0_incr_updt_st	2002Sep3
PASS		
0	type0_incr_updt_stress	2002Sep3
PASS		
0	type0_ind1_ind2_a	2002Sep3
PASS		
0	type0_ind1_ind2_b	2002Sep3
PASS		
0	type0_indr_buff	2002Sep3
PASS		
0	Type0_IQ2_BM	2002Sep3
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep3
PASS		
0	Type0_IQ_BM	2002Sep3
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep3
PASS		
0	Type0_PQ_BM	2002Sep3

PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep3
PASS		
0	type1_4pckts	2002Sep3
PASS		
0	type1_indr_buff	2002Sep3
PASS		
0	type1_pckt	2002Sep3
PASS		
0	Type3_Bitblt	2002Sep3
PASS		
0	Type3_Load_Palette	2002Sep3
PASS		
0	Type3_NOP	2002Sep3
PASS		
0	type_random	2002Sep3
PASS		
701	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
702	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep3
PASS		
0	uram_wrt_ucode	2002Sep3
PASS		
0	viz_query_a	2002Sep3
PASS		
0	viz_query_draw_indx	2002Sep3
PASS		
0	viz_query_draw_indx_indr1	2002Sep3
PASS		
0	viz_query_draw_indx_indr2	2002Sep3
PASS		
0	viz_query_draw_indx_multipass	2002Sep3
PASS		
0	viz_query_draw_indx_ring	2002Sep3
PASS		
0	viz_query_indr1	2002Sep3
PASS		
0	viz_query_indr1_simple	2002Sep3
PASS		
0	viz_query_indr2	2002Sep3
PASS		
0	viz_query_indr2_simple	2002Sep3
PASS		
0	viz_query_ring	2002Sep3
PASS		
0	viz_query_ring_simple	2002Sep3

PASS
0 wait_for_idle 2002Sep3
PASS
0 wait_reg_mem_poll_mem0 2002Sep3
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep3
PASS
0 wait_reg_mem_poll_mem1 2002Sep3
PASS
0 wait_reg_mem_poll_mem2 2002Sep3
PASS
0 wait_reg_mem_poll_mem3 2002Sep3
PASS
0 wait_reg_mem_poll_reg0 2002Sep3
PASS
0 wait_reg_mem_poll_reg1 2002Sep3
PASS
0 wait_reg_mem_poll_reg2 2002Sep3
PASS
0 wait_reg_mem_poll_reg3 2002Sep3
PASS

=====
Tests_Run_count = 726 Pass_count = 723 Fail_count = 3 Incomplete_count = 0
=====

=====
Regression Report For CP - Project = r400 Created on Tue Oct 15 23:45:04 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buf_d_sm_meq		2002Oct15
PASS			
0	2d_indr_buf_g_sm_meq		2002Oct15
PASS			
0	2d_indr_buffer_a		2002Oct15
PASS			
0	2d_indr_buffer_b		2002Oct15
PASS			
0	2d_indr_buffer_c		2002Oct15
PASS			
0	2d_indr_buffer_d		2002Oct15
PASS			
0	2d_indr_buffer_e		2002Oct15
PASS			
0	2d_indr_buffer_f		2002Oct15
PASS			
0	2d_indr_buffer_f_sm_roq		2002Oct15
PASS			
0	2d_indr_buffer_f_sm_roq2		2002Oct15
PASS			
0	2d_indr_buffer_g		2002Oct15
PASS			
0	2d_indr_buffer_random		2002Oct15
PASS			
0	2d_indr_buffer_random2		2002Oct15
PASS			
0	3d_draw_indx_2		2002Oct15
PASS			
0	3d_draw_indx_2_b		2002Oct15
PASS			
0	IQ2_Mult		2002Oct15
PASS			
0	IQ2_Wrap_Legacy		2002Oct15
PASS			
0	IQ_Wrap		2002Oct15
PASS			
19	Isync_Control		2002Oct15
FAIL	cp_rbbm.cmp rbbmWr.cmp		
20	Isync_Control_2D_3D		2002Oct15

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
21  Isync_Control_3D_2D                2002Oct15
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   RB_BUFSZ_0                          2002Oct15
PASS
0   RB_BUFSZ_1                          2002Oct15
PASS
0   RB_Wrap                              2002Oct15
PASS
0   Small_Text_BM                       2002Oct15
PASS
0   Type0_IQ2_BM                        2002Oct15
PASS
0   Type0_IQ2_BM_IB_Packet             2002Oct15
PASS
0   Type0_IQ_BM                         2002Oct15
PASS
0   Type0_IQ_BM_IB_Packet              2002Oct15
PASS
0   Type0_PQ_BM                        2002Oct15
PASS
0   Type0_PQ_BM_Pre_Write_Timer        2002Oct15
PASS
0   Type3_Bitblt                       2002Oct15
PASS
0   Type3_Hostdata_blt                 2002Oct15
PASS
0   Type3_Hostdata_blt_PIO            2002Oct15
PASS
0   Type3_Load_Palette                 2002Oct15
PASS
0   Type3_NOP                          2002Oct15
PASS
0   Type3_Polyline                    2002Oct15
PASS
0   Type3_Polyline_Large               2002Oct15
PASS
0   bitblt_multi                      2002Oct15
PASS
0   brush_32x1mono                    2002Oct15
PASS
0   brush_8x8color_16                 2002Oct15
PASS
0   brush_8x8color_32                 2002Oct15
PASS
0   brush_8x8color_64                 2002Oct15
PASS
0   brush_8x8color_dst_0_1            2002Oct15

```

PASS		
0	brush_8x8mono	2002Oct15
PASS		
0	brush_color_mono_stress	2002Oct15
PASS		
0	brush_flag	2002Oct15
PASS		
0	brush_type0	2002Oct15
PASS		
0	brush_type1	2002Oct15
PASS		
0	brush_type10	2002Oct15
PASS		
0	brush_type13	2002Oct15
PASS		
0	brush_type14	2002Oct15
PASS		
0	brush_type6	2002Oct15
PASS		
0	brush_type7	2002Oct15
PASS		
0	brush_type_other	2002Oct15
PASS		
0	brush_types_13_14	2002Oct15
PASS		
0	change_queue_sizes	2002Oct15
PASS		
0	clear_const_wr_en_on_me_init	2002Oct15
PASS		
0	clear_const_wr_en_with_lcc	2002Oct15
PASS		
0	cond_write_mem_poll_mem	2002Oct15
PASS		
0	cond_write_mem_poll_mem2	2002Oct15
PASS		
0	cond_write_mem_poll_mem3	2002Oct15
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct15
PASS		
0	cond_write_mem_poll_reg	2002Oct15
PASS		
0	cond_write_mem_poll_reg2	2002Oct15
PASS		
0	cond_write_mem_poll_reg3	2002Oct15
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct15
PASS		
0	cp_bios_scratch	2002Oct15

PASS		
0	cp_high_stress	2002Oct15
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base010_bufszXXX_sm_roq	2002Oct15
PASS		
0	cp_ib2_base011_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct15
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct15
PASS		
0	cp_ib_base000_bufszXXX	2002Oct15
PASS		
0	cp_ib_base001_bufszXXX	2002Oct15
PASS		
0	cp_ib_base010_bufszXXX	2002Oct15
PASS		
0	cp_ib_base011_bufszXXX	2002Oct15
PASS		
0	cp_ib_base100_bufszXXX	2002Oct15
PASS		
0	cp_ib_base101_bufszXXX	2002Oct15
PASS		
0	cp_ib_base101_bufszXXX_sm_roq	2002Oct15
PASS		
0	cp_ib_base110_bufszXXX	2002Oct15
PASS		
0	cp_ib_base111_bufszXXX	2002Oct15
PASS		
0	cp_interrupt	2002Oct15
PASS		
0	cp_interrupt_rt_events_16	2002Oct15
PASS		
0	cp_iq2_alignment_000	2002Oct15
PASS		
0	cp_iq2_alignment_001	2002Oct15
PASS		
0	cp_iq2_alignment_010	2002Oct15

PASS		
0	cp_iq2_alignment_011	2002Oct15
PASS		
0	cp_iq2_alignment_100	2002Oct15
PASS		
0	cp_iq2_alignment_101	2002Oct15
PASS		
0	cp_iq2_alignment_110	2002Oct15
PASS		
0	cp_iq2_alignment_111	2002Oct15
PASS		
0	cp_iq_alignment_000	2002Oct15
PASS		
0	cp_iq_alignment_001	2002Oct15
PASS		
0	cp_iq_alignment_010	2002Oct15
PASS		
0	cp_iq_alignment_011	2002Oct15
PASS		
0	cp_iq_alignment_100	2002Oct15
PASS		
0	cp_iq_alignment_101	2002Oct15
PASS		
0	cp_iq_alignment_110	2002Oct15
PASS		
0	cp_iq_alignment_111	2002Oct15
PASS		
0	cp_max_size_packet	2002Oct15
PASS		
0	cp_max_size_packet_random	2002Oct15
PASS		
0	cp_nrt_rt_protection_mode	2002Oct15
PASS		
0	cp_output_stress	2002Oct15
PASS		
0	cp_output_stress_random	2002Oct15
PASS		
0	cp_protection_mode	2002Oct15
PASS		
0	cp_rb_alignment_000	2002Oct15
PASS		
0	cp_rb_alignment_001	2002Oct15
PASS		
0	cp_rb_alignment_010	2002Oct15
PASS		
0	cp_rb_alignment_011	2002Oct15
PASS		
0	cp_rb_alignment_100	2002Oct15

PASS		
0	cp_rb_alignment_101	2002Oct15
PASS		
0	cp_rb_alignment_110	2002Oct15
PASS		
0	cp_rb_alignment_111	2002Oct15
PASS		
0	cp_rb_wptrXXX_base000	2002Oct15
PASS		
0	cp_rb_wptrXXX_base001	2002Oct15
PASS		
0	cp_rb_wptrXXX_base010	2002Oct15
PASS		
0	cp_rb_wptrXXX_base011	2002Oct15
PASS		
0	cp_rb_wptrXXX_base100	2002Oct15
PASS		
0	cp_rb_wptrXXX_base101	2002Oct15
PASS		
0	cp_rb_wptrXXX_base101_sm_roq	2002Oct15
PASS		
0	cp_rb_wptrXXX_base110	2002Oct15
PASS		
0	cp_rb_wptrXXX_base111	2002Oct15
PASS		
0	cp_reg_rw_test	2002Oct15
PASS		
0	cp_register_reads	2002Oct15
PASS		
0	cp_rt_protection_mode	2002Oct15
PASS		
0	cp_scratch	2002Oct15
PASS		
0	cp_scratch_multi	2002Oct15
PASS		
0	csf_reg_rd_default	2002Oct15
PASS		
135	csf_reg_rd_only	2002Oct15
FAIL		
0	csf_reg_wr_rd	2002Oct15
PASS		
0	dma_cp_sync	2002Oct15
PASS		
0	dma_dscrstr_chckn_bit	2002Oct15
PASS		
0	dma_dscrstr_flush1	2002Oct15
PASS		
0	dma_dscrstr_flush2	2002Oct15

PASS		
0	dma_dscrstr_flush3	2002Oct15
PASS		
0	dma_m2m_src00_dstXX	2002Oct15
PASS		
0	dma_m2m_src01_dstXX	2002Oct15
PASS		
0	dma_m2m_src10_dstXX	2002Oct15
PASS		
0	dma_m2m_src11_dstXX	2002Oct15
PASS		
0	dma_mem2mem	2002Oct15
PASS		
0	dma_mem2mem_abort	2002Oct15
PASS		
0	dma_mem2mem_alignments	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment1	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment2	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment3	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment4	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct15
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct15
PASS		
0	dma_mem2mem_large	2002Oct15
PASS		
0	dma_mem2mem_pio_abort	2002Oct15
PASS		
0	dma_mem2mem_random	2002Oct15
PASS		
0	dma_mem2reg	2002Oct15
PASS		
0	dma_mem2reg_abort	2002Oct15
PASS		
0	dma_mem2reg_dtaq_stress	2002Oct15
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Oct15
PASS		
0	dma_mem2reg_pio_stressbm	2002Oct15

PASS		
0	dma_mem2reg_pio_stresspio	2002Oct15
PASS		
0	dma_mem2reg_sync	2002Oct15
PASS		
0	dma_r2r_m2m_r2r	2002Oct15
PASS		
0	dma_r2r_m2r_r2r	2002Oct15
PASS		
0	dma_r2r_r2m_r2r	2002Oct15
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct15
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct15
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct15
PASS		
0	dma_reg2mem	2002Oct15
PASS		
0	dma_reg2mem_abort	2002Oct15
PASS		
0	dma_reg2reg	2002Oct15
PASS		
0	dma_reg2reg_2dw	2002Oct15
PASS		
0	dma_reg2reg_daic	2002Oct15
PASS		
0	dma_reg2reg_mult	2002Oct15
PASS		
0	dma_reg2reg_mult2	2002Oct15
PASS		
0	dma_reg2reg_mult3	2002Oct15
PASS		
0	dma_reg2reg_saic	2002Oct15
PASS		
0	dma_reg2reg_sdaic	2002Oct15
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct15
PASS		
0	dma_reg2reg_stress	2002Oct15
PASS		
0	draw_indx_a	2002Oct15
PASS		
0	draw_indx_b	2002Oct15
PASS		
0	draw_indx_c	2002Oct15
PASS		
0	draw_indx_indr1_a	2002Oct15

PASS		
0	draw_indx_indr1_b	2002Oct15
PASS		
0	draw_indx_indr1_short_a	2002Oct15
PASS		
0	draw_indx_indr1_short_b	2002Oct15
PASS		
0	draw_indx_indr1_stress	2002Oct15
PASS		
0	draw_indx_indr2_a	2002Oct15
PASS		
0	draw_indx_indr2_b	2002Oct15
PASS		
0	draw_indx_indr2_short_a	2002Oct15
PASS		
0	draw_indx_indr2_short_b	2002Oct15
PASS		
0	draw_indx_indr2_stress	2002Oct15
PASS		
0	draw_indx_random_a	2002Oct15
PASS		
0	draw_indx_random_b	2002Oct15
PASS		
0	draw_indx_random_c	2002Oct15
PASS		
0	draw_indx_ring1	2002Oct15
PASS		
0	draw_indx_ring2	2002Oct15
PASS		
0	draw_indx_ring_short1	2002Oct15
PASS		
0	draw_indx_ring_short2	2002Oct15
PASS		
0	draw_indx_ring_stress	2002Oct15
PASS		
0	draw_indx_short_a	2002Oct15
PASS		
0	draw_indx_short_b	2002Oct15
PASS		
0	draw_indx_short_c	2002Oct15
PASS		
0	draw_indx_stress_a	2002Oct15
PASS		
0	draw_indx_stress_b	2002Oct15
PASS		
0	draw_indx_stress_b_sm_meq	2002Oct15
PASS		
0	draw_indx_stress_b_sm_roq	2002Oct15

PASS		
0	draw_indx_stress_b_sm_roq2	2002Oct15
PASS		
0	draw_indx_stress_c	2002Oct15
PASS		
0	draw_indx_stress_c_sm_meq	2002Oct15
PASS		
0	event_timestamp_write	2002Oct15
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct15
PASS		
0	fix2flt_reg	2002Oct15
PASS		
0	ft_indr1_july22	2002Oct15
PASS		
0	ft_indr2_july22	2002Oct15
PASS		
0	ft_july22	2002Oct15
PASS		
0	ft_july22_random	2002Oct15
PASS		
0	header_dump	2002Oct15
PASS		
0	header_dump_rt	2002Oct15
PASS		
0	header_dump_small	2002Oct15
PASS		
0	hostdata_blt	2002Oct15
PASS		
0	im_ld_cid_mismatch_sm_meq	2002Oct15
PASS		
0	im_ld_invalidate_sm_meq	2002Oct15
PASS		
0	im_ld_match2_sm_meq	2002Oct15
PASS		
0	im_ld_match_sm_meq	2002Oct15
PASS		
0	im_ld_sz_mismatch_sm_meq	2002Oct15
PASS		
0	im_load_a	2002Oct15
PASS		
0	im_load_b	2002Oct15
PASS		
0	im_load_cid_mismatch	2002Oct15
PASS		
0	im_load_immediate	2002Oct15
PASS		
0	im_load_immediate_nrt_me	2002Oct15

PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct15
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct15
PASS		
239	im_load_immediate_nrt_me_ps_wrap	2002Oct15
FAIL	mh_cp_rd.cmp	
240	im_load_immediate_nrt_me_vs_nowrap	2002Oct15
FAIL	mh_cp_rd.cmp	
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct15
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct15
PASS		
243	im_load_immediate_nrt_me_vs_wrap	2002Oct15
FAIL	mh_cp_rd.cmp	
0	im_load_indr1_a	2002Oct15
PASS		
0	im_load_indr1_b	2002Oct15
PASS		
0	im_load_indr2_a	2002Oct15
PASS		
0	im_load_indr2_b	2002Oct15
PASS		
0	im_load_invalidate	2002Oct15
PASS		
0	im_load_invalidate_sm_roq	2002Oct15
PASS		
0	im_load_match	2002Oct15
PASS		
0	im_load_match2	2002Oct15
PASS		
0	im_load_nrt_me	2002Oct15
PASS		
253	im_load_nrt_me_ps_nowrap	2002Oct15
FAIL	cp_rbbm.cmp rbbmWr.cmp	
254	im_load_nrt_me_ps_wrap	2002Oct15
FAIL	cp_rbbm.cmp rbbmWr.cmp	
255	im_load_nrt_me_vs_nowrap	2002Oct15
FAIL	cp_rbbm.cmp rbbmWr.cmp	
256	im_load_nrt_me_vs_wrap	2002Oct15
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_pfmatch_disable	2002Oct15
PASS		
0	im_load_random_a	2002Oct15
PASS		
0	im_load_random_b	2002Oct15
PASS		
0	im_load_random_c	2002Oct15

PASS		
0	im_load_ring_a	2002Oct15
PASS		
0	im_load_ring_b	2002Oct15
PASS		
0	im_load_rt	2002Oct15
PASS		
0	im_load_rt2	2002Oct15
PASS		
0	im_load_rt3	2002Oct15
PASS		
0	im_load_rt3_random	2002Oct15
PASS		
0	im_load_rt4	2002Oct15
PASS		
0	im_load_rt4_random	2002Oct15
PASS		
0	im_load_sz_mismatch	2002Oct15
PASS		
0	incr_update_cnst_random	2002Oct15
PASS		
0	incr_update_const_sm_meq	2002Oct15
PASS		
0	incr_update_instr_sm_meq	2002Oct15
PASS		
0	incr_update_state_sm_meq	2002Oct15
PASS		
0	incremental_update_const	2002Oct15
PASS		
0	incremental_update_instr	2002Oct15
PASS		
0	incremental_update_state	2002Oct15
PASS		
0	incremental_update_state_sm_roq	2002Oct15
PASS		
0	indirect_buffer_a	2002Oct15
PASS		
0	indirect_buffer_b	2002Oct15
PASS		
0	indirect_buffer_c	2002Oct15
PASS		
0	indirect_buffer_d	2002Oct15
PASS		
282	indirect_buffer_e	2002Oct15
INCOMPLETE	rbbmWr.cmp	
0	indirect_buffer_f	2002Oct15
PASS		
0	indirect_buffer_f_sm_roq	2002Oct15

PASS		
0	indirect_buffer_g	2002Oct15
PASS		
0	indirect_buffer_h	2002Oct15
PASS		
0	indirect_buffer_i	2002Oct15
PASS		
0	indirect_buffer_j	2002Oct15
PASS		
0	indirect_buffer_k	2002Oct15
PASS		
0	indirect_buffer_l	2002Oct15
PASS		
0	indirect_buffer_mp_a	2002Oct15
PASS		
0	indirect_buffer_mp_b	2002Oct15
PASS		
0	indirect_buffer_mp_c	2002Oct15
PASS		
0	indirect_buffer_mp_d	2002Oct15
PASS		
0	indirect_buffer_mp_e	2002Oct15
PASS		
0	indirect_buffer_mp_f	2002Oct15
PASS		
0	indirect_buffer_mp_f_sm_roq	2002Oct15
PASS		
0	indirect_buffer_mp_random1	2002Oct15
PASS		
0	indirect_buffer_mp_random2	2002Oct15
PASS		
0	indirect_buffer_random1	2002Oct15
PASS		
0	indirect_buffer_random2	2002Oct15
PASS		
0	indr_buf_e_sm_meq	2002Oct15
PASS		
0	indr_buf_k_sm_meq	2002Oct15
PASS		
0	indr_buf_mp_c_sm_meq	2002Oct15
PASS		
0	indr_buf_mp_f_sm_meq	2002Oct15
PASS		
0	invalid_opcode	2002Oct15
PASS		
0	invalidate_on_me_init	2002Oct15
PASS		
0	invalidate_on_me_init_random	2002Oct15

PASS		
0	invalidate_on_me_init_sm_meq	2002Oct15
PASS		
0	invalidate_state_constants	2002Oct15
PASS		
0	invalidate_state_shaders	2002Oct15
PASS		
0	invalidate_state_shaders2	2002Oct15
PASS		
0	invalidate_state_shaders_random	2002Oct15
PASS		
0	invalidate_state_shaders_sm_meq	2002Oct15
PASS		
0	invalidate_state_sublks	2002Oct15
PASS		
0	invalidate_state_sublks2	2002Oct15
PASS		
0	invalidate_state_sublks2_sm_meq	2002Oct15
PASS		
0	invalidate_state_sublks_random	2002Oct15
PASS		
0	invalidate_state_sublks_sm_roq	2002Oct15
PASS		
0	iq1_iq2_mult	2002Oct15
PASS		
0	iq1_iq2_push_wrap	2002Oct15
PASS		
0	iq2_csq_full	2002Oct15
PASS		
0	lcc_coherency_cntrl_sm_meq	2002Oct15
PASS		
0	lcc_coherency_cntr2_sm_meq	2002Oct15
PASS		
325	lcc_coherency_cntr_random	2002Oct15
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	lcc_match_random	2002Oct15
PASS		
0	lcc_match_sm_meq	2002Oct15
PASS		
0	lcc_match_valid_preserved_sm_meq	2002Oct15
PASS		
0	lcc_mismatch_alutex_random	2002Oct15
PASS		
0	lcc_mismatch_alutex_sm_meq	2002Oct15
PASS		
0	lcc_mismatch_base_random	2002Oct15
PASS		
0	lcc_mismatch_base_sm_meq	2002Oct15

PASS		
0	lcc_rt1	2002Oct15
PASS		
0	lcc_rt2	2002Oct15
PASS		
0	lcc_rt3	2002Oct15
PASS		
0	lcc_rt3_random	2002Oct15
PASS		
0	lcc_rt4	2002Oct15
PASS		
0	lcc_rt4_random	2002Oct15
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct15
PASS		
0	ld_cnst_cntx_coherency_cntrl_sm_roq	2002Oct15
PASS		
0	ld_cnst_cntx_coherency_cntrl2	2002Oct15
PASS		
0	ld_cnst_cntx_coherency_cntrl2_sm_roq	2002Oct15
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct15
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct15
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct15
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct15
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct15
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct15
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct15
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct15
PASS		
0	ld_cnst_cntx_match	2002Oct15
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct15
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct15
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct15
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct15
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct15

PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct15
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct15
PASS		
0	ld_cnst_cntx_replicate8	2002Oct15
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct15
PASS		
0	ld_cnst_cntx_replicate_tex	2002Oct15
PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct15
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct15
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct15
PASS		
0	ld_cnst_cntx_ring_simple	2002Oct15
PASS		
0	ld_cnst_cntx_simple_alu	2002Oct15
PASS		
0	ld_cnst_cntx_simple_tex	2002Oct15
PASS		
0	load_constant_context_rt	2002Oct15
PASS		
0	load_palette	2002Oct15
PASS		
0	lp_2d3d	2002Oct15
PASS		
0	me_reg_wr_rd	2002Oct15
PASS		
0	mem_write	2002Oct15
PASS		
0	mem_write_rt	2002Oct15
PASS		
0	mpeg_index	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_0	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_1	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_2	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_3	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_4	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct15

PASS		
0	mpeg_index_multi_context_max_context_6	2002Oct15
PASS		
0	mpeg_index_multi_context_max_context_7	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct15
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct15
PASS		
0	nop_eating_disabled	2002Oct15
PASS		
0	nrt_rt_interaction1	2002Oct15
PASS		
0	nrt_rt_interaction1_random	2002Oct15
PASS		
0	nrt_rt_interaction2	2002Oct15
PASS		
0	nrt_rt_interaction3	2002Oct15
PASS		
0	nrt_rt_interaction4	2002Oct15
PASS		
0	paint_multi	2002Oct15
PASS		
0	paint_packet	2002Oct15
PASS		
0	paint_packet_gui_control	2002Oct15
PASS		
0	pfpr_reg_reads1	2002Oct15
PASS		
0	pfpr_reg_reads2	2002Oct15
PASS		
0	pitch_clip	2002Oct15
PASS		
0	ply_nextscan	2002Oct15
PASS		
0	rbbm_NQWU_BIF_01	2002Oct15
PASS		
0	rbbm_NQWU_BIF_02	2002Oct15

PASS		
0	rbbm_NQWU_CF_01	2002Oct15
PASS		
0	rbbm_NQWU_CPnq_01	2002Oct15
PASS		
0	rbbm_WU_2D_idle_01	2002Oct15
PASS		
0	rbbm_WU_2D_idle_02	2002Oct15
PASS		
0	rbbm_WU_2D_idle_03	2002Oct15
PASS		
0	rbbm_WU_2D_idle_04	2002Oct15
PASS		
0	rbbm_WU_2D_idle_05	2002Oct15
PASS		
0	rbbm_WU_2D_idle_06	2002Oct15
PASS		
0	rbbm_WU_2D_idle_07	2002Oct15
PASS		
0	rbbm_WU_2D_idle_08	2002Oct15
PASS		
0	rbbm_WU_2D_idle_09	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct15
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Oct15
PASS		
0	rbbm_WU_3D_idle_01	2002Oct15

PASS		
0	rbbm_WU_3D_idle_02	2002Oct15
PASS		
0	rbbm_WU_3D_idle_03	2002Oct15
PASS		
0	rbbm_WU_3D_idle_04	2002Oct15
PASS		
0	rbbm_WU_3D_idle_05	2002Oct15
PASS		
0	rbbm_WU_3D_idle_06	2002Oct15
PASS		
0	rbbm_WU_3D_idle_07	2002Oct15
PASS		
0	rbbm_WU_3D_idle_08	2002Oct15
PASS		
0	rbbm_WU_3D_idle_09	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct15
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct15
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct15
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Oct15
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct15
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct15

PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct15
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct15
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct15
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct15
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct15
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct15
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct15
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct15
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct15
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct15
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct15
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct15
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct15
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct15
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct15
PASS		
0	rbbm_WU_idct_semaphore	2002Oct15
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct15
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct15
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Oct15
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct15

PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct15
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct15
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct15
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct15
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_01	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_02	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_04	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Oct15
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Oct15
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Oct15
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Oct15
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Oct15
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Oct15
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Oct15
PASS		
0	rbbm_clk_active_deassert_min_max	2002Oct15
PASS		
0	rbbm_clk_active_each_pipe	2002Oct15
PASS		
0	rbbm_clk_active_for_read_error	2002Oct15

PASS		
0	rbbm_clk_active_for_read_request	2002Oct15
PASS		
0	rbbm_clk_active_on_reset	2002Oct15
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Oct15
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Oct15
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Oct15
PASS		
0	rbbm_cp_queued_rdwr	2002Oct15
PASS		
0	rbbm_hi_data_and_be_swap	2002Oct15
PASS		
0	rbbm_hi_data_endian_swap	2002Oct15
PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Oct15
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Oct15
PASS		
0	rbbm_int_ack	2002Oct15
PASS		
0	rbbm_int_each_client	2002Oct15
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Oct15
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Oct15
PASS		
0	rbbm_int_rderr	2002Oct15
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Oct15
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Oct15
PASS		
0	rbbm_isync_wait_idlegui	2002Oct15
PASS		
0	rbbm_soft_reset	2002Oct15
PASS		
0	rbbm_vgt_default_req	2002Oct15
PASS		
0	rbbm_vgt_max_req	2002Oct15
PASS		
0	rbbm_vgt_min_req	2002Oct15
PASS		
0	reg_to_mem	2002Oct15
PASS		
0	reg_to_mem_rt	2002Oct15

PASS		
0	ring_i1_pio_i2_bm	2002Oct15
PASS		
0	ring_pio_ibs_bm_stress	2002Oct15
PASS		
0	rop_cfd_wmd	2002Oct15
PASS		
0	roq_reg_reads	2002Oct15
PASS		
0	rstr_ordr	2002Oct15
PASS		
0	rt_invalid_opcode	2002Oct15
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct15
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct15
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct15

PASS		
0	rtee_discretes_falling_16_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct15
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct15

PASS		
0	rtee_discretes_invert_08_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_09_events_16	2002Oct15
PASS		
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PASS		
0	rtee_discretes_invert_11_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct15
PASS		
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PASS		
0	rtee_discretes_invert_25_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct15
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct15

PASS		
0	rtee_discretes_level_00_events_16	2002Oct15
PASS		
0	rtee_discretes_level_01_events_16	2002Oct15
PASS		
0	rtee_discretes_level_02_events_16	2002Oct15
PASS		
0	rtee_discretes_level_03_events_16	2002Oct15
PASS		
0	rtee_discretes_level_04_events_16	2002Oct15
PASS		
0	rtee_discretes_level_05_events_16	2002Oct15
PASS		
0	rtee_discretes_level_06_events_16	2002Oct15
PASS		
0	rtee_discretes_level_07_events_16	2002Oct15
PASS		
0	rtee_discretes_level_08_events_16	2002Oct15
PASS		
0	rtee_discretes_level_09_events_16	2002Oct15
PASS		
0	rtee_discretes_level_10_events_16	2002Oct15
PASS		
0	rtee_discretes_level_11_events_16	2002Oct15
PASS		
0	rtee_discretes_level_12_events_16	2002Oct15
PASS		
0	rtee_discretes_level_13_events_16	2002Oct15
PASS		
0	rtee_discretes_level_14_events_16	2002Oct15
PASS		
0	rtee_discretes_level_15_events_16	2002Oct15
PASS		
0	rtee_discretes_level_16_events_16	2002Oct15
PASS		
0	rtee_discretes_level_17_events_16	2002Oct15
PASS		
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PASS		
0	rtee_discretes_level_19_events_16	2002Oct15
PASS		
0	rtee_discretes_level_20_events_16	2002Oct15
PASS		
0	rtee_discretes_level_21_events_16	2002Oct15
PASS		
0	rtee_discretes_level_22_events_16	2002Oct15
PASS		
0	rtee_discretes_level_23_events_16	2002Oct15

PASS		
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PASS		
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PASS		
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PASS		
0	rtee_discretes_level_28_events_16	2002Oct15
PASS		
0	rtee_discretes_level_29_events_16	2002Oct15
PASS		
0	rtee_discretes_level_30_events_16	2002Oct15
PASS		
0	rtee_discretes_level_31_events_16	2002Oct15
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct15
PASS		
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PASS		
0	rtee_discretes_rising_09_events_16	2002Oct15
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PASS		
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PASS		
0	rtee_discretes_rising_14_events_16	2002Oct15

PASS		
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PASS		
0	rtee_discretes_rising_16_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct15
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0	rtee_discretes_rising_19_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct15
PASS		
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PASS		
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PASS		
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0	rtee_discretes_rising_27_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct15
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct15
PASS		
0	rtee_display1_comp_events_16	2002Oct15
PASS		
0	rtee_display2_comp_events_16	2002Oct15
PASS		
0	rtee_mem_linear_search_events_2	2002Oct15
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct15

PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct15
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct15
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct15
PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct15
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct15
PASS		
0	rtee_pio	2002Oct15
PASS		
0	rtee_prefetch_parse_nops	2002Oct15
PASS		
0	rtee_reg_linear_search_events_2	2002Oct15
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Oct15
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Oct15
PASS		
0	rtee_vip_comp_events_16	2002Oct15
PASS		
0	scratch_mem_read	2002Oct15
PASS		
0	scratch_mem_read2	2002Oct15

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PASS
0   set_cnst2_sm_meq                2002Oct15
PASS
0   set_cnst_alu_max1_sm_meq        2002Oct15
PASS
695 set_cnst_alu_max_random          2002Oct15
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   set_cnst_bool_max2_sm_meq       2002Oct15
PASS
0   set_cnst_loop_max2_sm_meq       2002Oct15
PASS
0   set_cnst_sm_meq                 2002Oct15
PASS
0   set_cnst_tex_max2_sm_meq        2002Oct15
PASS
700 set_cnst_we_lcc_random           2002Oct15
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
701 set_cnst_we_random               2002Oct15
**FAIL**      cp_mh_req.cmp
0   set_cnst_write_enable_lcc2_sm_meq 2002Oct15
PASS
0   set_cnst_write_enable_sm_meq     2002Oct15
PASS
0   set_constant                    2002Oct15
PASS
0   set_constant2                   2002Oct15
PASS
0   set_constant_alu_max1            2002Oct15
PASS
0   set_constant_alu_max2            2002Oct15
PASS
0   set_constant_bool_max1           2002Oct15
PASS
0   set_constant_bool_max2           2002Oct15
PASS
0   set_constant_indr1_short          2002Oct15
PASS
0   set_constant_indr1_short2        2002Oct15
PASS
0   set_constant_indr1_write_enable   2002Oct15
PASS
0   set_constant_indr1_write_enable_lcc 2002Oct15
PASS
0   set_constant_indr2_short          2002Oct15
PASS
0   set_constant_indr2_short2        2002Oct15
PASS
0   set_constant_indr2_write_enable   2002Oct15

```

PASS		
0	set_constant_indr2_write_enable_lcc	2002Oct15
PASS		
0	set_constant_loop_max1	2002Oct15
PASS		
0	set_constant_loop_max2	2002Oct15
PASS		
0	set_constant_ring_short	2002Oct15
PASS		
0	set_constant_ring_short2	2002Oct15
PASS		
0	set_constant_ring_write_enable	2002Oct15
PASS		
0	set_constant_ring_write_enable_lcc	2002Oct15
PASS		
0	set_constant_short	2002Oct15
PASS		
0	set_constant_short2	2002Oct15
PASS		
0	set_constant_tex_max1	2002Oct15
PASS		
0	set_constant_tex_max2	2002Oct15
PASS		
0	set_constant_tex_max2_sm_roq	2002Oct15
PASS		
0	set_constant_write_enable	2002Oct15
PASS		
0	set_constant_write_enable_lcc	2002Oct15
PASS		
0	set_constant_write_enable_lcc2	2002Oct15
PASS		
0	set_constant_write_enable_lcc2_sm_roq	2002Oct15
PASS		
0	set_scissors	2002Oct15
PASS		
0	set_st_indrl_match	2002Oct15
PASS		
0	set_st_indrl_mismatch	2002Oct15
PASS		
0	set_st_indrl_mismatch2	2002Oct15
PASS		
0	set_st_indrl_mismatch3	2002Oct15
PASS		
0	set_st_indrl_mismatch4	2002Oct15
PASS		
0	set_st_indrl_ps	2002Oct15
PASS		
0	set_st_indrl_ps_only	2002Oct15

PASS		
0	set_st_indr1_sublk0	2002Oct15
PASS		
0	set_st_indr1_sublk1	2002Oct15
PASS		
0	set_st_indr1_sublk1_only	2002Oct15
PASS		
0	set_st_indr1_sublk2	2002Oct15
PASS		
0	set_st_indr1_sublk2_only	2002Oct15
PASS		
0	set_st_indr1_sublk3	2002Oct15
PASS		
0	set_st_indr1_sublk3_only	2002Oct15
PASS		
0	set_st_indr1_sublk4	2002Oct15
PASS		
0	set_st_indr1_sublk4_only	2002Oct15
PASS		
0	set_st_indr1_sublk5	2002Oct15
PASS		
0	set_st_indr1_sublk5_only	2002Oct15
PASS		
0	set_st_indr1_sublk6	2002Oct15
PASS		
0	set_st_indr1_sublk6_only	2002Oct15
PASS		
0	set_st_indr1_sublk7	2002Oct15
PASS		
0	set_st_indr1_sublk7_only	2002Oct15
PASS		
0	set_st_indr1_vs	2002Oct15
PASS		
0	set_st_indr1_vs_only	2002Oct15
PASS		
0	set_st_indr2_match	2002Oct15
PASS		
0	set_st_indr2_mismatch	2002Oct15
PASS		
0	set_st_indr2_mismatch2	2002Oct15
PASS		
0	set_st_indr2_mismatch3	2002Oct15
PASS		
0	set_st_indr2_mismatch4	2002Oct15
PASS		
0	set_st_indr2_ps	2002Oct15
PASS		
0	set_st_indr2_ps_only	2002Oct15

PASS		
0	set_st_indr2_sublk0	2002Oct15
PASS		
0	set_st_indr2_sublk1	2002Oct15
PASS		
0	set_st_indr2_sublk1_only	2002Oct15
PASS		
0	set_st_indr2_sublk2	2002Oct15
PASS		
0	set_st_indr2_sublk2_only	2002Oct15
PASS		
0	set_st_indr2_sublk3	2002Oct15
PASS		
0	set_st_indr2_sublk3_only	2002Oct15
PASS		
0	set_st_indr2_sublk4	2002Oct15
PASS		
0	set_st_indr2_sublk4_only	2002Oct15
PASS		
0	set_st_indr2_sublk5	2002Oct15
PASS		
0	set_st_indr2_sublk5_only	2002Oct15
PASS		
0	set_st_indr2_sublk6	2002Oct15
PASS		
0	set_st_indr2_sublk6_only	2002Oct15
PASS		
0	set_st_indr2_sublk7	2002Oct15
PASS		
0	set_st_indr2_sublk7_only	2002Oct15
PASS		
0	set_st_indr2_vs	2002Oct15
PASS		
0	set_st_indr2_vs_only	2002Oct15
PASS		
0	set_st_pfmacth_disabled	2002Oct15
PASS		
0	set_st_ring_match	2002Oct15
PASS		
0	set_st_ring_mismatch	2002Oct15
PASS		
0	set_st_ring_mismatch2	2002Oct15
PASS		
0	set_st_ring_mismatch3	2002Oct15
PASS		
0	set_st_ring_mismatch4	2002Oct15
PASS		
0	set_st_ring_ps	2002Oct15

PASS		
0	set_st_ring_ps_only	2002Oct15
PASS		
0	set_st_ring_sublk0	2002Oct15
PASS		
0	set_st_ring_sublk1	2002Oct15
PASS		
0	set_st_ring_sublk1_only	2002Oct15
PASS		
0	set_st_ring_sublk2	2002Oct15
PASS		
0	set_st_ring_sublk2_only	2002Oct15
PASS		
0	set_st_ring_sublk3	2002Oct15
PASS		
0	set_st_ring_sublk3_only	2002Oct15
PASS		
0	set_st_ring_sublk4	2002Oct15
PASS		
0	set_st_ring_sublk4_only	2002Oct15
PASS		
0	set_st_ring_sublk5	2002Oct15
PASS		
0	set_st_ring_sublk5_only	2002Oct15
PASS		
0	set_st_ring_sublk6	2002Oct15
PASS		
0	set_st_ring_sublk6_only	2002Oct15
PASS		
0	set_st_ring_sublk7	2002Oct15
PASS		
0	set_st_ring_sublk7_only	2002Oct15
PASS		
0	set_st_ring_vs	2002Oct15
PASS		
0	set_st_ring_vs_only	2002Oct15
PASS		
0	set_st_stress_a	2002Oct15
PASS		
0	set_st_stress_b	2002Oct15
PASS		
0	set_st_stress_b_random	2002Oct15
PASS		
0	set_st_stress_b_sm_meq	2002Oct15
PASS		
0	set_st_stress_c	2002Oct15
PASS		
0	set_st_stress_c_sm_roq	2002Oct15

PASS		
0	set_st_stress_d	2002Oct15
PASS		
0	set_st_stress_d_random	2002Oct15
PASS		
0	set_st_stress_d_sm_meq	2002Oct15
PASS		
0	set_st_stress_e	2002Oct15
PASS		
0	set_st_stress_e_sm_meq	2002Oct15
PASS		
0	set_st_stress_f	2002Oct15
PASS		
0	set_st_stress_f_random	2002Oct15
PASS		
0	set_state_rt_a	2002Oct15
PASS		
0	set_state_rt_b	2002Oct15
PASS		
0	set_state_rt_c	2002Oct15
PASS		
0	set_state_rt_d	2002Oct15
PASS		
0	set_state_rt_d_random	2002Oct15
PASS		
0	set_state_rt_e	2002Oct15
PASS		
0	set_state_rt_e_random	2002Oct15
PASS		
0	smoke_nrt	2002Oct15
PASS		
0	src_clip	2002Oct15
PASS		
0	srf_cohr	2002Oct15
PASS		
0	switch_pio_bm	2002Oct15
PASS		
0	trans_bitblt	2002Oct15
PASS		
0	type0_2long_pckts	2002Oct15
PASS		
0	type0_3indr_buffs	2002Oct15
PASS		
0	type0_4pckts	2002Oct15
PASS		
0	type0_incr_updt_cnst	2002Oct15
PASS		
0	type0_incr_updt_instrmem	2002Oct15

PASS		
0	type0_incr_updt_st	2002Oct15
PASS		
0	type0_incr_updt_stress	2002Oct15
PASS		
0	type0_ind1_ind2_a	2002Oct15
PASS		
0	type0_ind1_ind2_b	2002Oct15
PASS		
0	type0_indr_buff	2002Oct15
PASS		
0	type0_real_time	2002Oct15
PASS		
0	type0_real_time_random	2002Oct15
PASS		
0	type1_4pckts	2002Oct15
PASS		
0	type1_indr_buff	2002Oct15
PASS		
0	type1_pckt	2002Oct15
PASS		
0	type_random	2002Oct15
PASS		
0	uram_wrt_rt_ucose	2002Oct15
PASS		
0	uram_wrt_ucose	2002Oct15
PASS		
0	viz_query_a	2002Oct15
PASS		
0	viz_query_draw_indx	2002Oct15
PASS		
0	viz_query_draw_indx_indr1	2002Oct15
PASS		
0	viz_query_draw_indx_indr2	2002Oct15
PASS		
0	viz_query_draw_indx_multipass	2002Oct15
PASS		
0	viz_query_draw_indx_ring	2002Oct15
PASS		
0	viz_query_indr1	2002Oct15
PASS		
0	viz_query_indr1_simple	2002Oct15
PASS		
0	viz_query_indr2	2002Oct15
PASS		
0	viz_query_indr2_simple	2002Oct15
PASS		
0	viz_query_ring	2002Oct15

```
PASS
0 viz_query_ring_simple 2002Oct15
PASS
0 wait_for_idle 2002Oct15
PASS
0 wait_reg_mem_poll_mem0 2002Oct15
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct15
PASS
0 wait_reg_mem_poll_mem1 2002Oct15
PASS
0 wait_reg_mem_poll_mem2 2002Oct15
PASS
0 wait_reg_mem_poll_mem3 2002Oct15
PASS
0 wait_reg_mem_poll_reg0 2002Oct15
PASS
0 wait_reg_mem_poll_reg1 2002Oct15
PASS
0 wait_reg_mem_poll_reg2 2002Oct15
PASS
0 wait_reg_mem_poll_reg3 2002Oct15
PASS
```

```
=====  
Tests_Run_count = 871 Pass_count = 855 Fail_count = 15 Incomplete_count = 1  
=====
```

=====
Regression Report For CP - Project = r400 Created on Sat Sep 7 8:01:15 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

=====
0 2d_indr_buffer_a 2002Sep7
PASS
0 2d_indr_buffer_b 2002Sep7
PASS
0 2d_indr_buffer_c 2002Sep7
PASS
0 2d_indr_buffer_d 2002Sep7
PASS
0 2d_indr_buffer_e 2002Sep7
PASS
0 2d_indr_buffer_f 2002Sep7
PASS
0 2d_indr_buffer_g 2002Sep7
PASS
0 3d_draw_indx_2 2002Sep7
PASS
0 3d_draw_indx_2_b 2002Sep7
PASS
0 IQ2_Mult 2002Sep7
PASS
0 IQ2_Wrap_Legacy 2002Sep7
PASS
0 IQ_Wrap 2002Sep7
PASS
0 RB_BUFSZ_0 2002Sep7
PASS
0 RB_BUFSZ_1 2002Sep7
PASS
0 RB_Wrap 2002Sep7
PASS
16 Small_Text_BM 2002Sep7
FAIL cp_rbbm.cmp rbbmWr.cmp
0 Type0_IQ2_BM 2002Sep7
PASS
0 Type0_IQ2_BM_IB_Packet 2002Sep7
PASS
0 Type0_IQ_BM 2002Sep7
PASS
0 Type0_IQ_BM_IB_Packet 2002Sep7

PASS		
0	Type0_PQ_BM	2002Sep7
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep7
PASS		
0	Type3_Bitblt	2002Sep7
PASS		
0	Type3_Load_Palette	2002Sep7
PASS		
0	Type3_NOP	2002Sep7
PASS		
0	bitblt_multi	2002Sep7
PASS		
0	brush_32x1mono	2002Sep7
PASS		
0	brush_8x8color_16	2002Sep7
PASS		
29	brush_8x8color_32	2002Sep7
FAIL	cp_mh_req.cmp	
0	brush_8x8color_64	2002Sep7
PASS		
0	brush_8x8color_dst_0_1	2002Sep7
PASS		
0	brush_8x8mono	2002Sep7
PASS		
0	brush_color_mono_stress	2002Sep7
PASS		
34	brush_flag	2002Sep7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	brush_type0	2002Sep7
PASS		
0	brush_type1	2002Sep7
PASS		
0	brush_type10	2002Sep7
PASS		
0	brush_type13	2002Sep7
PASS		
0	brush_type14	2002Sep7
PASS		
0	brush_type6	2002Sep7
PASS		
0	brush_type7	2002Sep7
PASS		
0	brush_type_other	2002Sep7
PASS		
0	brush_types_13_14	2002Sep7
PASS		
0	clear_const_wr_en_on_me_init	2002Sep7

PASS		
0	clear_const_wr_en_with_lcc	2002Sep7
PASS		
0	cond_write_mem_poll_mem	2002Sep7
PASS		
0	cond_write_mem_poll_mem2	2002Sep7
PASS		
0	cond_write_mem_poll_mem3	2002Sep7
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep7
PASS		
0	cond_write_mem_poll_reg	2002Sep7
PASS		
0	cond_write_mem_poll_reg2	2002Sep7
PASS		
0	cond_write_mem_poll_reg3	2002Sep7
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep7
PASS		
0	cp_bios_scratch	2002Sep7
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep7
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep7
PASS		
0	cp_ib_base000_bufszXXX	2002Sep7
PASS		
0	cp_ib_base001_bufszXXX	2002Sep7
PASS		
0	cp_ib_base010_bufszXXX	2002Sep7
PASS		
0	cp_ib_base011_bufszXXX	2002Sep7
PASS		
0	cp_ib_base100_bufszXXX	2002Sep7
PASS		
0	cp_ib_base101_bufszXXX	2002Sep7

PASS		
0	cp_ib_base110_bufszXXX	2002Sep7
PASS		
0	cp_ib_base111_bufszXXX	2002Sep7
PASS		
0	cp_interrupt	2002Sep7
PASS		
0	cp_interrupt_rt_events_16	2002Sep7
PASS		
0	cp_iq2_alignment_000	2002Sep7
PASS		
0	cp_iq2_alignment_001	2002Sep7
PASS		
0	cp_iq2_alignment_010	2002Sep7
PASS		
0	cp_iq2_alignment_011	2002Sep7
PASS		
0	cp_iq2_alignment_100	2002Sep7
PASS		
0	cp_iq2_alignment_101	2002Sep7
PASS		
0	cp_iq2_alignment_110	2002Sep7
PASS		
0	cp_iq2_alignment_111	2002Sep7
PASS		
0	cp_iq_alignment_000	2002Sep7
PASS		
0	cp_iq_alignment_001	2002Sep7
PASS		
0	cp_iq_alignment_010	2002Sep7
PASS		
0	cp_iq_alignment_011	2002Sep7
PASS		
0	cp_iq_alignment_100	2002Sep7
PASS		
0	cp_iq_alignment_101	2002Sep7
PASS		
0	cp_iq_alignment_110	2002Sep7
PASS		
0	cp_iq_alignment_111	2002Sep7
PASS		
0	cp_max_size_packet	2002Sep7
PASS		
0	cp_max_size_packet_random	2002Sep7
PASS		
0	cp_nrt_rt_protection_mode	2002Sep7
PASS		
0	cp_output_stress	2002Sep7

PASS		
0	cp_output_stress_random	2002Sep7
PASS		
0	cp_protection_mode	2002Sep7
PASS		
0	cp_rb_alignment_000	2002Sep7
PASS		
0	cp_rb_alignment_001	2002Sep7
PASS		
0	cp_rb_alignment_010	2002Sep7
PASS		
0	cp_rb_alignment_011	2002Sep7
PASS		
0	cp_rb_alignment_100	2002Sep7
PASS		
0	cp_rb_alignment_101	2002Sep7
PASS		
0	cp_rb_alignment_110	2002Sep7
PASS		
0	cp_rb_alignment_111	2002Sep7
PASS		
0	cp_rb_wptrXXX_base000	2002Sep7
PASS		
0	cp_rb_wptrXXX_base001	2002Sep7
PASS		
0	cp_rb_wptrXXX_base010	2002Sep7
PASS		
0	cp_rb_wptrXXX_base011	2002Sep7
PASS		
0	cp_rb_wptrXXX_base100	2002Sep7
PASS		
0	cp_rb_wptrXXX_base101	2002Sep7
PASS		
0	cp_rb_wptrXXX_base110	2002Sep7
PASS		
0	cp_rb_wptrXXX_base111	2002Sep7
PASS		
0	cp_reg_rw_test	2002Sep7
PASS		
0	cp_register_reads	2002Sep7
PASS		
0	cp_rt_protection_mode	2002Sep7
PASS		
0	cp_scratch	2002Sep7
PASS		
0	cp_scratch_multi	2002Sep7
PASS		
0	dma_cp_sync	2002Sep7

PASS		
0	dma_dscrstr_chckn_bit	2002Sep7
PASS		
0	dma_dscrstr_flush1	2002Sep7
PASS		
0	dma_dscrstr_flush2	2002Sep7
PASS		
0	dma_dscrstr_flush3	2002Sep7
PASS		
0	dma_m2m_src00_dstXX	2002Sep7
PASS		
0	dma_m2m_src01_dstXX	2002Sep7
PASS		
0	dma_m2m_src10_dstXX	2002Sep7
PASS		
0	dma_m2m_src11_dstXX	2002Sep7
PASS		
0	dma_mem2mem	2002Sep7
PASS		
0	dma_mem2mem_abort	2002Sep7
PASS		
0	dma_mem2mem_alignments	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep7
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep7
PASS		
0	dma_mem2mem_pio_abort	2002Sep7
PASS		
0	dma_mem2mem_random	2002Sep7
PASS		
0	dma_mem2reg	2002Sep7
PASS		
0	dma_mem2reg_abort	2002Sep7
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep7

PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep7
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep7
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep7
PASS		
0	dma_mem2reg_sync	2002Sep7
PASS		
0	dma_r2r_m2m_r2r	2002Sep7
PASS		
0	dma_r2r_m2r_r2r	2002Sep7
PASS		
0	dma_r2r_r2m_r2r	2002Sep7
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep7
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep7
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep7
PASS		
0	dma_reg2mem	2002Sep7
PASS		
0	dma_reg2mem_abort	2002Sep7
PASS		
0	dma_reg2reg	2002Sep7
PASS		
0	dma_reg2reg_2dw	2002Sep7
PASS		
0	dma_reg2reg_daic	2002Sep7
PASS		
0	dma_reg2reg_mult	2002Sep7
PASS		
0	dma_reg2reg_mult2	2002Sep7
PASS		
0	dma_reg2reg_mult3	2002Sep7
PASS		
0	dma_reg2reg_saic	2002Sep7
PASS		
0	dma_reg2reg_sdaic	2002Sep7
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep7
PASS		
0	dma_reg2reg_stress	2002Sep7
PASS		
0	draw_indx_a	2002Sep7
PASS		
0	draw_indx_b	2002Sep7

PASS		
0	draw_indx_c	2002Sep7
PASS		
0	draw_indx_indr1_a	2002Sep7
PASS		
0	draw_indx_indr1_b	2002Sep7
PASS		
0	draw_indx_indr1_short_a	2002Sep7
PASS		
0	draw_indx_indr1_short_b	2002Sep7
PASS		
0	draw_indx_indr1_stress	2002Sep7
PASS		
0	draw_indx_indr2_a	2002Sep7
PASS		
0	draw_indx_indr2_b	2002Sep7
PASS		
0	draw_indx_indr2_short_a	2002Sep7
PASS		
0	draw_indx_indr2_short_b	2002Sep7
PASS		
0	draw_indx_indr2_stress	2002Sep7
PASS		
0	draw_indx_ring1	2002Sep7
PASS		
0	draw_indx_ring2	2002Sep7
PASS		
0	draw_indx_ring_short1	2002Sep7
PASS		
0	draw_indx_ring_short2	2002Sep7
PASS		
0	draw_indx_ring_stress	2002Sep7
PASS		
0	draw_indx_short_a	2002Sep7
PASS		
0	draw_indx_short_b	2002Sep7
PASS		
0	draw_indx_short_c	2002Sep7
PASS		
0	draw_indx_stress_a	2002Sep7
PASS		
0	draw_indx_stress_b	2002Sep7
PASS		
0	draw_indx_stress_c	2002Sep7
PASS		
0	event_timestamp_write	2002Sep7
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep7

PASS		
0	ft_indr1_july22	2002Sep7
PASS		
0	ft_indr2_july22	2002Sep7
PASS		
0	ft_july22	2002Sep7
PASS		
0	ft_july22_random	2002Sep7
PASS		
0	header_dump	2002Sep7
PASS		
0	header_dump_rt	2002Sep7
PASS		
0	header_dump_small	2002Sep7
PASS		
196	hostdata_blt	2002Sep7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep7
PASS		
0	im_load_b	2002Sep7
PASS		
0	im_load_cid_mismatch	2002Sep7
PASS		
0	im_load_immediate	2002Sep7
PASS		
0	im_load_immediate_nrt_me	2002Sep7
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep7
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep7
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep7
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep7
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep7
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep7
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep7
PASS		
0	im_load_indr1_a	2002Sep7
PASS		
0	im_load_indr1_b	2002Sep7
PASS		
0	im_load_indr2_a	2002Sep7
PASS		
0	im_load_indr2_b	2002Sep7

PASS		
0	im_load_invalidate	2002Sep7
PASS		
0	im_load_match	2002Sep7
PASS		
0	im_load_match2	2002Sep7
PASS		
0	im_load_nrt_me	2002Sep7
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep7
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep7
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep7
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep7
PASS		
0	im_load_pfmacth_disable	2002Sep7
PASS		
0	im_load_ring_a	2002Sep7
PASS		
0	im_load_ring_b	2002Sep7
PASS		
0	im_load_rt	2002Sep7
PASS		
0	im_load_rt2	2002Sep7
PASS		
0	im_load_rt3	2002Sep7
PASS		
0	im_load_rt4	2002Sep7
PASS		
0	im_load_sz_mismatch	2002Sep7
PASS		
0	incremental_update_const	2002Sep7
PASS		
0	incremental_update_instr	2002Sep7
PASS		
0	incremental_update_state	2002Sep7
PASS		
0	indirect_buffer_a	2002Sep7
PASS		
0	indirect_buffer_b	2002Sep7
PASS		
0	indirect_buffer_c	2002Sep7
PASS		
0	indirect_buffer_d	2002Sep7
PASS		
0	indirect_buffer_e	2002Sep7

PASS		
0	indirect_buffer_f	2002Sep7
PASS		
0	indirect_buffer_g	2002Sep7
PASS		
0	indirect_buffer_h	2002Sep7
PASS		
0	indirect_buffer_i	2002Sep7
PASS		
0	indirect_buffer_j	2002Sep7
PASS		
0	indirect_buffer_k	2002Sep7
PASS		
0	indirect_buffer_l	2002Sep7
PASS		
0	indirect_buffer_mp_a	2002Sep7
PASS		
0	indirect_buffer_mp_b	2002Sep7
PASS		
0	indirect_buffer_mp_c	2002Sep7
PASS		
0	indirect_buffer_mp_d	2002Sep7
PASS		
0	indirect_buffer_mp_e	2002Sep7
PASS		
0	indirect_buffer_mp_f	2002Sep7
PASS		
0	invalid_opcode	2002Sep7
PASS		
0	invalidate_on_me_init	2002Sep7
PASS		
0	invalidate_state_constants	2002Sep7
PASS		
0	invalidate_state_shaders	2002Sep7
PASS		
0	invalidate_state_shaders2	2002Sep7
PASS		
0	invalidate_state_sublks	2002Sep7
PASS		
0	invalidate_state_sublks2	2002Sep7
PASS		
0	iq1_iq2_mult	2002Sep7
PASS		
0	iq1_iq2_push_wrap	2002Sep7
PASS		
0	iq2_csq_full	2002Sep7
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep7

PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep7
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep7
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep7
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep7
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep7
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep7
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep7
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep7
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep7
PASS		
0	ld_cnst_cntx_match	2002Sep7
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep7
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep7
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep7
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep7
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep7
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep7
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep7
PASS		
0	ld_cnst_cntx_replicate8	2002Sep7
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep7
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep7
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep7
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep7
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep7
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep7

PASS		
0	ld_cnst_cntx_simple_alu	2002Sep7
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep7
PASS		
0	load_constant_context_rt	2002Sep7
PASS		
0	load_palette	2002Sep7
PASS		
0	lp_2d3d	2002Sep7
PASS		
0	mem_write	2002Sep7
PASS		
0	mem_write_rt	2002Sep7
PASS		
0	mpeg_index	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep7
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep7
PASS		
0	nop_eating_disabled	2002Sep7

PASS		
0	paint_multi	2002Sep7
PASS		
0	pitch_clip	2002Sep7
PASS		
0	ply_nextscan	2002Sep7
PASS		
0	rbbm_WU_2D_idle_01	2002Sep7
PASS		
0	rbbm_WU_2D_idle_02	2002Sep7
PASS		
0	rbbm_WU_2D_idle_03	2002Sep7
PASS		
0	rbbm_WU_2D_idle_04	2002Sep7
PASS		
0	rbbm_WU_2D_idle_05	2002Sep7
PASS		
0	rbbm_WU_2D_idle_06	2002Sep7
PASS		
0	rbbm_WU_2D_idle_07	2002Sep7
PASS		
0	rbbm_WU_2D_idle_08	2002Sep7
PASS		
0	rbbm_WU_2D_idle_09	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep7
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep7

PASS		
0	rbbm_WU_3D_idle_01	2002Sep7
PASS		
0	rbbm_WU_3D_idle_02	2002Sep7
PASS		
0	rbbm_WU_3D_idle_03	2002Sep7
PASS		
0	rbbm_WU_3D_idle_04	2002Sep7
PASS		
0	rbbm_WU_3D_idle_05	2002Sep7
PASS		
0	rbbm_WU_3D_idle_06	2002Sep7
PASS		
0	rbbm_WU_3D_idle_07	2002Sep7
PASS		
0	rbbm_WU_3D_idle_08	2002Sep7
PASS		
0	rbbm_WU_3D_idle_09	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep7
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep7
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep7
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep7
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep7

PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep7
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep7
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep7
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep7
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep7
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep7
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep7
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep7
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep7
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep7
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep7
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep7
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep7
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep7
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep7
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep7
PASS		
0	rbbm_WU_idct_semaphore	2002Sep7
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep7
PASS		
375	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep7
FAIL	cp_rbbm.cmp	
376	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep7
FAIL	cp_rbbm.cmp	
377	rbbm_WUrt_CP_dma_idle	2002Sep7
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
378	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep7
FAIL	cp_rbbm.cmp	
379	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep7
FAIL	cp_rbbm.cmp	
380	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep7

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**FAIL**      cp_rbbm.cmp
381  rbbm_WUrt_CRTC_D2MODE_vline_fe          2002Sep7
**FAIL**      cp_rbbm.cmp
382  rbbm_WUrt_CRTC_D2MODE_vline_on          2002Sep7
**FAIL**      cp_rbbm.cmp
383  rbbm_WUrt_CRTC_D2MODE_vline_re          2002Sep7
**FAIL**      cp_rbbm.cmp
384  rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep7
**FAIL**      cp_rbbm.cmp
385  rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep7
**FAIL**      cp_rbbm.cmp
386  rbbm_WUrt_MH_hdp_clean                   2002Sep7
**FAIL**      cp_rbbm.cmp
387  rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off 2002Sep7
**FAIL**      cp_rbbm.cmp
388  rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off 2002Sep7
**FAIL**      cp_rbbm.cmp
389  rbbm_WUrt_RT_idle_01                     2002Sep7
**FAIL**      cp_rbbm.cmp
390  rbbm_WUrt_RT_idle_02                     2002Sep7
**FAIL**      cp_rbbm.cmp
391  rbbm_WUrt_RT_idle_03                     2002Sep7
**FAIL**      cp_rbbm.cmp
392  rbbm_WUrt_RT_idle_04                     2002Sep7
**FAIL**      cp_rbbm.cmp
393  rbbm_WUrt_RT_idle_clean_01               2002Sep7
**FAIL**      cp_rbbm.cmp
394  rbbm_WUrt_RT_idle_clean_02               2002Sep7
**FAIL**      cp_rbbm.cmp
395  rbbm_WUrt_RT_idle_clean_03               2002Sep7
**FAIL**      cp_rbbm.cmp
396  rbbm_WUrt_RT_idle_clean_04               2002Sep7
**FAIL**      cp_rbbm.cmp
397  rbbm_WUrt_RT_idle_clean_05               2002Sep7
**FAIL**      cp_rbbm.cmp
398  rbbm_WUrt_VIPH0_dma_idle                 2002Sep7
**FAIL**      cp_rbbm.cmp
399  rbbm_WUrt_VIPH1_dma_idle                 2002Sep7
**FAIL**      cp_rbbm.cmp
400  rbbm_WUrt_VIPH2_dma_idle                 2002Sep7
**FAIL**      cp_rbbm.cmp
401  rbbm_WUrt_VIPH3_dma_idle                 2002Sep7
**FAIL**      cp_rbbm.cmp
402  rbbm_WUrt_pad_ext_sig_rising_edge        2002Sep7
**FAIL**      cp_rbbm.cmp
0    rbbm_cp_non_queued_rdwr_01               2002Sep7
PASS
0    rbbm_cp_non_queued_rdwr_02               2002Sep7

```

PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep7
PASS		
0	rbbm_cp_queued_rdwr	2002Sep7
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep7
PASS		
0	rbbm_isync_wait_idlegui	2002Sep7
PASS		
0	rbbm_soft_reset	2002Sep7
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0	reg_to_mem	2002Sep7
PASS		
0	reg_to_mem_rt	2002Sep7
PASS		
0	rop_cfd_wmd	2002Sep7
PASS		
0	rstr_ordr	2002Sep7
PASS		
0	rt_invalid_opcode	2002Sep7
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0	rtee_arm_wait_idle_trigger_events_16	2002Sep7
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0	rtee_discretes_arm_rearm_events_32	2002Sep7
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep7
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep7
PASS		
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PASS		
0	rtee_discretes_falling_03_events_16	2002Sep7
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0	rtee_discretes_falling_04_events_16	2002Sep7
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep7
PASS		
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0	rtee_discretes_falling_10_events_16	2002Sep7
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0	rtee_discretes_falling_11_events_16	2002Sep7

PASS		
0	rtee_discretes_falling_12_events_16	2002Sep7
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep7
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0	rtee_discretes_falling_14_events_16	2002Sep7
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep7
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0	rtee_discretes_falling_16_events_16	2002Sep7
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0	rtee_discretes_falling_17_events_16	2002Sep7
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0	rtee_discretes_falling_18_events_16	2002Sep7
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0	rtee_discretes_falling_19_events_16	2002Sep7
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0	rtee_discretes_falling_20_events_16	2002Sep7
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0	rtee_discretes_falling_22_events_16	2002Sep7
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0	rtee_discretes_falling_23_events_16	2002Sep7
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0	rtee_discretes_falling_28_events_16	2002Sep7
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0	rtee_discretes_falling_29_events_16	2002Sep7
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0	rtee_discretes_falling_30_events_16	2002Sep7
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0	rtee_discretes_falling_31_events_16	2002Sep7
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0	rtee_discretes_invert_00_events_16	2002Sep7
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep7
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0	rtee_discretes_invert_02_events_16	2002Sep7
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0	rtee_discretes_invert_03_events_16	2002Sep7

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0	rtee_discretes_invert_06_events_16	2002Sep7
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0	rtee_discretes_level_05_events_16	2002Sep7
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0	rtee_discretes_level_06_events_16	2002Sep7
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0	rtee_discretes_level_07_events_16	2002Sep7
PASS		
0	rtee_discretes_level_08_events_16	2002Sep7
PASS		
0	rtee_discretes_level_09_events_16	2002Sep7
PASS		
0	rtee_discretes_level_10_events_16	2002Sep7
PASS		
0	rtee_discretes_level_11_events_16	2002Sep7
PASS		
0	rtee_discretes_level_12_events_16	2002Sep7
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0	rtee_discretes_level_13_events_16	2002Sep7
PASS		
0	rtee_discretes_level_14_events_16	2002Sep7
PASS		
0	rtee_discretes_level_15_events_16	2002Sep7
PASS		
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0	rtee_discretes_level_19_events_16	2002Sep7

PASS		
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PASS		
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0	rtee_discretes_manual_disarm_events_16	2002Sep7
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0	rtee_discretes_rising_06_events_16	2002Sep7
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PASS		
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0	rtee_discretes_rising_12_events_16	2002Sep7
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0	rtee_display1_comp_events_16	2002Sep7
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0	rtee_display2_comp_events_16	2002Sep7
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0	rtee_mem_linear_search_events_2	2002Sep7

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0	rtee_mem_poll_comp_000_events_16	2002Sep7
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0	rtee_mem_poll_comp_001_events_16	2002Sep7
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0	rtee_mem_poll_comp_010_events_16	2002Sep7
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0	rtee_mem_poll_comp_011_events_16	2002Sep7
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0	rtee_mem_poll_comp_100_events_16	2002Sep7
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0	rtee_mem_poll_comp_101_events_16	2002Sep7
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0	rtee_mem_poll_comp_110_events_16	2002Sep7
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0	rtee_mem_poll_comp_111_events_16	2002Sep7
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0	rtee_mem_poll_comp_masks_events_16	2002Sep7
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0	rtee_mem_poll_comp_masks_events_16_random	2002Sep7
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0	rtee_mem_poll_comp_swaps_events_16	2002Sep7
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0	rtee_mem_poll_intervals_events_16	2002Sep7
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0	rtee_prefetch_parse_nops	2002Sep7
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0	rtee_reg_linear_search_events_2	2002Sep7
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0	rtee_reg_poll_comp_000_events_16	2002Sep7
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0	rtee_reg_poll_comp_001_events_16	2002Sep7
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0	rtee_reg_poll_comp_010_events_16	2002Sep7
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0	rtee_reg_poll_comp_011_events_16	2002Sep7
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0	rtee_reg_poll_comp_100_events_16	2002Sep7
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0	rtee_reg_poll_comp_101_events_16	2002Sep7
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0	rtee_reg_poll_comp_110_events_16	2002Sep7
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0	rtee_reg_poll_comp_111_events_16	2002Sep7
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0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep7
PASS		
0	rtee_vip_comp_events_16	2002Sep7

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0	scratch_mem_read2	2002Sep7
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0	set_constant2	2002Sep7
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0	set_constant_alu_max1	2002Sep7
PASS		
0	set_constant_alu_max2	2002Sep7
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0	set_constant_bool_max1	2002Sep7
PASS		
0	set_constant_bool_max2	2002Sep7
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0	set_constant_indr1_short	2002Sep7
PASS		
0	set_constant_indr1_short2	2002Sep7
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0	set_constant_indr1_write_enable	2002Sep7
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0	set_constant_indr1_write_enable_lcc	2002Sep7
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0	set_constant_indr2_short	2002Sep7
PASS		
0	set_constant_indr2_short2	2002Sep7
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0	set_constant_indr2_write_enable	2002Sep7
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0	set_constant_indr2_write_enable_lcc	2002Sep7
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0	set_constant_loop_max1	2002Sep7
PASS		
0	set_constant_loop_max2	2002Sep7
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PASS		
0	set_constant_ring_short2	2002Sep7
PASS		
0	set_constant_ring_write_enable	2002Sep7
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep7
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0	set_constant_short	2002Sep7
PASS		
0	set_constant_short2	2002Sep7

PASS		
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PASS		
0	set_constant_tex_max2	2002Sep7
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0	set_constant_write_enable	2002Sep7
PASS		
0	set_constant_write_enable_lcc	2002Sep7
PASS		
0	set_constant_write_enable_lcc2	2002Sep7
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0	set_scissors	2002Sep7
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PASS		
0	set_st_indrl_mismatch	2002Sep7
PASS		
0	set_st_indrl_mismatch2	2002Sep7
PASS		
0	set_st_indrl_mismatch3	2002Sep7
PASS		
0	set_st_indrl_mismatch4	2002Sep7
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0	set_st_indrl_ps	2002Sep7
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0	set_st_indrl_ps_only	2002Sep7
PASS		
0	set_st_indrl_sublk0	2002Sep7
PASS		
0	set_st_indrl_sublk1	2002Sep7
PASS		
0	set_st_indrl_sublk1_only	2002Sep7
PASS		
0	set_st_indrl_sublk2	2002Sep7
PASS		
0	set_st_indrl_sublk2_only	2002Sep7
PASS		
0	set_st_indrl_sublk3	2002Sep7
PASS		
0	set_st_indrl_sublk3_only	2002Sep7
PASS		
0	set_st_indrl_sublk4	2002Sep7
PASS		
0	set_st_indrl_sublk4_only	2002Sep7
PASS		
0	set_st_indrl_sublk5	2002Sep7
PASS		
0	set_st_indrl_sublk5_only	2002Sep7

PASS		
0	set_st_indr1_sublk6	2002Sep7
PASS		
0	set_st_indr1_sublk6_only	2002Sep7
PASS		
0	set_st_indr1_sublk7	2002Sep7
PASS		
0	set_st_indr1_sublk7_only	2002Sep7
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PASS		
0	set_st_indr1_vs_only	2002Sep7
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0	set_st_indr2_match	2002Sep7
PASS		
0	set_st_indr2_mismatch	2002Sep7
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0	set_st_indr2_mismatch2	2002Sep7
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0	set_st_indr2_mismatch3	2002Sep7
PASS		
0	set_st_indr2_mismatch4	2002Sep7
PASS		
0	set_st_indr2_ps	2002Sep7
PASS		
0	set_st_indr2_ps_only	2002Sep7
PASS		
0	set_st_indr2_sublk0	2002Sep7
PASS		
0	set_st_indr2_sublk1	2002Sep7
PASS		
0	set_st_indr2_sublk1_only	2002Sep7
PASS		
0	set_st_indr2_sublk2	2002Sep7
PASS		
0	set_st_indr2_sublk2_only	2002Sep7
PASS		
0	set_st_indr2_sublk3	2002Sep7
PASS		
0	set_st_indr2_sublk3_only	2002Sep7
PASS		
0	set_st_indr2_sublk4	2002Sep7
PASS		
0	set_st_indr2_sublk4_only	2002Sep7
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0	set_st_indr2_sublk5	2002Sep7
PASS		
0	set_st_indr2_sublk5_only	2002Sep7

PASS		
0	set_st_indr2_sublk6	2002Sep7
PASS		
0	set_st_indr2_sublk6_only	2002Sep7
PASS		
0	set_st_indr2_sublk7	2002Sep7
PASS		
0	set_st_indr2_sublk7_only	2002Sep7
PASS		
0	set_st_indr2_vs	2002Sep7
PASS		
0	set_st_indr2_vs_only	2002Sep7
PASS		
0	set_st_pfmatch_disabled	2002Sep7
PASS		
0	set_st_ring_match	2002Sep7
PASS		
0	set_st_ring_mismatch	2002Sep7
PASS		
0	set_st_ring_mismatch2	2002Sep7
PASS		
0	set_st_ring_mismatch3	2002Sep7
PASS		
0	set_st_ring_mismatch4	2002Sep7
PASS		
0	set_st_ring_ps	2002Sep7
PASS		
0	set_st_ring_ps_only	2002Sep7
PASS		
0	set_st_ring_sublk0	2002Sep7
PASS		
0	set_st_ring_sublk1	2002Sep7
PASS		
0	set_st_ring_sublk1_only	2002Sep7
PASS		
0	set_st_ring_sublk2	2002Sep7
PASS		
0	set_st_ring_sublk2_only	2002Sep7
PASS		
0	set_st_ring_sublk3	2002Sep7
PASS		
0	set_st_ring_sublk3_only	2002Sep7
PASS		
0	set_st_ring_sublk4	2002Sep7
PASS		
0	set_st_ring_sublk4_only	2002Sep7
PASS		
0	set_st_ring_sublk5	2002Sep7

PASS		
0	set_st_ring_sublk5_only	2002Sep7
PASS		
0	set_st_ring_sublk6	2002Sep7
PASS		
0	set_st_ring_sublk6_only	2002Sep7
PASS		
0	set_st_ring_sublk7	2002Sep7
PASS		
0	set_st_ring_sublk7_only	2002Sep7
PASS		
0	set_st_ring_vs	2002Sep7
PASS		
0	set_st_ring_vs_only	2002Sep7
PASS		
0	set_st_stress_a	2002Sep7
PASS		
0	set_st_stress_b	2002Sep7
PASS		
0	set_st_stress_c	2002Sep7
PASS		
0	set_st_stress_d	2002Sep7
PASS		
0	set_st_stress_e	2002Sep7
PASS		
0	set_st_stress_f	2002Sep7
PASS		
0	set_state_rt_a	2002Sep7
PASS		
0	set_state_rt_b	2002Sep7
PASS		
0	set_state_rt_c	2002Sep7
PASS		
0	set_state_rt_d	2002Sep7
PASS		
0	smoke_nrt	2002Sep7
PASS		
0	src_clip	2002Sep7
PASS		
0	srf_cohr	2002Sep7
PASS		
0	trans_bitblt	2002Sep7
PASS		
0	type0_2long_pckts	2002Sep7
PASS		
0	type0_3indr_buffs	2002Sep7
PASS		
0	type0_4pckts	2002Sep7

PASS		
0	type0_incr_updt_cnst	2002Sep7
PASS		
0	type0_incr_updt_instrmem	2002Sep7
PASS		
0	type0_incr_updt_st	2002Sep7
PASS		
0	type0_incr_updt_stress	2002Sep7
PASS		
0	type0_ind1_ind2_a	2002Sep7
PASS		
0	type0_ind1_ind2_b	2002Sep7
PASS		
0	type0_indr_buff	2002Sep7
PASS		
0	type1_4pckts	2002Sep7
PASS		
0	type1_indr_buff	2002Sep7
PASS		
0	type1_pckt	2002Sep7
PASS		
0	type_random	2002Sep7
PASS		
704	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
705	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep7
PASS		
0	uram_wrt_ucode	2002Sep7
PASS		
0	viz_query_a	2002Sep7
PASS		
0	viz_query_draw_indx	2002Sep7
PASS		
0	viz_query_draw_indx_indr1	2002Sep7
PASS		
0	viz_query_draw_indx_indr2	2002Sep7
PASS		
712	viz_query_draw_indx_multipass	2002Sep7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_draw_indx_ring	2002Sep7
PASS		
0	viz_query_indr1	2002Sep7
PASS		
0	viz_query_indr1_simple	2002Sep7
PASS		
0	viz_query_indr2	2002Sep7

PASS
0 viz_query_indr2_simple 2002Sep7
PASS
0 viz_query_ring 2002Sep7
PASS
0 viz_query_ring_simple 2002Sep7
PASS
0 wait_for_idle 2002Sep7
PASS
0 wait_reg_mem_poll_mem0 2002Sep7
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep7
PASS
0 wait_reg_mem_poll_mem1 2002Sep7
PASS
0 wait_reg_mem_poll_mem2 2002Sep7
PASS
0 wait_reg_mem_poll_mem3 2002Sep7
PASS
0 wait_reg_mem_poll_reg0 2002Sep7
PASS
0 wait_reg_mem_poll_reg1 2002Sep7
PASS
0 wait_reg_mem_poll_reg2 2002Sep7
PASS
0 wait_reg_mem_poll_reg3 2002Sep7
PASS

=====
Tests_Run_count = 729 Pass_count = 694 Fail_count = 35 Incomplete_count = 0
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep11
	PASS		
0	2d_indr_buffer_b		2002Sep11
	PASS		
0	2d_indr_buffer_c		2002Sep11
	PASS		
0	2d_indr_buffer_d		2002Sep11
	PASS		
0	2d_indr_buffer_e		2002Sep11
	PASS		
0	2d_indr_buffer_f		2002Sep11
	PASS		
0	2d_indr_buffer_g		2002Sep11
	PASS		
0	3d_draw_indx_2		2002Sep11
	PASS		
0	3d_draw_indx_2_b		2002Sep11
	PASS		
0	IQ2_Mult		2002Sep11
	PASS		
0	IQ2_Wrap_Legacy		2002Sep11
	PASS		
0	IQ_Wrap		2002Sep11
	PASS		
0	RB_BUFSZ_0		2002Sep11
	PASS		
0	RB_BUFSZ_1		2002Sep11
	PASS		
0	RB_Wrap		2002Sep11
	PASS		
16	Small_Text_BM		2002Sep11
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
0	Type0_IQ2_BM		2002Sep11
	PASS		
0	Type0_IQ2_BM_IB_Packet		2002Sep11
	PASS		
0	Type0_IQ_BM		2002Sep11
	PASS		
0	Type0_IQ_BM_IB_Packet		2002Sep11

```

PASS
0   Type0_PQ_BM                               2002Sep11
PASS
0   Type0_PQ_BM_Pre_Write_Timer              2002Sep11
PASS
23  Type3_Bitblt                              2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
24  Type3_Load_Palette                        2002Sep11
**FAIL**          cp_mh_req.cmp rbbmWr.cmp
0   Type3_NOP                                  2002Sep11
PASS
26  Type3_Polyline                            2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
27  Type3_Polyline_Large                      2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
28  bitblt_multi                             2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
29  brush_32x1mono                           2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
30  brush_8x8color_16                        2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
31  brush_8x8color_32                        2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
32  brush_8x8color_64                        2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
33  brush_8x8color_dst_0_1                  2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
34  brush_8x8mono                            2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
35  brush_color_mono_stress                  2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
36  brush_flag                               2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
37  brush_type0                              2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
38  brush_type1                              2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
39  brush_type10                             2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
40  brush_type13                             2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
41  brush_type14                             2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
42  brush_type6                              2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
43  brush_type7                              2002Sep11
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
44  brush_type_other                          2002Sep11

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**FAIL**          cp_rbbm.cmp rbbmWr.cmp
45  brush_types_13_14                                2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   clear_const_wr_en_on_me_init                     2002Sep11
PASS
0   clear_const_wr_en_with_lcc                       2002Sep11
PASS
0   cond_write_mem_poll_mem                          2002Sep11
PASS
0   cond_write_mem_poll_mem2                         2002Sep11
PASS
0   cond_write_mem_poll_mem3                         2002Sep11
PASS
0   cond_write_mem_poll_mem_rt                       2002Sep11
PASS
0   cond_write_mem_poll_reg                          2002Sep11
PASS
0   cond_write_mem_poll_reg2                         2002Sep11
PASS
0   cond_write_mem_poll_reg3                         2002Sep11
PASS
0   cp_acpi_recovery_soft_reset                      2002Sep11
PASS
0   cp_bios_scratch                                  2002Sep11
PASS
0   cp_ib2_base000_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base001_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base010_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base011_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base100_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base101_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base110_bufszXXX                           2002Sep11
PASS
0   cp_ib2_base111_bufszXXX                           2002Sep11
PASS
0   cp_ib_base000_bufszXXX                             2002Sep11
PASS
0   cp_ib_base001_bufszXXX                             2002Sep11
PASS
0   cp_ib_base010_bufszXXX                             2002Sep11
PASS
0   cp_ib_base011_bufszXXX                             2002Sep11

```


PASS		
0	cp_ib_base100_bufszXXX	2002Sep11
PASS		
0	cp_ib_base101_bufszXXX	2002Sep11
PASS		
0	cp_ib_base110_bufszXXX	2002Sep11
PASS		
0	cp_ib_base111_bufszXXX	2002Sep11
PASS		
0	cp_interrupt	2002Sep11
PASS		
0	cp_interrupt_rt_events_16	2002Sep11
PASS		
0	cp_iq2_alignment_000	2002Sep11
PASS		
0	cp_iq2_alignment_001	2002Sep11
PASS		
0	cp_iq2_alignment_010	2002Sep11
PASS		
0	cp_iq2_alignment_011	2002Sep11
PASS		
0	cp_iq2_alignment_100	2002Sep11
PASS		
0	cp_iq2_alignment_101	2002Sep11
PASS		
0	cp_iq2_alignment_110	2002Sep11
PASS		
0	cp_iq2_alignment_111	2002Sep11
PASS		
0	cp_iq_alignment_000	2002Sep11
PASS		
0	cp_iq_alignment_001	2002Sep11
PASS		
0	cp_iq_alignment_010	2002Sep11
PASS		
0	cp_iq_alignment_011	2002Sep11
PASS		
0	cp_iq_alignment_100	2002Sep11
PASS		
0	cp_iq_alignment_101	2002Sep11
PASS		
0	cp_iq_alignment_110	2002Sep11
PASS		
0	cp_iq_alignment_111	2002Sep11
PASS		
0	cp_max_size_packet	2002Sep11
PASS		
0	cp_max_size_packet_random	2002Sep11

PASS		
0	cp_nrt_rt_protection_mode	2002Sep11
PASS		
0	cp_output_stress	2002Sep11
PASS		
0	cp_output_stress_random	2002Sep11
PASS		
0	cp_protection_mode	2002Sep11
PASS		
0	cp_rb_alignment_000	2002Sep11
PASS		
0	cp_rb_alignment_001	2002Sep11
PASS		
0	cp_rb_alignment_010	2002Sep11
PASS		
0	cp_rb_alignment_011	2002Sep11
PASS		
0	cp_rb_alignment_100	2002Sep11
PASS		
0	cp_rb_alignment_101	2002Sep11
PASS		
0	cp_rb_alignment_110	2002Sep11
PASS		
0	cp_rb_alignment_111	2002Sep11
PASS		
0	cp_rb_wptrXXX_base000	2002Sep11
PASS		
0	cp_rb_wptrXXX_base001	2002Sep11
PASS		
0	cp_rb_wptrXXX_base010	2002Sep11
PASS		
0	cp_rb_wptrXXX_base011	2002Sep11
PASS		
0	cp_rb_wptrXXX_base100	2002Sep11
PASS		
0	cp_rb_wptrXXX_base101	2002Sep11
PASS		
0	cp_rb_wptrXXX_base110	2002Sep11
PASS		
0	cp_rb_wptrXXX_base111	2002Sep11
PASS		
0	cp_reg_rw_test	2002Sep11
PASS		
0	cp_register_reads	2002Sep11
PASS		
0	cp_rt_protection_mode	2002Sep11
PASS		
0	cp_scratch	2002Sep11

PASS		
0	cp_scratch_multi	2002Sep11
PASS		
0	dma_cp_sync	2002Sep11
PASS		
0	dma_dscrstr_chckn_bit	2002Sep11
PASS		
0	dma_dscrstr_flush1	2002Sep11
PASS		
0	dma_dscrstr_flush2	2002Sep11
PASS		
0	dma_dscrstr_flush3	2002Sep11
PASS		
0	dma_m2m_src00_dstXX	2002Sep11
PASS		
0	dma_m2m_src01_dstXX	2002Sep11
PASS		
0	dma_m2m_src10_dstXX	2002Sep11
PASS		
0	dma_m2m_src11_dstXX	2002Sep11
PASS		
0	dma_mem2mem	2002Sep11
PASS		
0	dma_mem2mem_abort	2002Sep11
PASS		
0	dma_mem2mem_alignments	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep11
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep11
PASS		
0	dma_mem2mem_pio_abort	2002Sep11
PASS		
0	dma_mem2mem_random	2002Sep11
PASS		
0	dma_mem2reg	2002Sep11

PASS		
0	dma_mem2reg_abort	2002Sep11
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep11
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep11
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep11
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep11
PASS		
0	dma_mem2reg_sync	2002Sep11
PASS		
0	dma_r2r_m2m_r2r	2002Sep11
PASS		
0	dma_r2r_m2r_r2r	2002Sep11
PASS		
0	dma_r2r_r2m_r2r	2002Sep11
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep11
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep11
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep11
PASS		
0	dma_reg2mem	2002Sep11
PASS		
0	dma_reg2mem_abort	2002Sep11
PASS		
0	dma_reg2reg	2002Sep11
PASS		
0	dma_reg2reg_2dw	2002Sep11
PASS		
0	dma_reg2reg_daic	2002Sep11
PASS		
0	dma_reg2reg_mult	2002Sep11
PASS		
0	dma_reg2reg_mult2	2002Sep11
PASS		
0	dma_reg2reg_mult3	2002Sep11
PASS		
0	dma_reg2reg_saic	2002Sep11
PASS		
0	dma_reg2reg_sdaic	2002Sep11
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep11
PASS		
0	dma_reg2reg_stress	2002Sep11

PASS		
0	draw_indx_a	2002Sep11
PASS		
0	draw_indx_b	2002Sep11
PASS		
0	draw_indx_c	2002Sep11
PASS		
0	draw_indx_indr1_a	2002Sep11
PASS		
0	draw_indx_indr1_b	2002Sep11
PASS		
0	draw_indx_indr1_short_a	2002Sep11
PASS		
0	draw_indx_indr1_short_b	2002Sep11
PASS		
0	draw_indx_indr1_stress	2002Sep11
PASS		
0	draw_indx_indr2_a	2002Sep11
PASS		
0	draw_indx_indr2_b	2002Sep11
PASS		
0	draw_indx_indr2_short_a	2002Sep11
PASS		
0	draw_indx_indr2_short_b	2002Sep11
PASS		
0	draw_indx_indr2_stress	2002Sep11
PASS		
0	draw_indx_ring1	2002Sep11
PASS		
0	draw_indx_ring2	2002Sep11
PASS		
0	draw_indx_ring_short1	2002Sep11
PASS		
0	draw_indx_ring_short2	2002Sep11
PASS		
0	draw_indx_ring_stress	2002Sep11
PASS		
0	draw_indx_short_a	2002Sep11
PASS		
0	draw_indx_short_b	2002Sep11
PASS		
0	draw_indx_short_c	2002Sep11
PASS		
0	draw_indx_stress_a	2002Sep11
PASS		
0	draw_indx_stress_b	2002Sep11
PASS		
0	draw_indx_stress_c	2002Sep11

PASS		
0	event_timestamp_write	2002Sep11
PASS		
190	event_timestamp_write_multicontext_maxcontext_7	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	ft_indr1_july22	2002Sep11
PASS		
0	ft_indr2_july22	2002Sep11
PASS		
0	ft_july22	2002Sep11
PASS		
194	ft_july22_random	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
195	header_dump	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	header_dump_rt	2002Sep11
PASS		
197	header_dump_small	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
198	hostdata_blt	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep11
PASS		
0	im_load_b	2002Sep11
PASS		
0	im_load_cid_mismatch	2002Sep11
PASS		
0	im_load_immediate	2002Sep11
PASS		
0	im_load_immediate_nrt_me	2002Sep11
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep11
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep11
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep11
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep11
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep11
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep11
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep11
PASS		
0	im_load_indr1_a	2002Sep11
PASS		
0	im_load_indr1_b	2002Sep11

PASS		
0	im_load_indr2_a	2002Sep11
PASS		
0	im_load_indr2_b	2002Sep11
PASS		
0	im_load_invalidate	2002Sep11
PASS		
0	im_load_match	2002Sep11
PASS		
0	im_load_match2	2002Sep11
PASS		
0	im_load_nrt_me	2002Sep11
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep11
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep11
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep11
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep11
PASS		
0	im_load_pfmismatch_disable	2002Sep11
PASS		
0	im_load_ring_a	2002Sep11
PASS		
0	im_load_ring_b	2002Sep11
PASS		
0	im_load_rt	2002Sep11
PASS		
0	im_load_rt2	2002Sep11
PASS		
0	im_load_rt3	2002Sep11
PASS		
0	im_load_rt4	2002Sep11
PASS		
0	im_load_sz_mismatch	2002Sep11
PASS		
0	incremental_update_const	2002Sep11
PASS		
232	incremental_update_instr	2002Sep11
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	incremental_update_state	2002Sep11
PASS		
0	indirect_buffer_a	2002Sep11
PASS		
0	indirect_buffer_b	2002Sep11
PASS		
0	indirect_buffer_c	2002Sep11

PASS		
0	indirect_buffer_d	2002Sep11
PASS		
0	indirect_buffer_e	2002Sep11
PASS		
0	indirect_buffer_f	2002Sep11
PASS		
0	indirect_buffer_g	2002Sep11
PASS		
0	indirect_buffer_h	2002Sep11
PASS		
0	indirect_buffer_i	2002Sep11
PASS		
0	indirect_buffer_j	2002Sep11
PASS		
0	indirect_buffer_k	2002Sep11
PASS		
0	indirect_buffer_l	2002Sep11
PASS		
0	indirect_buffer_mp_a	2002Sep11
PASS		
0	indirect_buffer_mp_b	2002Sep11
PASS		
0	indirect_buffer_mp_c	2002Sep11
PASS		
0	indirect_buffer_mp_d	2002Sep11
PASS		
0	indirect_buffer_mp_e	2002Sep11
PASS		
0	indirect_buffer_mp_f	2002Sep11
PASS		
0	invalid_opcode	2002Sep11
PASS		
0	invalidate_on_me_init	2002Sep11
PASS		
0	invalidate_state_constants	2002Sep11
PASS		
0	invalidate_state_shaders	2002Sep11
PASS		
0	invalidate_state_shaders2	2002Sep11
PASS		
0	invalidate_state_sublks	2002Sep11
PASS		
0	invalidate_state_sublks2	2002Sep11
PASS		
0	iq1_iq2_mult	2002Sep11
PASS		
0	iq1_iq2_push_wrap	2002Sep11

PASS		
0	iq2_csq_full	2002Sep11
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep11
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep11
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep11
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep11
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep11
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep11
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep11
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep11
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep11
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep11
PASS		
0	ld_cnst_cntx_match	2002Sep11
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep11
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep11
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep11
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep11
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep11
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep11
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep11
PASS		
0	ld_cnst_cntx_replicate8	2002Sep11
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep11
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep11
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep11
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep11

PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep11
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep11
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep11
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep11
PASS		
0	load_constant_context_rt	2002Sep11
PASS		
290	load_palette	2002Sep11
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
291	lp_2d3d	2002Sep11
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	2002Sep11
PASS		
0	mem_write_rt	2002Sep11
PASS		
294	mpeg_index	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
295	mpeg_index_multi_context_max_context_0	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
296	mpeg_index_multi_context_max_context_1	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
297	mpeg_index_multi_context_max_context_2	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
298	mpeg_index_multi_context_max_context_3	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
299	mpeg_index_multi_context_max_context_4	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
300	mpeg_index_multi_context_max_context_5	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
301	mpeg_index_multi_context_max_context_6	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
302	mpeg_index_multi_context_max_context_7	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
303	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
304	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
305	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
306	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
307	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
308	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep11

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
309 mpeg_index_multi_context_rt_enabled_max_context_7      2002Sep11
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0  nop_eating_disabled      2002Sep11
PASS
311 paint_multi      2002Sep11
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
312 paint_packet      2002Sep11
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
313 paint_packet_gui_control      2002Sep11
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
314 pitch_clip      2002Sep11
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
315 ply_nextscan      2002Sep11
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0  rbbm_WU_2D_idle_01      2002Sep11
PASS
0  rbbm_WU_2D_idle_02      2002Sep11
PASS
0  rbbm_WU_2D_idle_03      2002Sep11
PASS
0  rbbm_WU_2D_idle_04      2002Sep11
PASS
0  rbbm_WU_2D_idle_05      2002Sep11
PASS
0  rbbm_WU_2D_idle_06      2002Sep11
PASS
0  rbbm_WU_2D_idle_07      2002Sep11
PASS
0  rbbm_WU_2D_idle_08      2002Sep11
PASS
0  rbbm_WU_2D_idle_09      2002Sep11
PASS
325 rbbm_WU_2D_idle_clean_01      2002Sep11
**FAIL**
326 rbbm_WU_2D_idle_clean_02      2002Sep11
**FAIL**
327 rbbm_WU_2D_idle_clean_03      2002Sep11
**FAIL**
328 rbbm_WU_2D_idle_clean_04      2002Sep11
**FAIL**
0  rbbm_WU_2D_idle_clean_05      2002Sep11
PASS
330 rbbm_WU_2D_idle_clean_06      2002Sep11
**FAIL**
331 rbbm_WU_2D_idle_clean_07      2002Sep11
**FAIL**
332 rbbm_WU_2D_idle_clean_08      2002Sep11

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FAIL		
333	rbbm_WU_2D_idle_clean_09	2002Sep11
FAIL		
334	rbbm_WU_2D_idle_clean_10	2002Sep11
FAIL		
335	rbbm_WU_2D_idle_clean_11	2002Sep11
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Sep11
PASS		
0	rbbm_WU_3D_idle_01	2002Sep11
PASS		
0	rbbm_WU_3D_idle_02	2002Sep11
PASS		
0	rbbm_WU_3D_idle_03	2002Sep11
PASS		
0	rbbm_WU_3D_idle_04	2002Sep11
PASS		
0	rbbm_WU_3D_idle_05	2002Sep11
PASS		
0	rbbm_WU_3D_idle_06	2002Sep11
PASS		
0	rbbm_WU_3D_idle_07	2002Sep11
PASS		
0	rbbm_WU_3D_idle_08	2002Sep11
PASS		
0	rbbm_WU_3D_idle_09	2002Sep11
PASS		
346	rbbm_WU_3D_idle_clean_01	2002Sep11
FAIL		
347	rbbm_WU_3D_idle_clean_02	2002Sep11
FAIL		
348	rbbm_WU_3D_idle_clean_03	2002Sep11
FAIL		
349	rbbm_WU_3D_idle_clean_04	2002Sep11
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Sep11
PASS		
351	rbbm_WU_3D_idle_clean_06	2002Sep11
FAIL		
352	rbbm_WU_3D_idle_clean_07	2002Sep11
FAIL		
353	rbbm_WU_3D_idle_clean_08	2002Sep11
FAIL		
354	rbbm_WU_3D_idle_clean_09	2002Sep11
FAIL		
355	rbbm_WU_3D_idle_clean_10	2002Sep11
FAIL		
356	rbbm_WU_3D_idle_clean_11	2002Sep11

FAIL

0	rbbm_WU_3D_idle_clean_12	2002Sep11
	PASS	
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep11
	PASS	
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Sep11
	PASS	
0	rbbm_WU_CP_dma_idle	2002Sep11
	PASS	
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep11
	PASS	
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep11
	PASS	
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep11
	PASS	
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep11
	PASS	
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep11
	PASS	
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep11
	PASS	
0	rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Sep11
	PASS	
0	rbbm_WU_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Sep11
	PASS	
0	rbbm_WU_MH_hdp_clean	2002Sep11
	PASS	
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep11
	PASS	
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep11
	PASS	
0	rbbm_WU_VIPH0_dma_idle	2002Sep11
	PASS	
0	rbbm_WU_VIPH1_dma_idle	2002Sep11
	PASS	
0	rbbm_WU_VIPH2_dma_idle	2002Sep11
	PASS	
0	rbbm_WU_VIPH3_dma_idle	2002Sep11
	PASS	
0	rbbm_WU_cmdfifo_entries	2002Sep11
	PASS	
0	rbbm_WU_idct_semaphore	2002Sep11
	PASS	
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep11
	PASS	
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep11
	PASS	
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep11

PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep11
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep11
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep11
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep11
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep11
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep11
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep11
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep11
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep11

PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep11
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep11
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep11
PASS		
408	rbbm_cp_non_queued_rdwr_02	2002Sep11
FAIL	cp_rbbm.cmp	
409	rbbm_cp_queued_holds_nonqueued	2002Sep11
FAIL	cp_rbbm.cmp	
410	rbbm_cp_queued_rdwr	2002Sep11
FAIL	cp_rbbm.cmp	
0	rbbm_isync_cpscratch_idlegui	2002Sep11
PASS		
0	rbbm_isync_wait_idlegui	2002Sep11
PASS		
0	rbbm_soft_reset	2002Sep11
PASS		
0	reg_to_mem	2002Sep11
PASS		
0	reg_to_mem_rt	2002Sep11
PASS		
416	rop_cfd_wmd	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
417	rstr_ordr	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rt_invalid_opcode	2002Sep11
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Sep11
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep11
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep11

PASS		
0	rtee_discretes_falling_08_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep11
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0	rtee_discretes_falling_11_events_16	2002Sep11
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0	rtee_discretes_falling_12_events_16	2002Sep11
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0	rtee_discretes_falling_13_events_16	2002Sep11
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0	rtee_discretes_falling_14_events_16	2002Sep11
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0	rtee_discretes_falling_15_events_16	2002Sep11
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0	rtee_discretes_falling_16_events_16	2002Sep11
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0	rtee_discretes_falling_17_events_16	2002Sep11
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0	rtee_discretes_falling_18_events_16	2002Sep11
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0	rtee_discretes_falling_19_events_16	2002Sep11
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0	rtee_discretes_falling_20_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep11
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0	rtee_discretes_falling_24_events_16	2002Sep11
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0	rtee_discretes_falling_25_events_16	2002Sep11
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0	rtee_discretes_falling_26_events_16	2002Sep11
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0	rtee_discretes_falling_27_events_16	2002Sep11
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0	rtee_discretes_falling_28_events_16	2002Sep11
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0	rtee_discretes_falling_29_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep11
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep11

PASS		
0	rtee_discretes_invert_00_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep11
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0	rtee_discretes_invert_08_events_16	2002Sep11
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0	rtee_discretes_invert_09_events_16	2002Sep11
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0	rtee_discretes_invert_10_events_16	2002Sep11
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0	rtee_discretes_invert_11_events_16	2002Sep11
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0	rtee_discretes_invert_12_events_16	2002Sep11
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0	rtee_discretes_invert_13_events_16	2002Sep11
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0	rtee_discretes_invert_14_events_16	2002Sep11
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0	rtee_discretes_invert_15_events_16	2002Sep11
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0	rtee_discretes_invert_16_events_16	2002Sep11
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0	rtee_discretes_invert_17_events_16	2002Sep11
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0	rtee_discretes_invert_19_events_16	2002Sep11
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0	rtee_discretes_invert_20_events_16	2002Sep11
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0	rtee_discretes_invert_22_events_16	2002Sep11
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0	rtee_discretes_invert_23_events_16	2002Sep11

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0	rtee_discretes_invert_26_events_16	2002Sep11
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0	rtee_discretes_invert_27_events_16	2002Sep11
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0	rtee_discretes_invert_28_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep11
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep11
PASS		
0	rtee_discretes_level_00_events_16	2002Sep11
PASS		
0	rtee_discretes_level_01_events_16	2002Sep11
PASS		
0	rtee_discretes_level_02_events_16	2002Sep11
PASS		
0	rtee_discretes_level_03_events_16	2002Sep11
PASS		
0	rtee_discretes_level_04_events_16	2002Sep11
PASS		
0	rtee_discretes_level_05_events_16	2002Sep11
PASS		
0	rtee_discretes_level_06_events_16	2002Sep11
PASS		
0	rtee_discretes_level_07_events_16	2002Sep11
PASS		
0	rtee_discretes_level_08_events_16	2002Sep11
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0	rtee_discretes_level_09_events_16	2002Sep11
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0	rtee_discretes_level_10_events_16	2002Sep11
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0	rtee_discretes_level_11_events_16	2002Sep11
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0	rtee_discretes_level_12_events_16	2002Sep11
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0	rtee_discretes_level_13_events_16	2002Sep11
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0	rtee_discretes_level_14_events_16	2002Sep11
PASS		
0	rtee_discretes_level_15_events_16	2002Sep11

PASS		
0	rtee_discretes_level_16_events_16	2002Sep11
PASS		
0	rtee_discretes_level_17_events_16	2002Sep11
PASS		
0	rtee_discretes_level_18_events_16	2002Sep11
PASS		
0	rtee_discretes_level_19_events_16	2002Sep11
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0	rtee_discretes_level_21_events_16	2002Sep11
PASS		
0	rtee_discretes_level_22_events_16	2002Sep11
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0	rtee_discretes_level_23_events_16	2002Sep11
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0	rtee_discretes_level_24_events_16	2002Sep11
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0	rtee_discretes_level_25_events_16	2002Sep11
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0	rtee_discretes_level_26_events_16	2002Sep11
PASS		
0	rtee_discretes_level_27_events_16	2002Sep11
PASS		
0	rtee_discretes_level_28_events_16	2002Sep11
PASS		
0	rtee_discretes_level_29_events_16	2002Sep11
PASS		
0	rtee_discretes_level_30_events_16	2002Sep11
PASS		
0	rtee_discretes_level_31_events_16	2002Sep11
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0	rtee_discretes_manual_disarm_events_16	2002Sep11
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0	rtee_discretes_rising_00_events_16	2002Sep11
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0	rtee_discretes_rising_01_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep11
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0	rtee_discretes_rising_04_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep11

PASS		
0	rtee_discretes_rising_07_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep11
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0	rtee_discretes_rising_09_events_16	2002Sep11
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0	rtee_discretes_rising_10_events_16	2002Sep11
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0	rtee_discretes_rising_11_events_16	2002Sep11
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0	rtee_discretes_rising_12_events_16	2002Sep11
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0	rtee_discretes_rising_13_events_16	2002Sep11
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0	rtee_discretes_rising_14_events_16	2002Sep11
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0	rtee_discretes_rising_15_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep11
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0	rtee_discretes_rising_21_events_16	2002Sep11
PASS		
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PASS		
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PASS		
0	rtee_discretes_rising_24_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep11
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep11

PASS		
0	rtee_discretes_rising_31_events_16	2002Sep11
PASS		
0	rtee_display1_comp_events_16	2002Sep11
PASS		
0	rtee_display2_comp_events_16	2002Sep11
PASS		
0	rtee_mem_linear_search_events_2	2002Sep11
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep11
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep11
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep11
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep11
PASS		
0	rtee_prefetch_parse_nops	2002Sep11
PASS		
0	rtee_reg_linear_search_events_2	2002Sep11
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep11

PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep11
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep11
PASS		
0	rtee_vip_comp_events_16	2002Sep11
PASS		
0	scratch_mem_read	2002Sep11
PASS		
0	scratch_mem_read2	2002Sep11
PASS		
0	set_constant	2002Sep11
PASS		
0	set_constant2	2002Sep11
PASS		
0	set_constant_alu_max1	2002Sep11
PASS		
0	set_constant_alu_max2	2002Sep11
PASS		
0	set_constant_bool_max1	2002Sep11
PASS		
0	set_constant_bool_max2	2002Sep11
PASS		
0	set_constant_indr1_short	2002Sep11
PASS		
0	set_constant_indr1_short2	2002Sep11
PASS		
0	set_constant_indr1_write_enable	2002Sep11
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep11
PASS		
0	set_constant_indr2_short	2002Sep11
PASS		
0	set_constant_indr2_short2	2002Sep11
PASS		
0	set_constant_indr2_write_enable	2002Sep11
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep11
PASS		
0	set_constant_loop_max1	2002Sep11
PASS		
0	set_constant_loop_max2	2002Sep11
PASS		
0	set_constant_ring_short	2002Sep11
PASS		
0	set_constant_ring_short2	2002Sep11

PASS		
0	set_constant_ring_write_enable	2002Sep11
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep11
PASS		
0	set_constant_short	2002Sep11
PASS		
0	set_constant_short2	2002Sep11
PASS		
0	set_constant_tex_max1	2002Sep11
PASS		
0	set_constant_tex_max2	2002Sep11
PASS		
0	set_constant_write_enable	2002Sep11
PASS		
0	set_constant_write_enable_lcc	2002Sep11
PASS		
0	set_constant_write_enable_lcc2	2002Sep11
PASS		
0	set_scissors	2002Sep11
PASS		
0	set_st_indrl_match	2002Sep11
PASS		
0	set_st_indrl_mismatch	2002Sep11
PASS		
0	set_st_indrl_mismatch2	2002Sep11
PASS		
0	set_st_indrl_mismatch3	2002Sep11
PASS		
0	set_st_indrl_mismatch4	2002Sep11
PASS		
0	set_st_indrl_ps	2002Sep11
PASS		
0	set_st_indrl_ps_only	2002Sep11
PASS		
0	set_st_indrl_sublk0	2002Sep11
PASS		
0	set_st_indrl_sublk1	2002Sep11
PASS		
0	set_st_indrl_sublk1_only	2002Sep11
PASS		
0	set_st_indrl_sublk2	2002Sep11
PASS		
0	set_st_indrl_sublk2_only	2002Sep11
PASS		
0	set_st_indrl_sublk3	2002Sep11
PASS		
0	set_st_indrl_sublk3_only	2002Sep11

PASS		
0	set_st_indr1_sublk4	2002Sep11
PASS		
0	set_st_indr1_sublk4_only	2002Sep11
PASS		
0	set_st_indr1_sublk5	2002Sep11
PASS		
0	set_st_indr1_sublk5_only	2002Sep11
PASS		
0	set_st_indr1_sublk6	2002Sep11
PASS		
0	set_st_indr1_sublk6_only	2002Sep11
PASS		
0	set_st_indr1_sublk7	2002Sep11
PASS		
0	set_st_indr1_sublk7_only	2002Sep11
PASS		
0	set_st_indr1_vs	2002Sep11
PASS		
0	set_st_indr1_vs_only	2002Sep11
PASS		
0	set_st_indr2_match	2002Sep11
PASS		
0	set_st_indr2_mismatch	2002Sep11
PASS		
0	set_st_indr2_mismatch2	2002Sep11
PASS		
0	set_st_indr2_mismatch3	2002Sep11
PASS		
0	set_st_indr2_mismatch4	2002Sep11
PASS		
0	set_st_indr2_ps	2002Sep11
PASS		
0	set_st_indr2_ps_only	2002Sep11
PASS		
0	set_st_indr2_sublk0	2002Sep11
PASS		
0	set_st_indr2_sublk1	2002Sep11
PASS		
0	set_st_indr2_sublk1_only	2002Sep11
PASS		
0	set_st_indr2_sublk2	2002Sep11
PASS		
0	set_st_indr2_sublk2_only	2002Sep11
PASS		
0	set_st_indr2_sublk3	2002Sep11
PASS		
0	set_st_indr2_sublk3_only	2002Sep11

PASS		
0	set_st_indr2_sublk4	2002Sep11
PASS		
0	set_st_indr2_sublk4_only	2002Sep11
PASS		
0	set_st_indr2_sublk5	2002Sep11
PASS		
0	set_st_indr2_sublk5_only	2002Sep11
PASS		
0	set_st_indr2_sublk6	2002Sep11
PASS		
0	set_st_indr2_sublk6_only	2002Sep11
PASS		
0	set_st_indr2_sublk7	2002Sep11
PASS		
0	set_st_indr2_sublk7_only	2002Sep11
PASS		
0	set_st_indr2_vs	2002Sep11
PASS		
0	set_st_indr2_vs_only	2002Sep11
PASS		
0	set_st_pfmacth_disabled	2002Sep11
PASS		
0	set_st_ring_match	2002Sep11
PASS		
0	set_st_ring_mismatch	2002Sep11
PASS		
0	set_st_ring_mismatch2	2002Sep11
PASS		
0	set_st_ring_mismatch3	2002Sep11
PASS		
0	set_st_ring_mismatch4	2002Sep11
PASS		
0	set_st_ring_ps	2002Sep11
PASS		
0	set_st_ring_ps_only	2002Sep11
PASS		
0	set_st_ring_sublk0	2002Sep11
PASS		
0	set_st_ring_sublk1	2002Sep11
PASS		
0	set_st_ring_sublk1_only	2002Sep11
PASS		
0	set_st_ring_sublk2	2002Sep11
PASS		
0	set_st_ring_sublk2_only	2002Sep11
PASS		
0	set_st_ring_sublk3	2002Sep11

PASS		
0	set_st_ring_sublk3_only	2002Sep11
PASS		
0	set_st_ring_sublk4	2002Sep11
PASS		
0	set_st_ring_sublk4_only	2002Sep11
PASS		
0	set_st_ring_sublk5	2002Sep11
PASS		
0	set_st_ring_sublk5_only	2002Sep11
PASS		
0	set_st_ring_sublk6	2002Sep11
PASS		
0	set_st_ring_sublk6_only	2002Sep11
PASS		
0	set_st_ring_sublk7	2002Sep11
PASS		
0	set_st_ring_sublk7_only	2002Sep11
PASS		
0	set_st_ring_vs	2002Sep11
PASS		
0	set_st_ring_vs_only	2002Sep11
PASS		
0	set_st_stress_a	2002Sep11
PASS		
681	set_st_stress_b	2002Sep11
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Sep11
PASS		
0	set_st_stress_d	2002Sep11
PASS		
0	set_st_stress_e	2002Sep11
PASS		
0	set_st_stress_f	2002Sep11
PASS		
0	set_state_rt_a	2002Sep11
PASS		
0	set_state_rt_b	2002Sep11
PASS		
0	set_state_rt_c	2002Sep11
PASS		
0	set_state_rt_d	2002Sep11
PASS		
0	smoke_nrt	2002Sep11
PASS		
691	src_clip	2002Sep11
FAIL	cp_rbbm.cmp rbbmWr.cmp	
692	srf_cohr	2002Sep11

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
693  trans_bitblt                                     2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
694  type0_2long_pckts                                2002Sep11
**FAIL**          cp_mh_req.cmp
0    type0_3indr_buffs                               2002Sep11
PASS
0    type0_4pckts                                     2002Sep11
PASS
0    type0_incr_updt_cnst                             2002Sep11
PASS
0    type0_incr_updt_instrmem                         2002Sep11
PASS
0    type0_incr_updt_st                               2002Sep11
PASS
0    type0_incr_updt_stress                           2002Sep11
PASS
0    type0_ind1_ind2_a                               2002Sep11
PASS
0    type0_ind1_ind2_b                               2002Sep11
PASS
0    type0_indr_buff                                 2002Sep11
PASS
0    type1_4pckts                                     2002Sep11
PASS
0    type1_indr_buff                                 2002Sep11
PASS
0    type1_pckt                                       2002Sep11
PASS
707  type_random                                     2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    uram_wrt_rt_ucode                               2002Sep11
PASS
709  uram_wrt_ucode                                   2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    viz_query_a                                      2002Sep11
PASS
0    viz_query_draw_indx                             2002Sep11
PASS
0    viz_query_draw_indx_indr1                       2002Sep11
PASS
0    viz_query_draw_indx_indr2                       2002Sep11
PASS
714  viz_query_draw_indx_multipass                   2002Sep11
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    viz_query_draw_indx_ring                         2002Sep11
PASS
0    viz_query_indr1                                  2002Sep11

```

```

PASS
0 viz_query_indr1_simple 2002Sep11
PASS
0 viz_query_indr2 2002Sep11
PASS
0 viz_query_indr2_simple 2002Sep11
PASS
0 viz_query_ring 2002Sep11
PASS
0 viz_query_ring_simple 2002Sep11
PASS
0 wait_for_idle 2002Sep11
PASS
0 wait_reg_mem_poll_mem0 2002Sep11
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep11
PASS
725 wait_reg_mem_poll_mem1 2002Sep11
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
726 wait_reg_mem_poll_mem2 2002Sep11
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
727 wait_reg_mem_poll_mem3 2002Sep11
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_reg0 2002Sep11
PASS
729 wait_reg_mem_poll_reg1 2002Sep11
**FAIL** cp_rbbm.cmp
730 wait_reg_mem_poll_reg2 2002Sep11
**FAIL** cp_rbbm.cmp
731 wait_reg_mem_poll_reg3 2002Sep11
**FAIL** cp_rbbm.cmp
=====
Tests_Run_count = 731 Pass_count = 640 Fail_count = 91 Incomplete_count = 0
=====

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Fri Oct 11  0:23:49 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          740         740         615         83
              RBBM        120         120          83          69
+++++
              TOTAL        860         860         698          81
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Aug 24  5:26:30 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC               0          0          0          0
              CL               0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU               0          0          0          0
              CP              617        617        607         98
              RBBM            92         92         92        100
+++++
              TOTAL            709        709        699         98
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug30
PASS			
0	2d_indr_buffer_b		2002Aug30
PASS			
0	2d_indr_buffer_c		2002Aug30
PASS			
0	2d_indr_buffer_d		2002Aug30
PASS			
0	2d_indr_buffer_e		2002Aug30
PASS			
0	2d_indr_buffer_f		2002Aug30
PASS			
0	2d_indr_buffer_g		2002Aug30
PASS			
0	3d_draw_indx_2		2002Aug30
PASS			
0	3d_draw_indx_2_b		2002Aug30
PASS			
0	bitblt_multi		2002Aug30
PASS			
0	brush_32x1mono		2002Aug30
PASS			
0	brush_8x8color_16		2002Aug30
PASS			
0	brush_8x8color_32		2002Aug30
PASS			
0	brush_8x8color_64		2002Aug30
PASS			
0	brush_8x8color_dst_0_1		2002Aug30
PASS			
0	brush_8x8mono		2002Aug30
PASS			
0	brush_color_mono_stress		2002Aug30
PASS			
0	brush_flag		2002Aug30
PASS			
0	brush_type0		2002Aug30
PASS			
0	brush_type1		2002Aug30

PASS		
0	brush_type10	2002Aug30
PASS		
0	brush_type13	2002Aug30
PASS		
0	brush_type14	2002Aug30
PASS		
0	brush_type6	2002Aug30
PASS		
0	brush_type7	2002Aug30
PASS		
0	brush_type_other	2002Aug30
PASS		
0	brush_types_13_14	2002Aug30
PASS		
0	clear_const_wr_en_on_me_init	2002Aug30
PASS		
0	clear_const_wr_en_with_lcc	2002Aug30
PASS		
0	cond_write_mem_poll_mem	2002Aug30
PASS		
0	cond_write_mem_poll_mem2	2002Aug30
PASS		
0	cond_write_mem_poll_mem3	2002Aug30
PASS		
0	cond_write_mem_poll_mem_rt	2002Aug30
PASS		
0	cond_write_mem_poll_reg	2002Aug30
PASS		
0	cond_write_mem_poll_reg2	2002Aug30
PASS		
0	cond_write_mem_poll_reg3	2002Aug30
PASS		
0	cp_acpi_recovery_soft_reset	2002Aug30
PASS		
0	cp_bios_scratch	2002Aug30
PASS		
0	cp_ib2_base000_bufszXXX	2002Aug30
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug30
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug30
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug30
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug30
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug30

PASS		
0	cp_ib2_base110_bufszXXX	2002Aug30
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug30
PASS		
0	cp_ib_base000_bufszXXX	2002Aug30
PASS		
0	cp_ib_base001_bufszXXX	2002Aug30
PASS		
0	cp_ib_base010_bufszXXX	2002Aug30
PASS		
0	cp_ib_base011_bufszXXX	2002Aug30
PASS		
0	cp_ib_base100_bufszXXX	2002Aug30
PASS		
0	cp_ib_base101_bufszXXX	2002Aug30
PASS		
0	cp_ib_base110_bufszXXX	2002Aug30
PASS		
0	cp_ib_base111_bufszXXX	2002Aug30
PASS		
0	cp_interrupt	2002Aug30
PASS		
0	cp_interrupt_rt_events_16	2002Aug30
PASS		
0	cp_iq2_alignment_000	2002Aug30
PASS		
0	cp_iq2_alignment_001	2002Aug30
PASS		
0	cp_iq2_alignment_010	2002Aug30
PASS		
0	cp_iq2_alignment_011	2002Aug30
PASS		
0	cp_iq2_alignment_100	2002Aug30
PASS		
0	cp_iq2_alignment_101	2002Aug30
PASS		
0	cp_iq2_alignment_110	2002Aug30
PASS		
0	cp_iq2_alignment_111	2002Aug30
PASS		
0	cp_iq_alignment_000	2002Aug30
PASS		
0	cp_iq_alignment_001	2002Aug30
PASS		
0	cp_iq_alignment_010	2002Aug30
PASS		
0	cp_iq_alignment_011	2002Aug30

PASS		
0	cp_iq_alignment_100	2002Aug30
PASS		
0	cp_iq_alignment_101	2002Aug30
PASS		
0	cp_iq_alignment_110	2002Aug30
PASS		
0	cp_iq_alignment_111	2002Aug30
PASS		
0	cp_max_size_packet	2002Aug30
PASS		
74	cp_max_size_packet_random	2002Aug30
FAIL	rbbmWr.cmp	
0	cp_nrt_rt_protection_mode	2002Aug30
PASS		
0	cp_output_stress	2002Aug30
PASS		
0	cp_protection_mode	2002Aug30
PASS		
0	cp_rb_alignment_000	2002Aug30
PASS		
0	cp_rb_alignment_001	2002Aug30
PASS		
0	cp_rb_alignment_010	2002Aug30
PASS		
0	cp_rb_alignment_011	2002Aug30
PASS		
0	cp_rb_alignment_100	2002Aug30
PASS		
0	cp_rb_alignment_101	2002Aug30
PASS		
0	cp_rb_alignment_110	2002Aug30
PASS		
0	cp_rb_alignment_111	2002Aug30
PASS		
0	cp_rb_wptrXXX_base000	2002Aug30
PASS		
0	cp_rb_wptrXXX_base001	2002Aug30
PASS		
0	cp_rb_wptrXXX_base010	2002Aug30
PASS		
0	cp_rb_wptrXXX_base011	2002Aug30
PASS		
0	cp_rb_wptrXXX_base100	2002Aug30
PASS		
0	cp_rb_wptrXXX_base101	2002Aug30
PASS		
0	cp_rb_wptrXXX_base110	2002Aug30

PASS		
0	cp_rb_wptraXXX_base11	2002Aug30
PASS		
0	cp_register_reads	2002Aug30
PASS		
0	cp_reg_rw_test	2002Aug30
PASS		
0	cp_rt_protection_mode	2002Aug30
PASS		
0	cp_scratch	2002Aug30
PASS		
0	cp_scratch_multi	2002Aug30
PASS		
0	dma_cp_sync	2002Aug30
PASS		
0	dma_dscrstr_chckn_bit	2002Aug30
PASS		
0	dma_dscrstr_flush1	2002Aug30
PASS		
0	dma_dscrstr_flush2	2002Aug30
PASS		
0	dma_dscrstr_flush3	2002Aug30
PASS		
0	dma_m2m_src00_dstXX	2002Aug30
PASS		
0	dma_m2m_src01_dstXX	2002Aug30
PASS		
0	dma_m2m_src10_dstXX	2002Aug30
PASS		
0	dma_m2m_src11_dstXX	2002Aug30
PASS		
0	dma_mem2mem	2002Aug30
PASS		
0	dma_mem2mem_abort	2002Aug30
PASS		
0	dma_mem2mem_alignments	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug30

PASS		
0	dma_mem2mem_byte_alignment6	2002Aug30
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug30
PASS		
0	dma_mem2mem_pio_abort	2002Aug30
PASS		
0	dma_mem2reg	2002Aug30
PASS		
0	dma_mem2reg_abort	2002Aug30
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug30
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug30
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug30
PASS		
0	dma_mem2reg_sync	2002Aug30
PASS		
0	dma_r2r_m2m_r2r	2002Aug30
PASS		
0	dma_r2r_m2r_r2r	2002Aug30
PASS		
0	dma_r2r_r2m_r2r	2002Aug30
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug30
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug30
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug30
PASS		
0	dma_reg2mem	2002Aug30
PASS		
0	dma_reg2mem_abort	2002Aug30
PASS		
0	dma_reg2reg	2002Aug30
PASS		
0	dma_reg2reg_2dw	2002Aug30
PASS		
0	dma_reg2reg_daic	2002Aug30
PASS		
0	dma_reg2reg_mult	2002Aug30
PASS		
0	dma_reg2reg_mult2	2002Aug30
PASS		
0	dma_reg2reg_mult3	2002Aug30
PASS		
0	dma_reg2reg_saic	2002Aug30

PASS		
0	dma_reg2reg_sdaic	2002Aug30
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug30
PASS		
0	dma_reg2reg_stress	2002Aug30
PASS		
0	draw_indx_a	2002Aug30
PASS		
0	draw_indx_b	2002Aug30
PASS		
0	draw_indx_c	2002Aug30
PASS		
0	draw_indx_indr1_a	2002Aug30
PASS		
0	draw_indx_indr1_b	2002Aug30
PASS		
0	draw_indx_indr1_short_a	2002Aug30
PASS		
0	draw_indx_indr1_short_b	2002Aug30
PASS		
0	draw_indx_indr1_stress	2002Aug30
PASS		
0	draw_indx_indr2_a	2002Aug30
PASS		
0	draw_indx_indr2_b	2002Aug30
PASS		
0	draw_indx_indr2_short_a	2002Aug30
PASS		
0	draw_indx_indr2_short_b	2002Aug30
PASS		
0	draw_indx_indr2_stress	2002Aug30
PASS		
0	draw_indx_ring1	2002Aug30
PASS		
0	draw_indx_ring2	2002Aug30
PASS		
0	draw_indx_ring_short1	2002Aug30
PASS		
0	draw_indx_ring_short2	2002Aug30
PASS		
0	draw_indx_ring_stress	2002Aug30
PASS		
0	draw_indx_short_a	2002Aug30
PASS		
0	draw_indx_short_b	2002Aug30
PASS		
0	draw_indx_short_c	2002Aug30

PASS		
0	draw_indx_stress_a	2002Aug30
PASS		
0	draw_indx_stress_b	2002Aug30
PASS		
0	draw_indx_stress_c	2002Aug30
PASS		
168	event_timestamp_write	2002Aug30
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug30
PASS		
0	ft_indr1_july22	2002Aug30
PASS		
0	ft_indr2_july22	2002Aug30
PASS		
0	ft_july22	2002Aug30
PASS		
0	ft_july22_random	2002Aug30
PASS		
0	header_dump	2002Aug30
PASS		
0	header_dump_rt	2002Aug30
PASS		
0	header_dump_small	2002Aug30
PASS		
0	hostdata_blt	2002Aug30
PASS		
0	im_load_a	2002Aug30
PASS		
0	im_load_b	2002Aug30
PASS		
0	im_load_cid_mismatch	2002Aug30
PASS		
0	im_load_immediate	2002Aug30
PASS		
0	im_load_immediate_nrt_me	2002Aug30
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug30
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug30
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug30
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug30
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug30
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug30

PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug30
PASS		
0	im_load_indr1_a	2002Aug30
PASS		
0	im_load_indr1_b	2002Aug30
PASS		
0	im_load_indr2_a	2002Aug30
PASS		
0	im_load_indr2_b	2002Aug30
PASS		
0	im_load_invalidate	2002Aug30
PASS		
0	im_load_match	2002Aug30
PASS		
0	im_load_match2	2002Aug30
PASS		
0	im_load_nrt_me	2002Aug30
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug30
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug30
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug30
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug30
PASS		
0	im_load_pfmatch_disable	2002Aug30
PASS		
0	im_load_ring_a	2002Aug30
PASS		
0	im_load_ring_b	2002Aug30
PASS		
0	im_load_rt	2002Aug30
PASS		
0	im_load_rt2	2002Aug30
PASS		
0	im_load_rt3	2002Aug30
PASS		
0	im_load_rt4	2002Aug30
PASS		
0	im_load_sz_mismatch	2002Aug30
PASS		
0	incremental_update_const	2002Aug30
PASS		
0	incremental_update_instr	2002Aug30
PASS		
0	incremental_update_state	2002Aug30

PASS		
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PASS		
0	indirect_buffer_b	2002Aug30
PASS		
0	indirect_buffer_c	2002Aug30
PASS		
0	indirect_buffer_d	2002Aug30
PASS		
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PASS		
0	indirect_buffer_f	2002Aug30
PASS		
0	indirect_buffer_g	2002Aug30
PASS		
0	indirect_buffer_h	2002Aug30
PASS		
0	indirect_buffer_i	2002Aug30
PASS		
0	indirect_buffer_j	2002Aug30
PASS		
0	indirect_buffer_k	2002Aug30
PASS		
0	indirect_buffer_l	2002Aug30
PASS		
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PASS		
0	indirect_buffer_mp_b	2002Aug30
PASS		
0	indirect_buffer_mp_c	2002Aug30
PASS		
0	indirect_buffer_mp_d	2002Aug30
PASS		
0	indirect_buffer_mp_e	2002Aug30
PASS		
0	indirect_buffer_mp_f	2002Aug30
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0	invalidate_on_me_init	2002Aug30
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0	invalidate_state_constants	2002Aug30
PASS		
0	invalidate_state_shaders	2002Aug30
PASS		
0	invalidate_state_shaders2	2002Aug30
PASS		
0	invalidate_state_sublks	2002Aug30
PASS		
0	invalidate_state_sublks2	2002Aug30

PASS		
0	invalid_opcode	2002Aug30
PASS		
0	iq1_iq2_mult	2002Aug30
PASS		
0	iq1_iq2_push_wrap	2002Aug30
PASS		
0	iq2_csq_full	2002Aug30
PASS		
0	IQ2_Mult	2002Aug30
PASS		
0	IQ2_Wrap_Legacy	2002Aug30
PASS		
0	IQ_Wrap	2002Aug30
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Aug30
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Aug30
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug30
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug30
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug30
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug30
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug30
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug30
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug30
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug30
PASS		
0	ld_cnst_cntx_match	2002Aug30
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug30
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug30
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug30
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0	ld_cnst_cntx_mismatch_force	2002Aug30
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug30
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug30

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug30
PASS		
0	ld_cnst_cntx_replicate8	2002Aug30
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug30
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug30
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0	ld_cnst_cntx_ring_replicate	2002Aug30
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug30
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug30
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0	ld_cnst_cntx_ring_simple	2002Aug30
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0	ld_cnst_cntx_simple_alu	2002Aug30
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0	ld_cnst_cntx_simple_tex	2002Aug30
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0	load_constant_context_rt	2002Aug30
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0	load_palette	2002Aug30
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PASS		
0	mem_write_rt	2002Aug30
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0	mpeg_index_multi_context_max_context_0	2002Aug30
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0	mpeg_index_multi_context_max_context_1	2002Aug30
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0	mpeg_index_multi_context_max_context_2	2002Aug30
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0	mpeg_index_multi_context_max_context_3	2002Aug30
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0	mpeg_index_multi_context_max_context_4	2002Aug30
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug30
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0	mpeg_index_multi_context_max_context_6	2002Aug30
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug30

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug30
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0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug30
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug30
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug30
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug30
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug30
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug30
PASS		
0	nop_eating_disabled	2002Aug30
PASS		
0	paint_multi	2002Aug30
PASS		
0	pitch_clip	2002Aug30
PASS		
0	ply_nextscan	2002Aug30
PASS		
0	rbbm_soft_reset	2002Aug30
PASS		
297	rbbm_WU_2D_idle_01	2002Aug30
FAIL		
298	rbbm_WU_2D_idle_02	2002Aug30
FAIL		
299	rbbm_WU_2D_idle_03	2002Aug30
FAIL		
300	rbbm_WU_2D_idle_04	2002Aug30
FAIL		
0	rbbm_WU_2D_idle_05	2002Aug30
PASS		
302	rbbm_WU_2D_idle_06	2002Aug30
FAIL		
303	rbbm_WU_2D_idle_07	2002Aug30
FAIL		
304	rbbm_WU_2D_idle_08	2002Aug30
FAIL		
305	rbbm_WU_2D_idle_09	2002Aug30
FAIL		
306	rbbm_WU_2D_idle_clean_01	2002Aug30
FAIL		
307	rbbm_WU_2D_idle_clean_02	2002Aug30
FAIL		
308	rbbm_WU_2D_idle_clean_03	2002Aug30

FAIL		
309	rbbm_WU_2D_idle_clean_04	2002Aug30
FAIL		
0	rbbm_WU_2D_idle_clean_05	2002Aug30
PASS		
311	rbbm_WU_2D_idle_clean_06	2002Aug30
FAIL		
312	rbbm_WU_2D_idle_clean_07	2002Aug30
FAIL		
313	rbbm_WU_2D_idle_clean_08	2002Aug30
FAIL		
314	rbbm_WU_2D_idle_clean_09	2002Aug30
FAIL		
315	rbbm_WU_2D_idle_clean_10	2002Aug30
FAIL		
316	rbbm_WU_2D_idle_clean_11	2002Aug30
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Aug30
PASS		
318	rbbm_WU_3D_idle_01	2002Aug30
FAIL		
319	rbbm_WU_3D_idle_02	2002Aug30
FAIL		
320	rbbm_WU_3D_idle_03	2002Aug30
FAIL		
321	rbbm_WU_3D_idle_04	2002Aug30
FAIL		
0	rbbm_WU_3D_idle_05	2002Aug30
PASS		
323	rbbm_WU_3D_idle_06	2002Aug30
FAIL		
324	rbbm_WU_3D_idle_07	2002Aug30
FAIL		
325	rbbm_WU_3D_idle_08	2002Aug30
FAIL		
326	rbbm_WU_3D_idle_09	2002Aug30
FAIL		
327	rbbm_WU_3D_idle_clean_01	2002Aug30
FAIL		
328	rbbm_WU_3D_idle_clean_02	2002Aug30
FAIL		
329	rbbm_WU_3D_idle_clean_03	2002Aug30
FAIL		
330	rbbm_WU_3D_idle_clean_04	2002Aug30
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Aug30
PASS		
332	rbbm_WU_3D_idle_clean_06	2002Aug30

```

**FAIL**
333  rbbm_WU_3D_idle_clean_07                2002Aug30
**FAIL**
334  rbbm_WU_3D_idle_clean_08                2002Aug30
**FAIL**
335  rbbm_WU_3D_idle_clean_09                2002Aug30
**FAIL**
336  rbbm_WU_3D_idle_clean_10               2002Aug30
**FAIL**
337  rbbm_WU_3D_idle_clean_11               2002Aug30
**FAIL**
0    rbbm_WU_3D_idle_clean_12               2002Aug30
PASS
339  rbbm_WU_BIF_RBBM_agp_flush_off         2002Aug30
**FAIL**
340  rbbm_WU_BOTH_GRP_H_update_pending_off  2002Aug30
**FAIL**
0    rbbm_WU_cmdfifo_entries                2002Aug30
PASS
0    rbbm_WU_CP_dma_idle                    2002Aug30
PASS
343  rbbm_WU_CRTC_D1MODE_vline_fe          2002Aug30
**FAIL**
344  rbbm_WU_CRTC_D1MODE_vline_on          2002Aug30
**FAIL**
345  rbbm_WU_CRTC_D1MODE_vline_re          2002Aug30
**FAIL**
346  rbbm_WU_CRTC_D2MODE_vline_fe          2002Aug30
**FAIL**
347  rbbm_WU_CRTC_D2MODE_vline_on          2002Aug30
**FAIL**
348  rbbm_WU_CRTC_D2MODE_vline_re          2002Aug30
**FAIL**
349  rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off  2002Aug30
**FAIL**
350  rbbm_WU_CRTC_PFLIP_D2GRP_H_update_pending_off  2002Aug30
**FAIL**
351  rbbm_WU_idct_semaphore                 2002Aug30
**FAIL**
352  rbbm_WU_MH_hdp_clean                   2002Aug30
**FAIL**
353  rbbm_WU_OV_FLIP_D1OVL_update_pending_off  2002Aug30
**FAIL**
354  rbbm_WU_OV_FLIP_D2OVL_update_pending_off  2002Aug30
**FAIL**
355  rbbm_WU_pad_ext_sig_rising_edge        2002Aug30
**FAIL**
0    rbbm_WUrt_BIF_RBBM_agp_flush_off      2002Aug30

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PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Aug30
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Aug30
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Aug30
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Aug30
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Aug30
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Aug30
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_01	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_02	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_03	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_04	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Aug30
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Aug30
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Aug30

PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Aug30
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Aug30
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Aug30
PASS		
384	rbbm_WU_VIPH0_dma_idle	2002Aug30
FAIL		
385	rbbm_WU_VIPH1_dma_idle	2002Aug30
FAIL		
386	rbbm_WU_VIPH2_dma_idle	2002Aug30
FAIL		
387	rbbm_WU_VIPH3_dma_idle	2002Aug30
FAIL		
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PASS		
0	RB_BUFSZ_1	2002Aug30
PASS		
0	RB_Wrap	2002Aug30
PASS		
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PASS		
0	reg_to_mem_rt	2002Aug30
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PASS		
0	rtee_discretes_arm_rearm_events_32	2002Aug30
PASS		
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PASS		
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PASS		
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PASS		
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PASS		
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0	rtee_discretes_falling_31_events_16	2002Aug30

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PASS		
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0	rtee_discretes_rising_15_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug30
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug30

PASS		
0	rtee_discretes_rising_31_events_16	2002Aug30
PASS		
526	rtee_display1_comp_events_16	2002Aug30
FAIL	rbbmWr.cmp	
527	rtee_display2_comp_events_16	2002Aug30
FAIL	rbbmWr.cmp	
0	rtee_mem_linear_search_events_2	2002Aug30
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug30
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug30
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug30
PASS		
0	rtee_prefetch_parse_nops	2002Aug30
PASS		
0	rtee_reg_linear_search_events_2	2002Aug30
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug30

PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug30
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug30
PASS		
551	rtee_vip_comp_events_16	2002Aug30
FAIL	rbbmWr.cmp	
0	rt_invalid_opcode	2002Aug30
PASS		
0	scratch_mem_read	2002Aug30
PASS		
0	scratch_mem_read2	2002Aug30
PASS		
0	set_constant	2002Aug30
PASS		
0	set_constant2	2002Aug30
PASS		
0	set_constant_alu_max1	2002Aug30
PASS		
0	set_constant_alu_max2	2002Aug30
PASS		
0	set_constant_bool_max1	2002Aug30
PASS		
0	set_constant_bool_max2	2002Aug30
PASS		
0	set_constant_indr1_short	2002Aug30
PASS		
0	set_constant_indr1_short2	2002Aug30
PASS		
0	set_constant_indr1_write_enable	2002Aug30
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug30
PASS		
0	set_constant_indr2_short	2002Aug30
PASS		
0	set_constant_indr2_short2	2002Aug30
PASS		
0	set_constant_indr2_write_enable	2002Aug30
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug30
PASS		
0	set_constant_loop_max1	2002Aug30
PASS		
0	set_constant_loop_max2	2002Aug30
PASS		
0	set_constant_ring_short	2002Aug30
PASS		
0	set_constant_ring_short2	2002Aug30

PASS		
0	set_constant_ring_write_enable	2002Aug30
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug30
PASS		
0	set_constant_short	2002Aug30
PASS		
0	set_constant_short2	2002Aug30
PASS		
0	set_constant_tex_max1	2002Aug30
PASS		
0	set_constant_tex_max2	2002Aug30
PASS		
0	set_constant_write_enable	2002Aug30
PASS		
0	set_constant_write_enable_lcc	2002Aug30
PASS		
0	set_constant_write_enable_lcc2	2002Aug30
PASS		
0	set_scissors	2002Aug30
PASS		
0	set_state_rt_a	2002Aug30
PASS		
0	set_state_rt_b	2002Aug30
PASS		
0	set_state_rt_c	2002Aug30
PASS		
0	set_state_rt_d	2002Aug30
PASS		
0	set_st_indrl_match	2002Aug30
PASS		
0	set_st_indrl_mismatch	2002Aug30
PASS		
0	set_st_indrl_mismatch2	2002Aug30
PASS		
0	set_st_indrl_mismatch3	2002Aug30
PASS		
0	set_st_indrl_mismatch4	2002Aug30
PASS		
0	set_st_indrl_ps	2002Aug30
PASS		
0	set_st_indrl_ps_only	2002Aug30
PASS		
0	set_st_indrl_sublk0	2002Aug30
PASS		
0	set_st_indrl_sublk1	2002Aug30
PASS		
0	set_st_indrl_sublk1_only	2002Aug30

PASS		
0	set_st_indr1_sublk2	2002Aug30
PASS		
0	set_st_indr1_sublk2_only	2002Aug30
PASS		
0	set_st_indr1_sublk3	2002Aug30
PASS		
0	set_st_indr1_sublk3_only	2002Aug30
PASS		
0	set_st_indr1_sublk4	2002Aug30
PASS		
0	set_st_indr1_sublk4_only	2002Aug30
PASS		
0	set_st_indr1_sublk5	2002Aug30
PASS		
0	set_st_indr1_sublk5_only	2002Aug30
PASS		
0	set_st_indr1_sublk6	2002Aug30
PASS		
0	set_st_indr1_sublk6_only	2002Aug30
PASS		
0	set_st_indr1_sublk7	2002Aug30
PASS		
0	set_st_indr1_sublk7_only	2002Aug30
PASS		
0	set_st_indr1_vs	2002Aug30
PASS		
0	set_st_indr1_vs_only	2002Aug30
PASS		
0	set_st_indr2_match	2002Aug30
PASS		
0	set_st_indr2_mismatch	2002Aug30
PASS		
0	set_st_indr2_mismatch2	2002Aug30
PASS		
0	set_st_indr2_mismatch3	2002Aug30
PASS		
0	set_st_indr2_mismatch4	2002Aug30
PASS		
0	set_st_indr2_ps	2002Aug30
PASS		
0	set_st_indr2_ps_only	2002Aug30
PASS		
0	set_st_indr2_sublk0	2002Aug30
PASS		
0	set_st_indr2_sublk1	2002Aug30
PASS		
0	set_st_indr2_sublk1_only	2002Aug30

PASS		
0	set_st_indr2_sublk2	2002Aug30
PASS		
0	set_st_indr2_sublk2_only	2002Aug30
PASS		
0	set_st_indr2_sublk3	2002Aug30
PASS		
0	set_st_indr2_sublk3_only	2002Aug30
PASS		
0	set_st_indr2_sublk4	2002Aug30
PASS		
0	set_st_indr2_sublk4_only	2002Aug30
PASS		
0	set_st_indr2_sublk5	2002Aug30
PASS		
0	set_st_indr2_sublk5_only	2002Aug30
PASS		
0	set_st_indr2_sublk6	2002Aug30
PASS		
0	set_st_indr2_sublk6_only	2002Aug30
PASS		
0	set_st_indr2_sublk7	2002Aug30
PASS		
0	set_st_indr2_sublk7_only	2002Aug30
PASS		
0	set_st_indr2_vs	2002Aug30
PASS		
0	set_st_indr2_vs_only	2002Aug30
PASS		
0	set_st_pfmatch_disabled	2002Aug30
PASS		
0	set_st_ring_match	2002Aug30
PASS		
0	set_st_ring_mismatch	2002Aug30
PASS		
0	set_st_ring_mismatch2	2002Aug30
PASS		
0	set_st_ring_mismatch3	2002Aug30
PASS		
0	set_st_ring_mismatch4	2002Aug30
PASS		
0	set_st_ring_ps	2002Aug30
PASS		
0	set_st_ring_ps_only	2002Aug30
PASS		
0	set_st_ring_sublk0	2002Aug30
PASS		
0	set_st_ring_sublk1	2002Aug30

PASS		
0	set_st_ring_subblk1_only	2002Aug30
PASS		
0	set_st_ring_subblk2	2002Aug30
PASS		
0	set_st_ring_subblk2_only	2002Aug30
PASS		
0	set_st_ring_subblk3	2002Aug30
PASS		
0	set_st_ring_subblk3_only	2002Aug30
PASS		
0	set_st_ring_subblk4	2002Aug30
PASS		
0	set_st_ring_subblk4_only	2002Aug30
PASS		
0	set_st_ring_subblk5	2002Aug30
PASS		
0	set_st_ring_subblk5_only	2002Aug30
PASS		
0	set_st_ring_subblk6	2002Aug30
PASS		
0	set_st_ring_subblk6_only	2002Aug30
PASS		
0	set_st_ring_subblk7	2002Aug30
PASS		
0	set_st_ring_subblk7_only	2002Aug30
PASS		
0	set_st_ring_vs	2002Aug30
PASS		
0	set_st_ring_vs_only	2002Aug30
PASS		
0	set_st_stress_a	2002Aug30
PASS		
0	set_st_stress_b	2002Aug30
PASS		
0	set_st_stress_c	2002Aug30
PASS		
0	set_st_stress_d	2002Aug30
PASS		
0	set_st_stress_e	2002Aug30
PASS		
0	set_st_stress_f	2002Aug30
PASS		
0	Small_Text_BM	2002Aug30
PASS		
0	smoke_nrt	2002Aug30
PASS		
0	src_clip	2002Aug30

PASS		
0	srf_cohr	2002Aug30
PASS		
0	trans_bitblt	2002Aug30
PASS		
0	type0_2long_pckts	2002Aug30
PASS		
0	type0_3indr_buffs	2002Aug30
PASS		
0	type0_4pckts	2002Aug30
PASS		
0	type0_incr_updt_cnst	2002Aug30
PASS		
0	type0_incr_updt_instrmem	2002Aug30
PASS		
0	type0_incr_updt_st	2002Aug30
PASS		
0	type0_incr_updt_stress	2002Aug30
PASS		
0	type0_ind1_ind2_a	2002Aug30
PASS		
0	type0_ind1_ind2_b	2002Aug30
PASS		
0	type0_indr_buff	2002Aug30
PASS		
0	Type0_IQ2_BM	2002Aug30
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug30
PASS		
0	Type0_IQ_BM	2002Aug30
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug30
PASS		
0	Type0_PQ_BM	2002Aug30
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug30
PASS		
0	type1_4pckts	2002Aug30
PASS		
0	type1_indr_buff	2002Aug30
PASS		
0	type1_pckt	2002Aug30
PASS		
0	Type3_Bitblt	2002Aug30
PASS		
0	Type3_Load_Palette	2002Aug30
PASS		
0	Type3_NOP	2002Aug30

PASS		
693	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
694	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Aug30
PASS		
0	uram_wrt_ucode	2002Aug30
PASS		
0	viz_query_a	2002Aug30
PASS		
0	viz_query_draw_indx	2002Aug30
PASS		
0	viz_query_draw_indx_indr1	2002Aug30
PASS		
0	viz_query_draw_indx_indr2	2002Aug30
PASS		
0	viz_query_draw_indx_multipass	2002Aug30
PASS		
0	viz_query_draw_indx_ring	2002Aug30
PASS		
0	viz_query_indr1	2002Aug30
PASS		
0	viz_query_indr1_simple	2002Aug30
PASS		
0	viz_query_indr2	2002Aug30
PASS		
0	viz_query_indr2_simple	2002Aug30
PASS		
0	viz_query_ring	2002Aug30
PASS		
0	viz_query_ring_simple	2002Aug30
PASS		
0	wait_for_idle	2002Aug30
PASS		
0	wait_reg_mem_poll_mem0	2002Aug30
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Aug30
PASS		
0	wait_reg_mem_poll_mem1	2002Aug30
PASS		
0	wait_reg_mem_poll_mem2	2002Aug30
PASS		
0	wait_reg_mem_poll_mem3	2002Aug30
PASS		
0	wait_reg_mem_poll_reg0	2002Aug30
PASS		
0	wait_reg_mem_poll_reg1	2002Aug30

PASS

0 wait_reg_mem_poll_reg2

2002Aug30

PASS

0 wait_reg_mem_poll_reg3

2002Aug30

PASS

=====

Tests_Run_count = 718 Pass_count = 656 Fail_count = 62 Incomplete_count = 0

=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Oct  5 23:01:36 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          676        676        656        97
              RBBM        120        120        120        100
+++++
              TOTAL        796        796        776        97
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buf_d_sm_meq		2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
2	2d_indr_buf_g_sm_meq		2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	2d_indr_buffer_a		2002Oct12
PASS			
0	2d_indr_buffer_b		2002Oct12
PASS			
0	2d_indr_buffer_c		2002Oct12
PASS			
0	2d_indr_buffer_d		2002Oct12
PASS			
0	2d_indr_buffer_e		2002Oct12
PASS			
0	2d_indr_buffer_f		2002Oct12
PASS			
0	2d_indr_buffer_f_sm_roq		2002Oct12
PASS			
0	2d_indr_buffer_f_sm_roq2		2002Oct12
PASS			
0	2d_indr_buffer_g		2002Oct12
PASS			
12	2d_indr_buffer_random		2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
13	2d_indr_buffer_random2		2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	3d_draw_indx_2		2002Oct12
PASS			
0	3d_draw_indx_2_b		2002Oct12
PASS			
0	IQ2_Mult		2002Oct12
PASS			
0	IQ2_Wrap_Legacy		2002Oct12
PASS			
0	IQ_Wrap		2002Oct12
PASS			
19	Isync_Control		2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp		
20	Isync_Control_2D_3D		2002Oct12

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
21  Isync_Control_3D_2D                2002Oct12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   RB_BUFSZ_0                          2002Oct12
PASS
0   RB_BUFSZ_1                          2002Oct12
PASS
0   RB_Wrap                             2002Oct12
PASS
0   Small_Text_BM                       2002Oct12
PASS
0   Type0_IQ2_BM                        2002Oct12
PASS
0   Type0_IQ2_BM_IB_Packet              2002Oct12
PASS
0   Type0_IQ_BM                         2002Oct12
PASS
0   Type0_IQ_BM_IB_Packet               2002Oct12
PASS
0   Type0_PQ_BM                         2002Oct12
PASS
0   Type0_PQ_BM_Pre_Write_Timer         2002Oct12
PASS
0   Type3_Bitblt                        2002Oct12
PASS
0   Type3_Hostdata_blt                  2002Oct12
PASS
0   Type3_Hostdata_blt_PIO              2002Oct12
PASS
0   Type3_Load_Palette                   2002Oct12
PASS
0   Type3_NOP                            2002Oct12
PASS
0   Type3_Polyline                       2002Oct12
PASS
0   Type3_Polyline_Large                 2002Oct12
PASS
0   bitblt_multi                         2002Oct12
PASS
0   brush_32x1mono                       2002Oct12
PASS
0   brush_8x8color_16                    2002Oct12
PASS
0   brush_8x8color_32                    2002Oct12
PASS
0   brush_8x8color_64                    2002Oct12
PASS
0   brush_8x8color_dst_0_1              2002Oct12

```


PASS		
0	brush_8x8mono	2002Oct12
PASS		
0	brush_color_mono_stress	2002Oct12
PASS		
0	brush_flag	2002Oct12
PASS		
0	brush_type0	2002Oct12
PASS		
0	brush_type1	2002Oct12
PASS		
0	brush_type10	2002Oct12
PASS		
0	brush_type13	2002Oct12
PASS		
0	brush_type14	2002Oct12
PASS		
0	brush_type6	2002Oct12
PASS		
0	brush_type7	2002Oct12
PASS		
0	brush_type_other	2002Oct12
PASS		
0	brush_types_13_14	2002Oct12
PASS		
57	change_queue_sizes	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	clear_const_wr_en_on_me_init	2002Oct12
PASS		
0	clear_const_wr_en_with_lcc	2002Oct12
PASS		
0	cond_write_mem_poll_mem	2002Oct12
PASS		
0	cond_write_mem_poll_mem2	2002Oct12
PASS		
0	cond_write_mem_poll_mem3	2002Oct12
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct12
PASS		
0	cond_write_mem_poll_reg	2002Oct12
PASS		
0	cond_write_mem_poll_reg2	2002Oct12
PASS		
0	cond_write_mem_poll_reg3	2002Oct12
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct12
PASS		
0	cp_bios_scratch	2002Oct12

PASS		
0	cp_high_stress	2002Oct12
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base010_bufszXXX_sm_roq	2002Oct12
PASS		
0	cp_ib2_base011_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct12
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct12
PASS		
0	cp_ib_base000_bufszXXX	2002Oct12
PASS		
0	cp_ib_base001_bufszXXX	2002Oct12
PASS		
0	cp_ib_base010_bufszXXX	2002Oct12
PASS		
0	cp_ib_base011_bufszXXX	2002Oct12
PASS		
0	cp_ib_base100_bufszXXX	2002Oct12
PASS		
0	cp_ib_base101_bufszXXX	2002Oct12
PASS		
0	cp_ib_base101_bufszXXX_sm_roq	2002Oct12
PASS		
0	cp_ib_base110_bufszXXX	2002Oct12
PASS		
0	cp_ib_base111_bufszXXX	2002Oct12
PASS		
0	cp_interrupt	2002Oct12
PASS		
0	cp_interrupt_rt_events_16	2002Oct12
PASS		
0	cp_iq2_alignment_000	2002Oct12
PASS		
0	cp_iq2_alignment_001	2002Oct12
PASS		
0	cp_iq2_alignment_010	2002Oct12

PASS		
0	cp_iq2_alignment_011	2002Oct12
PASS		
0	cp_iq2_alignment_100	2002Oct12
PASS		
0	cp_iq2_alignment_101	2002Oct12
PASS		
0	cp_iq2_alignment_110	2002Oct12
PASS		
0	cp_iq2_alignment_111	2002Oct12
PASS		
0	cp_iq_alignment_000	2002Oct12
PASS		
0	cp_iq_alignment_001	2002Oct12
PASS		
0	cp_iq_alignment_010	2002Oct12
PASS		
0	cp_iq_alignment_011	2002Oct12
PASS		
0	cp_iq_alignment_100	2002Oct12
PASS		
0	cp_iq_alignment_101	2002Oct12
PASS		
0	cp_iq_alignment_110	2002Oct12
PASS		
0	cp_iq_alignment_111	2002Oct12
PASS		
0	cp_max_size_packet	2002Oct12
PASS		
0	cp_max_size_packet_random	2002Oct12
PASS		
0	cp_nrt_rt_protection_mode	2002Oct12
PASS		
0	cp_output_stress	2002Oct12
PASS		
0	cp_output_stress_random	2002Oct12
PASS		
0	cp_protection_mode	2002Oct12
PASS		
0	cp_rb_alignment_000	2002Oct12
PASS		
0	cp_rb_alignment_001	2002Oct12
PASS		
0	cp_rb_alignment_010	2002Oct12
PASS		
0	cp_rb_alignment_011	2002Oct12
PASS		
0	cp_rb_alignment_100	2002Oct12

PASS		
0	cp_rb_alignment_101	2002Oct12
PASS		
0	cp_rb_alignment_110	2002Oct12
PASS		
0	cp_rb_alignment_111	2002Oct12
PASS		
0	cp_rb_wptrXXX_base000	2002Oct12
PASS		
0	cp_rb_wptrXXX_base001	2002Oct12
PASS		
0	cp_rb_wptrXXX_base010	2002Oct12
PASS		
0	cp_rb_wptrXXX_base011	2002Oct12
PASS		
0	cp_rb_wptrXXX_base100	2002Oct12
PASS		
0	cp_rb_wptrXXX_base101	2002Oct12
PASS		
0	cp_rb_wptrXXX_base101_sm_roq	2002Oct12
PASS		
0	cp_rb_wptrXXX_base110	2002Oct12
PASS		
0	cp_rb_wptrXXX_base111	2002Oct12
PASS		
0	cp_reg_rw_test	2002Oct12
PASS		
0	cp_register_reads	2002Oct12
PASS		
0	cp_rt_protection_mode	2002Oct12
PASS		
0	cp_scratch	2002Oct12
PASS		
0	cp_scratch_multi	2002Oct12
PASS		
0	csf_reg_rd_default	2002Oct12
PASS		
135	csf_reg_rd_only	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	csf_reg_wr_rd	2002Oct12
PASS		
0	dma_cp_sync	2002Oct12
PASS		
0	dma_dscrstr_chckn_bit	2002Oct12
PASS		
0	dma_dscrstr_flush1	2002Oct12
PASS		
0	dma_dscrstr_flush2	2002Oct12

PASS		
0	dma_dscrstr_flush3	2002Oct12
PASS		
0	dma_m2m_src00_dstXX	2002Oct12
PASS		
0	dma_m2m_src01_dstXX	2002Oct12
PASS		
0	dma_m2m_src10_dstXX	2002Oct12
PASS		
0	dma_m2m_src11_dstXX	2002Oct12
PASS		
0	dma_mem2mem	2002Oct12
PASS		
0	dma_mem2mem_abort	2002Oct12
PASS		
0	dma_mem2mem_alignments	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment1	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment2	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment3	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment4	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct12
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct12
PASS		
0	dma_mem2mem_large	2002Oct12
PASS		
0	dma_mem2mem_pio_abort	2002Oct12
PASS		
0	dma_mem2mem_random	2002Oct12
PASS		
0	dma_mem2reg	2002Oct12
PASS		
0	dma_mem2reg_abort	2002Oct12
PASS		
0	dma_mem2reg_dtaq_stress	2002Oct12
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Oct12
PASS		
0	dma_mem2reg_pio_stressbm	2002Oct12

PASS		
0	dma_mem2reg_pio_stresspio	2002Oct12
PASS		
0	dma_mem2reg_sync	2002Oct12
PASS		
0	dma_r2r_m2m_r2r	2002Oct12
PASS		
0	dma_r2r_m2r_r2r	2002Oct12
PASS		
0	dma_r2r_r2m_r2r	2002Oct12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct12
PASS		
0	dma_reg2mem	2002Oct12
PASS		
0	dma_reg2mem_abort	2002Oct12
PASS		
0	dma_reg2reg	2002Oct12
PASS		
0	dma_reg2reg_2dw	2002Oct12
PASS		
0	dma_reg2reg_daic	2002Oct12
PASS		
0	dma_reg2reg_mult	2002Oct12
PASS		
0	dma_reg2reg_mult2	2002Oct12
PASS		
0	dma_reg2reg_mult3	2002Oct12
PASS		
0	dma_reg2reg_saic	2002Oct12
PASS		
0	dma_reg2reg_sdaic	2002Oct12
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct12
PASS		
0	dma_reg2reg_stress	2002Oct12
PASS		
0	draw_indx_a	2002Oct12
PASS		
0	draw_indx_b	2002Oct12
PASS		
0	draw_indx_c	2002Oct12
PASS		
0	draw_indx_indr1_a	2002Oct12

PASS		
0	draw_indx_indr1_b	2002Oct12
PASS		
0	draw_indx_indr1_short_a	2002Oct12
PASS		
0	draw_indx_indr1_short_b	2002Oct12
PASS		
0	draw_indx_indr1_stress	2002Oct12
PASS		
0	draw_indx_indr2_a	2002Oct12
PASS		
0	draw_indx_indr2_b	2002Oct12
PASS		
0	draw_indx_indr2_short_a	2002Oct12
PASS		
0	draw_indx_indr2_short_b	2002Oct12
PASS		
0	draw_indx_indr2_stress	2002Oct12
PASS		
198	draw_indx_random_a	2002Oct12
FAIL	mh_cp_rd.cmp	
199	draw_indx_random_b	2002Oct12
FAIL	mh_cp_rd.cmp	
200	draw_indx_random_c	2002Oct12
FAIL	mh_cp_rd.cmp	
0	draw_indx_ring1	2002Oct12
PASS		
0	draw_indx_ring2	2002Oct12
PASS		
0	draw_indx_ring_short1	2002Oct12
PASS		
0	draw_indx_ring_short2	2002Oct12
PASS		
0	draw_indx_ring_stress	2002Oct12
PASS		
0	draw_indx_short_a	2002Oct12
PASS		
0	draw_indx_short_b	2002Oct12
PASS		
0	draw_indx_short_c	2002Oct12
PASS		
0	draw_indx_stress_a	2002Oct12
PASS		
0	draw_indx_stress_b	2002Oct12
PASS		
211	draw_indx_stress_b_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
0	draw_indx_stress_b_sm_roq	2002Oct12

PASS		
0	draw_indx_stress_b_sm_roq2	2002Oct12
PASS		
0	draw_indx_stress_c	2002Oct12
PASS		
215	draw_indx_stress_c_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
0	event_timestamp_write	2002Oct12
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct12
PASS		
0	fix2flt_reg	2002Oct12
PASS		
0	ft_indr1_july22	2002Oct12
PASS		
0	ft_indr2_july22	2002Oct12
PASS		
0	ft_july22	2002Oct12
PASS		
0	ft_july22_random	2002Oct12
PASS		
0	header_dump	2002Oct12
PASS		
0	header_dump_rt	2002Oct12
PASS		
0	header_dump_small	2002Oct12
PASS		
0	hostdata_blt	2002Oct12
PASS		
227	im_ld_cid_mismatch_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
228	im_ld_invalidate_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
229	im_ld_match2_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
230	im_ld_match_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
231	im_ld_sz_mismatch_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
0	im_load_a	2002Oct12
PASS		
0	im_load_b	2002Oct12
PASS		
0	im_load_cid_mismatch	2002Oct12
PASS		
0	im_load_immediate	2002Oct12
PASS		
0	im_load_immediate_nrt_me	2002Oct12

PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct12
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct12
PASS		
239	im_load_immediate_nrt_me_ps_wrap	2002Oct12
FAIL	mh_cp_rd.cmp	
240	im_load_immediate_nrt_me_vs_nowrap	2002Oct12
FAIL	mh_cp_rd.cmp	
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct12
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct12
PASS		
243	im_load_immediate_nrt_me_vs_wrap	2002Oct12
FAIL	mh_cp_rd.cmp	
0	im_load_indr1_a	2002Oct12
PASS		
0	im_load_indr1_b	2002Oct12
PASS		
0	im_load_indr2_a	2002Oct12
PASS		
0	im_load_indr2_b	2002Oct12
PASS		
0	im_load_invalidate	2002Oct12
PASS		
249	im_load_invalidate_sm_roq	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	im_load_match	2002Oct12
PASS		
0	im_load_match2	2002Oct12
PASS		
0	im_load_nrt_me	2002Oct12
PASS		
253	im_load_nrt_me_ps_nowrap	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
254	im_load_nrt_me_ps_wrap	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
255	im_load_nrt_me_vs_nowrap	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
256	im_load_nrt_me_vs_wrap	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_pfmatch_disable	2002Oct12
PASS		
258	im_load_random_a	2002Oct12
FAIL	mh_cp_rd.cmp	
259	im_load_random_b	2002Oct12
FAIL	mh_cp_rd.cmp	
260	im_load_random_c	2002Oct12

```

**FAIL**      mh_cp_rd.cmp
0  im_load_ring_a      20020ct12
PASS
0  im_load_ring_b      20020ct12
PASS
0  im_load_rt          20020ct12
PASS
0  im_load_rt2         20020ct12
PASS
0  im_load_rt3         20020ct12
PASS
0  im_load_rt3_random  20020ct12
PASS
0  im_load_rt4         20020ct12
PASS
0  im_load_rt4_random  20020ct12
PASS
0  im_load_sz_mismatch 20020ct12
PASS
270 incr_update_cnst_random 20020ct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
271 incr_update_const_sm_meq 20020ct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
272 incr_update_instr_sm_meq 20020ct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
273 incr_update_state_sm_meq 20020ct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_const 20020ct12
PASS
0  incremental_update_instr 20020ct12
PASS
0  incremental_update_state 20020ct12
PASS
0  incremental_update_state_sm_roq 20020ct12
PASS
0  indirect_buffer_a      20020ct12
PASS
0  indirect_buffer_b      20020ct12
PASS
0  indirect_buffer_c      20020ct12
PASS
0  indirect_buffer_d      20020ct12
PASS
282 indirect_buffer_e      20020ct12
**FAIL**      rbbmWr.cmp
0  indirect_buffer_f      20020ct12
PASS
0  indirect_buffer_f_sm_roq 20020ct12

```

PASS		
0	indirect_buffer_g	2002Oct12
PASS		
0	indirect_buffer_h	2002Oct12
PASS		
0	indirect_buffer_i	2002Oct12
PASS		
0	indirect_buffer_j	2002Oct12
PASS		
0	indirect_buffer_k	2002Oct12
PASS		
0	indirect_buffer_l	2002Oct12
PASS		
0	indirect_buffer_mp_a	2002Oct12
PASS		
0	indirect_buffer_mp_b	2002Oct12
PASS		
0	indirect_buffer_mp_c	2002Oct12
PASS		
0	indirect_buffer_mp_d	2002Oct12
PASS		
0	indirect_buffer_mp_e	2002Oct12
PASS		
0	indirect_buffer_mp_f	2002Oct12
PASS		
0	indirect_buffer_mp_f_sm_roq	2002Oct12
PASS		
298	indirect_buffer_mp_random1	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
299	indirect_buffer_mp_random2	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
300	indirect_buffer_random1	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
301	indirect_buffer_random2	2002Oct12
FAIL	mh_cp_rd.cmp	
302	indr_buf_e_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
303	indr_buf_k_sm_meq	2002Oct12
FAIL	mh_cp_rd.cmp	
304	indr_buf_mp_c_sm_meq	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
305	indr_buf_mp_f_sm_meq	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalid_opcode	2002Oct12
PASS		
0	invalidate_on_me_init	2002Oct12
PASS		
308	invalidate_on_me_init_random	2002Oct12

```

**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
309 invalidate_on_me_init_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   invalidate_state_constants      2002Oct12
PASS
0   invalidate_state_shaders      2002Oct12
PASS
0   invalidate_state_shaders2      2002Oct12
PASS
313 invalidate_state_shaders_random      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
314 invalidate_state_shaders_sm_meq      2002Oct12
**FAIL**      mh_cp_rd.cmp
0   invalidate_state_sublks      2002Oct12
PASS
0   invalidate_state_sublks2      2002Oct12
PASS
317 invalidate_state_sublks2_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
318 invalidate_state_sublks_random      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   invalidate_state_sublks_sm_roq      2002Oct12
PASS
0   iq1_iq2_mult      2002Oct12
PASS
0   iq1_iq2_push_wrap      2002Oct12
PASS
0   iq2_csq_full      2002Oct12
PASS
323 lcc_coherency_cntrl_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
324 lcc_coherency_cntr2_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
325 lcc_coherency_cntr_random      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
326 lcc_match_random      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
327 lcc_match_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
328 lcc_match_valid_preserved_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
329 lcc_mismatch_alutex_random      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
330 lcc_mismatch_alutex_sm_meq      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
331 lcc_mismatch_base_random      2002Oct12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
332 lcc_mismatch_base_sm_meq      2002Oct12

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**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0   lcc_rt1                2002Oct12
PASS
0   lcc_rt2                2002Oct12
PASS
0   lcc_rt3                2002Oct12
PASS
0   lcc_rt3_random        2002Oct12
PASS
0   lcc_rt4                2002Oct12
PASS
0   lcc_rt4_random        2002Oct12
PASS
0   ld_cnst_cntx_coherency_cntrl 2002Oct12
PASS
0   ld_cnst_cntx_coherency_cntrl_sm_roq 2002Oct12
PASS
0   ld_cnst_cntx_coherency_cntrl2 2002Oct12
PASS
0   ld_cnst_cntx_coherency_cntrl2_sm_roq 2002Oct12
PASS
0   ld_cnst_cntx_indr1_replicate 2002Oct12
PASS
0   ld_cnst_cntx_indr1_replicate4 2002Oct12
PASS
0   ld_cnst_cntx_indr1_replicate8 2002Oct12
PASS
0   ld_cnst_cntx_indr1_simple 2002Oct12
PASS
0   ld_cnst_cntx_indr2_replicate 2002Oct12
PASS
0   ld_cnst_cntx_indr2_replicate4 2002Oct12
PASS
0   ld_cnst_cntx_indr2_replicate8 2002Oct12
PASS
0   ld_cnst_cntx_indr2_simple 2002Oct12
PASS
0   ld_cnst_cntx_match    2002Oct12
PASS
0   ld_cnst_cntx_match_valid_preserved 2002Oct12
PASS
0   ld_cnst_cntx_mismatch_alutex 2002Oct12
PASS
0   ld_cnst_cntx_mismatch_base 2002Oct12
PASS
0   ld_cnst_cntx_mismatch_force 2002Oct12
PASS
0   ld_cnst_cntx_pfmatch_disable 2002Oct12

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PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct12
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct12
PASS		
0	ld_cnst_cntx_replicate8	2002Oct12
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct12
PASS		
0	ld_cnst_cntx_replicate_tex	2002Oct12
PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct12
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct12
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct12
PASS		
0	ld_cnst_cntx_ring_simple	2002Oct12
PASS		
0	ld_cnst_cntx_simple_alu	2002Oct12
PASS		
0	ld_cnst_cntx_simple_tex	2002Oct12
PASS		
0	load_constant_context_rt	2002Oct12
PASS		
0	load_palette	2002Oct12
PASS		
0	lp_2d3d	2002Oct12
PASS		
371	me_reg_wr_rd	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	2002Oct12
PASS		
0	mem_write_rt	2002Oct12
PASS		
0	mpeg_index	2002Oct12
PASS		
0	mpeg_index_multi_context_max_context_0	2002Oct12
PASS		
0	mpeg_index_multi_context_max_context_1	2002Oct12
PASS		
0	mpeg_index_multi_context_max_context_2	2002Oct12
PASS		
0	mpeg_index_multi_context_max_context_3	2002Oct12
PASS		
0	mpeg_index_multi_context_max_context_4	2002Oct12
PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct12

PASS		
0	mpeg_index_multi_context_max_context_6	20020ct12
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	20020ct12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	20020ct12
PASS		
0	nop_eating_disabled	20020ct12
PASS		
0	nrt_rt_interaction1	20020ct12
PASS		
0	nrt_rt_interaction1_random	20020ct12
PASS		
0	nrt_rt_interaction2	20020ct12
PASS		
0	nrt_rt_interaction3	20020ct12
PASS		
0	paint_multi	20020ct12
PASS		
0	paint_packet	20020ct12
PASS		
0	paint_packet_gui_control	20020ct12
PASS		
398	pfpr_reg_reads1	20020ct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
399	pfpr_reg_reads2	20020ct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	pitch_clip	20020ct12
PASS		
0	ply_nextscan	20020ct12
PASS		
0	rbbm_NQWU_BIF_01	20020ct12
PASS		
0	rbbm_NQWU_BIF_02	20020ct12
PASS		
0	rbbm_NQWU_CF_01	20020ct12

PASS		
0	rbbm_NQWU_CPnq_01	2002Oct12
PASS		
0	rbbm_WU_2D_idle_01	2002Oct12
PASS		
0	rbbm_WU_2D_idle_02	2002Oct12
PASS		
0	rbbm_WU_2D_idle_03	2002Oct12
PASS		
0	rbbm_WU_2D_idle_04	2002Oct12
PASS		
0	rbbm_WU_2D_idle_05	2002Oct12
PASS		
0	rbbm_WU_2D_idle_06	2002Oct12
PASS		
0	rbbm_WU_2D_idle_07	2002Oct12
PASS		
0	rbbm_WU_2D_idle_08	2002Oct12
PASS		
0	rbbm_WU_2D_idle_09	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct12
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Oct12
PASS		
0	rbbm_WU_3D_idle_01	2002Oct12
PASS		
0	rbbm_WU_3D_idle_02	2002Oct12

PASS		
0	rbbm_WU_3D_idle_03	2002Oct12
PASS		
0	rbbm_WU_3D_idle_04	2002Oct12
PASS		
0	rbbm_WU_3D_idle_05	2002Oct12
PASS		
0	rbbm_WU_3D_idle_06	2002Oct12
PASS		
0	rbbm_WU_3D_idle_07	2002Oct12
PASS		
0	rbbm_WU_3D_idle_08	2002Oct12
PASS		
0	rbbm_WU_3D_idle_09	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct12
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct12
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct12
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Oct12
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct12

PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct12
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct12
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct12
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct12
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct12
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct12
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct12
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct12
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct12
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct12
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct12
PASS		
0	rbbm_WU_idct_semaphore	2002Oct12
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct12
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct12
PASS		
0	rbbm_WUrt_BOTH_GRPH_update_pending_off	2002Oct12
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct12

PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct12
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct12
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct12
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct12
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_01	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_02	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_04	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Oct12
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Oct12
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Oct12
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Oct12
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Oct12
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Oct12
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Oct12
PASS		
0	rbbm_clk_active_deassert_min_max	2002Oct12
PASS		
0	rbbm_clk_active_each_pipe	2002Oct12
PASS		
0	rbbm_clk_active_for_read_error	2002Oct12
PASS		
0	rbbm_clk_active_for_read_request	2002Oct12

```

PASS
0   rbbm_clk_active_on_reset                2002Oct12
PASS
0   rbbm_cp_non_queued_rdwr_01              2002Oct12
PASS
0   rbbm_cp_non_queued_rdwr_02              2002Oct12
PASS
504 rbbm_cp_queued_holds_nonqueued          2002Oct12
**FAIL**      cp_rbbm.cmp
0   rbbm_cp_queued_rdwr                     2002Oct12
PASS
506 rbbm_hi_data_and_be_swap                 2002Oct12
**FAIL**      rbbmWr.cmp
0   rbbm_hi_data_endian_swap                2002Oct12
PASS
0   rbbm_hi_data_no_swap_for_cppush         2002Oct12
PASS
0   rbbm_hi_data_swap_for_cppush           2002Oct12
PASS
0   rbbm_int_ack                            2002Oct12
PASS
0   rbbm_int_each_client                    2002Oct12
PASS
0   rbbm_int_nrt_gfxpipe_idle              2002Oct12
PASS
0   rbbm_int_nrt_gfxpipe_idle_rderr        2002Oct12
PASS
0   rbbm_int_rderr                          2002Oct12
PASS
0   rbbm_int_st_restore_nrt_gfxpipe_idle_rderr 2002Oct12
PASS
0   rbbm_isync_cpscratch_idlegui           2002Oct12
PASS
0   rbbm_isync_wait_idlegui                2002Oct12
PASS
0   rbbm_soft_reset                         2002Oct12
PASS
0   rbbm_vgt_default_req                    2002Oct12
PASS
0   rbbm_vgt_max_req                        2002Oct12
PASS
0   rbbm_vgt_min_req                        2002Oct12
PASS
0   reg_to_mem                              2002Oct12
PASS
0   reg_to_mem_rt                           2002Oct12
PASS
0   ring_il_pio_i2_bm                       2002Oct12

```

PASS		
525	ring_pio_ibs_bm_stress	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rop_cfd_wmd	2002Oct12
PASS		
527	roq_reg_reads	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rstr_ordr	2002Oct12
PASS		
0	rt_invalid_opcode	2002Oct12
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct12
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct12
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct12

PASS		
0	rtee_discretes_falling_17_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct12
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct12

PASS		
0	rtee_discretes_invert_09_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_11_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct12
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct12
PASS		
0	rtee_discretes_level_00_events_16	2002Oct12

PASS		
0	rtee_discretes_level_01_events_16	2002Oct12
PASS		
0	rtee_discretes_level_02_events_16	2002Oct12
PASS		
0	rtee_discretes_level_03_events_16	2002Oct12
PASS		
0	rtee_discretes_level_04_events_16	2002Oct12
PASS		
0	rtee_discretes_level_05_events_16	2002Oct12
PASS		
0	rtee_discretes_level_06_events_16	2002Oct12
PASS		
0	rtee_discretes_level_07_events_16	2002Oct12
PASS		
0	rtee_discretes_level_08_events_16	2002Oct12
PASS		
0	rtee_discretes_level_09_events_16	2002Oct12
PASS		
0	rtee_discretes_level_10_events_16	2002Oct12
PASS		
0	rtee_discretes_level_11_events_16	2002Oct12
PASS		
0	rtee_discretes_level_12_events_16	2002Oct12
PASS		
0	rtee_discretes_level_13_events_16	2002Oct12
PASS		
0	rtee_discretes_level_14_events_16	2002Oct12
PASS		
0	rtee_discretes_level_15_events_16	2002Oct12
PASS		
0	rtee_discretes_level_16_events_16	2002Oct12
PASS		
0	rtee_discretes_level_17_events_16	2002Oct12
PASS		
0	rtee_discretes_level_18_events_16	2002Oct12
PASS		
0	rtee_discretes_level_19_events_16	2002Oct12
PASS		
0	rtee_discretes_level_20_events_16	2002Oct12
PASS		
0	rtee_discretes_level_21_events_16	2002Oct12
PASS		
0	rtee_discretes_level_22_events_16	2002Oct12
PASS		
0	rtee_discretes_level_23_events_16	2002Oct12
PASS		
0	rtee_discretes_level_24_events_16	2002Oct12

PASS		
0	rtee_discretes_level_25_events_16	2002Oct12
PASS		
0	rtee_discretes_level_26_events_16	2002Oct12
PASS		
0	rtee_discretes_level_27_events_16	2002Oct12
PASS		
0	rtee_discretes_level_28_events_16	2002Oct12
PASS		
0	rtee_discretes_level_29_events_16	2002Oct12
PASS		
0	rtee_discretes_level_30_events_16	2002Oct12
PASS		
0	rtee_discretes_level_31_events_16	2002Oct12
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct12

PASS		
0	rtee_discretes_rising_16_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_23_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct12
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct12
PASS		
0	rtee_display1_comp_events_16	2002Oct12
PASS		
0	rtee_display2_comp_events_16	2002Oct12
PASS		
0	rtee_mem_linear_search_events_2	2002Oct12
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct12

PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct12
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct12
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct12
PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct12
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct12
PASS		
0	rtee_pio	2002Oct12
PASS		
0	rtee_prefetch_parse_nops	2002Oct12
PASS		
0	rtee_reg_linear_search_events_2	2002Oct12
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Oct12
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Oct12
PASS		
0	rtee_vip_comp_events_16	2002Oct12
PASS		
0	scratch_mem_read	2002Oct12
PASS		
0	scratch_mem_read2	2002Oct12
PASS		
692	set_cnst2_sm_meq	2002Oct12

```

**FAIL**      mh_cp_rd.cmp
693  set_cnst_alu_max1_sm_meq                2002Oct12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
694  set_cnst_alu_max_random                2002Oct12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0    set_cnst_bool_max2_sm_meq            2002Oct12
PASS
696  set_cnst_loop_max2_sm_meq            2002Oct12
**FAIL**      mh_cp_rd.cmp
697  set_cnst_sm_meq                      2002Oct12
**FAIL**      mh_cp_rd.cmp
698  set_cnst_tex_max2_sm_meq            2002Oct12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
699  set_cnst_we_lcc_random                2002Oct12
**FAIL**      mh_cp_rd.cmp
700  set_cnst_we_random                    2002Oct12
**FAIL**      mh_cp_rd.cmp
701  set_cnst_write_enable_lcc2_sm_meq    2002Oct12
**FAIL**      mh_cp_rd.cmp
702  set_cnst_write_enable_sm_meq        2002Oct12
**FAIL**      mh_cp_rd.cmp
0    set_constant                          2002Oct12
PASS
0    set_constant2                        2002Oct12
PASS
705  set_constant_alu_max1                2002Oct12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
706  set_constant_alu_max2                2002Oct12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0    set_constant_bool_max1                2002Oct12
PASS
0    set_constant_bool_max2                2002Oct12
PASS
0    set_constant_indr1_short              2002Oct12
PASS
0    set_constant_indr1_short2            2002Oct12
PASS
0    set_constant_indr1_write_enable       2002Oct12
PASS
0    set_constant_indr1_write_enable_lcc   2002Oct12
PASS
0    set_constant_indr2_short              2002Oct12
PASS
0    set_constant_indr2_short2            2002Oct12
PASS
0    set_constant_indr2_write_enable       2002Oct12
PASS
0    set_constant_indr2_write_enable_lcc   2002Oct12

```

PASS		
0	set_constant_loop_max1	2002Oct12
PASS		
0	set_constant_loop_max2	2002Oct12
PASS		
0	set_constant_ring_short	2002Oct12
PASS		
0	set_constant_ring_short2	2002Oct12
PASS		
0	set_constant_ring_write_enable	2002Oct12
PASS		
0	set_constant_ring_write_enable_lcc	2002Oct12
PASS		
0	set_constant_short	2002Oct12
PASS		
0	set_constant_short2	2002Oct12
PASS		
725	set_constant_tex_max1	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
726	set_constant_tex_max2	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
727	set_constant_tex_max2_sm_roq	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	set_constant_write_enable	2002Oct12
PASS		
0	set_constant_write_enable_lcc	2002Oct12
PASS		
0	set_constant_write_enable_lcc2	2002Oct12
PASS		
0	set_constant_write_enable_lcc2_sm_roq	2002Oct12
PASS		
0	set_scissors	2002Oct12
PASS		
0	set_st_indrl_match	2002Oct12
PASS		
0	set_st_indrl_mismatch	2002Oct12
PASS		
0	set_st_indrl_mismatch2	2002Oct12
PASS		
0	set_st_indrl_mismatch3	2002Oct12
PASS		
0	set_st_indrl_mismatch4	2002Oct12
PASS		
0	set_st_indrl_ps	2002Oct12
PASS		
0	set_st_indrl_ps_only	2002Oct12
PASS		
0	set_st_indrl_sublk0	2002Oct12

PASS		
0	set_st_indr1_sublk1	2002Oct12
PASS		
0	set_st_indr1_sublk1_only	2002Oct12
PASS		
0	set_st_indr1_sublk2	2002Oct12
PASS		
0	set_st_indr1_sublk2_only	2002Oct12
PASS		
0	set_st_indr1_sublk3	2002Oct12
PASS		
0	set_st_indr1_sublk3_only	2002Oct12
PASS		
0	set_st_indr1_sublk4	2002Oct12
PASS		
0	set_st_indr1_sublk4_only	2002Oct12
PASS		
0	set_st_indr1_sublk5	2002Oct12
PASS		
0	set_st_indr1_sublk5_only	2002Oct12
PASS		
0	set_st_indr1_sublk6	2002Oct12
PASS		
0	set_st_indr1_sublk6_only	2002Oct12
PASS		
0	set_st_indr1_sublk7	2002Oct12
PASS		
0	set_st_indr1_sublk7_only	2002Oct12
PASS		
0	set_st_indr1_vs	2002Oct12
PASS		
0	set_st_indr1_vs_only	2002Oct12
PASS		
0	set_st_indr2_match	2002Oct12
PASS		
0	set_st_indr2_mismatch	2002Oct12
PASS		
0	set_st_indr2_mismatch2	2002Oct12
PASS		
0	set_st_indr2_mismatch3	2002Oct12
PASS		
0	set_st_indr2_mismatch4	2002Oct12
PASS		
0	set_st_indr2_ps	2002Oct12
PASS		
0	set_st_indr2_ps_only	2002Oct12
PASS		
0	set_st_indr2_sublk0	2002Oct12

PASS		
0	set_st_indr2_sublk1	2002Oct12
PASS		
0	set_st_indr2_sublk1_only	2002Oct12
PASS		
0	set_st_indr2_sublk2	2002Oct12
PASS		
0	set_st_indr2_sublk2_only	2002Oct12
PASS		
0	set_st_indr2_sublk3	2002Oct12
PASS		
0	set_st_indr2_sublk3_only	2002Oct12
PASS		
0	set_st_indr2_sublk4	2002Oct12
PASS		
0	set_st_indr2_sublk4_only	2002Oct12
PASS		
0	set_st_indr2_sublk5	2002Oct12
PASS		
0	set_st_indr2_sublk5_only	2002Oct12
PASS		
0	set_st_indr2_sublk6	2002Oct12
PASS		
0	set_st_indr2_sublk6_only	2002Oct12
PASS		
0	set_st_indr2_sublk7	2002Oct12
PASS		
0	set_st_indr2_sublk7_only	2002Oct12
PASS		
0	set_st_indr2_vs	2002Oct12
PASS		
0	set_st_indr2_vs_only	2002Oct12
PASS		
0	set_st_pfmatch_disabled	2002Oct12
PASS		
0	set_st_ring_match	2002Oct12
PASS		
0	set_st_ring_mismatch	2002Oct12
PASS		
0	set_st_ring_mismatch2	2002Oct12
PASS		
0	set_st_ring_mismatch3	2002Oct12
PASS		
0	set_st_ring_mismatch4	2002Oct12
PASS		
0	set_st_ring_ps	2002Oct12
PASS		
0	set_st_ring_ps_only	2002Oct12

PASS		
0	set_st_ring_sublk0	2002Oct12
PASS		
0	set_st_ring_sublk1	2002Oct12
PASS		
0	set_st_ring_sublk1_only	2002Oct12
PASS		
0	set_st_ring_sublk2	2002Oct12
PASS		
0	set_st_ring_sublk2_only	2002Oct12
PASS		
0	set_st_ring_sublk3	2002Oct12
PASS		
0	set_st_ring_sublk3_only	2002Oct12
PASS		
0	set_st_ring_sublk4	2002Oct12
PASS		
0	set_st_ring_sublk4_only	2002Oct12
PASS		
0	set_st_ring_sublk5	2002Oct12
PASS		
0	set_st_ring_sublk5_only	2002Oct12
PASS		
0	set_st_ring_sublk6	2002Oct12
PASS		
0	set_st_ring_sublk6_only	2002Oct12
PASS		
0	set_st_ring_sublk7	2002Oct12
PASS		
0	set_st_ring_sublk7_only	2002Oct12
PASS		
0	set_st_ring_vs	2002Oct12
PASS		
0	set_st_ring_vs_only	2002Oct12
PASS		
0	set_st_stress_a	2002Oct12
PASS		
807	set_st_stress_b	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
808	set_st_stress_b_random	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
809	set_st_stress_b_sm_meq	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Oct12
PASS		
0	set_st_stress_c_sm_roq	2002Oct12
PASS		
0	set_st_stress_d	2002Oct12

PASS		
813	set_st_stress_d_random	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
814	set_st_stress_d_sm_meq	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_e	2002Oct12
PASS		
816	set_st_stress_e_sm_meq	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_f	2002Oct12
PASS		
818	set_st_stress_f_random	2002Oct12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_state_rt_a	2002Oct12
PASS		
0	set_state_rt_b	2002Oct12
PASS		
0	set_state_rt_c	2002Oct12
PASS		
0	set_state_rt_d	2002Oct12
PASS		
0	set_state_rt_d_random	2002Oct12
PASS		
0	set_state_rt_e	2002Oct12
PASS		
0	set_state_rt_e_random	2002Oct12
PASS		
0	smoke_nrt	2002Oct12
PASS		
0	src_clip	2002Oct12
PASS		
0	srf_cohr	2002Oct12
PASS		
829	switch_pio_bm	2002Oct12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	trans_bitblt	2002Oct12
PASS		
0	type0_2long_pckts	2002Oct12
PASS		
0	type0_3indr_buffs	2002Oct12
PASS		
0	type0_4pckts	2002Oct12
PASS		
0	type0_incr_updt_cnst	2002Oct12
PASS		
0	type0_incr_updt_instrmem	2002Oct12
PASS		
0	type0_incr_updt_st	2002Oct12

PASS		
0	type0_incr_updt_stress	2002Oct12
PASS		
0	type0_ind1_ind2_a	2002Oct12
PASS		
0	type0_ind1_ind2_b	2002Oct12
PASS		
0	type0_indr_buff	2002Oct12
PASS		
0	type0_real_time	2002Oct12
PASS		
0	type0_real_time_random	2002Oct12
PASS		
0	type1_4pckts	2002Oct12
PASS		
0	type1_indr_buff	2002Oct12
PASS		
0	type1_pckt	2002Oct12
PASS		
846	type_random	2002Oct12
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	uram_wrt_rt_ucose	2002Oct12
PASS		
0	uram_wrt_ucose	2002Oct12
PASS		
0	viz_query_a	2002Oct12
PASS		
0	viz_query_draw_indx	2002Oct12
PASS		
0	viz_query_draw_indx_indr1	2002Oct12
PASS		
0	viz_query_draw_indx_indr2	2002Oct12
PASS		
0	viz_query_draw_indx_multipass	2002Oct12
PASS		
0	viz_query_draw_indx_ring	2002Oct12
PASS		
0	viz_query_indr1	2002Oct12
PASS		
0	viz_query_indr1_simple	2002Oct12
PASS		
0	viz_query_indr2	2002Oct12
PASS		
0	viz_query_indr2_simple	2002Oct12
PASS		
0	viz_query_ring	2002Oct12
PASS		
0	viz_query_ring_simple	2002Oct12

PASS
0 wait_for_idle 2002Oct12
PASS
0 wait_reg_mem_poll_mem0 2002Oct12
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct12
PASS
0 wait_reg_mem_poll_mem1 2002Oct12
PASS
0 wait_reg_mem_poll_mem2 2002Oct12
PASS
0 wait_reg_mem_poll_mem3 2002Oct12
PASS
0 wait_reg_mem_poll_reg0 2002Oct12
PASS
0 wait_reg_mem_poll_reg1 2002Oct12
PASS
0 wait_reg_mem_poll_reg2 2002Oct12
PASS
0 wait_reg_mem_poll_reg3 2002Oct12
PASS

=====
Tests_Run_count = 870 Pass_count = 780 Fail_count = 90 Incomplete_count = 0
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep29
	PASS		
0	2d_indr_buffer_b		2002Sep29
	PASS		
0	2d_indr_buffer_c		2002Sep29
	PASS		
0	2d_indr_buffer_d		2002Sep29
	PASS		
0	2d_indr_buffer_e		2002Sep29
	PASS		
0	2d_indr_buffer_f		2002Sep29
	PASS		
0	2d_indr_buffer_g		2002Sep29
	PASS		
8	2d_indr_buffer_random		2002Sep29
	FAIL cp_mh_req.cmp mh_cp_rd.cmp		
9	2d_indr_buffer_random2		2002Sep29
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep29
	PASS		
0	3d_draw_indx_2_b		2002Sep29
	PASS		
0	IQ2_Mult		2002Sep29
	PASS		
0	IQ2_Wrap_Legacy		2002Sep29
	PASS		
0	IQ_Wrap		2002Sep29
	PASS		
0	Isync_Control		2002Sep29
	PASS		
0	Isync_Control_2D_3D		2002Sep29
	PASS		
0	Isync_Control_3D_2D		2002Sep29
	PASS		
0	RB_BUFSZ_0		2002Sep29
	PASS		
0	RB_BUFSZ_1		2002Sep29
	PASS		
0	RB_Wrap		2002Sep29

PASS		
0	Small_Text_BM	2002Sep29
PASS		
0	Type0_IQ2_BM	2002Sep29
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep29
PASS		
0	Type0_IQ_BM	2002Sep29
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep29
PASS		
0	Type0_PQ_BM	2002Sep29
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep29
PASS		
0	Type3_Bitblt	2002Sep29
PASS		
0	Type3_Load_Palette	2002Sep29
PASS		
0	Type3_NOP	2002Sep29
PASS		
0	Type3_Polyline	2002Sep29
PASS		
0	Type3_Polyline_Large	2002Sep29
PASS		
0	bitblt_multi	2002Sep29
PASS		
0	brush_32x1mono	2002Sep29
PASS		
0	brush_8x8color_16	2002Sep29
PASS		
0	brush_8x8color_32	2002Sep29
PASS		
0	brush_8x8color_64	2002Sep29
PASS		
0	brush_8x8color_dst_0_1	2002Sep29
PASS		
0	brush_8x8mono	2002Sep29
PASS		
0	brush_color_mono_stress	2002Sep29
PASS		
0	brush_flag	2002Sep29
PASS		
0	brush_type0	2002Sep29
PASS		
0	brush_type1	2002Sep29
PASS		
0	brush_type10	2002Sep29

PASS		
0	brush_type13	2002Sep29
PASS		
0	brush_type14	2002Sep29
PASS		
0	brush_type6	2002Sep29
PASS		
0	brush_type7	2002Sep29
PASS		
0	brush_type_other	2002Sep29
PASS		
0	brush_types_13_14	2002Sep29
PASS		
51	change_queue_sizes	2002Sep29
FAIL	cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	clear_const_wr_en_on_me_init	2002Sep29
PASS		
0	clear_const_wr_en_with_lcc	2002Sep29
PASS		
0	cond_write_mem_poll_mem	2002Sep29
PASS		
0	cond_write_mem_poll_mem2	2002Sep29
PASS		
0	cond_write_mem_poll_mem3	2002Sep29
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep29
PASS		
0	cond_write_mem_poll_reg	2002Sep29
PASS		
0	cond_write_mem_poll_reg2	2002Sep29
PASS		
0	cond_write_mem_poll_reg3	2002Sep29
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep29
PASS		
0	cp_bios_scratch	2002Sep29
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep29
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep29
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep29
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep29
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep29
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep29

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep29
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep29
PASS		
0	cp_ib_base000_bufszXXX	2002Sep29
PASS		
0	cp_ib_base001_bufszXXX	2002Sep29
PASS		
0	cp_ib_base010_bufszXXX	2002Sep29
PASS		
0	cp_ib_base011_bufszXXX	2002Sep29
PASS		
0	cp_ib_base100_bufszXXX	2002Sep29
PASS		
0	cp_ib_base101_bufszXXX	2002Sep29
PASS		
0	cp_ib_base110_bufszXXX	2002Sep29
PASS		
0	cp_ib_base111_bufszXXX	2002Sep29
PASS		
0	cp_interrupt	2002Sep29
PASS		
0	cp_interrupt_rt_events_16	2002Sep29
PASS		
0	cp_iq2_alignment_000	2002Sep29
PASS		
0	cp_iq2_alignment_001	2002Sep29
PASS		
0	cp_iq2_alignment_010	2002Sep29
PASS		
0	cp_iq2_alignment_011	2002Sep29
PASS		
0	cp_iq2_alignment_100	2002Sep29
PASS		
0	cp_iq2_alignment_101	2002Sep29
PASS		
0	cp_iq2_alignment_110	2002Sep29
PASS		
0	cp_iq2_alignment_111	2002Sep29
PASS		
0	cp_iq_alignment_000	2002Sep29
PASS		
0	cp_iq_alignment_001	2002Sep29
PASS		
0	cp_iq_alignment_010	2002Sep29
PASS		
0	cp_iq_alignment_011	2002Sep29

PASS		
0	cp_iq_alignment_100	2002Sep29
PASS		
0	cp_iq_alignment_101	2002Sep29
PASS		
0	cp_iq_alignment_110	2002Sep29
PASS		
0	cp_iq_alignment_111	2002Sep29
PASS		
0	cp_max_size_packet	2002Sep29
PASS		
0	cp_max_size_packet_random	2002Sep29
PASS		
0	cp_nrt_rt_protection_mode	2002Sep29
PASS		
0	cp_output_stress	2002Sep29
PASS		
101	cp_output_stress_random	2002Sep29
FAIL	cp_mh_req.cmp	
0	cp_protection_mode	2002Sep29
PASS		
0	cp_rb_alignment_000	2002Sep29
PASS		
0	cp_rb_alignment_001	2002Sep29
PASS		
0	cp_rb_alignment_010	2002Sep29
PASS		
0	cp_rb_alignment_011	2002Sep29
PASS		
0	cp_rb_alignment_100	2002Sep29
PASS		
0	cp_rb_alignment_101	2002Sep29
PASS		
0	cp_rb_alignment_110	2002Sep29
PASS		
0	cp_rb_alignment_111	2002Sep29
PASS		
111	cp_rb_wptrXXX_base000	2002Sep29
FAIL	rbbmWr.cmp	
112	cp_rb_wptrXXX_base001	2002Sep29
FAIL	rbbmWr.cmp	
113	cp_rb_wptrXXX_base010	2002Sep29
FAIL	rbbmWr.cmp	
114	cp_rb_wptrXXX_base011	2002Sep29
FAIL	rbbmWr.cmp	
115	cp_rb_wptrXXX_base100	2002Sep29
FAIL	rbbmWr.cmp	
116	cp_rb_wptrXXX_base101	2002Sep29


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**FAIL**      rbbmWr.cmp
117 cp_rb_wptrXXX_base110      2002Sep29
**FAIL**      rbbmWr.cmp
118 cp_rb_wptrXXX_base111      2002Sep29
**FAIL**      rbbmWr.cmp
0   cp_reg_rw_test              2002Sep29
PASS
0   cp_register_reads            2002Sep29
PASS
0   cp_rt_protection_mode        2002Sep29
PASS
0   cp_scratch                   2002Sep29
PASS
123 cp_scratch_multi            2002Sep29
**FAIL**      cp_mh_req.cmp
0   csf_reg_rd_default           2002Sep29
PASS
125 csf_reg_rd_only              2002Sep29
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   csf_reg_wr_rd                2002Sep29
PASS
0   dma_cp_sync                  2002Sep29
PASS
0   dma_dscrstr_chckn_bit        2002Sep29
PASS
0   dma_dscrstr_flush1           2002Sep29
PASS
0   dma_dscrstr_flush2           2002Sep29
PASS
0   dma_dscrstr_flush3           2002Sep29
PASS
0   dma_m2m_src00_dstXX          2002Sep29
PASS
0   dma_m2m_src01_dstXX          2002Sep29
PASS
0   dma_m2m_src10_dstXX          2002Sep29
PASS
0   dma_m2m_src11_dstXX          2002Sep29
PASS
0   dma_mem2mem                  2002Sep29
PASS
0   dma_mem2mem_abort            2002Sep29
PASS
0   dma_mem2mem_alignments       2002Sep29
PASS
0   dma_mem2mem_byte_alignment0   2002Sep29
PASS
0   dma_mem2mem_byte_alignment1   2002Sep29

```

PASS		
0	dma_mem2mem_byte_alignment2	2002Sep29
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep29
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep29
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep29
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep29
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep29
PASS		
0	dma_mem2mem_large	2002Sep29
PASS		
148	dma_mem2mem_pio_abort	2002Sep29
FAIL		
0	dma_mem2mem_random	2002Sep29
PASS		
0	dma_mem2reg	2002Sep29
PASS		
151	dma_mem2reg_abort	2002Sep29
FAIL	rbbmWr.cmp	
0	dma_mem2reg_dtaq_stress	2002Sep29
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep29
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep29
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep29
PASS		
0	dma_mem2reg_sync	2002Sep29
PASS		
0	dma_r2r_m2m_r2r	2002Sep29
PASS		
0	dma_r2r_m2r_r2r	2002Sep29
PASS		
0	dma_r2r_r2m_r2r	2002Sep29
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep29
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep29
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep29
PASS		
0	dma_reg2mem	2002Sep29
PASS		
0	dma_reg2mem_abort	2002Sep29

PASS		
0	dma_reg2reg	2002Sep29
PASS		
0	dma_reg2reg_2dw	2002Sep29
PASS		
0	dma_reg2reg_daic	2002Sep29
PASS		
0	dma_reg2reg_mult	2002Sep29
PASS		
0	dma_reg2reg_mult2	2002Sep29
PASS		
0	dma_reg2reg_mult3	2002Sep29
PASS		
0	dma_reg2reg_saic	2002Sep29
PASS		
0	dma_reg2reg_sdaic	2002Sep29
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep29
PASS		
0	dma_reg2reg_stress	2002Sep29
PASS		
0	draw_indx_a	2002Sep29
PASS		
0	draw_indx_b	2002Sep29
PASS		
0	draw_indx_c	2002Sep29
PASS		
0	draw_indx_indr1_a	2002Sep29
PASS		
0	draw_indx_indr1_b	2002Sep29
PASS		
0	draw_indx_indr1_short_a	2002Sep29
PASS		
0	draw_indx_indr1_short_b	2002Sep29
PASS		
0	draw_indx_indr1_stress	2002Sep29
PASS		
0	draw_indx_indr2_a	2002Sep29
PASS		
0	draw_indx_indr2_b	2002Sep29
PASS		
0	draw_indx_indr2_short_a	2002Sep29
PASS		
0	draw_indx_indr2_short_b	2002Sep29
PASS		
0	draw_indx_indr2_stress	2002Sep29
PASS		
188	draw_indx_random_a	2002Sep29

```

**FAIL**          mh_cp_rd.cmp
0  draw_indx_random_b          2002Sep29
PASS
0  draw_indx_random_c          2002Sep29
PASS
0  draw_indx_ring1             2002Sep29
PASS
0  draw_indx_ring2             2002Sep29
PASS
0  draw_indx_ring_short1       2002Sep29
PASS
0  draw_indx_ring_short2       2002Sep29
PASS
0  draw_indx_ring_stress       2002Sep29
PASS
0  draw_indx_short_a           2002Sep29
PASS
0  draw_indx_short_b           2002Sep29
PASS
0  draw_indx_short_c           2002Sep29
PASS
0  draw_indx_stress_a          2002Sep29
PASS
0  draw_indx_stress_b          2002Sep29
PASS
0  draw_indx_stress_c          2002Sep29
PASS
0  event_timestamp_write       2002Sep29
PASS
0  event_timestamp_write_multicontext_maxcontext_7 2002Sep29
PASS
0  fix2flt_reg                 2002Sep29
PASS
0  ft_indr1_july22             2002Sep29
PASS
0  ft_indr2_july22             2002Sep29
PASS
0  ft_july22                   2002Sep29
PASS
208 ft_july22_random           2002Sep29
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  header_dump                 2002Sep29
PASS
0  header_dump_rt              2002Sep29
PASS
0  header_dump_small           2002Sep29
PASS
0  hostdata_blt                2002Sep29

```

PASS		
0	im_load_a	2002Sep29
PASS		
0	im_load_b	2002Sep29
PASS		
0	im_load_cid_mismatch	2002Sep29
PASS		
0	im_load_immediate	2002Sep29
PASS		
0	im_load_immediate_nrt_me	2002Sep29
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep29
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep29
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep29
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep29
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep29
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep29
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep29
PASS		
0	im_load_indr1_a	2002Sep29
PASS		
0	im_load_indr1_b	2002Sep29
PASS		
0	im_load_indr2_a	2002Sep29
PASS		
0	im_load_indr2_b	2002Sep29
PASS		
0	im_load_invalidate	2002Sep29
PASS		
0	im_load_match	2002Sep29
PASS		
0	im_load_match2	2002Sep29
PASS		
0	im_load_nrt_me	2002Sep29
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep29
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep29
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep29
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep29

PASS		
0	im_load_pfmatach_disable	2002Sep29
PASS		
0	im_load_random_a	2002Sep29
PASS		
0	im_load_random_b	2002Sep29
PASS		
240	im_load_random_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Sep29
PASS		
0	im_load_ring_b	2002Sep29
PASS		
0	im_load_rt	2002Sep29
PASS		
0	im_load_rt2	2002Sep29
PASS		
0	im_load_rt3	2002Sep29
PASS		
0	im_load_rt4	2002Sep29
PASS		
0	im_load_sz_mismatch	2002Sep29
PASS		
0	incremental_update_const	2002Sep29
PASS		
0	incremental_update_instr	2002Sep29
PASS		
0	incremental_update_state	2002Sep29
PASS		
0	indirect_buffer_a	2002Sep29
PASS		
0	indirect_buffer_b	2002Sep29
PASS		
0	indirect_buffer_c	2002Sep29
PASS		
0	indirect_buffer_d	2002Sep29
PASS		
0	indirect_buffer_e	2002Sep29
PASS		
0	indirect_buffer_f	2002Sep29
PASS		
0	indirect_buffer_g	2002Sep29
PASS		
0	indirect_buffer_h	2002Sep29
PASS		
0	indirect_buffer_i	2002Sep29
PASS		
0	indirect_buffer_j	2002Sep29

PASS		
0	indirect_buffer_k	2002Sep29
PASS		
0	indirect_buffer_l	2002Sep29
PASS		
0	indirect_buffer_mp_a	2002Sep29
PASS		
0	indirect_buffer_mp_b	2002Sep29
PASS		
0	indirect_buffer_mp_c	2002Sep29
PASS		
0	indirect_buffer_mp_d	2002Sep29
PASS		
0	indirect_buffer_mp_e	2002Sep29
PASS		
0	indirect_buffer_mp_f	2002Sep29
PASS		
269	indirect_buffer_mp_random1	2002Sep29
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
270	indirect_buffer_mp_random2	2002Sep29
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
271	indirect_buffer_random1	2002Sep29
FAIL	mh_cp_rd.cmp	
272	indirect_buffer_random2	2002Sep29
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	2002Sep29
PASS		
0	invalidate_on_me_init	2002Sep29
PASS		
0	invalidate_state_constants	2002Sep29
PASS		
0	invalidate_state_shaders	2002Sep29
PASS		
0	invalidate_state_shaders2	2002Sep29
PASS		
0	invalidate_state_sublks	2002Sep29
PASS		
0	invalidate_state_sublks2	2002Sep29
PASS		
0	iq1_iq2_mult	2002Sep29
PASS		
0	iq1_iq2_push_wrap	2002Sep29
PASS		
0	iq2_csq_full	2002Sep29
PASS		
283	lcc_coherency_cntr_random	2002Sep29
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Sep29

PASS		
285	lcc_mismatch_alutex_random	2002Sep29
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
286	lcc_mismatch_base_random	2002Sep29
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_coherency_cntrl	2002Sep29
PASS		
288	ld_cnst_cntx_coherency_cntr2	2002Sep29
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Sep29
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep29
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep29
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep29
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep29
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep29
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep29
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep29
PASS		
0	ld_cnst_cntx_match	2002Sep29
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep29
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep29
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep29
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep29
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep29
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep29
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep29
PASS		
0	ld_cnst_cntx_replicate8	2002Sep29
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep29
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep29
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep29

PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep29
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep29
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep29
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep29
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep29
PASS		
0	load_constant_context_rt	2002Sep29
PASS		
0	load_palette	2002Sep29
PASS		
0	lp_2d3d	2002Sep29
PASS		
0	me_reg_wr_rd	2002Sep29
PASS		
0	mem_write	2002Sep29
PASS		
0	mem_write_rt	2002Sep29
PASS		
0	mpeg_index	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep29
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep29
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep29
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep29
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep29
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep29

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep29
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep29
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep29
PASS		
0	nop_eating_disabled	2002Sep29
PASS		
0	nrt_rt_interaction1	2002Sep29
PASS		
0	paint_multi	2002Sep29
PASS		
0	paint_packet	2002Sep29
PASS		
0	paint_packet_gui_control	2002Sep29
PASS		
0	pfp_reg_reads1	2002Sep29
PASS		
0	pfp_reg_reads2	2002Sep29
PASS		
0	pitch_clip	2002Sep29
PASS		
0	ply_nextscan	2002Sep29
PASS		
0	rbbm_WU_2D_idle_01	2002Sep29
PASS		
0	rbbm_WU_2D_idle_02	2002Sep29
PASS		
0	rbbm_WU_2D_idle_03	2002Sep29
PASS		
0	rbbm_WU_2D_idle_04	2002Sep29
PASS		
0	rbbm_WU_2D_idle_05	2002Sep29
PASS		
0	rbbm_WU_2D_idle_06	2002Sep29
PASS		
0	rbbm_WU_2D_idle_07	2002Sep29
PASS		
0	rbbm_WU_2D_idle_08	2002Sep29
PASS		
0	rbbm_WU_2D_idle_09	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep29

PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep29
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep29
PASS		
0	rbbm_WU_3D_idle_01	2002Sep29
PASS		
0	rbbm_WU_3D_idle_02	2002Sep29
PASS		
0	rbbm_WU_3D_idle_03	2002Sep29
PASS		
0	rbbm_WU_3D_idle_04	2002Sep29
PASS		
0	rbbm_WU_3D_idle_05	2002Sep29
PASS		
0	rbbm_WU_3D_idle_06	2002Sep29
PASS		
0	rbbm_WU_3D_idle_07	2002Sep29
PASS		
0	rbbm_WU_3D_idle_08	2002Sep29
PASS		
0	rbbm_WU_3D_idle_09	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep29

PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep29
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep29
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep29
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Sep29
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep29
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep29
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep29
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep29
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep29
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep29
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep29
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Sep29
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Sep29
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep29
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep29
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep29
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep29
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep29
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep29
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep29

PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep29
PASS		
0	rbbm_WU_idct_semaphore	2002Sep29
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep29
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep29
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep29
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep29
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep29
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep29
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep29
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep29

PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep29
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep29
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep29
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep29
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep29
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep29
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep29
PASS		
0	rbbm_clk_active_deassert_min_max	2002Sep29
PASS		
0	rbbm_clk_active_each_pipe	2002Sep29
PASS		
0	rbbm_clk_active_for_read_error	2002Sep29
PASS		
0	rbbm_clk_active_for_read_request	2002Sep29
PASS		
0	rbbm_clk_active_on_reset	2002Sep29
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep29
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep29
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep29
PASS		
0	rbbm_cp_queued_rdwr	2002Sep29
PASS		
0	rbbm_hi_data_and_be_swap	2002Sep29
PASS		
0	rbbm_hi_data_endian_swap	2002Sep29
PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Sep29
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Sep29
PASS		
0	rbbm_int_ack	2002Sep29
PASS		
0	rbbm_int_each_client	2002Sep29
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Sep29
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep29

PASS		
0	rbbm_int_rderr	2002Sep29
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep29
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep29
PASS		
0	rbbm_isync_wait_idlegui	2002Sep29
PASS		
0	rbbm_soft_reset	2002Sep29
PASS		
0	reg_to_mem	2002Sep29
PASS		
0	reg_to_mem_rt	2002Sep29
PASS		
0	rop_cfd_wmd	2002Sep29
PASS		
461	roq_reg_reads	2002Sep29
FAIL	cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	rstr_ordr	2002Sep29
PASS		
0	rt_invalid_opcode	2002Sep29
PASS		
464	rtee_arm_wait_idle_trigger_events_16	2002Sep29
FAIL		
0	rtee_discretes_arm_rearm_events_32	2002Sep29
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep29

PASS		
0	rtee_discretes_falling_11_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep29
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0	rtee_discretes_falling_14_events_16	2002Sep29
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0	rtee_discretes_falling_15_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep29
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0	rtee_discretes_falling_25_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep29
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep29

PASS		
0	rtee_discretes_invert_03_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep29
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0	rtee_discretes_invert_05_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep29
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0	rtee_discretes_invert_07_events_16	2002Sep29
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0	rtee_discretes_invert_08_events_16	2002Sep29
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0	rtee_discretes_invert_09_events_16	2002Sep29
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0	rtee_discretes_invert_10_events_16	2002Sep29
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0	rtee_discretes_invert_11_events_16	2002Sep29
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0	rtee_discretes_invert_12_events_16	2002Sep29
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0	rtee_discretes_invert_13_events_16	2002Sep29
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0	rtee_discretes_invert_14_events_16	2002Sep29
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0	rtee_discretes_invert_15_events_16	2002Sep29
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0	rtee_discretes_invert_16_events_16	2002Sep29
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0	rtee_discretes_invert_17_events_16	2002Sep29
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0	rtee_discretes_invert_18_events_16	2002Sep29
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0	rtee_discretes_invert_19_events_16	2002Sep29
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0	rtee_discretes_invert_20_events_16	2002Sep29
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0	rtee_discretes_invert_21_events_16	2002Sep29
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0	rtee_discretes_invert_22_events_16	2002Sep29
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0	rtee_discretes_invert_23_events_16	2002Sep29
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0	rtee_discretes_invert_24_events_16	2002Sep29
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0	rtee_discretes_invert_25_events_16	2002Sep29
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0	rtee_discretes_invert_26_events_16	2002Sep29

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0	rtee_discretes_invert_27_events_16	2002Sep29
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0	rtee_discretes_invert_28_events_16	2002Sep29
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0	rtee_discretes_invert_29_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep29
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep29
PASS		
0	rtee_discretes_level_00_events_16	2002Sep29
PASS		
0	rtee_discretes_level_01_events_16	2002Sep29
PASS		
0	rtee_discretes_level_02_events_16	2002Sep29
PASS		
0	rtee_discretes_level_03_events_16	2002Sep29
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0	rtee_discretes_level_04_events_16	2002Sep29
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0	rtee_discretes_level_05_events_16	2002Sep29
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0	rtee_discretes_level_06_events_16	2002Sep29
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0	rtee_discretes_level_07_events_16	2002Sep29
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0	rtee_discretes_level_08_events_16	2002Sep29
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0	rtee_discretes_level_09_events_16	2002Sep29
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0	rtee_discretes_level_10_events_16	2002Sep29
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0	rtee_discretes_level_11_events_16	2002Sep29
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0	rtee_discretes_level_12_events_16	2002Sep29
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0	rtee_discretes_level_13_events_16	2002Sep29
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0	rtee_discretes_level_14_events_16	2002Sep29
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0	rtee_discretes_level_15_events_16	2002Sep29
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0	rtee_discretes_level_16_events_16	2002Sep29
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0	rtee_discretes_level_17_events_16	2002Sep29
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0	rtee_discretes_level_18_events_16	2002Sep29

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0	rtee_discretes_level_19_events_16	2002Sep29
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0	rtee_discretes_level_24_events_16	2002Sep29
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0	rtee_discretes_level_25_events_16	2002Sep29
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0	rtee_discretes_level_26_events_16	2002Sep29
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0	rtee_discretes_level_27_events_16	2002Sep29
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0	rtee_discretes_level_28_events_16	2002Sep29
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0	rtee_discretes_level_29_events_16	2002Sep29
PASS		
0	rtee_discretes_level_30_events_16	2002Sep29
PASS		
0	rtee_discretes_level_31_events_16	2002Sep29
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep29
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0	rtee_discretes_rising_08_events_16	2002Sep29
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0	rtee_discretes_rising_09_events_16	2002Sep29

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0	rtee_discretes_rising_10_events_16	2002Sep29
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0	rtee_discretes_rising_11_events_16	2002Sep29
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0	rtee_discretes_rising_12_events_16	2002Sep29
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0	rtee_discretes_rising_13_events_16	2002Sep29
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0	rtee_discretes_rising_14_events_16	2002Sep29
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0	rtee_discretes_rising_17_events_16	2002Sep29
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0	rtee_discretes_rising_18_events_16	2002Sep29
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0	rtee_discretes_rising_19_events_16	2002Sep29
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0	rtee_discretes_rising_20_events_16	2002Sep29
PASS		
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PASS		
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PASS		
0	rtee_discretes_rising_23_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep29
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0	rtee_discretes_rising_25_events_16	2002Sep29
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0	rtee_discretes_rising_26_events_16	2002Sep29
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0	rtee_discretes_rising_27_events_16	2002Sep29
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0	rtee_discretes_rising_28_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep29
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep29
PASS		
0	rtee_display1_comp_events_16	2002Sep29
PASS		
0	rtee_display2_comp_events_16	2002Sep29

PASS		
0	rtee_mem_linear_search_events_2	2002Sep29
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep29
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep29
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep29
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep29
PASS		
0	rtee_pio	2002Sep29
PASS		
0	rtee_prefetch_parse_nops	2002Sep29
PASS		
0	rtee_reg_linear_search_events_2	2002Sep29
PASS		
613	rtee_reg_poll_comp_000_events_16	2002Sep29
FAIL		
614	rtee_reg_poll_comp_001_events_16	2002Sep29
FAIL		
615	rtee_reg_poll_comp_010_events_16	2002Sep29
FAIL		
616	rtee_reg_poll_comp_011_events_16	2002Sep29
FAIL		
617	rtee_reg_poll_comp_100_events_16	2002Sep29
FAIL		
618	rtee_reg_poll_comp_101_events_16	2002Sep29
FAIL		
619	rtee_reg_poll_comp_110_events_16	2002Sep29
FAIL		
620	rtee_reg_poll_comp_111_events_16	2002Sep29

```

**FAIL**
621 rtee_reg_poll_comp_queued_nonqueued_events_16          2002Sep29
**FAIL**          cp_rbbm.cmp
0 rtee_vip_comp_events_16                                  2002Sep29
PASS
0 scratch_mem_read                                         2002Sep29
PASS
0 scratch_mem_read2                                        2002Sep29
PASS
0 set_constant                                             2002Sep29
PASS
0 set_constant2                                            2002Sep29
PASS
0 set_constant_alu_max1                                    2002Sep29
PASS
0 set_constant_alu_max2                                    2002Sep29
PASS
0 set_constant_bool_max1                                   2002Sep29
PASS
0 set_constant_bool_max2                                   2002Sep29
PASS
0 set_constant_indr1_short                                 2002Sep29
PASS
0 set_constant_indr1_short2                                2002Sep29
PASS
0 set_constant_indr1_write_enable                          2002Sep29
PASS
0 set_constant_indr1_write_enable_lcc                      2002Sep29
PASS
0 set_constant_indr2_short                                 2002Sep29
PASS
0 set_constant_indr2_short2                                2002Sep29
PASS
0 set_constant_indr2_write_enable                          2002Sep29
PASS
0 set_constant_indr2_write_enable_lcc                      2002Sep29
PASS
0 set_constant_loop_max1                                   2002Sep29
PASS
0 set_constant_loop_max2                                   2002Sep29
PASS
0 set_constant_ring_short                                  2002Sep29
PASS
0 set_constant_ring_short2                                 2002Sep29
PASS
0 set_constant_ring_write_enable                           2002Sep29
PASS
0 set_constant_ring_write_enable_lcc                      2002Sep29

```

PASS		
0	set_constant_short	2002Sep29
PASS		
0	set_constant_short2	2002Sep29
PASS		
0	set_constant_tex_max1	2002Sep29
PASS		
0	set_constant_tex_max2	2002Sep29
PASS		
0	set_constant_write_enable	2002Sep29
PASS		
0	set_constant_write_enable_lcc	2002Sep29
PASS		
0	set_constant_write_enable_lcc2	2002Sep29
PASS		
0	set_scissors	2002Sep29
PASS		
0	set_st_indrl_match	2002Sep29
PASS		
0	set_st_indrl_mismatch	2002Sep29
PASS		
0	set_st_indrl_mismatch2	2002Sep29
PASS		
0	set_st_indrl_mismatch3	2002Sep29
PASS		
0	set_st_indrl_mismatch4	2002Sep29
PASS		
0	set_st_indrl_ps	2002Sep29
PASS		
0	set_st_indrl_ps_only	2002Sep29
PASS		
0	set_st_indrl_sublk0	2002Sep29
PASS		
0	set_st_indrl_sublk1	2002Sep29
PASS		
0	set_st_indrl_sublk1_only	2002Sep29
PASS		
0	set_st_indrl_sublk2	2002Sep29
PASS		
0	set_st_indrl_sublk2_only	2002Sep29
PASS		
0	set_st_indrl_sublk3	2002Sep29
PASS		
0	set_st_indrl_sublk3_only	2002Sep29
PASS		
0	set_st_indrl_sublk4	2002Sep29
PASS		
0	set_st_indrl_sublk4_only	2002Sep29

PASS		
0	set_st_indr1_sublk5	2002Sep29
PASS		
0	set_st_indr1_sublk5_only	2002Sep29
PASS		
0	set_st_indr1_sublk6	2002Sep29
PASS		
0	set_st_indr1_sublk6_only	2002Sep29
PASS		
0	set_st_indr1_sublk7	2002Sep29
PASS		
0	set_st_indr1_sublk7_only	2002Sep29
PASS		
0	set_st_indr1_vs	2002Sep29
PASS		
0	set_st_indr1_vs_only	2002Sep29
PASS		
0	set_st_indr2_match	2002Sep29
PASS		
0	set_st_indr2_mismatch	2002Sep29
PASS		
0	set_st_indr2_mismatch2	2002Sep29
PASS		
0	set_st_indr2_mismatch3	2002Sep29
PASS		
0	set_st_indr2_mismatch4	2002Sep29
PASS		
0	set_st_indr2_ps	2002Sep29
PASS		
0	set_st_indr2_ps_only	2002Sep29
PASS		
0	set_st_indr2_sublk0	2002Sep29
PASS		
0	set_st_indr2_sublk1	2002Sep29
PASS		
0	set_st_indr2_sublk1_only	2002Sep29
PASS		
0	set_st_indr2_sublk2	2002Sep29
PASS		
0	set_st_indr2_sublk2_only	2002Sep29
PASS		
0	set_st_indr2_sublk3	2002Sep29
PASS		
0	set_st_indr2_sublk3_only	2002Sep29
PASS		
0	set_st_indr2_sublk4	2002Sep29
PASS		
0	set_st_indr2_sublk4_only	2002Sep29

PASS		
0	set_st_indr2_sublk5	2002Sep29
PASS		
0	set_st_indr2_sublk5_only	2002Sep29
PASS		
0	set_st_indr2_sublk6	2002Sep29
PASS		
0	set_st_indr2_sublk6_only	2002Sep29
PASS		
0	set_st_indr2_sublk7	2002Sep29
PASS		
0	set_st_indr2_sublk7_only	2002Sep29
PASS		
0	set_st_indr2_vs	2002Sep29
PASS		
0	set_st_indr2_vs_only	2002Sep29
PASS		
0	set_st_pfmatch_disabled	2002Sep29
PASS		
0	set_st_ring_match	2002Sep29
PASS		
0	set_st_ring_mismatch	2002Sep29
PASS		
0	set_st_ring_mismatch2	2002Sep29
PASS		
0	set_st_ring_mismatch3	2002Sep29
PASS		
0	set_st_ring_mismatch4	2002Sep29
PASS		
0	set_st_ring_ps	2002Sep29
PASS		
0	set_st_ring_ps_only	2002Sep29
PASS		
0	set_st_ring_sublk0	2002Sep29
PASS		
0	set_st_ring_sublk1	2002Sep29
PASS		
0	set_st_ring_sublk1_only	2002Sep29
PASS		
0	set_st_ring_sublk2	2002Sep29
PASS		
0	set_st_ring_sublk2_only	2002Sep29
PASS		
0	set_st_ring_sublk3	2002Sep29
PASS		
0	set_st_ring_sublk3_only	2002Sep29
PASS		
0	set_st_ring_sublk4	2002Sep29

PASS		
0	set_st_ring_subblk4_only	2002Sep29
PASS		
0	set_st_ring_subblk5	2002Sep29
PASS		
0	set_st_ring_subblk5_only	2002Sep29
PASS		
0	set_st_ring_subblk6	2002Sep29
PASS		
0	set_st_ring_subblk6_only	2002Sep29
PASS		
0	set_st_ring_subblk7	2002Sep29
PASS		
0	set_st_ring_subblk7_only	2002Sep29
PASS		
0	set_st_ring_vs	2002Sep29
PASS		
0	set_st_ring_vs_only	2002Sep29
PASS		
0	set_st_stress_a	2002Sep29
PASS		
0	set_st_stress_b	2002Sep29
PASS		
0	set_st_stress_c	2002Sep29
PASS		
0	set_st_stress_d	2002Sep29
PASS		
0	set_st_stress_e	2002Sep29
PASS		
0	set_st_stress_f	2002Sep29
PASS		
732	set_state_rt_a	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
733	set_state_rt_b	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
734	set_state_rt_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
735	set_state_rt_d	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep29
PASS		
0	src_clip	2002Sep29
PASS		
0	srf_cohr	2002Sep29
PASS		
0	switch_pio_bm	2002Sep29
PASS		
0	trans_bitblt	2002Sep29

PASS		
741	type0_2long_pckts	2002Sep29
FAIL	cp_mh_req.cmp	
0	type0_3indr_buffs	2002Sep29
PASS		
0	type0_4pckts	2002Sep29
PASS		
0	type0_incr_updt_cnst	2002Sep29
PASS		
0	type0_incr_updt_instrmem	2002Sep29
PASS		
0	type0_incr_updt_st	2002Sep29
PASS		
0	type0_incr_updt_stress	2002Sep29
PASS		
0	type0_ind1_ind2_a	2002Sep29
PASS		
0	type0_ind1_ind2_b	2002Sep29
PASS		
0	type0_indr_buff	2002Sep29
PASS		
0	type1_4pckts	2002Sep29
PASS		
0	type1_indr_buff	2002Sep29
PASS		
0	type1_pckt	2002Sep29
PASS		
0	type_random	2002Sep29
PASS		
755	uram_wrt_rt_ucose	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
756	uram_wrt_ucose	2002Sep29
FAIL	rbbmWr.cmp	
757	viz_query_a	2002Sep29
FAIL	rbbmWr.cmp	
758	viz_query_draw_indx	2002Sep29
FAIL	rbbmWr.cmp	
759	viz_query_draw_indx_indr1	2002Sep29
FAIL	rbbmWr.cmp	
760	viz_query_draw_indx_indr2	2002Sep29
FAIL	rbbmWr.cmp	
761	viz_query_draw_indx_multipass	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
762	viz_query_draw_indx_ring	2002Sep29
FAIL	rbbmWr.cmp	
763	viz_query_indr1	2002Sep29
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep29

```
PASS
765 viz_query_indr2 2002Sep29
**FAIL**          rbbmWr.cmp
0 viz_query_indr2_simple 2002Sep29
PASS
767 viz_query_ring 2002Sep29
**FAIL**          rbbmWr.cmp
0 viz_query_ring_simple 2002Sep29
PASS
0 wait_for_idle 2002Sep29
PASS
0 wait_reg_mem_poll_mem0 2002Sep29
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep29
PASS
0 wait_reg_mem_poll_mem1 2002Sep29
PASS
0 wait_reg_mem_poll_mem2 2002Sep29
PASS
0 wait_reg_mem_poll_mem3 2002Sep29
PASS
0 wait_reg_mem_poll_reg0 2002Sep29
PASS
0 wait_reg_mem_poll_reg1 2002Sep29
PASS
0 wait_reg_mem_poll_reg2 2002Sep29
PASS
0 wait_reg_mem_poll_reg3 2002Sep29
PASS
```

```
=====
Tests_Run_count = 778 Pass_count = 724 Fail_count = 54 Incomplete_count = 0
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Aug 28  6:04:05 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          626        626        577         92
              RBBM        92         92         92        100
+++++
              TOTAL        718        718        669         93
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug27
PASS			
0	2d_indr_buffer_b		2002Aug27
PASS			
0	2d_indr_buffer_c		2002Aug27
PASS			
0	2d_indr_buffer_d		2002Aug27
PASS			
0	2d_indr_buffer_e		2002Aug27
PASS			
0	2d_indr_buffer_f		2002Aug27
PASS			
0	2d_indr_buffer_g		2002Aug27
PASS			
0	3d_draw_indx_2		2002Aug27
PASS			
0	3d_draw_indx_2_b		2002Aug27
PASS			
0	bitblt_multi		2002Aug27
PASS			
0	brush_32x1mono		2002Aug27
PASS			
0	brush_8x8color_16		2002Aug27
PASS			
0	brush_8x8color_32		2002Aug27
PASS			
0	brush_8x8color_64		2002Aug27
PASS			
0	brush_8x8color_dst_0_1		2002Aug27
PASS			
0	brush_8x8mono		2002Aug27
PASS			
0	brush_color_mono_stress		2002Aug27
PASS			
0	brush_flag		2002Aug27
PASS			
0	brush_type0		2002Aug27
PASS			
0	brush_type1		2002Aug27

PASS		
0	brush_type10	2002Aug27
PASS		
0	brush_type13	2002Aug27
PASS		
0	brush_type14	2002Aug27
PASS		
0	brush_type6	2002Aug27
PASS		
0	brush_type7	2002Aug27
PASS		
0	brush_type_other	2002Aug27
PASS		
0	brush_types_13_14	2002Aug27
PASS		
0	clear_const_wr_en_on_me_init	2002Aug27
PASS		
0	clear_const_wr_en_with_lcc	2002Aug27
PASS		
0	cond_write_mem_poll_mem	2002Aug27
PASS		
0	cond_write_mem_poll_mem2	2002Aug27
PASS		
0	cond_write_mem_poll_mem3	2002Aug27
PASS		
0	cond_write_mem_poll_mem_rt	2002Aug27
PASS		
0	cond_write_mem_poll_reg	2002Aug27
PASS		
0	cond_write_mem_poll_reg2	2002Aug27
PASS		
0	cond_write_mem_poll_reg3	2002Aug27
PASS		
0	cp_acpi_recovery_soft_reset	2002Aug27
PASS		
0	cp_bios_scratch	2002Aug27
PASS		
39	cp_ib2_base000_bufszXXX	2002Aug27
FAIL	rbbmWr.cmp	
40	cp_ib2_base001_bufszXXX	2002Aug27
FAIL	rbbmWr.cmp	
41	cp_ib2_base010_bufszXXX	2002Aug27
FAIL	rbbmWr.cmp	
42	cp_ib2_base011_bufszXXX	2002Aug27
FAIL	rbbmWr.cmp	
43	cp_ib2_base100_bufszXXX	2002Aug27
FAIL	rbbmWr.cmp	
44	cp_ib2_base101_bufszXXX	2002Aug27

```

**FAIL**      rbbmWr.cmp
45  cp_ib2_base110_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
46  cp_ib2_base111_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
47  cp_ib_base000_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
48  cp_ib_base001_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
49  cp_ib_base010_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
50  cp_ib_base011_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
51  cp_ib_base100_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
52  cp_ib_base101_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
53  cp_ib_base110_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
54  cp_ib_base111_bufszXXX      2002Aug27
**FAIL**      rbbmWr.cmp
0   cp_interrupt                  2002Aug27
PASS
0   cp_interrupt_rt_events_16    2002Aug27
PASS
57  cp_iq2_alignment_000        2002Aug27
**FAIL**      rbbmWr.cmp
58  cp_iq2_alignment_001        2002Aug27
**FAIL**      rbbmWr.cmp
59  cp_iq2_alignment_010        2002Aug27
**FAIL**      rbbmWr.cmp
60  cp_iq2_alignment_011        2002Aug27
**FAIL**      rbbmWr.cmp
61  cp_iq2_alignment_100        2002Aug27
**FAIL**      rbbmWr.cmp
62  cp_iq2_alignment_101        2002Aug27
**FAIL**      rbbmWr.cmp
63  cp_iq2_alignment_110        2002Aug27
**FAIL**      rbbmWr.cmp
64  cp_iq2_alignment_111        2002Aug27
**FAIL**      rbbmWr.cmp
65  cp_iq_alignment_000         2002Aug27
**FAIL**      rbbmWr.cmp
66  cp_iq_alignment_001         2002Aug27
**FAIL**      rbbmWr.cmp
67  cp_iq_alignment_010         2002Aug27
**FAIL**      rbbmWr.cmp
68  cp_iq_alignment_011         2002Aug27

```



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**FAIL**      rbbmWr.cmp
69  cp_iq_alignment_100      2002Aug27
**FAIL**      rbbmWr.cmp
70  cp_iq_alignment_101      2002Aug27
**FAIL**      rbbmWr.cmp
71  cp_iq_alignment_110      2002Aug27
**FAIL**      rbbmWr.cmp
72  cp_iq_alignment_111      2002Aug27
**FAIL**      rbbmWr.cmp
0   cp_max_size_packet      2002Aug27
PASS
0   cp_nrt_rt_protection_mode 2002Aug27
PASS
0   cp_output_stress        2002Aug27
PASS
0   cp_protection_mode      2002Aug27
PASS
0   cp_rb_alignment_000     2002Aug27
PASS
0   cp_rb_alignment_001     2002Aug27
PASS
0   cp_rb_alignment_010     2002Aug27
PASS
0   cp_rb_alignment_011     2002Aug27
PASS
0   cp_rb_alignment_100     2002Aug27
PASS
0   cp_rb_alignment_101     2002Aug27
PASS
0   cp_rb_alignment_110     2002Aug27
PASS
0   cp_rb_alignment_111     2002Aug27
PASS
0   cp_rb_wptrXXX_base000   2002Aug27
PASS
0   cp_rb_wptrXXX_base001   2002Aug27
PASS
0   cp_rb_wptrXXX_base010   2002Aug27
PASS
0   cp_rb_wptrXXX_base011   2002Aug27
PASS
0   cp_rb_wptrXXX_base100   2002Aug27
PASS
0   cp_rb_wptrXXX_base101   2002Aug27
PASS
0   cp_rb_wptrXXX_base110   2002Aug27
PASS
0   cp_rb_wptrXXX_base111   2002Aug27

```

PASS		
0	cp_register_reads	2002Aug27
PASS		
0	cp_reg_rw_test	2002Aug27
PASS		
0	cp_rt_protection_mode	2002Aug27
PASS		
0	cp_scratch	2002Aug27
PASS		
0	cp_scratch_multi	2002Aug27
PASS		
0	dma_cp_sync	2002Aug27
PASS		
0	dma_dscrstr_chckn_bit	2002Aug27
PASS		
0	dma_dscrstr_flush1	2002Aug27
PASS		
0	dma_dscrstr_flush2	2002Aug27
PASS		
0	dma_dscrstr_flush3	2002Aug27
PASS		
0	dma_m2m_src00_dstXX	2002Aug27
PASS		
0	dma_m2m_src01_dstXX	2002Aug27
PASS		
0	dma_m2m_src10_dstXX	2002Aug27
PASS		
0	dma_m2m_src11_dstXX	2002Aug27
PASS		
0	dma_mem2mem	2002Aug27
PASS		
0	dma_mem2mem_abort	2002Aug27
PASS		
0	dma_mem2mem_alignments	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug27
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug27

PASS		
0	dma_mem2mem_byte_alignment7	2002Aug27
PASS		
0	dma_mem2mem_pio_abort	2002Aug27
PASS		
0	dma_mem2reg	2002Aug27
PASS		
0	dma_mem2reg_abort	2002Aug27
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug27
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug27
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug27
PASS		
0	dma_mem2reg_sync	2002Aug27
PASS		
0	dma_r2r_m2m_r2r	2002Aug27
PASS		
0	dma_r2r_m2r_r2r	2002Aug27
PASS		
0	dma_r2r_r2m_r2r	2002Aug27
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug27
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug27
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug27
PASS		
0	dma_reg2mem	2002Aug27
PASS		
0	dma_reg2mem_abort	2002Aug27
PASS		
0	dma_reg2reg	2002Aug27
PASS		
0	dma_reg2reg_2dw	2002Aug27
PASS		
0	dma_reg2reg_daic	2002Aug27
PASS		
0	dma_reg2reg_mult	2002Aug27
PASS		
0	dma_reg2reg_mult2	2002Aug27
PASS		
0	dma_reg2reg_mult3	2002Aug27
PASS		
0	dma_reg2reg_saic	2002Aug27
PASS		
0	dma_reg2reg_sdaic	2002Aug27

PASS		
0	dma_reg2reg_sdaic_stress	2002Aug27
PASS		
0	dma_reg2reg_stress	2002Aug27
PASS		
0	draw_indx_a	2002Aug27
PASS		
0	draw_indx_b	2002Aug27
PASS		
0	draw_indx_c	2002Aug27
PASS		
0	draw_indx_indr1_a	2002Aug27
PASS		
0	draw_indx_indr1_b	2002Aug27
PASS		
0	draw_indx_indr1_short_a	2002Aug27
PASS		
0	draw_indx_indr1_short_b	2002Aug27
PASS		
0	draw_indx_indr1_stress	2002Aug27
PASS		
0	draw_indx_indr2_a	2002Aug27
PASS		
0	draw_indx_indr2_b	2002Aug27
PASS		
0	draw_indx_indr2_short_a	2002Aug27
PASS		
0	draw_indx_indr2_short_b	2002Aug27
PASS		
0	draw_indx_indr2_stress	2002Aug27
PASS		
0	draw_indx_ring1	2002Aug27
PASS		
0	draw_indx_ring2	2002Aug27
PASS		
0	draw_indx_ring_short1	2002Aug27
PASS		
0	draw_indx_ring_short2	2002Aug27
PASS		
0	draw_indx_ring_stress	2002Aug27
PASS		
0	draw_indx_short_a	2002Aug27
PASS		
0	draw_indx_short_b	2002Aug27
PASS		
0	draw_indx_short_c	2002Aug27
PASS		
0	draw_indx_stress_a	2002Aug27

PASS		
0	draw_indx_stress_b	2002Aug27
PASS		
0	draw_indx_stress_c	2002Aug27
PASS		
0	event_timestamp_write	2002Aug27
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug27
PASS		
0	ft_indr1_july22	2002Aug27
PASS		
0	ft_indr2_july22	2002Aug27
PASS		
0	ft_july22	2002Aug27
PASS		
0	header_dump	2002Aug27
PASS		
0	header_dump_rt	2002Aug27
PASS		
0	header_dump_small	2002Aug27
PASS		
175	hostdata_blt	2002Aug27
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Aug27
PASS		
0	im_load_b	2002Aug27
PASS		
0	im_load_cid_mismatch	2002Aug27
PASS		
0	im_load_immediate	2002Aug27
PASS		
0	im_load_immediate_nrt_me	2002Aug27
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug27
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug27
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug27
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug27
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug27
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug27
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug27
PASS		
0	im_load_indr1_a	2002Aug27

PASS		
0	im_load_indr1_b	2002Aug27
PASS		
0	im_load_indr2_a	2002Aug27
PASS		
0	im_load_indr2_b	2002Aug27
PASS		
0	im_load_invalidate	2002Aug27
PASS		
0	im_load_match	2002Aug27
PASS		
0	im_load_match2	2002Aug27
PASS		
0	im_load_nrt_me	2002Aug27
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug27
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug27
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug27
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug27
PASS		
0	im_load_pfmatch_disable	2002Aug27
PASS		
0	im_load_ring_a	2002Aug27
PASS		
0	im_load_ring_b	2002Aug27
PASS		
0	im_load_rt	2002Aug27
PASS		
0	im_load_rt2	2002Aug27
PASS		
0	im_load_rt3	2002Aug27
PASS		
0	im_load_rt4	2002Aug27
PASS		
0	im_load_sz_mismatch	2002Aug27
PASS		
0	incremental_update_const	2002Aug27
PASS		
0	incremental_update_instr	2002Aug27
PASS		
0	incremental_update_state	2002Aug27
PASS		
0	indirect_buffer_a	2002Aug27
PASS		
0	indirect_buffer_b	2002Aug27

PASS		
0	indirect_buffer_c	2002Aug27
PASS		
0	indirect_buffer_d	2002Aug27
PASS		
0	indirect_buffer_e	2002Aug27
PASS		
0	indirect_buffer_f	2002Aug27
PASS		
0	indirect_buffer_g	2002Aug27
PASS		
0	indirect_buffer_h	2002Aug27
PASS		
0	indirect_buffer_i	2002Aug27
PASS		
0	indirect_buffer_j	2002Aug27
PASS		
0	indirect_buffer_k	2002Aug27
PASS		
0	indirect_buffer_l	2002Aug27
PASS		
0	indirect_buffer_mp_a	2002Aug27
PASS		
0	indirect_buffer_mp_b	2002Aug27
PASS		
0	indirect_buffer_mp_c	2002Aug27
PASS		
0	indirect_buffer_mp_d	2002Aug27
PASS		
0	indirect_buffer_mp_e	2002Aug27
PASS		
0	indirect_buffer_mp_f	2002Aug27
PASS		
0	invalidate_on_me_init	2002Aug27
PASS		
0	invalidate_state_constants	2002Aug27
PASS		
0	invalidate_state_shaders	2002Aug27
PASS		
0	invalidate_state_shaders2	2002Aug27
PASS		
0	invalidate_state_sublks	2002Aug27
PASS		
0	invalidate_state_sublks2	2002Aug27
PASS		
0	invalid_opcode	2002Aug27
PASS		
236	iql_iq2_mult	2002Aug27

```

**FAIL**      rbbmWr.cmp
237 iq1_iq2_push_wrap      2002Aug27
**FAIL**      rbbmWr.cmp
0 iq2_csq_full      2002Aug27
PASS
0 IQ2_Mult      2002Aug27
PASS
240 IQ2_Wrap_Legacy      2002Aug27
**FAIL**      rbbmWr.cmp
241 IQ_Wrap      2002Aug27
**FAIL**      rbbmWr.cmp
0 ld_cnst_cntx_coherency_cntrl      2002Aug27
PASS
0 ld_cnst_cntx_coherency_cntrl2      2002Aug27
PASS
0 ld_cnst_cntx_indr1_replicate      2002Aug27
PASS
0 ld_cnst_cntx_indr1_replicate4      2002Aug27
PASS
0 ld_cnst_cntx_indr1_replicate8      2002Aug27
PASS
0 ld_cnst_cntx_indr1_simple      2002Aug27
PASS
0 ld_cnst_cntx_indr2_replicate      2002Aug27
PASS
0 ld_cnst_cntx_indr2_replicate4      2002Aug27
PASS
0 ld_cnst_cntx_indr2_replicate8      2002Aug27
PASS
0 ld_cnst_cntx_indr2_simple      2002Aug27
PASS
0 ld_cnst_cntx_match      2002Aug27
PASS
0 ld_cnst_cntx_match_valid_preserved      2002Aug27
PASS
0 ld_cnst_cntx_mismatch_alutex      2002Aug27
PASS
0 ld_cnst_cntx_mismatch_base      2002Aug27
PASS
0 ld_cnst_cntx_mismatch_force      2002Aug27
PASS
0 ld_cnst_cntx_pfmatch_disable      2002Aug27
PASS
0 ld_cnst_cntx_replicate4_alu      2002Aug27
PASS
0 ld_cnst_cntx_replicate4_tex      2002Aug27
PASS
0 ld_cnst_cntx_replicate8      2002Aug27

```


PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug27
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug27
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug27
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug27
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug27
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug27
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug27
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug27
PASS		
0	load_constant_context_rt	2002Aug27
PASS		
0	load_palette	2002Aug27
PASS		
0	lp_2d3d	2002Aug27
PASS		
0	mem_write	2002Aug27
PASS		
0	mem_write_rt	2002Aug27
PASS		
0	mpeg_index	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug27
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug27
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug27
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug27

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug27
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug27
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug27
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug27
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug27
PASS		
0	nop_eating_disabled	2002Aug27
PASS		
0	paint_multi	2002Aug27
PASS		
0	pitch_clip	2002Aug27
PASS		
0	ply_nextscan	2002Aug27
PASS		
0	rbbm_soft_reset	2002Aug27
PASS		
0	rbbm_WU_2D_idle_01	2002Aug27
PASS		
0	rbbm_WU_2D_idle_02	2002Aug27
PASS		
0	rbbm_WU_2D_idle_03	2002Aug27
PASS		
0	rbbm_WU_2D_idle_04	2002Aug27
PASS		
0	rbbm_WU_2D_idle_05	2002Aug27
PASS		
0	rbbm_WU_2D_idle_06	2002Aug27
PASS		
0	rbbm_WU_2D_idle_07	2002Aug27
PASS		
0	rbbm_WU_2D_idle_08	2002Aug27
PASS		
0	rbbm_WU_2D_idle_09	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Aug27

PASS		
0	rbbm_WU_2D_idle_clean_06	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Aug27
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Aug27
PASS		
0	rbbm_WU_3D_idle_01	2002Aug27
PASS		
0	rbbm_WU_3D_idle_02	2002Aug27
PASS		
0	rbbm_WU_3D_idle_03	2002Aug27
PASS		
0	rbbm_WU_3D_idle_04	2002Aug27
PASS		
0	rbbm_WU_3D_idle_05	2002Aug27
PASS		
0	rbbm_WU_3D_idle_06	2002Aug27
PASS		
0	rbbm_WU_3D_idle_07	2002Aug27
PASS		
0	rbbm_WU_3D_idle_08	2002Aug27
PASS		
0	rbbm_WU_3D_idle_09	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Aug27

PASS		
0	rbbm_WU_3D_idle_clean_09	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Aug27
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Aug27
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug27
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Aug27
PASS		
0	rbbm_WU_cmdfifo_entries	2002Aug27
PASS		
0	rbbm_WU_CP_dma_idle	2002Aug27
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug27
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug27
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug27
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug27
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug27
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug27
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug27
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug27
PASS		
0	rbbm_WU_idct_semaphore	2002Aug27
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug27
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug27
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug27
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug27
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Aug27
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Aug27
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Aug27

PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Aug27
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Aug27
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Aug27
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Aug27
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Aug27
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Aug27
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0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug27
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug27
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Aug27
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0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Aug27
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0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Aug27
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0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Aug27
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0	rbbm_WUrt_RT_idle_01	2002Aug27
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0	rbbm_WUrt_RT_idle_02	2002Aug27
PASS		
0	rbbm_WUrt_RT_idle_03	2002Aug27
PASS		
0	rbbm_WUrt_RT_idle_04	2002Aug27
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0	rbbm_WUrt_RT_idle_clean_01	2002Aug27
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Aug27
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Aug27
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Aug27
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0	rbbm_WUrt_RT_idle_clean_05	2002Aug27
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0	rbbm_WUrt_VIPH0_dma_idle	2002Aug27
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0	rbbm_WUrt_VIPH1_dma_idle	2002Aug27
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0	rbbm_WUrt_VIPH2_dma_idle	2002Aug27

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0	rbbm_WUrt_VIPH3_dma_idle	2002Aug27
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0	rbbm_WU_VIPH0_dma_idle	2002Aug27
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Aug27
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0	rbbm_WU_VIPH2_dma_idle	2002Aug27
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Aug27
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0	RB_BUFSZ_0	2002Aug27
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0	RB_BUFSZ_1	2002Aug27
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0	RB_Wrap	2002Aug27
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0	reg_to_mem	2002Aug27
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0	reg_to_mem_rt	2002Aug27
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0	rop_cfd_wmd	2002Aug27
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0	rstr_ordr	2002Aug27
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0	rtee_arm_wait_idle_trigger_events_16	2002Aug27
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0	rtee_discretes_arm_rearm_events_32	2002Aug27
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0	rtee_discretes_falling_00_events_16	2002Aug27
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0	rtee_discretes_falling_01_events_16	2002Aug27
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0	rtee_discretes_falling_02_events_16	2002Aug27
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0	rtee_discretes_falling_03_events_16	2002Aug27
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0	rtee_discretes_falling_05_events_16	2002Aug27
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0	rtee_discretes_falling_31_events_16	2002Aug27
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0	rtee_discretes_level_02_events_16	2002Aug27
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0	rtee_discretes_level_03_events_16	2002Aug27
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0	rtee_discretes_level_04_events_16	2002Aug27
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0	rtee_discretes_level_05_events_16	2002Aug27
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0	rtee_discretes_level_06_events_16	2002Aug27
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0	rtee_discretes_level_07_events_16	2002Aug27
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0	rtee_discretes_level_08_events_16	2002Aug27
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0	rtee_discretes_level_09_events_16	2002Aug27
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0	rtee_discretes_level_11_events_16	2002Aug27
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0	rtee_discretes_level_12_events_16	2002Aug27
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0	rtee_discretes_level_14_events_16	2002Aug27
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0	rtee_discretes_level_15_events_16	2002Aug27
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0	rtee_discretes_level_16_events_16	2002Aug27
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0	rtee_discretes_level_17_events_16	2002Aug27

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0	rtee_discretes_level_22_events_16	2002Aug27
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0	rtee_discretes_level_23_events_16	2002Aug27
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0	rtee_discretes_level_24_events_16	2002Aug27
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0	rtee_discretes_level_25_events_16	2002Aug27
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0	rtee_discretes_level_26_events_16	2002Aug27
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0	rtee_discretes_level_27_events_16	2002Aug27
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0	rtee_discretes_level_28_events_16	2002Aug27
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0	rtee_discretes_level_29_events_16	2002Aug27
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0	rtee_discretes_level_30_events_16	2002Aug27
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0	rtee_discretes_level_31_events_16	2002Aug27
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0	rtee_discretes_manual_disarm_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug27

PASS		
0	rtee_discretes_rising_09_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug27
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug27
PASS		
0	rtee_display1_comp_events_16	2002Aug27

PASS		
0	rtee_display2_comp_events_16	2002Aug27
PASS		
0	rtee_mem_linear_search_events_2	2002Aug27
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug27
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug27
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug27
PASS		
0	rtee_prefetch_parse_nops	2002Aug27
PASS		
0	rtee_reg_linear_search_events_2	2002Aug27
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug27
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug27

PASS		
0	rtee_vip_comp_events_16	2002Aug27
PASS		
0	rt_invalid_opcode	2002Aug27
PASS		
0	scratch_mem_read	2002Aug27
PASS		
0	scratch_mem_read2	2002Aug27
PASS		
0	set_constant	2002Aug27
PASS		
0	set_constant2	2002Aug27
PASS		
0	set_constant_alu_max1	2002Aug27
PASS		
0	set_constant_alu_max2	2002Aug27
PASS		
0	set_constant_bool_max1	2002Aug27
PASS		
0	set_constant_bool_max2	2002Aug27
PASS		
0	set_constant_indr1_short	2002Aug27
PASS		
0	set_constant_indr1_short2	2002Aug27
PASS		
0	set_constant_indr1_write_enable	2002Aug27
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug27
PASS		
0	set_constant_indr2_short	2002Aug27
PASS		
0	set_constant_indr2_short2	2002Aug27
PASS		
0	set_constant_indr2_write_enable	2002Aug27
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug27
PASS		
0	set_constant_loop_max1	2002Aug27
PASS		
0	set_constant_loop_max2	2002Aug27
PASS		
0	set_constant_ring_short	2002Aug27
PASS		
0	set_constant_ring_short2	2002Aug27
PASS		
0	set_constant_ring_write_enable	2002Aug27
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug27

PASS		
0	set_constant_short	2002Aug27
PASS		
0	set_constant_short2	2002Aug27
PASS		
0	set_constant_tex_max1	2002Aug27
PASS		
0	set_constant_tex_max2	2002Aug27
PASS		
0	set_constant_write_enable	2002Aug27
PASS		
0	set_constant_write_enable_lcc	2002Aug27
PASS		
0	set_constant_write_enable_lcc2	2002Aug27
PASS		
0	set_scissors	2002Aug27
PASS		
0	set_state_rt_a	2002Aug27
PASS		
0	set_state_rt_b	2002Aug27
PASS		
0	set_state_rt_c	2002Aug27
PASS		
584	set_state_rt_d	2002Aug27
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_indrl_match	2002Aug27
PASS		
0	set_st_indrl_mismatch	2002Aug27
PASS		
0	set_st_indrl_mismatch2	2002Aug27
PASS		
0	set_st_indrl_mismatch3	2002Aug27
PASS		
0	set_st_indrl_mismatch4	2002Aug27
PASS		
0	set_st_indrl_ps	2002Aug27
PASS		
0	set_st_indrl_ps_only	2002Aug27
PASS		
0	set_st_indrl_sublk0	2002Aug27
PASS		
0	set_st_indrl_sublk1	2002Aug27
PASS		
0	set_st_indrl_sublk1_only	2002Aug27
PASS		
0	set_st_indrl_sublk2	2002Aug27
PASS		
0	set_st_indrl_sublk2_only	2002Aug27

PASS		
0	set_st_indr1_sublk3	2002Aug27
PASS		
0	set_st_indr1_sublk3_only	2002Aug27
PASS		
0	set_st_indr1_sublk4	2002Aug27
PASS		
0	set_st_indr1_sublk4_only	2002Aug27
PASS		
0	set_st_indr1_sublk5	2002Aug27
PASS		
0	set_st_indr1_sublk5_only	2002Aug27
PASS		
0	set_st_indr1_sublk6	2002Aug27
PASS		
0	set_st_indr1_sublk6_only	2002Aug27
PASS		
0	set_st_indr1_sublk7	2002Aug27
PASS		
0	set_st_indr1_sublk7_only	2002Aug27
PASS		
0	set_st_indr1_vs	2002Aug27
PASS		
0	set_st_indr1_vs_only	2002Aug27
PASS		
0	set_st_indr2_match	2002Aug27
PASS		
0	set_st_indr2_mismatch	2002Aug27
PASS		
0	set_st_indr2_mismatch2	2002Aug27
PASS		
0	set_st_indr2_mismatch3	2002Aug27
PASS		
0	set_st_indr2_mismatch4	2002Aug27
PASS		
0	set_st_indr2_ps	2002Aug27
PASS		
0	set_st_indr2_ps_only	2002Aug27
PASS		
0	set_st_indr2_sublk0	2002Aug27
PASS		
0	set_st_indr2_sublk1	2002Aug27
PASS		
0	set_st_indr2_sublk1_only	2002Aug27
PASS		
0	set_st_indr2_sublk2	2002Aug27
PASS		
0	set_st_indr2_sublk2_only	2002Aug27

PASS		
0	set_st_indr2_sublk3	2002Aug27
PASS		
0	set_st_indr2_sublk3_only	2002Aug27
PASS		
0	set_st_indr2_sublk4	2002Aug27
PASS		
0	set_st_indr2_sublk4_only	2002Aug27
PASS		
0	set_st_indr2_sublk5	2002Aug27
PASS		
0	set_st_indr2_sublk5_only	2002Aug27
PASS		
0	set_st_indr2_sublk6	2002Aug27
PASS		
0	set_st_indr2_sublk6_only	2002Aug27
PASS		
0	set_st_indr2_sublk7	2002Aug27
PASS		
0	set_st_indr2_sublk7_only	2002Aug27
PASS		
0	set_st_indr2_vs	2002Aug27
PASS		
0	set_st_indr2_vs_only	2002Aug27
PASS		
0	set_st_pfmacth_disabled	2002Aug27
PASS		
0	set_st_ring_match	2002Aug27
PASS		
0	set_st_ring_mismatch	2002Aug27
PASS		
0	set_st_ring_mismatch2	2002Aug27
PASS		
0	set_st_ring_mismatch3	2002Aug27
PASS		
0	set_st_ring_mismatch4	2002Aug27
PASS		
0	set_st_ring_ps	2002Aug27
PASS		
0	set_st_ring_ps_only	2002Aug27
PASS		
0	set_st_ring_sublk0	2002Aug27
PASS		
0	set_st_ring_sublk1	2002Aug27
PASS		
0	set_st_ring_sublk1_only	2002Aug27
PASS		
0	set_st_ring_sublk2	2002Aug27

PASS
0 set_st_ring_sublk2_only 2002Aug27
PASS
0 set_st_ring_sublk3 2002Aug27
PASS
0 set_st_ring_sublk3_only 2002Aug27
PASS
0 set_st_ring_sublk4 2002Aug27
PASS
0 set_st_ring_sublk4_only 2002Aug27
PASS
0 set_st_ring_sublk5 2002Aug27
PASS
0 set_st_ring_sublk5_only 2002Aug27
PASS
0 set_st_ring_sublk6 2002Aug27
PASS
0 set_st_ring_sublk6_only 2002Aug27
PASS
0 set_st_ring_sublk7 2002Aug27
PASS
0 set_st_ring_sublk7_only 2002Aug27
PASS
0 set_st_ring_vs 2002Aug27
PASS
0 set_st_ring_vs_only 2002Aug27
PASS
0 set_st_stress_a 2002Aug27
PASS
0 set_st_stress_b 2002Aug27
PASS
0 set_st_stress_c 2002Aug27
PASS
0 set_st_stress_d 2002Aug27
PASS
0 set_st_stress_e 2002Aug27
PASS
0 set_st_stress_f 2002Aug27
PASS
664 Small_Text_BM 2002Aug27
FAIL cp_rbbm.cmp rbbmWr.cmp
0 smoke_nrt 2002Aug27
PASS
0 src_clip 2002Aug27
PASS
0 srf_cohr 2002Aug27
PASS
0 trans_bitblt 2002Aug27

PASS		
0	type0_2long_pckts	2002Aug27
PASS		
0	type0_3indr_buffs	2002Aug27
PASS		
0	type0_4pckts	2002Aug27
PASS		
0	type0_incr_updt_cnst	2002Aug27
PASS		
0	type0_incr_updt_instrmem	2002Aug27
PASS		
0	type0_incr_updt_st	2002Aug27
PASS		
0	type0_incr_updt_stress	2002Aug27
PASS		
0	type0_ind1_ind2_a	2002Aug27
PASS		
0	type0_ind1_ind2_b	2002Aug27
PASS		
0	type0_indr_buff	2002Aug27
PASS		
0	Type0_IQ2_BM	2002Aug27
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug27
PASS		
0	Type0_IQ_BM	2002Aug27
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug27
PASS		
0	Type0_PQ_BM	2002Aug27
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug27
PASS		
0	type1_4pckts	2002Aug27
PASS		
0	type1_indr_buff	2002Aug27
PASS		
0	type1_pckt	2002Aug27
PASS		
0	Type3_Bitblt	2002Aug27
PASS		
689	Type3_Load_Palette	2002Aug27
FAIL	rbbmWr.cmp	
0	Type3_NOP	2002Aug27
PASS		
691	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
692	uram_rd_wrt_rt	2002Aug23

```

**FAIL**      rbbmWr.cmp
0    uram_wrt_rt_ucode                2002Aug27
PASS
0    uram_wrt_ucode                    2002Aug27
PASS
0    viz_query_a                        2002Aug27
PASS
696  viz_query_draw_indx               2002Aug27
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
697  viz_query_draw_indx_indr1         2002Aug27
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
698  viz_query_draw_indx_indr2         2002Aug27
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
699  viz_query_draw_indx_multipass     2002Aug27
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
700  viz_query_draw_indx_ring          2002Aug27
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0    viz_query_indr1                   2002Aug27
PASS
0    viz_query_indr1_simple             2002Aug27
PASS
0    viz_query_indr2                   2002Aug27
PASS
0    viz_query_indr2_simple             2002Aug27
PASS
0    viz_query_ring                     2002Aug27
PASS
0    viz_query_ring_simple              2002Aug27
PASS
0    wait_for_idle                      2002Aug27
PASS
0    wait_reg_mem_poll_mem0             2002Aug27
PASS
0    wait_reg_mem_poll_mem0_rt          2002Aug27
PASS
0    wait_reg_mem_poll_mem1             2002Aug27
PASS
0    wait_reg_mem_poll_mem2             2002Aug27
PASS
0    wait_reg_mem_poll_mem3             2002Aug27
PASS
0    wait_reg_mem_poll_reg0             2002Aug27
PASS
0    wait_reg_mem_poll_reg1             2002Aug27
PASS
0    wait_reg_mem_poll_reg2             2002Aug27
PASS
0    wait_reg_mem_poll_reg3             2002Aug27

```

PASS

```
=====  
Tests_Run_count = 716 Pass_count = 669 Fail_count = 47 Incomplete_count = 0  
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep 18  6:49:13 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          639        639        590        92
              RBBM        98         98         60         61
+++++
              TOTAL        737        737        650        88
+++++

```

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+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sun Oct 13 23:42:15 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC               0          0          0          0
              CL               0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU               0          0          0          0
              CP              752        752        691         91
              RBBM            120        120        118         98
+++++
              TOTAL            872        872        809         92
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep6
	PASS		
0	2d_indr_buffer_b		2002Sep6
	PASS		
0	2d_indr_buffer_c		2002Sep6
	PASS		
0	2d_indr_buffer_d		2002Sep6
	PASS		
0	2d_indr_buffer_e		2002Sep6
	PASS		
0	2d_indr_buffer_f		2002Sep6
	PASS		
0	2d_indr_buffer_g		2002Sep6
	PASS		
0	3d_draw_indx_2		2002Sep6
	PASS		
0	3d_draw_indx_2_b		2002Sep6
	PASS		
0	bitblt_multi		2002Sep6
	PASS		
0	brush_32x1mono		2002Sep6
	PASS		
0	brush_8x8color_16		2002Sep6
	PASS		
0	brush_8x8color_32		2002Sep6
	PASS		
0	brush_8x8color_64		2002Sep6
	PASS		
0	brush_8x8color_dst_0_1		2002Sep6
	PASS		
0	brush_8x8mono		2002Sep6
	PASS		
0	brush_color_mono_stress		2002Sep6
	PASS		
18	brush_flag		2002Sep6
FAIL	cp_rbbm.cmp rbbmWr.cmp		
0	brush_type0		2002Sep6
	PASS		
0	brush_type1		2002Sep6

PASS		
0	brush_type10	2002Sep6
PASS		
0	brush_type13	2002Sep6
PASS		
0	brush_type14	2002Sep6
PASS		
0	brush_type6	2002Sep6
PASS		
0	brush_type7	2002Sep6
PASS		
0	brush_type_other	2002Sep6
PASS		
0	brush_types_13_14	2002Sep6
PASS		
0	clear_const_wr_en_on_me_init	2002Sep6
PASS		
0	clear_const_wr_en_with_lcc	2002Sep6
PASS		
0	cond_write_mem_poll_mem	2002Sep6
PASS		
0	cond_write_mem_poll_mem2	2002Sep6
PASS		
0	cond_write_mem_poll_mem3	2002Sep6
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep6
PASS		
0	cond_write_mem_poll_reg	2002Sep6
PASS		
0	cond_write_mem_poll_reg2	2002Sep6
PASS		
0	cond_write_mem_poll_reg3	2002Sep6
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep6
PASS		
0	cp_bios_scratch	2002Sep6
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep6
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep6
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep6
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep6
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep6
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep6

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep6
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep6
PASS		
0	cp_ib_base000_bufszXXX	2002Sep6
PASS		
0	cp_ib_base001_bufszXXX	2002Sep6
PASS		
0	cp_ib_base010_bufszXXX	2002Sep6
PASS		
0	cp_ib_base011_bufszXXX	2002Sep6
PASS		
0	cp_ib_base100_bufszXXX	2002Sep6
PASS		
0	cp_ib_base101_bufszXXX	2002Sep6
PASS		
0	cp_ib_base110_bufszXXX	2002Sep6
PASS		
0	cp_ib_base111_bufszXXX	2002Sep6
PASS		
0	cp_interrupt	2002Sep6
PASS		
0	cp_interrupt_rt_events_16	2002Sep6
PASS		
0	cp_iq2_alignment_000	2002Sep6
PASS		
0	cp_iq2_alignment_001	2002Sep6
PASS		
0	cp_iq2_alignment_010	2002Sep6
PASS		
0	cp_iq2_alignment_011	2002Sep6
PASS		
0	cp_iq2_alignment_100	2002Sep6
PASS		
0	cp_iq2_alignment_101	2002Sep6
PASS		
0	cp_iq2_alignment_110	2002Sep6
PASS		
0	cp_iq2_alignment_111	2002Sep6
PASS		
0	cp_iq_alignment_000	2002Sep6
PASS		
0	cp_iq_alignment_001	2002Sep6
PASS		
0	cp_iq_alignment_010	2002Sep6
PASS		
0	cp_iq_alignment_011	2002Sep6

PASS		
0	cp_iq_alignment_100	2002Sep6
PASS		
0	cp_iq_alignment_101	2002Sep6
PASS		
0	cp_iq_alignment_110	2002Sep6
PASS		
0	cp_iq_alignment_111	2002Sep6
PASS		
0	cp_max_size_packet	2002Sep6
PASS		
0	cp_max_size_packet_random	2002Sep6
PASS		
0	cp_nrt_rt_protection_mode	2002Sep6
PASS		
0	cp_output_stress	2002Sep6
PASS		
0	cp_output_stress_random	2002Sep6
PASS		
0	cp_protection_mode	2002Sep6
PASS		
0	cp_rb_alignment_000	2002Sep6
PASS		
0	cp_rb_alignment_001	2002Sep6
PASS		
0	cp_rb_alignment_010	2002Sep6
PASS		
0	cp_rb_alignment_011	2002Sep6
PASS		
0	cp_rb_alignment_100	2002Sep6
PASS		
0	cp_rb_alignment_101	2002Sep6
PASS		
0	cp_rb_alignment_110	2002Sep6
PASS		
0	cp_rb_alignment_111	2002Sep6
PASS		
0	cp_rb_wptrXXX_base000	2002Sep6
PASS		
0	cp_rb_wptrXXX_base001	2002Sep6
PASS		
0	cp_rb_wptrXXX_base010	2002Sep6
PASS		
0	cp_rb_wptrXXX_base011	2002Sep6
PASS		
0	cp_rb_wptrXXX_base100	2002Sep6
PASS		
0	cp_rb_wptrXXX_base101	2002Sep6

PASS		
0	cp_rb_wptrXXX_base110	2002Sep6
PASS		
0	cp_rb_wptrXXX_base111	2002Sep6
PASS		
0	cp_register_reads	2002Sep6
PASS		
0	cp_reg_rw_test	2002Sep6
PASS		
0	cp_rt_protection_mode	2002Sep6
PASS		
0	cp_scratch	2002Sep6
PASS		
0	cp_scratch_multi	2002Sep6
PASS		
0	dma_cp_sync	2002Sep6
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep6
PASS		
0	dma_dscrstr_flush1	2002Sep6
PASS		
0	dma_dscrstr_flush2	2002Sep6
PASS		
0	dma_dscrstr_flush3	2002Sep6
PASS		
0	dma_m2m_src00_dstXX	2002Sep6
PASS		
0	dma_m2m_src01_dstXX	2002Sep6
PASS		
0	dma_m2m_src10_dstXX	2002Sep6
PASS		
0	dma_m2m_src11_dstXX	2002Sep6
PASS		
0	dma_mem2mem	2002Sep6
PASS		
0	dma_mem2mem_abort	2002Sep6
PASS		
0	dma_mem2mem_alignments	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep6

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep6
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep6
PASS		
0	dma_mem2mem_pio_abort	2002Sep6
PASS		
0	dma_mem2mem_random	2002Sep6
PASS		
0	dma_mem2reg	2002Sep6
PASS		
0	dma_mem2reg_abort	2002Sep6
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep6
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep6
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep6
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep6
PASS		
0	dma_mem2reg_sync	2002Sep6
PASS		
0	dma_r2r_m2m_r2r	2002Sep6
PASS		
0	dma_r2r_m2r_r2r	2002Sep6
PASS		
0	dma_r2r_r2m_r2r	2002Sep6
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep6
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep6
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep6
PASS		
0	dma_reg2mem	2002Sep6
PASS		
0	dma_reg2mem_abort	2002Sep6
PASS		
0	dma_reg2reg	2002Sep6
PASS		
0	dma_reg2reg_2dw	2002Sep6
PASS		
0	dma_reg2reg_daic	2002Sep6
PASS		
0	dma_reg2reg_mult	2002Sep6

PASS		
0	dma_reg2reg_mult2	2002Sep6
PASS		
0	dma_reg2reg_mult3	2002Sep6
PASS		
0	dma_reg2reg_saic	2002Sep6
PASS		
0	dma_reg2reg_sdaic	2002Sep6
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep6
PASS		
0	dma_reg2reg_stress	2002Sep6
PASS		
0	draw_indx_a	2002Sep6
PASS		
0	draw_indx_b	2002Sep6
PASS		
0	draw_indx_c	2002Sep6
PASS		
0	draw_indx_indr1_a	2002Sep6
PASS		
0	draw_indx_indr1_b	2002Sep6
PASS		
0	draw_indx_indr1_short_a	2002Sep6
PASS		
0	draw_indx_indr1_short_b	2002Sep6
PASS		
0	draw_indx_indr1_stress	2002Sep6
PASS		
0	draw_indx_indr2_a	2002Sep6
PASS		
0	draw_indx_indr2_b	2002Sep6
PASS		
0	draw_indx_indr2_short_a	2002Sep6
PASS		
0	draw_indx_indr2_short_b	2002Sep6
PASS		
0	draw_indx_indr2_stress	2002Sep6
PASS		
0	draw_indx_ring1	2002Sep6
PASS		
0	draw_indx_ring2	2002Sep6
PASS		
0	draw_indx_ring_short1	2002Sep6
PASS		
0	draw_indx_ring_short2	2002Sep6
PASS		
0	draw_indx_ring_stress	2002Sep6

PASS		
0	draw_indx_short_a	2002Sep6
PASS		
0	draw_indx_short_b	2002Sep6
PASS		
0	draw_indx_short_c	2002Sep6
PASS		
0	draw_indx_stress_a	2002Sep6
PASS		
0	draw_indx_stress_b	2002Sep6
PASS		
0	draw_indx_stress_c	2002Sep6
PASS		
0	event_timestamp_write	2002Sep6
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep6
PASS		
0	ft_indr1_july22	2002Sep6
PASS		
0	ft_indr2_july22	2002Sep6
PASS		
0	ft_july22	2002Sep6
PASS		
0	ft_july22_random	2002Sep6
PASS		
0	header_dump	2002Sep6
PASS		
0	header_dump_rt	2002Sep6
PASS		
0	header_dump_small	2002Sep6
PASS		
0	hostdata_blt	2002Sep6
PASS		
0	im_load_a	2002Sep6
PASS		
0	im_load_b	2002Sep6
PASS		
0	im_load_cid_mismatch	2002Sep6
PASS		
0	im_load_immediate	2002Sep6
PASS		
0	im_load_immediate_nrt_me	2002Sep6
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep6
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep6
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep6

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep6
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep6
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep6
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep6
PASS		
0	im_load_indr1_a	2002Sep6
PASS		
0	im_load_indr1_b	2002Sep6
PASS		
0	im_load_indr2_a	2002Sep6
PASS		
0	im_load_indr2_b	2002Sep6
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0	im_load_invalidate	2002Sep6
PASS		
0	im_load_match	2002Sep6
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0	im_load_match2	2002Sep6
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0	im_load_nrt_me	2002Sep6
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep6
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0	im_load_nrt_me_ps_wrap	2002Sep6
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep6
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep6
PASS		
0	im_load_pfmatch_disable	2002Sep6
PASS		
0	im_load_ring_a	2002Sep6
PASS		
0	im_load_ring_b	2002Sep6
PASS		
0	im_load_rt	2002Sep6
PASS		
0	im_load_rt2	2002Sep6
PASS		
0	im_load_rt3	2002Sep6
PASS		
0	im_load_rt4	2002Sep6
PASS		
0	im_load_sz_mismatch	2002Sep6

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0	incremental_update_const	2002Sep6
PASS		
0	incremental_update_instr	2002Sep6
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0	incremental_update_state	2002Sep6
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0	indirect_buffer_a	2002Sep6
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0	indirect_buffer_b	2002Sep6
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0	indirect_buffer_c	2002Sep6
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0	indirect_buffer_d	2002Sep6
PASS		
0	indirect_buffer_e	2002Sep6
PASS		
0	indirect_buffer_f	2002Sep6
PASS		
0	indirect_buffer_g	2002Sep6
PASS		
0	indirect_buffer_h	2002Sep6
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0	indirect_buffer_i	2002Sep6
PASS		
0	indirect_buffer_j	2002Sep6
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0	indirect_buffer_k	2002Sep6
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0	indirect_buffer_l	2002Sep6
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0	indirect_buffer_mp_a	2002Sep6
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0	indirect_buffer_mp_b	2002Sep6
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0	indirect_buffer_mp_c	2002Sep6
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0	indirect_buffer_mp_d	2002Sep6
PASS		
0	indirect_buffer_mp_e	2002Sep6
PASS		
0	indirect_buffer_mp_f	2002Sep6
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0	invalidate_on_me_init	2002Sep6
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0	invalidate_state_constants	2002Sep6
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0	invalidate_state_shaders	2002Sep6

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0	invalidate_state_shaders2	2002Sep6
PASS		
0	invalidate_state_sublks	2002Sep6
PASS		
0	invalidate_state_sublks2	2002Sep6
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0	invalid_opcode	2002Sep6
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0	iq1_iq2_mult	2002Sep6
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0	iq1_iq2_push_wrap	2002Sep6
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0	iq2_csq_full	2002Sep6
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0	IQ2_Mult	2002Sep6
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0	IQ2_Wrap_Legacy	2002Sep6
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0	IQ_Wrap	2002Sep6
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0	ld_cnst_cntx_coherency_cntrl	2002Sep6
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep6
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0	ld_cnst_cntx_indr1_replicate	2002Sep6
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0	ld_cnst_cntx_indr1_replicate4	2002Sep6
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep6
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0	ld_cnst_cntx_indr1_simple	2002Sep6
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep6
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep6
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep6
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep6
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0	ld_cnst_cntx_match	2002Sep6
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0	ld_cnst_cntx_match_valid_preserved	2002Sep6
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0	ld_cnst_cntx_mismatch_alutex	2002Sep6
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0	ld_cnst_cntx_mismatch_base	2002Sep6

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0	ld_cnst_cntx_mismatch_force	2002Sep6
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0	ld_cnst_cntx_pfmacth_disable	2002Sep6
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0	ld_cnst_cntx_replicate4_alu	2002Sep6
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0	ld_cnst_cntx_replicate4_tex	2002Sep6
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0	ld_cnst_cntx_replicate_alu	2002Sep6
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0	ld_cnst_cntx_ring_replicate	2002Sep6
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0	ld_cnst_cntx_ring_replicate4	2002Sep6
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0	ld_cnst_cntx_ring_replicate8	2002Sep6
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0	ld_cnst_cntx_ring_simple	2002Sep6
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0	ld_cnst_cntx_simple_tex	2002Sep6
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0	load_constant_context_rt	2002Sep6
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0	load_palette	2002Sep6
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0	lp_2d3d	2002Sep6
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0	mem_write	2002Sep6
PASS		
0	mem_write_rt	2002Sep6
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0	mpeg_index	2002Sep6
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0	mpeg_index_multi_context_max_context_0	2002Sep6
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0	mpeg_index_multi_context_max_context_1	2002Sep6
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0	mpeg_index_multi_context_max_context_2	2002Sep6
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep6
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep6

PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep6
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep6
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep6
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0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep6
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0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep6
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0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep6
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0	nop_eating_disabled	2002Sep6
PASS		
0	paint_multi	2002Sep6
PASS		
0	pitch_clip	2002Sep6
PASS		
0	ply_nextscan	2002Sep6
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0	rbbm_cp_non_queued_rdwr_01	2002Sep5
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep5
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep5
PASS		
0	rbbm_cp_queued_rdwr	2002Sep5
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep5
PASS		
0	rbbm_isync_wait_idlegui	2002Sep5
PASS		
0	rbbm_soft_reset	2002Sep6
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0	rbbm_WU_2D_idle_01	2002Sep6
PASS		
0	rbbm_WU_2D_idle_02	2002Sep6
PASS		
0	rbbm_WU_2D_idle_03	2002Sep6

PASS		
0	rbbm_WU_2D_idle_04	2002Sep6
PASS		
0	rbbm_WU_2D_idle_05	2002Sep6
PASS		
0	rbbm_WU_2D_idle_06	2002Sep6
PASS		
0	rbbm_WU_2D_idle_07	2002Sep6
PASS		
0	rbbm_WU_2D_idle_08	2002Sep6
PASS		
0	rbbm_WU_2D_idle_09	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep6
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep6
PASS		
0	rbbm_WU_3D_idle_01	2002Sep6
PASS		
0	rbbm_WU_3D_idle_02	2002Sep6
PASS		
0	rbbm_WU_3D_idle_03	2002Sep6
PASS		
0	rbbm_WU_3D_idle_04	2002Sep6
PASS		
0	rbbm_WU_3D_idle_05	2002Sep6
PASS		
0	rbbm_WU_3D_idle_06	2002Sep6

PASS		
0	rbbm_WU_3D_idle_07	2002Sep6
PASS		
0	rbbm_WU_3D_idle_08	2002Sep6
PASS		
0	rbbm_WU_3D_idle_09	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep6
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep6
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep6
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep6
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep6
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep6
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep6
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep6
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep6
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep6
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep6

PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep6
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep6
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep6
PASS		
0	rbbm_WU_idct_semaphore	2002Sep6
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep6
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep6
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep6
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep6
PASS		
365	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep6
FAIL	cp_rbbm.cmp	
366	rbbm_WUrt_BOTH_GRPH_update_pending_off	2002Sep6
FAIL	cp_rbbm.cmp	
367	rbbm_WUrt_CP_dma_idle	2002Sep6
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
368	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep6
FAIL	cp_rbbm.cmp	
369	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep6
FAIL	cp_rbbm.cmp	
370	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep6
FAIL	cp_rbbm.cmp	
371	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep6
FAIL	cp_rbbm.cmp	
372	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep6
FAIL	cp_rbbm.cmp	
373	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep6
FAIL	cp_rbbm.cmp	
374	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep6
FAIL	cp_rbbm.cmp	
375	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep6
FAIL	cp_rbbm.cmp	
376	rbbm_WUrt_MH_hdp_clean	2002Sep6
FAIL	cp_rbbm.cmp	
377	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep6
FAIL	cp_rbbm.cmp	
378	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep6
FAIL	cp_rbbm.cmp	
379	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep6
FAIL	cp_rbbm.cmp	
380	rbbm_WUrt_RT_idle_01	2002Sep6

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**FAIL**      cp_rbbm.cmp
381  rbbm_WUrt_RT_idle_02                2002Sep6
**FAIL**      cp_rbbm.cmp
382  rbbm_WUrt_RT_idle_03                2002Sep6
**FAIL**      cp_rbbm.cmp
383  rbbm_WUrt_RT_idle_04                2002Sep5
**FAIL**      cp_rbbm.cmp
384  rbbm_WUrt_RT_idle_clean_01          2002Sep6
**FAIL**      cp_rbbm.cmp
385  rbbm_WUrt_RT_idle_clean_02          2002Sep6
**FAIL**      cp_rbbm.cmp
386  rbbm_WUrt_RT_idle_clean_03          2002Sep6
**FAIL**      cp_rbbm.cmp
387  rbbm_WUrt_RT_idle_clean_04          2002Sep6
**FAIL**      cp_rbbm.cmp
388  rbbm_WUrt_RT_idle_clean_05          2002Sep6
**FAIL**      cp_rbbm.cmp
389  rbbm_WUrt_VIPH0_dma_idle            2002Sep6
**FAIL**      cp_rbbm.cmp
390  rbbm_WUrt_VIPH1_dma_idle            2002Sep6
**FAIL**      cp_rbbm.cmp
391  rbbm_WUrt_VIPH2_dma_idle            2002Sep6
**FAIL**      cp_rbbm.cmp
392  rbbm_WUrt_VIPH3_dma_idle            2002Sep6
**FAIL**      cp_rbbm.cmp
0    rbbm_WU_VIPH0_dma_idle              2002Sep6
PASS
0    rbbm_WU_VIPH1_dma_idle              2002Sep6
PASS
0    rbbm_WU_VIPH2_dma_idle              2002Sep6
PASS
0    rbbm_WU_VIPH3_dma_idle              2002Sep6
PASS
0    RB_BUFSZ_0                          2002Sep6
PASS
0    RB_BUFSZ_1                          2002Sep6
PASS
0    RB_Wrap                              2002Sep6
PASS
0    reg_to_mem                           2002Sep6
PASS
0    reg_to_mem_rt                        2002Sep6
PASS
0    rop_cfd_wmd                          2002Sep6
PASS
0    rstr_ordr                            2002Sep6
PASS
0    rtee_arm_wait_idle_trigger_events_16 2002Sep6

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PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep6
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep6
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0	rtee_discretes_falling_07_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep6
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0	rtee_discretes_falling_10_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep6
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep6
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0	rtee_discretes_falling_14_events_16	2002Sep6
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0	rtee_discretes_falling_15_events_16	2002Sep6
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0	rtee_discretes_falling_22_events_16	2002Sep6

PASS		
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PASS		
0	rtee_discretes_falling_24_events_16	2002Sep6
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PASS		
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0	rtee_discretes_falling_30_events_16	2002Sep6
PASS		
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0	rtee_discretes_invert_00_events_16	2002Sep6
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0	rtee_discretes_invert_02_events_16	2002Sep6
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0	rtee_discretes_invert_04_events_16	2002Sep6
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0	rtee_discretes_invert_08_events_16	2002Sep6
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0	rtee_discretes_invert_09_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep6

PASS		
0	rtee_discretes_invert_15_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep6
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep6
PASS		
0	rtee_discretes_level_00_events_16	2002Sep6
PASS		
0	rtee_discretes_level_01_events_16	2002Sep6
PASS		
0	rtee_discretes_level_02_events_16	2002Sep6
PASS		
0	rtee_discretes_level_03_events_16	2002Sep6
PASS		
0	rtee_discretes_level_04_events_16	2002Sep6
PASS		
0	rtee_discretes_level_05_events_16	2002Sep6
PASS		
0	rtee_discretes_level_06_events_16	2002Sep6

PASS		
0	rtee_discretes_level_07_events_16	2002Sep6
PASS		
0	rtee_discretes_level_08_events_16	2002Sep6
PASS		
0	rtee_discretes_level_09_events_16	2002Sep6
PASS		
0	rtee_discretes_level_10_events_16	2002Sep6
PASS		
0	rtee_discretes_level_11_events_16	2002Sep6
PASS		
0	rtee_discretes_level_12_events_16	2002Sep6
PASS		
0	rtee_discretes_level_13_events_16	2002Sep6
PASS		
0	rtee_discretes_level_14_events_16	2002Sep6
PASS		
0	rtee_discretes_level_15_events_16	2002Sep6
PASS		
0	rtee_discretes_level_16_events_16	2002Sep6
PASS		
0	rtee_discretes_level_17_events_16	2002Sep6
PASS		
0	rtee_discretes_level_18_events_16	2002Sep6
PASS		
0	rtee_discretes_level_19_events_16	2002Sep6
PASS		
0	rtee_discretes_level_20_events_16	2002Sep6
PASS		
0	rtee_discretes_level_21_events_16	2002Sep6
PASS		
0	rtee_discretes_level_22_events_16	2002Sep6
PASS		
0	rtee_discretes_level_23_events_16	2002Sep6
PASS		
0	rtee_discretes_level_24_events_16	2002Sep6
PASS		
0	rtee_discretes_level_25_events_16	2002Sep6
PASS		
0	rtee_discretes_level_26_events_16	2002Sep6
PASS		
0	rtee_discretes_level_27_events_16	2002Sep6
PASS		
0	rtee_discretes_level_28_events_16	2002Sep6
PASS		
0	rtee_discretes_level_29_events_16	2002Sep6
PASS		
0	rtee_discretes_level_30_events_16	2002Sep6

PASS		
0	rtee_discretes_level_31_events_16	2002Sep6
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep6

PASS		
0	rtee_discretes_rising_22_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep6
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep6
PASS		
0	rtee_display1_comp_events_16	2002Sep6
PASS		
0	rtee_display2_comp_events_16	2002Sep6
PASS		
0	rtee_mem_linear_search_events_2	2002Sep6
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep6
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep6
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep6

PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep6
PASS		
0	rtee_prefetch_parse_nops	2002Sep6
PASS		
0	rtee_reg_linear_search_events_2	2002Sep6
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep6
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep6
PASS		
0	rtee_vip_comp_events_16	2002Sep6
PASS		
0	rt_invalid_opcode	2002Sep6
PASS		
0	scratch_mem_read	2002Sep6
PASS		
0	scratch_mem_read2	2002Sep6
PASS		
0	set_constant	2002Sep6
PASS		
0	set_constant2	2002Sep6
PASS		
0	set_constant_alu_max1	2002Sep6
PASS		
0	set_constant_alu_max2	2002Sep6
PASS		
0	set_constant_bool_max1	2002Sep6
PASS		
0	set_constant_bool_max2	2002Sep6
PASS		
0	set_constant_indr1_short	2002Sep6
PASS		
0	set_constant_indr1_short2	2002Sep6

PASS		
0	set_constant_indr1_write_enable	2002Sep6
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep6
PASS		
0	set_constant_indr2_short	2002Sep6
PASS		
0	set_constant_indr2_short2	2002Sep6
PASS		
0	set_constant_indr2_write_enable	2002Sep6
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep6
PASS		
0	set_constant_loop_max1	2002Sep6
PASS		
0	set_constant_loop_max2	2002Sep6
PASS		
0	set_constant_ring_short	2002Sep6
PASS		
0	set_constant_ring_short2	2002Sep6
PASS		
0	set_constant_ring_write_enable	2002Sep6
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep6
PASS		
0	set_constant_short	2002Sep6
PASS		
0	set_constant_short2	2002Sep6
PASS		
0	set_constant_tex_max1	2002Sep6
PASS		
0	set_constant_tex_max2	2002Sep6
PASS		
0	set_constant_write_enable	2002Sep6
PASS		
0	set_constant_write_enable_lcc	2002Sep6
PASS		
0	set_constant_write_enable_lcc2	2002Sep6
PASS		
0	set_scissors	2002Sep6
PASS		
0	set_state_rt_a	2002Sep6
PASS		
0	set_state_rt_b	2002Sep6
PASS		
0	set_state_rt_c	2002Sep6
PASS		
0	set_state_rt_d	2002Sep6

PASS		
0	set_st_indrl_match	2002Sep6
PASS		
0	set_st_indrl_mismatch	2002Sep6
PASS		
0	set_st_indrl_mismatch2	2002Sep6
PASS		
0	set_st_indrl_mismatch3	2002Sep6
PASS		
0	set_st_indrl_mismatch4	2002Sep6
PASS		
0	set_st_indrl_ps	2002Sep6
PASS		
0	set_st_indrl_ps_only	2002Sep6
PASS		
0	set_st_indrl_sublk0	2002Sep6
PASS		
0	set_st_indrl_sublk1	2002Sep6
PASS		
0	set_st_indrl_sublk1_only	2002Sep6
PASS		
0	set_st_indrl_sublk2	2002Sep6
PASS		
0	set_st_indrl_sublk2_only	2002Sep6
PASS		
0	set_st_indrl_sublk3	2002Sep6
PASS		
0	set_st_indrl_sublk3_only	2002Sep6
PASS		
0	set_st_indrl_sublk4	2002Sep6
PASS		
0	set_st_indrl_sublk4_only	2002Sep6
PASS		
0	set_st_indrl_sublk5	2002Sep6
PASS		
0	set_st_indrl_sublk5_only	2002Sep6
PASS		
0	set_st_indrl_sublk6	2002Sep6
PASS		
0	set_st_indrl_sublk6_only	2002Sep6
PASS		
0	set_st_indrl_sublk7	2002Sep6
PASS		
0	set_st_indrl_sublk7_only	2002Sep6
PASS		
0	set_st_indrl_vs	2002Sep6
PASS		
0	set_st_indrl_vs_only	2002Sep6

PASS		
0	set_st_indr2_match	2002Sep6
PASS		
0	set_st_indr2_mismatch	2002Sep6
PASS		
0	set_st_indr2_mismatch2	2002Sep6
PASS		
0	set_st_indr2_mismatch3	2002Sep6
PASS		
0	set_st_indr2_mismatch4	2002Sep6
PASS		
0	set_st_indr2_ps	2002Sep6
PASS		
0	set_st_indr2_ps_only	2002Sep6
PASS		
0	set_st_indr2_sublk0	2002Sep6
PASS		
0	set_st_indr2_sublk1	2002Sep6
PASS		
0	set_st_indr2_sublk1_only	2002Sep6
PASS		
0	set_st_indr2_sublk2	2002Sep6
PASS		
0	set_st_indr2_sublk2_only	2002Sep6
PASS		
0	set_st_indr2_sublk3	2002Sep6
PASS		
0	set_st_indr2_sublk3_only	2002Sep6
PASS		
0	set_st_indr2_sublk4	2002Sep6
PASS		
0	set_st_indr2_sublk4_only	2002Sep6
PASS		
0	set_st_indr2_sublk5	2002Sep6
PASS		
0	set_st_indr2_sublk5_only	2002Sep6
PASS		
0	set_st_indr2_sublk6	2002Sep6
PASS		
0	set_st_indr2_sublk6_only	2002Sep6
PASS		
0	set_st_indr2_sublk7	2002Sep6
PASS		
0	set_st_indr2_sublk7_only	2002Sep6
PASS		
0	set_st_indr2_vs	2002Sep6
PASS		
0	set_st_indr2_vs_only	2002Sep6

PASS		
0	set_st_pfmatch_disabled	2002Sep6
PASS		
0	set_st_ring_match	2002Sep6
PASS		
0	set_st_ring_mismatch	2002Sep6
PASS		
0	set_st_ring_mismatch2	2002Sep6
PASS		
0	set_st_ring_mismatch3	2002Sep6
PASS		
0	set_st_ring_mismatch4	2002Sep6
PASS		
0	set_st_ring_ps	2002Sep6
PASS		
0	set_st_ring_ps_only	2002Sep6
PASS		
0	set_st_ring_subblk0	2002Sep6
PASS		
0	set_st_ring_subblk1	2002Sep6
PASS		
0	set_st_ring_subblk1_only	2002Sep6
PASS		
0	set_st_ring_subblk2	2002Sep6
PASS		
0	set_st_ring_subblk2_only	2002Sep6
PASS		
0	set_st_ring_subblk3	2002Sep6
PASS		
0	set_st_ring_subblk3_only	2002Sep6
PASS		
0	set_st_ring_subblk4	2002Sep6
PASS		
0	set_st_ring_subblk4_only	2002Sep6
PASS		
0	set_st_ring_subblk5	2002Sep6
PASS		
0	set_st_ring_subblk5_only	2002Sep6
PASS		
0	set_st_ring_subblk6	2002Sep6
PASS		
0	set_st_ring_subblk6_only	2002Sep6
PASS		
0	set_st_ring_subblk7	2002Sep6
PASS		
0	set_st_ring_subblk7_only	2002Sep6
PASS		
0	set_st_ring_vs	2002Sep6

PASS		
0	set_st_ring_vs_only	2002Sep6
PASS		
0	set_st_stress_a	2002Sep6
PASS		
0	set_st_stress_b	2002Sep6
PASS		
0	set_st_stress_c	2002Sep6
PASS		
0	set_st_stress_d	2002Sep6
PASS		
0	set_st_stress_e	2002Sep6
PASS		
0	set_st_stress_f	2002Sep6
PASS		
0	Small_Text_BM	2002Sep6
PASS		
0	smoke_nrt	2002Sep6
PASS		
0	src_clip	2002Sep6
PASS		
0	srf_cohr	2002Sep6
PASS		
0	trans_bitblt	2002Sep6
PASS		
0	type0_2long_pckts	2002Sep6
PASS		
0	type0_3indr_buffs	2002Sep6
PASS		
0	type0_4pckts	2002Sep6
PASS		
0	type0_incr_updt_cnst	2002Sep6
PASS		
0	type0_incr_updt_instrmem	2002Sep6
PASS		
0	type0_incr_updt_st	2002Sep6
PASS		
0	type0_incr_updt_stress	2002Sep6
PASS		
0	type0_ind1_ind2_a	2002Sep6
PASS		
0	type0_ind1_ind2_b	2002Sep6
PASS		
0	type0_indr_buff	2002Sep6
PASS		
0	Type0_IQ2_BM	2002Sep6
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep6

PASS		
0	Type0_IQ_BM	2002Sep6
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep6
PASS		
0	Type0_PQ_BM	2002Sep6
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep6
PASS		
0	typel_4pckts	2002Sep6
PASS		
0	typel_indr_buff	2002Sep6
PASS		
0	typel_pckt	2002Sep6
PASS		
0	Type3_Bitblt	2002Sep6
PASS		
0	Type3_Load_Palette	2002Sep6
PASS		
0	Type3_NOP	2002Sep6
PASS		
0	type_random	2002Sep6
PASS		
704	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
705	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep6
PASS		
0	uram_wrt_ucode	2002Sep6
PASS		
0	viz_query_a	2002Sep6
PASS		
0	viz_query_draw_indx	2002Sep6
PASS		
0	viz_query_draw_indx_indr1	2002Sep6
PASS		
0	viz_query_draw_indx_indr2	2002Sep6
PASS		
712	viz_query_draw_indx_multipass	2002Sep6
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_draw_indx_ring	2002Sep6
PASS		
0	viz_query_indr1	2002Sep6
PASS		
0	viz_query_indr1_simple	2002Sep6
PASS		
0	viz_query_indr2	2002Sep6

PASS
0 viz_query_indr2_simple 2002Sep6
PASS
0 viz_query_ring 2002Sep6
PASS
0 viz_query_ring_simple 2002Sep6
PASS
0 wait_for_idle 2002Sep6
PASS
0 wait_reg_mem_poll_mem0 2002Sep6
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep6
PASS
0 wait_reg_mem_poll_mem1 2002Sep6
PASS
0 wait_reg_mem_poll_mem2 2002Sep6
PASS
0 wait_reg_mem_poll_mem3 2002Sep6
PASS
0 wait_reg_mem_poll_reg0 2002Sep6
PASS
0 wait_reg_mem_poll_reg1 2002Sep6
PASS
0 wait_reg_mem_poll_reg2 2002Sep6
PASS
0 wait_reg_mem_poll_reg3 2002Sep6
PASS

=====
Tests_Run_count = 729 Pass_count = 697 Fail_count = 32 Incomplete_count = 0
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct4
	PASS		
0	2d_indr_buffer_b		2002Oct4
	PASS		
0	2d_indr_buffer_c		2002Oct4
	PASS		
0	2d_indr_buffer_d		2002Oct4
	PASS		
0	2d_indr_buffer_e		2002Oct4
	PASS		
0	2d_indr_buffer_f		2002Oct4
	PASS		
0	2d_indr_buffer_g		2002Oct4
	PASS		
0	2d_indr_buffer_random		2002Oct4
	PASS		
9	2d_indr_buffer_random2		2002Sep29
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Oct4
	PASS		
0	3d_draw_indx_2_b		2002Oct4
	PASS		
0	IQ2_Mult		2002Oct4
	PASS		
0	IQ2_Wrap_Legacy		2002Oct4
	PASS		
0	IQ_Wrap		2002Oct4
	PASS		
0	Isync_Control		2002Oct4
	PASS		
0	Isync_Control_2D_3D		2002Oct4
	PASS		
0	Isync_Control_3D_2D		2002Oct4
	PASS		
0	RB_BUFSZ_0		2002Oct4
	PASS		
0	RB_BUFSZ_1		2002Oct4
	PASS		
0	RB_Wrap		2002Oct4

PASS		
0	Small_Text_BM	2002Oct4
PASS		
0	Type0_IQ2_BM	2002Oct4
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Oct4
PASS		
0	Type0_IQ_BM	2002Oct4
PASS		
0	Type0_IQ_BM_IB_Packet	2002Oct4
PASS		
0	Type0_PQ_BM	2002Oct4
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Oct4
PASS		
0	Type3_Bitblt	2002Oct4
PASS		
0	Type3_Hostdata_blt	2002Oct4
PASS		
0	Type3_Hostdata_blt_PIO	2002Oct4
PASS		
0	Type3_Load_Palette	2002Oct4
PASS		
0	Type3_NOP	2002Oct4
PASS		
0	Type3_Polyline	2002Oct4
PASS		
0	Type3_Polyline_Large	2002Oct4
PASS		
0	bitblt_multi	2002Oct4
PASS		
0	brush_32x1mono	2002Oct4
PASS		
0	brush_8x8color_16	2002Oct4
PASS		
0	brush_8x8color_32	2002Oct4
PASS		
0	brush_8x8color_64	2002Oct4
PASS		
0	brush_8x8color_dst_0_1	2002Oct4
PASS		
0	brush_8x8mono	2002Oct4
PASS		
0	brush_color_mono_stress	2002Oct4
PASS		
0	brush_flag	2002Oct4
PASS		
0	brush_type0	2002Oct4

PASS		
0	brush_type1	2002Oct4
PASS		
0	brush_type10	2002Oct4
PASS		
0	brush_type13	2002Oct4
PASS		
0	brush_type14	2002Oct4
PASS		
0	brush_type6	2002Oct4
PASS		
0	brush_type7	2002Oct4
PASS		
0	brush_type_other	2002Oct4
PASS		
0	brush_types_13_14	2002Oct4
PASS		
0	change_queue_sizes	2002Oct4
PASS		
0	clear_const_wr_en_on_me_init	2002Oct4
PASS		
0	clear_const_wr_en_with_lcc	2002Oct4
PASS		
0	cond_write_mem_poll_mem	2002Oct4
PASS		
0	cond_write_mem_poll_mem2	2002Oct4
PASS		
0	cond_write_mem_poll_mem3	2002Oct4
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct4
PASS		
0	cond_write_mem_poll_reg	2002Oct4
PASS		
0	cond_write_mem_poll_reg2	2002Oct4
PASS		
0	cond_write_mem_poll_reg3	2002Oct4
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct4
PASS		
0	cp_bios_scratch	2002Oct4
PASS		
0	cp_high_stress	2002Oct4
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct4
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct4
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct4

PASS		
0	cp_ib2_base011_bufszXXX	2002Oct4
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct4
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct4
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct4
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct4
PASS		
0	cp_ib_base000_bufszXXX	2002Oct4
PASS		
0	cp_ib_base001_bufszXXX	2002Oct4
PASS		
0	cp_ib_base010_bufszXXX	2002Oct4
PASS		
0	cp_ib_base011_bufszXXX	2002Oct4
PASS		
0	cp_ib_base100_bufszXXX	2002Oct4
PASS		
0	cp_ib_base101_bufszXXX	2002Oct4
PASS		
0	cp_ib_base110_bufszXXX	2002Oct4
PASS		
0	cp_ib_base111_bufszXXX	2002Oct4
PASS		
0	cp_interrupt	2002Oct4
PASS		
0	cp_interrupt_rt_events_16	2002Oct4
PASS		
0	cp_iq2_alignment_000	2002Oct4
PASS		
0	cp_iq2_alignment_001	2002Oct4
PASS		
0	cp_iq2_alignment_010	2002Oct4
PASS		
0	cp_iq2_alignment_011	2002Oct4
PASS		
0	cp_iq2_alignment_100	2002Oct4
PASS		
0	cp_iq2_alignment_101	2002Oct4
PASS		
0	cp_iq2_alignment_110	2002Oct4
PASS		
0	cp_iq2_alignment_111	2002Oct4
PASS		
0	cp_iq_alignment_000	2002Oct4

PASS		
0	cp_iq_alignment_001	2002Oct4
PASS		
0	cp_iq_alignment_010	2002Oct4
PASS		
0	cp_iq_alignment_011	2002Oct4
PASS		
0	cp_iq_alignment_100	2002Oct4
PASS		
0	cp_iq_alignment_101	2002Oct4
PASS		
0	cp_iq_alignment_110	2002Oct4
PASS		
0	cp_iq_alignment_111	2002Oct4
PASS		
0	cp_max_size_packet	2002Oct4
PASS		
0	cp_max_size_packet_random	2002Oct4
PASS		
0	cp_nrt_rt_protection_mode	2002Oct4
PASS		
0	cp_output_stress	2002Oct4
PASS		
0	cp_output_stress_random	2002Oct4
PASS		
0	cp_protection_mode	2002Oct4
PASS		
0	cp_rb_alignment_000	2002Oct4
PASS		
0	cp_rb_alignment_001	2002Oct4
PASS		
0	cp_rb_alignment_010	2002Oct4
PASS		
0	cp_rb_alignment_011	2002Oct4
PASS		
0	cp_rb_alignment_100	2002Oct4
PASS		
0	cp_rb_alignment_101	2002Oct4
PASS		
0	cp_rb_alignment_110	2002Oct4
PASS		
0	cp_rb_alignment_111	2002Oct4
PASS		
0	cp_rb_wptrXXX_base000	2002Oct4
PASS		
0	cp_rb_wptrXXX_base001	2002Oct4
PASS		
0	cp_rb_wptrXXX_base010	2002Oct4

PASS		
0	cp_rb_wptrXXX_base011	2002Oct4
PASS		
0	cp_rb_wptrXXX_base100	2002Oct4
PASS		
0	cp_rb_wptrXXX_base101	2002Oct4
PASS		
0	cp_rb_wptrXXX_base110	2002Oct4
PASS		
0	cp_rb_wptrXXX_base111	2002Oct4
PASS		
0	cp_reg_rw_test	2002Oct4
PASS		
0	cp_register_reads	2002Oct4
PASS		
0	cp_rt_protection_mode	2002Oct4
PASS		
0	cp_scratch	2002Oct4
PASS		
0	cp_scratch_multi	2002Oct4
PASS		
0	csf_reg_rd_default	2002Oct4
PASS		
128	csf_reg_rd_only	2002Oct4
FAIL		
0	csf_reg_wr_rd	2002Oct4
PASS		
0	dma_cp_sync	2002Oct4
PASS		
0	dma_dscrstr_chckn_bit	2002Oct4
PASS		
0	dma_dscrstr_flush1	2002Oct4
PASS		
0	dma_dscrstr_flush2	2002Oct4
PASS		
0	dma_dscrstr_flush3	2002Oct4
PASS		
0	dma_m2m_src00_dstXX	2002Oct4
PASS		
0	dma_m2m_src01_dstXX	2002Oct4
PASS		
0	dma_m2m_src10_dstXX	2002Oct4
PASS		
0	dma_m2m_src11_dstXX	2002Oct4
PASS		
0	dma_mem2mem	2002Oct4
PASS		
0	dma_mem2mem_abort	2002Oct4

PASS		
0	dma_mem2mem_alignments	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment1	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment2	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment3	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment4	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct4
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct4
PASS		
0	dma_mem2mem_large	2002Oct4
PASS		
0	dma_mem2mem_pio_abort	2002Oct4
PASS		
0	dma_mem2mem_random	2002Oct4
PASS		
0	dma_mem2reg	2002Oct4
PASS		
0	dma_mem2reg_abort	2002Oct4
PASS		
0	dma_mem2reg_dtaq_stress	2002Oct4
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Oct4
PASS		
0	dma_mem2reg_pio_stressbm	2002Oct4
PASS		
0	dma_mem2reg_pio_stresspio	2002Oct4
PASS		
0	dma_mem2reg_sync	2002Oct4
PASS		
0	dma_r2r_m2m_r2r	2002Oct4
PASS		
0	dma_r2r_m2r_r2r	2002Oct4
PASS		
0	dma_r2r_r2m_r2r	2002Oct4
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct4
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct4

PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct4
PASS		
0	dma_reg2mem	2002Oct4
PASS		
0	dma_reg2mem_abort	2002Oct4
PASS		
0	dma_reg2reg	2002Oct4
PASS		
0	dma_reg2reg_2dw	2002Oct4
PASS		
0	dma_reg2reg_daic	2002Oct4
PASS		
0	dma_reg2reg_mult	2002Oct4
PASS		
0	dma_reg2reg_mult2	2002Oct4
PASS		
0	dma_reg2reg_mult3	2002Oct4
PASS		
0	dma_reg2reg_saic	2002Oct4
PASS		
0	dma_reg2reg_sdaic	2002Oct4
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct4
PASS		
0	dma_reg2reg_stress	2002Oct4
PASS		
0	draw_indx_a	2002Oct4
PASS		
0	draw_indx_b	2002Oct4
PASS		
0	draw_indx_c	2002Oct4
PASS		
0	draw_indx_indr1_a	2002Oct4
PASS		
0	draw_indx_indr1_b	2002Oct4
PASS		
0	draw_indx_indr1_short_a	2002Oct4
PASS		
0	draw_indx_indr1_short_b	2002Oct4
PASS		
0	draw_indx_indr1_stress	2002Oct4
PASS		
0	draw_indx_indr2_a	2002Oct4
PASS		
0	draw_indx_indr2_b	2002Oct4
PASS		
0	draw_indx_indr2_short_a	2002Oct4

PASS		
0	draw_indx_indr2_short_b	2002Oct4
PASS		
0	draw_indx_indr2_stress	2002Oct4
PASS		
0	draw_indx_random_a	2002Oct4
PASS		
0	draw_indx_random_b	2002Oct4
PASS		
0	draw_indx_random_c	2002Oct4
PASS		
0	draw_indx_ring1	2002Oct4
PASS		
0	draw_indx_ring2	2002Oct4
PASS		
0	draw_indx_ring_short1	2002Oct4
PASS		
0	draw_indx_ring_short2	2002Oct4
PASS		
0	draw_indx_ring_stress	2002Oct4
PASS		
0	draw_indx_short_a	2002Oct4
PASS		
0	draw_indx_short_b	2002Oct4
PASS		
0	draw_indx_short_c	2002Oct4
PASS		
0	draw_indx_stress_a	2002Oct4
PASS		
0	draw_indx_stress_b	2002Oct4
PASS		
0	draw_indx_stress_c	2002Oct4
PASS		
0	event_timestamp_write	2002Oct4
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct4
PASS		
0	fix2flt_reg	2002Oct4
PASS		
0	ft_indr1_july22	2002Oct4
PASS		
0	ft_indr2_july22	2002Oct4
PASS		
0	ft_july22	2002Oct4
PASS		
0	ft_july22_random	2002Oct4
PASS		
0	header_dump	2002Oct4

PASS		
0	header_dump_rt	2002Oct4
PASS		
0	header_dump_small	2002Oct4
PASS		
0	hostdata_blt	2002Oct4
PASS		
0	im_load_a	2002Oct4
PASS		
0	im_load_b	2002Oct4
PASS		
0	im_load_cid_mismatch	2002Oct4
PASS		
0	im_load_immediate	2002Oct4
PASS		
0	im_load_immediate_nrt_me	2002Oct4
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct4
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct4
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct4
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct4
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct4
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct4
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct4
PASS		
0	im_load_indr1_a	2002Oct4
PASS		
0	im_load_indr1_b	2002Oct4
PASS		
0	im_load_indr2_a	2002Oct4
PASS		
0	im_load_indr2_b	2002Oct4
PASS		
0	im_load_invalidate	2002Oct4
PASS		
0	im_load_match	2002Oct4
PASS		
0	im_load_match2	2002Oct4
PASS		
0	im_load_nrt_me	2002Oct4
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct4

PASS		
0	im_load_nrt_me_ps_wrap	2002Oct4
PASS		
0	im_load_nrt_me_vs_nowrap	2002Oct4
PASS		
0	im_load_nrt_me_vs_wrap	2002Oct4
PASS		
0	im_load_pfmatch_disable	2002Oct4
PASS		
241	im_load_random_a	2002Oct4
FAIL	mh_cp_rd.cmp	
242	im_load_random_b	2002Oct4
FAIL	mh_cp_rd.cmp	
243	im_load_random_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Oct4
PASS		
0	im_load_ring_b	2002Oct4
PASS		
0	im_load_rt	2002Oct4
PASS		
0	im_load_rt2	2002Oct4
PASS		
0	im_load_rt3	2002Oct4
PASS		
0	im_load_rt4	2002Oct4
PASS		
0	im_load_sz_mismatch	2002Oct4
PASS		
0	incremental_update_const	2002Oct4
PASS		
0	incremental_update_instr	2002Oct4
PASS		
0	incremental_update_state	2002Oct4
PASS		
0	indirect_buffer_a	2002Oct4
PASS		
0	indirect_buffer_b	2002Oct4
PASS		
0	indirect_buffer_c	2002Oct4
PASS		
0	indirect_buffer_d	2002Oct4
PASS		
0	indirect_buffer_e	2002Oct4
PASS		
0	indirect_buffer_f	2002Oct4
PASS		
0	indirect_buffer_g	2002Oct4

PASS		
0	indirect_buffer_h	2002Oct4
PASS		
0	indirect_buffer_i	2002Oct4
PASS		
0	indirect_buffer_j	2002Oct4
PASS		
0	indirect_buffer_k	2002Oct4
PASS		
0	indirect_buffer_l	2002Oct4
PASS		
0	indirect_buffer_mp_a	2002Oct4
PASS		
0	indirect_buffer_mp_b	2002Oct4
PASS		
0	indirect_buffer_mp_c	2002Oct4
PASS		
0	indirect_buffer_mp_d	2002Oct4
PASS		
0	indirect_buffer_mp_e	2002Oct4
PASS		
0	indirect_buffer_mp_f	2002Oct4
PASS		
272	indirect_buffer_mp_random1	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
273	indirect_buffer_mp_random2	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
274	indirect_buffer_random1	2002Oct4
FAIL	mh_cp_rd.cmp	
275	indirect_buffer_random2	2002Oct4
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	2002Oct4
PASS		
0	invalidate_on_me_init	2002Oct4
PASS		
0	invalidate_state_constants	2002Oct4
PASS		
0	invalidate_state_shaders	2002Oct4
PASS		
0	invalidate_state_shaders2	2002Oct4
PASS		
0	invalidate_state_sublks	2002Oct4
PASS		
0	invalidate_state_sublks2	2002Oct4
PASS		
0	iq1_iq2_mult	2002Oct4
PASS		
0	iq1_iq2_push_wrap	2002Oct4

PASS		
0	iq2_csq_full	2002Oct4
PASS		
286	lcc_coherency_cntr_random	2002Sep29
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Oct4
PASS		
0	lcc_mismatch_alutex_random	2002Oct4
PASS		
0	lcc_mismatch_base_random	2002Oct4
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct4
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Oct4
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct4
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct4
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct4
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct4
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct4
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct4
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct4
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct4
PASS		
0	ld_cnst_cntx_match	2002Oct4
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct4
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct4
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct4
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct4
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct4
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct4
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct4
PASS		
0	ld_cnst_cntx_replicate8	2002Oct4

PASS		
0	ld_cnst_cntx_replicate_alu	20020ct4
PASS		
0	ld_cnst_cntx_replicate_tex	20020ct4
PASS		
0	ld_cnst_cntx_ring_replicate	20020ct4
PASS		
0	ld_cnst_cntx_ring_replicate4	20020ct4
PASS		
0	ld_cnst_cntx_ring_replicate8	20020ct4
PASS		
0	ld_cnst_cntx_ring_simple	20020ct4
PASS		
0	ld_cnst_cntx_simple_alu	20020ct4
PASS		
0	ld_cnst_cntx_simple_tex	20020ct4
PASS		
0	load_constant_context_rt	20020ct4
PASS		
0	load_palette	20020ct4
PASS		
0	lp_2d3d	20020ct4
PASS		
0	me_reg_wr_rd	20020ct4
PASS		
0	mem_write	20020ct4
PASS		
0	mem_write_rt	20020ct4
PASS		
0	mpeg_index	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_5	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_6	20020ct4
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct4
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct4

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct4
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct4
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct4
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct4
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct4
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct4
PASS		
0	nop_eating_disabled	2002Oct4
PASS		
0	nrt_rt_interaction1	2002Oct4
PASS		
0	paint_multi	2002Oct4
PASS		
0	paint_packet	2002Oct4
PASS		
0	paint_packet_gui_control	2002Oct4
PASS		
0	pfpr_reg_reads1	2002Oct4
PASS		
0	pfpr_reg_reads2	2002Oct4
PASS		
0	pitch_clip	2002Oct4
PASS		
0	ply_nextscan	2002Oct4
PASS		
0	rbbm_NQWU_BIF_01	2002Oct4
PASS		
0	rbbm_NQWU_BIF_02	2002Oct4
PASS		
0	rbbm_NQWU_CF_01	2002Oct4
PASS		
0	rbbm_NQWU_CPnq_01	2002Oct4
PASS		
0	rbbm_WU_2D_idle_01	2002Oct4
PASS		
0	rbbm_WU_2D_idle_02	2002Oct4
PASS		
0	rbbm_WU_2D_idle_03	2002Oct4
PASS		
0	rbbm_WU_2D_idle_04	2002Oct4
PASS		
0	rbbm_WU_2D_idle_05	2002Oct4

PASS		
0	rbbm_WU_2D_idle_06	2002Oct4
PASS		
0	rbbm_WU_2D_idle_07	2002Oct4
PASS		
0	rbbm_WU_2D_idle_08	2002Oct4
PASS		
0	rbbm_WU_2D_idle_09	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct4
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Oct4
PASS		
0	rbbm_WU_3D_idle_01	2002Oct4
PASS		
0	rbbm_WU_3D_idle_02	2002Oct4
PASS		
0	rbbm_WU_3D_idle_03	2002Oct4
PASS		
0	rbbm_WU_3D_idle_04	2002Oct4
PASS		
0	rbbm_WU_3D_idle_05	2002Oct4
PASS		
0	rbbm_WU_3D_idle_06	2002Oct4
PASS		
0	rbbm_WU_3D_idle_07	2002Oct4
PASS		
0	rbbm_WU_3D_idle_08	2002Oct4

PASS		
0	rbbm_WU_3D_idle_09	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct4
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct4
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct4
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Oct4
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct4
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct4
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct4
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct4
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct4
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct4
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct4
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct4
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct4

PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct4
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct4
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct4
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct4
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct4
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct4
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct4
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct4
PASS		
0	rbbm_WU_idct_semaphore	2002Oct4
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct4
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct4
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Oct4
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Oct4
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Oct4
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct4
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct4
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct4

PASS		
0	rbbm_WUrt_RT_idle_01	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_02	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_04	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Oct4
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Oct4
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Oct4
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Oct4
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Oct4
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Oct4
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Oct4
PASS		
0	rbbm_clk_active_deassert_min_max	2002Oct4
PASS		
0	rbbm_clk_active_each_pipe	2002Oct4
PASS		
0	rbbm_clk_active_for_read_error	2002Oct4
PASS		
0	rbbm_clk_active_for_read_request	2002Oct4
PASS		
0	rbbm_clk_active_on_reset	2002Oct4
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Oct4
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Oct4
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Oct4
PASS		
0	rbbm_cp_queued_rdwr	2002Oct4
PASS		
0	rbbm_hi_data_and_be_swap	2002Oct4

PASS		
0	rbbm_hi_data_endian_swap	2002Oct4
PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Oct4
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Oct4
PASS		
0	rbbm_int_ack	2002Oct4
PASS		
0	rbbm_int_each_client	2002Oct4
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Oct4
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Oct4
PASS		
0	rbbm_int_rderr	2002Oct4
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Oct4
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Oct4
PASS		
0	rbbm_isync_wait_idlegui	2002Oct4
PASS		
0	rbbm_soft_reset	2002Oct4
PASS		
0	rbbm_vgt_default_req	2002Oct4
PASS		
0	rbbm_vgt_max_req	2002Oct4
PASS		
0	rbbm_vgt_min_req	2002Oct4
PASS		
0	reg_to_mem	2002Oct4
PASS		
0	reg_to_mem_rt	2002Oct4
PASS		
0	ring_i1_pio_i2_bm	2002Oct4
PASS		
0	ring_pio_ibs_bm_stress	2002Oct4
PASS		
0	rop_cfd_wmd	2002Oct4
PASS		
0	roq_reg_reads	2002Oct4
PASS		
0	rstr_ordr	2002Oct4
PASS		
0	rt_invalid_opcode	2002Oct4
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct4

PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct4
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct4
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0	rtee_discretes_falling_11_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct4
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0	rtee_discretes_falling_16_events_16	2002Oct4
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0	rtee_discretes_falling_17_events_16	2002Oct4
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0	rtee_discretes_falling_18_events_16	2002Oct4
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0	rtee_discretes_falling_19_events_16	2002Oct4
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0	rtee_discretes_falling_20_events_16	2002Oct4
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0	rtee_discretes_falling_21_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct4

PASS		
0	rtee_discretes_falling_23_events_16	2002Oct4
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0	rtee_discretes_falling_24_events_16	2002Oct4
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0	rtee_discretes_falling_25_events_16	2002Oct4
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0	rtee_discretes_falling_26_events_16	2002Oct4
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0	rtee_discretes_falling_27_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct4
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0	rtee_discretes_falling_29_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct4
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct4
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0	rtee_discretes_invert_04_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct4
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0	rtee_discretes_invert_07_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct4
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0	rtee_discretes_invert_09_events_16	2002Oct4
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0	rtee_discretes_invert_10_events_16	2002Oct4
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0	rtee_discretes_invert_11_events_16	2002Oct4
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0	rtee_discretes_invert_12_events_16	2002Oct4
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0	rtee_discretes_invert_13_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct4

PASS		
0	rtee_discretes_invert_15_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct4
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0	rtee_discretes_invert_18_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct4
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0	rtee_discretes_invert_20_events_16	2002Oct4
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0	rtee_discretes_invert_21_events_16	2002Oct4
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0	rtee_discretes_invert_22_events_16	2002Oct4
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0	rtee_discretes_invert_23_events_16	2002Oct4
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0	rtee_discretes_invert_24_events_16	2002Oct4
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0	rtee_discretes_invert_25_events_16	2002Oct4
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0	rtee_discretes_invert_26_events_16	2002Oct4
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0	rtee_discretes_invert_27_events_16	2002Oct4
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0	rtee_discretes_invert_28_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct4
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct4
PASS		
0	rtee_discretes_level_00_events_16	2002Oct4
PASS		
0	rtee_discretes_level_01_events_16	2002Oct4
PASS		
0	rtee_discretes_level_02_events_16	2002Oct4
PASS		
0	rtee_discretes_level_03_events_16	2002Oct4
PASS		
0	rtee_discretes_level_04_events_16	2002Oct4
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0	rtee_discretes_level_05_events_16	2002Oct4
PASS		
0	rtee_discretes_level_06_events_16	2002Oct4

PASS		
0	rtee_discretes_level_07_events_16	2002Oct4
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0	rtee_discretes_level_08_events_16	2002Oct4
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0	rtee_discretes_level_09_events_16	2002Oct4
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0	rtee_discretes_level_10_events_16	2002Oct4
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0	rtee_discretes_level_11_events_16	2002Oct4
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0	rtee_discretes_level_12_events_16	2002Oct4
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0	rtee_discretes_level_13_events_16	2002Oct4
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0	rtee_discretes_level_14_events_16	2002Oct4
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0	rtee_discretes_level_15_events_16	2002Oct4
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0	rtee_discretes_level_16_events_16	2002Oct4
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0	rtee_discretes_level_17_events_16	2002Oct4
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0	rtee_discretes_level_18_events_16	2002Oct4
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0	rtee_discretes_level_19_events_16	2002Oct4
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0	rtee_discretes_level_20_events_16	2002Oct4
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0	rtee_discretes_level_21_events_16	2002Oct4
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0	rtee_discretes_level_22_events_16	2002Oct4
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0	rtee_discretes_level_23_events_16	2002Oct4
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0	rtee_discretes_level_25_events_16	2002Oct4
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0	rtee_discretes_level_26_events_16	2002Oct4
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0	rtee_discretes_level_27_events_16	2002Oct4
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0	rtee_discretes_level_28_events_16	2002Oct4
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0	rtee_discretes_level_29_events_16	2002Oct4
PASS		
0	rtee_discretes_level_30_events_16	2002Oct4

PASS		
0	rtee_discretes_level_31_events_16	2002Oct4
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct4
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0	rtee_discretes_rising_10_events_16	2002Oct4
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0	rtee_discretes_rising_11_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct4
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0	rtee_discretes_rising_13_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_16_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct4

PASS		
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PASS		
0	rtee_discretes_rising_23_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct4
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct4
PASS		
0	rtee_display1_comp_events_16	2002Oct4
PASS		
0	rtee_display2_comp_events_16	2002Oct4
PASS		
0	rtee_mem_linear_search_events_2	2002Oct4
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct4
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct4
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct4

PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct4
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct4
PASS		
0	rtee_pio	2002Oct4
PASS		
0	rtee_prefetch_parse_nops	2002Oct4
PASS		
0	rtee_reg_linear_search_events_2	2002Oct4
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Oct4
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Oct4
PASS		
0	rtee_vip_comp_events_16	2002Oct4
PASS		
0	scratch_mem_read	2002Oct4
PASS		
0	scratch_mem_read2	2002Oct4
PASS		
0	set_cnst_alu_max_random	2002Oct4
PASS		
0	set_cnst_we_lcc_random	2002Oct4
PASS		
0	set_cnst_we_random	2002Oct4
PASS		
0	set_constant	2002Oct4
PASS		
0	set_constant2	2002Oct4
PASS		
0	set_constant_alu_max1	2002Oct4
PASS		
0	set_constant_alu_max2	2002Oct4

PASS		
0	set_constant_bool_max1	2002Oct4
PASS		
0	set_constant_bool_max2	2002Oct4
PASS		
0	set_constant_indr1_short	2002Oct4
PASS		
0	set_constant_indr1_short2	2002Oct4
PASS		
0	set_constant_indr1_write_enable	2002Oct4
PASS		
0	set_constant_indr1_write_enable_lcc	2002Oct4
PASS		
0	set_constant_indr2_short	2002Oct4
PASS		
0	set_constant_indr2_short2	2002Oct4
PASS		
0	set_constant_indr2_write_enable	2002Oct4
PASS		
0	set_constant_indr2_write_enable_lcc	2002Oct4
PASS		
0	set_constant_loop_max1	2002Oct4
PASS		
0	set_constant_loop_max2	2002Oct4
PASS		
0	set_constant_ring_short	2002Oct4
PASS		
0	set_constant_ring_short2	2002Oct4
PASS		
0	set_constant_ring_write_enable	2002Oct4
PASS		
0	set_constant_ring_write_enable_lcc	2002Oct4
PASS		
0	set_constant_short	2002Oct4
PASS		
0	set_constant_short2	2002Oct4
PASS		
0	set_constant_tex_max1	2002Oct4
PASS		
0	set_constant_tex_max2	2002Oct4
PASS		
0	set_constant_write_enable	2002Oct4
PASS		
0	set_constant_write_enable_lcc	2002Oct4
PASS		
0	set_constant_write_enable_lcc2	2002Oct4
PASS		
0	set_scissors	2002Oct4

PASS		
0	set_st_indrl_match	2002Oct4
PASS		
0	set_st_indrl_mismatch	2002Oct4
PASS		
0	set_st_indrl_mismatch2	2002Oct4
PASS		
0	set_st_indrl_mismatch3	2002Oct4
PASS		
0	set_st_indrl_mismatch4	2002Oct4
PASS		
0	set_st_indrl_ps	2002Oct4
PASS		
0	set_st_indrl_ps_only	2002Oct4
PASS		
0	set_st_indrl_sublk0	2002Oct4
PASS		
0	set_st_indrl_sublk1	2002Oct4
PASS		
0	set_st_indrl_sublk1_only	2002Oct4
PASS		
0	set_st_indrl_sublk2	2002Oct4
PASS		
0	set_st_indrl_sublk2_only	2002Oct4
PASS		
0	set_st_indrl_sublk3	2002Oct4
PASS		
0	set_st_indrl_sublk3_only	2002Oct4
PASS		
0	set_st_indrl_sublk4	2002Oct4
PASS		
0	set_st_indrl_sublk4_only	2002Oct4
PASS		
0	set_st_indrl_sublk5	2002Oct4
PASS		
0	set_st_indrl_sublk5_only	2002Oct4
PASS		
0	set_st_indrl_sublk6	2002Oct4
PASS		
0	set_st_indrl_sublk6_only	2002Oct4
PASS		
0	set_st_indrl_sublk7	2002Oct4
PASS		
0	set_st_indrl_sublk7_only	2002Oct4
PASS		
0	set_st_indrl_vs	2002Oct4
PASS		
0	set_st_indrl_vs_only	2002Oct4

PASS		
0	set_st_indr2_match	2002Oct4
PASS		
0	set_st_indr2_mismatch	2002Oct4
PASS		
0	set_st_indr2_mismatch2	2002Oct4
PASS		
0	set_st_indr2_mismatch3	2002Oct4
PASS		
0	set_st_indr2_mismatch4	2002Oct4
PASS		
0	set_st_indr2_ps	2002Oct4
PASS		
0	set_st_indr2_ps_only	2002Oct4
PASS		
0	set_st_indr2_sublk0	2002Oct4
PASS		
0	set_st_indr2_sublk1	2002Oct4
PASS		
0	set_st_indr2_sublk1_only	2002Oct4
PASS		
0	set_st_indr2_sublk2	2002Oct4
PASS		
0	set_st_indr2_sublk2_only	2002Oct4
PASS		
0	set_st_indr2_sublk3	2002Oct4
PASS		
0	set_st_indr2_sublk3_only	2002Oct4
PASS		
0	set_st_indr2_sublk4	2002Oct4
PASS		
0	set_st_indr2_sublk4_only	2002Oct4
PASS		
0	set_st_indr2_sublk5	2002Oct4
PASS		
0	set_st_indr2_sublk5_only	2002Oct4
PASS		
0	set_st_indr2_sublk6	2002Oct4
PASS		
0	set_st_indr2_sublk6_only	2002Oct4
PASS		
0	set_st_indr2_sublk7	2002Oct4
PASS		
0	set_st_indr2_sublk7_only	2002Oct4
PASS		
0	set_st_indr2_vs	2002Oct4
PASS		
0	set_st_indr2_vs_only	2002Oct4

PASS		
0	set_st_pfmatch_disabled	2002Oct4
PASS		
0	set_st_ring_match	2002Oct4
PASS		
0	set_st_ring_mismatch	2002Oct4
PASS		
0	set_st_ring_mismatch2	2002Oct4
PASS		
0	set_st_ring_mismatch3	2002Oct4
PASS		
0	set_st_ring_mismatch4	2002Oct4
PASS		
0	set_st_ring_ps	2002Oct4
PASS		
0	set_st_ring_ps_only	2002Oct4
PASS		
0	set_st_ring_subblk0	2002Oct4
PASS		
0	set_st_ring_subblk1	2002Oct4
PASS		
0	set_st_ring_subblk1_only	2002Oct4
PASS		
0	set_st_ring_subblk2	2002Oct4
PASS		
0	set_st_ring_subblk2_only	2002Oct4
PASS		
0	set_st_ring_subblk3	2002Oct4
PASS		
0	set_st_ring_subblk3_only	2002Oct4
PASS		
0	set_st_ring_subblk4	2002Oct4
PASS		
0	set_st_ring_subblk4_only	2002Oct4
PASS		
0	set_st_ring_subblk5	2002Oct4
PASS		
0	set_st_ring_subblk5_only	2002Oct4
PASS		
0	set_st_ring_subblk6	2002Oct4
PASS		
0	set_st_ring_subblk6_only	2002Oct4
PASS		
0	set_st_ring_subblk7	2002Oct4
PASS		
0	set_st_ring_subblk7_only	2002Oct4
PASS		
0	set_st_ring_vs	2002Oct4

PASS		
0	set_st_ring_vs_only	2002Oct4
PASS		
0	set_st_stress_a	2002Oct4
PASS		
0	set_st_stress_b	2002Oct4
PASS		
744	set_st_stress_c	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
745	set_st_stress_d	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
746	set_st_stress_e	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_f	2002Oct4
PASS		
0	set_state_rt_a	2002Oct4
PASS		
749	set_state_rt_b	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
750	set_state_rt_c	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
751	set_state_rt_d	2002Oct4
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	smoke_nrt	2002Oct4
PASS		
0	src_clip	2002Oct4
PASS		
0	srf_cohr	2002Oct4
PASS		
0	switch_pio_bm	2002Oct4
PASS		
0	trans_bitblt	2002Oct4
PASS		
0	type0_2long_pckts	2002Oct4
PASS		
0	type0_3indr_buffs	2002Oct4
PASS		
0	type0_4pckts	2002Oct4
PASS		
0	type0_incr_updt_cnst	2002Oct4
PASS		
0	type0_incr_updt_instrmem	2002Oct4
PASS		
0	type0_incr_updt_st	2002Oct4
PASS		
0	type0_incr_updt_stress	2002Oct4
PASS		
0	type0_ind1_ind2_a	2002Oct4

PASS		
0	type0_ind1_ind2_b	2002Oct4
PASS		
0	type0_indr_buff	2002Oct4
PASS		
0	type1_4pckts	2002Oct4
PASS		
0	type1_indr_buff	2002Oct4
PASS		
0	type1_pckt	2002Oct4
PASS		
0	type_random	2002Oct4
PASS		
771	uram_wrt_rt_ucose	2002Oct4
FAIL	cp_rbbm.cmp rbbmWr.cmp	
772	uram_wrt_ucose	2002Oct4
FAIL	rbbmWr.cmp	
0	viz_query_a	2002Oct4
PASS		
0	viz_query_draw_indx	2002Oct4
PASS		
0	viz_query_draw_indx_indr1	2002Oct4
PASS		
0	viz_query_draw_indx_indr2	2002Oct4
PASS		
0	viz_query_draw_indx_multipass	2002Oct4
PASS		
0	viz_query_draw_indx_ring	2002Oct4
PASS		
0	viz_query_indr1	2002Oct4
PASS		
0	viz_query_indr1_simple	2002Oct4
PASS		
0	viz_query_indr2	2002Oct4
PASS		
0	viz_query_indr2_simple	2002Oct4
PASS		
0	viz_query_ring	2002Oct4
PASS		
0	viz_query_ring_simple	2002Oct4
PASS		
0	wait_for_idle	2002Oct4
PASS		
0	wait_reg_mem_poll_mem0	2002Oct4
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Oct4
PASS		
0	wait_reg_mem_poll_mem1	2002Oct4

```
PASS
0 wait_reg_mem_poll_mem2 2002Oct4
PASS
0 wait_reg_mem_poll_mem3 2002Oct4
PASS
0 wait_reg_mem_poll_reg0 2002Oct4
PASS
0 wait_reg_mem_poll_reg1 2002Oct4
PASS
0 wait_reg_mem_poll_reg2 2002Oct4
PASS
0 wait_reg_mem_poll_reg3 2002Oct4
PASS
```

```
=====
Tests_Run_count = 794 Pass_count = 776 Fail_count = 18 Incomplete_count = 0
=====
```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep28
	PASS		
0	2d_indr_buffer_b		2002Sep28
	PASS		
0	2d_indr_buffer_c		2002Sep28
	PASS		
0	2d_indr_buffer_d		2002Sep28
	PASS		
0	2d_indr_buffer_e		2002Sep28
	PASS		
0	2d_indr_buffer_f		2002Sep28
	PASS		
0	2d_indr_buffer_g		2002Sep28
	PASS		
8	2d_indr_buffer_random		2002Sep28
	FAIL cp_mh_req.cmp mh_cp_rd.cmp		
9	2d_indr_buffer_random2		2002Sep28
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep28
	PASS		
0	3d_draw_indx_2_b		2002Sep28
	PASS		
0	IQ2_Mult		2002Sep28
	PASS		
0	IQ2_Wrap_Legacy		2002Sep28
	PASS		
0	IQ_Wrap		2002Sep28
	PASS		
0	Isync_Control		2002Sep28
	PASS		
0	Isync_Control_2D_3D		2002Sep28
	PASS		
0	Isync_Control_3D_2D		2002Sep28
	PASS		
0	RB_BUFSZ_0		2002Sep28
	PASS		
0	RB_BUFSZ_1		2002Sep28
	PASS		
0	RB_Wrap		2002Sep28

PASS		
0	Small_Text_BM	2002Sep28
PASS		
0	Type0_IQ2_BM	2002Sep28
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep28
PASS		
0	Type0_IQ_BM	2002Sep28
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep28
PASS		
0	Type0_PQ_BM	2002Sep28
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep28
PASS		
0	Type3_Bitblt	2002Sep28
PASS		
0	Type3_Load_Palette	2002Sep28
PASS		
0	Type3_NOP	2002Sep28
PASS		
0	Type3_Polyline	2002Sep28
PASS		
0	Type3_Polyline_Large	2002Sep28
PASS		
0	bitblt_multi	2002Sep28
PASS		
0	brush_32x1mono	2002Sep28
PASS		
0	brush_8x8color_16	2002Sep28
PASS		
0	brush_8x8color_32	2002Sep28
PASS		
0	brush_8x8color_64	2002Sep28
PASS		
0	brush_8x8color_dst_0_1	2002Sep28
PASS		
0	brush_8x8mono	2002Sep28
PASS		
0	brush_color_mono_stress	2002Sep28
PASS		
0	brush_flag	2002Sep28
PASS		
0	brush_type0	2002Sep28
PASS		
0	brush_type1	2002Sep28
PASS		
0	brush_type10	2002Sep28

PASS		
0	brush_type13	2002Sep28
PASS		
0	brush_type14	2002Sep28
PASS		
0	brush_type6	2002Sep28
PASS		
0	brush_type7	2002Sep28
PASS		
0	brush_type_other	2002Sep28
PASS		
0	brush_types_13_14	2002Sep28
PASS		
51	change_queue_sizes	2002Sep28
FAIL	cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	clear_const_wr_en_on_me_init	2002Sep28
PASS		
0	clear_const_wr_en_with_lcc	2002Sep28
PASS		
0	cond_write_mem_poll_mem	2002Sep28
PASS		
0	cond_write_mem_poll_mem2	2002Sep28
PASS		
0	cond_write_mem_poll_mem3	2002Sep28
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep28
PASS		
0	cond_write_mem_poll_reg	2002Sep28
PASS		
0	cond_write_mem_poll_reg2	2002Sep28
PASS		
0	cond_write_mem_poll_reg3	2002Sep28
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep28
PASS		
0	cp_bios_scratch	2002Sep28
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep28
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep28
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep28
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep28
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep28
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep28

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep28
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep28
PASS		
0	cp_ib_base000_bufszXXX	2002Sep28
PASS		
0	cp_ib_base001_bufszXXX	2002Sep28
PASS		
0	cp_ib_base010_bufszXXX	2002Sep28
PASS		
0	cp_ib_base011_bufszXXX	2002Sep28
PASS		
0	cp_ib_base100_bufszXXX	2002Sep28
PASS		
0	cp_ib_base101_bufszXXX	2002Sep28
PASS		
0	cp_ib_base110_bufszXXX	2002Sep28
PASS		
0	cp_ib_base111_bufszXXX	2002Sep28
PASS		
0	cp_interrupt	2002Sep28
PASS		
0	cp_interrupt_rt_events_16	2002Sep28
PASS		
0	cp_iq2_alignment_000	2002Sep28
PASS		
0	cp_iq2_alignment_001	2002Sep28
PASS		
0	cp_iq2_alignment_010	2002Sep28
PASS		
0	cp_iq2_alignment_011	2002Sep28
PASS		
0	cp_iq2_alignment_100	2002Sep28
PASS		
0	cp_iq2_alignment_101	2002Sep28
PASS		
0	cp_iq2_alignment_110	2002Sep28
PASS		
0	cp_iq2_alignment_111	2002Sep28
PASS		
0	cp_iq_alignment_000	2002Sep28
PASS		
0	cp_iq_alignment_001	2002Sep28
PASS		
0	cp_iq_alignment_010	2002Sep28
PASS		
0	cp_iq_alignment_011	2002Sep28

PASS		
0	cp_iq_alignment_100	2002Sep28
PASS		
0	cp_iq_alignment_101	2002Sep28
PASS		
0	cp_iq_alignment_110	2002Sep28
PASS		
0	cp_iq_alignment_111	2002Sep28
PASS		
0	cp_max_size_packet	2002Sep28
PASS		
0	cp_max_size_packet_random	2002Sep28
PASS		
0	cp_nrt_rt_protection_mode	2002Sep28
PASS		
0	cp_output_stress	2002Sep28
PASS		
101	cp_output_stress_random	2002Sep28
FAIL	cp_mh_req.cmp	
0	cp_protection_mode	2002Sep28
PASS		
0	cp_rb_alignment_000	2002Sep28
PASS		
0	cp_rb_alignment_001	2002Sep28
PASS		
0	cp_rb_alignment_010	2002Sep28
PASS		
0	cp_rb_alignment_011	2002Sep28
PASS		
0	cp_rb_alignment_100	2002Sep28
PASS		
0	cp_rb_alignment_101	2002Sep28
PASS		
0	cp_rb_alignment_110	2002Sep28
PASS		
0	cp_rb_alignment_111	2002Sep28
PASS		
111	cp_rb_wptrXXX_base000	2002Sep28
FAIL	rbbmWr.cmp	
112	cp_rb_wptrXXX_base001	2002Sep28
FAIL	rbbmWr.cmp	
113	cp_rb_wptrXXX_base010	2002Sep28
FAIL	rbbmWr.cmp	
114	cp_rb_wptrXXX_base011	2002Sep28
FAIL	rbbmWr.cmp	
115	cp_rb_wptrXXX_base100	2002Sep28
FAIL	rbbmWr.cmp	
116	cp_rb_wptrXXX_base101	2002Sep28

```

**FAIL**      rbbmWr.cmp
117 cp_rb_wptrXXX_base110      2002Sep28
**FAIL**      rbbmWr.cmp
118 cp_rb_wptrXXX_base111      2002Sep28
**FAIL**      rbbmWr.cmp
0   cp_reg_rw_test              2002Sep28
PASS
0   cp_register_reads            2002Sep28
PASS
0   cp_rt_protection_mode        2002Sep28
PASS
0   cp_scratch                   2002Sep28
PASS
123 cp_scratch_multi            2002Sep28
**FAIL**      cp_mh_req.cmp
0   csf_reg_rd_default           2002Sep28
PASS
125 csf_reg_rd_only             2002Sep28
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   csf_reg_wr_rd                2002Sep28
PASS
0   dma_cp_sync                  2002Sep28
PASS
0   dma_dscrstr_chckn_bit        2002Sep28
PASS
0   dma_dscrstr_flush1           2002Sep28
PASS
0   dma_dscrstr_flush2           2002Sep28
PASS
0   dma_dscrstr_flush3           2002Sep28
PASS
0   dma_m2m_src00_dstXX          2002Sep28
PASS
0   dma_m2m_src01_dstXX          2002Sep28
PASS
0   dma_m2m_src10_dstXX          2002Sep28
PASS
0   dma_m2m_src11_dstXX          2002Sep28
PASS
0   dma_mem2mem                  2002Sep28
PASS
0   dma_mem2mem_abort            2002Sep28
PASS
0   dma_mem2mem_alignments       2002Sep28
PASS
0   dma_mem2mem_byte_alignment0  2002Sep28
PASS
0   dma_mem2mem_byte_alignment1  2002Sep28

```

PASS		
0	dma_mem2mem_byte_alignment2	2002Sep28
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep28
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep28
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep28
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep28
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep28
PASS		
0	dma_mem2mem_large	2002Sep28
PASS		
148	dma_mem2mem_pio_abort	2002Sep28
FAIL		
0	dma_mem2mem_random	2002Sep28
PASS		
0	dma_mem2reg	2002Sep28
PASS		
151	dma_mem2reg_abort	2002Sep28
FAIL	rbbmWr.cmp	
0	dma_mem2reg_dtaq_stress	2002Sep28
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep28
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep28
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep28
PASS		
0	dma_mem2reg_sync	2002Sep28
PASS		
0	dma_r2r_m2m_r2r	2002Sep28
PASS		
0	dma_r2r_m2r_r2r	2002Sep28
PASS		
0	dma_r2r_r2m_r2r	2002Sep28
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep28
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep28
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep28
PASS		
0	dma_reg2mem	2002Sep28
PASS		
0	dma_reg2mem_abort	2002Sep28

PASS		
0	dma_reg2reg	2002Sep28
PASS		
0	dma_reg2reg_2dw	2002Sep28
PASS		
0	dma_reg2reg_daic	2002Sep28
PASS		
0	dma_reg2reg_mult	2002Sep28
PASS		
0	dma_reg2reg_mult2	2002Sep28
PASS		
0	dma_reg2reg_mult3	2002Sep28
PASS		
0	dma_reg2reg_saic	2002Sep28
PASS		
0	dma_reg2reg_sdaic	2002Sep28
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep28
PASS		
0	dma_reg2reg_stress	2002Sep28
PASS		
0	draw_indx_a	2002Sep28
PASS		
0	draw_indx_b	2002Sep28
PASS		
0	draw_indx_c	2002Sep28
PASS		
0	draw_indx_indr1_a	2002Sep28
PASS		
0	draw_indx_indr1_b	2002Sep28
PASS		
0	draw_indx_indr1_short_a	2002Sep28
PASS		
0	draw_indx_indr1_short_b	2002Sep28
PASS		
0	draw_indx_indr1_stress	2002Sep28
PASS		
0	draw_indx_indr2_a	2002Sep28
PASS		
0	draw_indx_indr2_b	2002Sep28
PASS		
0	draw_indx_indr2_short_a	2002Sep28
PASS		
0	draw_indx_indr2_short_b	2002Sep28
PASS		
0	draw_indx_indr2_stress	2002Sep28
PASS		
188	draw_indx_random_a	2002Sep28

```

**FAIL**          mh_cp_rd.cmp
0  draw_indx_random_b          2002Sep28
PASS
0  draw_indx_random_c          2002Sep28
PASS
0  draw_indx_ring1             2002Sep28
PASS
0  draw_indx_ring2             2002Sep28
PASS
0  draw_indx_ring_short1       2002Sep28
PASS
0  draw_indx_ring_short2       2002Sep28
PASS
0  draw_indx_ring_stress       2002Sep28
PASS
0  draw_indx_short_a           2002Sep28
PASS
0  draw_indx_short_b           2002Sep28
PASS
0  draw_indx_short_c           2002Sep28
PASS
0  draw_indx_stress_a          2002Sep28
PASS
0  draw_indx_stress_b          2002Sep28
PASS
0  draw_indx_stress_c          2002Sep28
PASS
0  event_timestamp_write        2002Sep28
PASS
0  event_timestamp_write_multicontext_maxcontext_7 2002Sep28
PASS
0  fix2flt_reg                 2002Sep28
PASS
0  ft_indr1_july22             2002Sep28
PASS
0  ft_indr2_july22             2002Sep28
PASS
0  ft_july22                   2002Sep28
PASS
208 ft_july22_random           2002Sep28
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  header_dump                 2002Sep28
PASS
0  header_dump_rt              2002Sep28
PASS
0  header_dump_small           2002Sep28
PASS
0  hostdata_blt                2002Sep28

```


PASS		
0	im_load_a	2002Sep28
PASS		
0	im_load_b	2002Sep28
PASS		
0	im_load_cid_mismatch	2002Sep28
PASS		
0	im_load_immediate	2002Sep28
PASS		
0	im_load_immediate_nrt_me	2002Sep28
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep28
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep28
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep28
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep28
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep28
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep28
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep28
PASS		
0	im_load_indr1_a	2002Sep28
PASS		
0	im_load_indr1_b	2002Sep28
PASS		
0	im_load_indr2_a	2002Sep28
PASS		
0	im_load_indr2_b	2002Sep28
PASS		
0	im_load_invalidate	2002Sep28
PASS		
0	im_load_match	2002Sep28
PASS		
0	im_load_match2	2002Sep28
PASS		
0	im_load_nrt_me	2002Sep28
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep28
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep28
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep28
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep28

PASS		
0	im_load_pfmatch_disable	2002Sep28
PASS		
0	im_load_random_a	2002Sep28
PASS		
0	im_load_random_b	2002Sep28
PASS		
240	im_load_random_c	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Sep28
PASS		
0	im_load_ring_b	2002Sep28
PASS		
0	im_load_rt	2002Sep28
PASS		
0	im_load_rt2	2002Sep28
PASS		
0	im_load_rt3	2002Sep28
PASS		
0	im_load_rt4	2002Sep28
PASS		
0	im_load_sz_mismatch	2002Sep28
PASS		
0	incremental_update_const	2002Sep28
PASS		
0	incremental_update_instr	2002Sep28
PASS		
0	incremental_update_state	2002Sep28
PASS		
0	indirect_buffer_a	2002Sep28
PASS		
0	indirect_buffer_b	2002Sep28
PASS		
0	indirect_buffer_c	2002Sep28
PASS		
0	indirect_buffer_d	2002Sep28
PASS		
0	indirect_buffer_e	2002Sep28
PASS		
0	indirect_buffer_f	2002Sep28
PASS		
0	indirect_buffer_g	2002Sep28
PASS		
0	indirect_buffer_h	2002Sep28
PASS		
0	indirect_buffer_i	2002Sep28
PASS		
0	indirect_buffer_j	2002Sep28

PASS		
0	indirect_buffer_k	2002Sep28
PASS		
0	indirect_buffer_l	2002Sep28
PASS		
0	indirect_buffer_mp_a	2002Sep28
PASS		
0	indirect_buffer_mp_b	2002Sep28
PASS		
0	indirect_buffer_mp_c	2002Sep28
PASS		
0	indirect_buffer_mp_d	2002Sep28
PASS		
0	indirect_buffer_mp_e	2002Sep28
PASS		
0	indirect_buffer_mp_f	2002Sep28
PASS		
269	indirect_buffer_mp_random1	2002Sep28
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
270	indirect_buffer_mp_random2	2002Sep28
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
271	indirect_buffer_random1	2002Sep28
FAIL	mh_cp_rd.cmp	
272	indirect_buffer_random2	2002Sep28
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	2002Sep28
PASS		
0	invalidate_on_me_init	2002Sep28
PASS		
0	invalidate_state_constants	2002Sep28
PASS		
0	invalidate_state_shaders	2002Sep28
PASS		
0	invalidate_state_shaders2	2002Sep28
PASS		
0	invalidate_state_sublks	2002Sep28
PASS		
0	invalidate_state_sublks2	2002Sep28
PASS		
0	iq1_iq2_mult	2002Sep28
PASS		
0	iq1_iq2_push_wrap	2002Sep28
PASS		
0	iq2_csq_full	2002Sep28
PASS		
283	lcc_coherency_cntr_random	2002Sep28
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Sep28

PASS		
285	lcc_mismatch_alutex_random	2002Sep28
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
286	lcc_mismatch_base_random	2002Sep28
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_coherency_cntrl	2002Sep28
PASS		
288	ld_cnst_cntx_coherency_cntr2	2002Sep28
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Sep28
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep28
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep28
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep28
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep28
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep28
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep28
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep28
PASS		
0	ld_cnst_cntx_match	2002Sep28
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep28
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep28
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep28
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep28
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep28
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep28
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep28
PASS		
0	ld_cnst_cntx_replicate8	2002Sep28
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep28
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep28
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep28

PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep28
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep28
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep28
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep28
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep28
PASS		
0	load_constant_context_rt	2002Sep28
PASS		
0	load_palette	2002Sep28
PASS		
0	lp_2d3d	2002Sep28
PASS		
0	me_reg_wr_rd	2002Sep28
PASS		
0	mem_write	2002Sep28
PASS		
0	mem_write_rt	2002Sep28
PASS		
0	mpeg_index	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep28
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep28

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep28
PASS		
0	nop_eating_disabled	2002Sep28
PASS		
0	nrt_rt_interaction1	2002Sep28
PASS		
0	paint_multi	2002Sep28
PASS		
0	paint_packet	2002Sep28
PASS		
0	paint_packet_gui_control	2002Sep28
PASS		
0	pfp_reg_reads1	2002Sep28
PASS		
0	pfp_reg_reads2	2002Sep28
PASS		
0	pitch_clip	2002Sep28
PASS		
0	ply_nextscan	2002Sep28
PASS		
0	rbbm_WU_2D_idle_01	2002Sep28
PASS		
0	rbbm_WU_2D_idle_02	2002Sep28
PASS		
0	rbbm_WU_2D_idle_03	2002Sep28
PASS		
0	rbbm_WU_2D_idle_04	2002Sep28
PASS		
0	rbbm_WU_2D_idle_05	2002Sep28
PASS		
0	rbbm_WU_2D_idle_06	2002Sep28
PASS		
0	rbbm_WU_2D_idle_07	2002Sep28
PASS		
0	rbbm_WU_2D_idle_08	2002Sep28
PASS		
0	rbbm_WU_2D_idle_09	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep28

PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep28
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep28
PASS		
0	rbbm_WU_3D_idle_01	2002Sep28
PASS		
0	rbbm_WU_3D_idle_02	2002Sep28
PASS		
0	rbbm_WU_3D_idle_03	2002Sep28
PASS		
0	rbbm_WU_3D_idle_04	2002Sep28
PASS		
0	rbbm_WU_3D_idle_05	2002Sep28
PASS		
0	rbbm_WU_3D_idle_06	2002Sep28
PASS		
0	rbbm_WU_3D_idle_07	2002Sep28
PASS		
0	rbbm_WU_3D_idle_08	2002Sep28
PASS		
0	rbbm_WU_3D_idle_09	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep28

PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep28
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep28
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep28
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Sep28
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep28
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep28
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep28
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep28
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep28
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep28
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep28
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Sep28
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Sep28
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep28
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep28
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep28
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep28
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep28
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep28
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep28

PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep28
PASS		
0	rbbm_WU_idct_semaphore	2002Sep28
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep28
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep28
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep28
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep28
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep28
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep28
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep28
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep28

PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep28
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep28
PASS		
0	rbbm_WUrt_VIPh0_dma_idle	2002Sep28
PASS		
0	rbbm_WUrt_VIPh1_dma_idle	2002Sep28
PASS		
0	rbbm_WUrt_VIPh2_dma_idle	2002Sep28
PASS		
0	rbbm_WUrt_VIPh3_dma_idle	2002Sep28
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep28
PASS		
0	rbbm_clk_active_deassert_min_max	2002Sep28
PASS		
0	rbbm_clk_active_each_pipe	2002Sep28
PASS		
0	rbbm_clk_active_for_read_error	2002Sep28
PASS		
0	rbbm_clk_active_for_read_request	2002Sep28
PASS		
0	rbbm_clk_active_on_reset	2002Sep28
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep28
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep28
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep28
PASS		
0	rbbm_cp_queued_rdwr	2002Sep28
PASS		
0	rbbm_hi_data_and_be_swap	2002Sep28
PASS		
0	rbbm_hi_data_endian_swap	2002Sep28
PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Sep28
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Sep28
PASS		
0	rbbm_int_ack	2002Sep28
PASS		
0	rbbm_int_each_client	2002Sep28
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Sep28
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep28

PASS		
0	rbbm_int_rderr	2002Sep28
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep28
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep28
PASS		
0	rbbm_isync_wait_idlegui	2002Sep28
PASS		
0	rbbm_soft_reset	2002Sep28
PASS		
0	reg_to_mem	2002Sep28
PASS		
0	reg_to_mem_rt	2002Sep28
PASS		
0	rop_cfd_wmd	2002Sep28
PASS		
461	roq_reg_reads	2002Sep28
FAIL	cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	rstr_ordr	2002Sep28
PASS		
0	rt_invalid_opcode	2002Sep28
PASS		
464	rtee_arm_wait_idle_trigger_events_16	2002Sep28
FAIL		
0	rtee_discretes_arm_rearm_events_32	2002Sep28
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep28

PASS		
0	rtee_discretes_falling_11_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep28
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep28

PASS		
0	rtee_discretes_invert_03_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep28

PASS		
0	rtee_discretes_invert_27_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep28
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep28
PASS		
0	rtee_discretes_level_00_events_16	2002Sep28
PASS		
0	rtee_discretes_level_01_events_16	2002Sep28
PASS		
0	rtee_discretes_level_02_events_16	2002Sep28
PASS		
0	rtee_discretes_level_03_events_16	2002Sep28
PASS		
0	rtee_discretes_level_04_events_16	2002Sep28
PASS		
0	rtee_discretes_level_05_events_16	2002Sep28
PASS		
0	rtee_discretes_level_06_events_16	2002Sep28
PASS		
0	rtee_discretes_level_07_events_16	2002Sep28
PASS		
0	rtee_discretes_level_08_events_16	2002Sep28
PASS		
0	rtee_discretes_level_09_events_16	2002Sep28
PASS		
0	rtee_discretes_level_10_events_16	2002Sep28
PASS		
0	rtee_discretes_level_11_events_16	2002Sep28
PASS		
0	rtee_discretes_level_12_events_16	2002Sep28
PASS		
0	rtee_discretes_level_13_events_16	2002Sep28
PASS		
0	rtee_discretes_level_14_events_16	2002Sep28
PASS		
0	rtee_discretes_level_15_events_16	2002Sep28
PASS		
0	rtee_discretes_level_16_events_16	2002Sep28
PASS		
0	rtee_discretes_level_17_events_16	2002Sep28
PASS		
0	rtee_discretes_level_18_events_16	2002Sep28

PASS		
0	rtee_discretes_level_19_events_16	2002Sep28
PASS		
0	rtee_discretes_level_20_events_16	2002Sep28
PASS		
0	rtee_discretes_level_21_events_16	2002Sep28
PASS		
0	rtee_discretes_level_22_events_16	2002Sep28
PASS		
0	rtee_discretes_level_23_events_16	2002Sep28
PASS		
0	rtee_discretes_level_24_events_16	2002Sep28
PASS		
0	rtee_discretes_level_25_events_16	2002Sep28
PASS		
0	rtee_discretes_level_26_events_16	2002Sep28
PASS		
0	rtee_discretes_level_27_events_16	2002Sep28
PASS		
0	rtee_discretes_level_28_events_16	2002Sep28
PASS		
0	rtee_discretes_level_29_events_16	2002Sep28
PASS		
0	rtee_discretes_level_30_events_16	2002Sep28
PASS		
0	rtee_discretes_level_31_events_16	2002Sep28
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep28

PASS		
0	rtee_discretes_rising_10_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep28
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep28
PASS		
0	rtee_display1_comp_events_16	2002Sep28
PASS		
0	rtee_display2_comp_events_16	2002Sep28

PASS		
0	rtee_mem_linear_search_events_2	2002Sep28
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep28
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep28
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep28
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep28
PASS		
0	rtee_pio	2002Sep28
PASS		
0	rtee_prefetch_parse_nops	2002Sep28
PASS		
0	rtee_reg_linear_search_events_2	2002Sep28
PASS		
613	rtee_reg_poll_comp_000_events_16	2002Sep28
FAIL		
614	rtee_reg_poll_comp_001_events_16	2002Sep28
FAIL		
615	rtee_reg_poll_comp_010_events_16	2002Sep28
FAIL		
616	rtee_reg_poll_comp_011_events_16	2002Sep28
FAIL		
617	rtee_reg_poll_comp_100_events_16	2002Sep28
FAIL		
618	rtee_reg_poll_comp_101_events_16	2002Sep28
FAIL		
619	rtee_reg_poll_comp_110_events_16	2002Sep28
FAIL		
620	rtee_reg_poll_comp_111_events_16	2002Sep28

```

**FAIL**
621 rtee_reg_poll_comp_queued_nonqueued_events_16          2002Sep28
**FAIL**          cp_rbbm.cmp
0 rtee_vip_comp_events_16                                  2002Sep28
PASS
0 scratch_mem_read                                         2002Sep28
PASS
0 scratch_mem_read2                                        2002Sep28
PASS
0 set_constant                                             2002Sep28
PASS
0 set_constant2                                            2002Sep28
PASS
0 set_constant_alu_max1                                    2002Sep28
PASS
0 set_constant_alu_max2                                    2002Sep28
PASS
0 set_constant_bool_max1                                  2002Sep28
PASS
0 set_constant_bool_max2                                  2002Sep28
PASS
0 set_constant_indr1_short                                 2002Sep28
PASS
0 set_constant_indr1_short2                                2002Sep28
PASS
0 set_constant_indr1_write_enable                          2002Sep28
PASS
0 set_constant_indr1_write_enable_lcc                     2002Sep28
PASS
0 set_constant_indr2_short                                 2002Sep28
PASS
0 set_constant_indr2_short2                                2002Sep28
PASS
0 set_constant_indr2_write_enable                          2002Sep28
PASS
0 set_constant_indr2_write_enable_lcc                     2002Sep28
PASS
0 set_constant_loop_max1                                   2002Sep28
PASS
0 set_constant_loop_max2                                   2002Sep28
PASS
0 set_constant_ring_short                                  2002Sep28
PASS
0 set_constant_ring_short2                                2002Sep28
PASS
0 set_constant_ring_write_enable                          2002Sep28
PASS
0 set_constant_ring_write_enable_lcc                      2002Sep28

```

PASS		
0	set_constant_short	2002Sep28
PASS		
0	set_constant_short2	2002Sep28
PASS		
0	set_constant_tex_max1	2002Sep28
PASS		
0	set_constant_tex_max2	2002Sep28
PASS		
0	set_constant_write_enable	2002Sep28
PASS		
0	set_constant_write_enable_lcc	2002Sep28
PASS		
0	set_constant_write_enable_lcc2	2002Sep28
PASS		
0	set_scissors	2002Sep28
PASS		
0	set_st_indrl_match	2002Sep28
PASS		
0	set_st_indrl_mismatch	2002Sep28
PASS		
0	set_st_indrl_mismatch2	2002Sep28
PASS		
0	set_st_indrl_mismatch3	2002Sep28
PASS		
0	set_st_indrl_mismatch4	2002Sep28
PASS		
0	set_st_indrl_ps	2002Sep28
PASS		
0	set_st_indrl_ps_only	2002Sep28
PASS		
0	set_st_indrl_sublk0	2002Sep28
PASS		
0	set_st_indrl_sublk1	2002Sep28
PASS		
0	set_st_indrl_sublk1_only	2002Sep28
PASS		
0	set_st_indrl_sublk2	2002Sep28
PASS		
0	set_st_indrl_sublk2_only	2002Sep28
PASS		
0	set_st_indrl_sublk3	2002Sep28
PASS		
0	set_st_indrl_sublk3_only	2002Sep28
PASS		
0	set_st_indrl_sublk4	2002Sep28
PASS		
0	set_st_indrl_sublk4_only	2002Sep28

PASS		
0	set_st_indr1_sublk5	2002Sep28
PASS		
0	set_st_indr1_sublk5_only	2002Sep28
PASS		
0	set_st_indr1_sublk6	2002Sep28
PASS		
0	set_st_indr1_sublk6_only	2002Sep28
PASS		
0	set_st_indr1_sublk7	2002Sep28
PASS		
0	set_st_indr1_sublk7_only	2002Sep28
PASS		
0	set_st_indr1_vs	2002Sep28
PASS		
0	set_st_indr1_vs_only	2002Sep28
PASS		
0	set_st_indr2_match	2002Sep28
PASS		
0	set_st_indr2_mismatch	2002Sep28
PASS		
0	set_st_indr2_mismatch2	2002Sep28
PASS		
0	set_st_indr2_mismatch3	2002Sep28
PASS		
0	set_st_indr2_mismatch4	2002Sep28
PASS		
0	set_st_indr2_ps	2002Sep28
PASS		
0	set_st_indr2_ps_only	2002Sep28
PASS		
0	set_st_indr2_sublk0	2002Sep28
PASS		
0	set_st_indr2_sublk1	2002Sep28
PASS		
0	set_st_indr2_sublk1_only	2002Sep28
PASS		
0	set_st_indr2_sublk2	2002Sep28
PASS		
0	set_st_indr2_sublk2_only	2002Sep28
PASS		
0	set_st_indr2_sublk3	2002Sep28
PASS		
0	set_st_indr2_sublk3_only	2002Sep28
PASS		
0	set_st_indr2_sublk4	2002Sep28
PASS		
0	set_st_indr2_sublk4_only	2002Sep28

PASS		
0	set_st_indr2_sublk5	2002Sep28
PASS		
0	set_st_indr2_sublk5_only	2002Sep28
PASS		
0	set_st_indr2_sublk6	2002Sep28
PASS		
0	set_st_indr2_sublk6_only	2002Sep28
PASS		
0	set_st_indr2_sublk7	2002Sep28
PASS		
0	set_st_indr2_sublk7_only	2002Sep28
PASS		
0	set_st_indr2_vs	2002Sep28
PASS		
0	set_st_indr2_vs_only	2002Sep28
PASS		
0	set_st_pfmatch_disabled	2002Sep28
PASS		
0	set_st_ring_match	2002Sep28
PASS		
0	set_st_ring_mismatch	2002Sep28
PASS		
0	set_st_ring_mismatch2	2002Sep28
PASS		
0	set_st_ring_mismatch3	2002Sep28
PASS		
0	set_st_ring_mismatch4	2002Sep28
PASS		
0	set_st_ring_ps	2002Sep28
PASS		
0	set_st_ring_ps_only	2002Sep28
PASS		
0	set_st_ring_sublk0	2002Sep28
PASS		
0	set_st_ring_sublk1	2002Sep28
PASS		
0	set_st_ring_sublk1_only	2002Sep28
PASS		
0	set_st_ring_sublk2	2002Sep28
PASS		
0	set_st_ring_sublk2_only	2002Sep28
PASS		
0	set_st_ring_sublk3	2002Sep28
PASS		
0	set_st_ring_sublk3_only	2002Sep28
PASS		
0	set_st_ring_sublk4	2002Sep28

PASS		
0	set_st_ring_sublk4_only	2002Sep28
PASS		
0	set_st_ring_sublk5	2002Sep28
PASS		
0	set_st_ring_sublk5_only	2002Sep28
PASS		
0	set_st_ring_sublk6	2002Sep28
PASS		
0	set_st_ring_sublk6_only	2002Sep28
PASS		
0	set_st_ring_sublk7	2002Sep28
PASS		
0	set_st_ring_sublk7_only	2002Sep28
PASS		
0	set_st_ring_vs	2002Sep28
PASS		
0	set_st_ring_vs_only	2002Sep28
PASS		
0	set_st_stress_a	2002Sep28
PASS		
0	set_st_stress_b	2002Sep28
PASS		
0	set_st_stress_c	2002Sep28
PASS		
0	set_st_stress_d	2002Sep28
PASS		
0	set_st_stress_e	2002Sep28
PASS		
0	set_st_stress_f	2002Sep28
PASS		
732	set_state_rt_a	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
733	set_state_rt_b	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
734	set_state_rt_c	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
735	set_state_rt_d	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep28
PASS		
0	src_clip	2002Sep28
PASS		
0	srf_cohr	2002Sep28
PASS		
0	switch_pio_bm	2002Sep28
PASS		
0	trans_bitblt	2002Sep28

PASS		
741	type0_2long_pckts	2002Sep28
FAIL	cp_mh_req.cmp	
0	type0_3indr_buffs	2002Sep28
PASS		
0	type0_4pckts	2002Sep28
PASS		
0	type0_incr_updt_cnst	2002Sep28
PASS		
0	type0_incr_updt_instrmem	2002Sep28
PASS		
0	type0_incr_updt_st	2002Sep28
PASS		
0	type0_incr_updt_stress	2002Sep28
PASS		
0	type0_ind1_ind2_a	2002Sep28
PASS		
0	type0_ind1_ind2_b	2002Sep28
PASS		
0	type0_indr_buff	2002Sep28
PASS		
0	type1_4pckts	2002Sep28
PASS		
0	type1_indr_buff	2002Sep28
PASS		
0	type1_pckt	2002Sep28
PASS		
0	type_random	2002Sep28
PASS		
755	uram_wrt_rt_ucose	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
756	uram_wrt_ucose	2002Sep28
FAIL	rbbmWr.cmp	
757	viz_query_a	2002Sep28
FAIL	rbbmWr.cmp	
758	viz_query_draw_indx	2002Sep28
FAIL	rbbmWr.cmp	
759	viz_query_draw_indx_indr1	2002Sep28
FAIL	rbbmWr.cmp	
760	viz_query_draw_indx_indr2	2002Sep28
FAIL	rbbmWr.cmp	
761	viz_query_draw_indx_multipass	2002Sep28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
762	viz_query_draw_indx_ring	2002Sep28
FAIL	rbbmWr.cmp	
763	viz_query_indr1	2002Sep28
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep28

```
PASS
765 viz_query_indr2 2002Sep28
**FAIL**          rbbmWr.cmp
0 viz_query_indr2_simple 2002Sep28
PASS
767 viz_query_ring 2002Sep28
**FAIL**          rbbmWr.cmp
0 viz_query_ring_simple 2002Sep28
PASS
0 wait_for_idle 2002Sep28
PASS
0 wait_reg_mem_poll_mem0 2002Sep28
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep28
PASS
0 wait_reg_mem_poll_mem1 2002Sep28
PASS
0 wait_reg_mem_poll_mem2 2002Sep28
PASS
0 wait_reg_mem_poll_mem3 2002Sep28
PASS
0 wait_reg_mem_poll_reg0 2002Sep28
PASS
0 wait_reg_mem_poll_reg1 2002Sep28
PASS
0 wait_reg_mem_poll_reg2 2002Sep28
PASS
0 wait_reg_mem_poll_reg3 2002Sep28
PASS
```

```
=====
Tests_Run_count = 778 Pass_count = 724 Fail_count = 54 Incomplete_count = 0
=====
```



```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sun Sep 29  1:41:58 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          667         667         611         91
              RBBM        113         113         113         100
+++++
              TOTAL        780         780         724         92
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep9
	PASS		
0	2d_indr_buffer_b		2002Sep9
	PASS		
0	2d_indr_buffer_c		2002Sep9
	PASS		
0	2d_indr_buffer_d		2002Sep9
	PASS		
0	2d_indr_buffer_e		2002Sep9
	PASS		
0	2d_indr_buffer_f		2002Sep9
	PASS		
0	2d_indr_buffer_g		2002Sep9
	PASS		
0	3d_draw_indx_2		2002Sep9
	PASS		
0	3d_draw_indx_2_b		2002Sep9
	PASS		
0	IQ2_Mult		2002Sep9
	PASS		
0	IQ2_Wrap_Legacy		2002Sep9
	PASS		
0	IQ_Wrap		2002Sep9
	PASS		
0	RB_BUFSZ_0		2002Sep9
	PASS		
0	RB_BUFSZ_1		2002Sep9
	PASS		
0	RB_Wrap		2002Sep9
	PASS		
16	Small_Text_BM		2002Sep9
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	Type0_IQ2_BM		2002Sep9
	PASS		
0	Type0_IQ2_BM_IB_Packet		2002Sep9
	PASS		
0	Type0_IQ_BM		2002Sep9
	PASS		
0	Type0_IQ_BM_IB_Packet		2002Sep9

PASS		
0	Type0_PQ_BM	2002Sep9
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep9
PASS		
0	Type3_Bitblt	2002Sep9
PASS		
0	Type3_Load_Palette	2002Sep9
PASS		
0	Type3_NOP	2002Sep9
PASS		
0	Type3_Polyline	2002Sep9
PASS		
0	Type3_Polyline_Large	2002Sep9
PASS		
0	bitblt_multi	2002Sep9
PASS		
0	brush_32x1mono	2002Sep9
PASS		
0	brush_8x8color_16	2002Sep9
PASS		
31	brush_8x8color_32	2002Sep9
FAIL	cp_mh_req.cmp	
0	brush_8x8color_64	2002Sep9
PASS		
0	brush_8x8color_dst_0_1	2002Sep9
PASS		
0	brush_8x8mono	2002Sep9
PASS		
0	brush_color_mono_stress	2002Sep9
PASS		
36	brush_flag	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	brush_type0	2002Sep9
PASS		
0	brush_type1	2002Sep9
PASS		
0	brush_type10	2002Sep9
PASS		
0	brush_type13	2002Sep9
PASS		
0	brush_type14	2002Sep9
PASS		
0	brush_type6	2002Sep9
PASS		
0	brush_type7	2002Sep9
PASS		
0	brush_type_other	2002Sep9

PASS		
0	brush_types_13_14	2002Sep9
PASS		
0	clear_const_wr_en_on_me_init	2002Sep9
PASS		
0	clear_const_wr_en_with_lcc	2002Sep9
PASS		
0	cond_write_mem_poll_mem	2002Sep9
PASS		
0	cond_write_mem_poll_mem2	2002Sep9
PASS		
0	cond_write_mem_poll_mem3	2002Sep9
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep9
PASS		
0	cond_write_mem_poll_reg	2002Sep9
PASS		
0	cond_write_mem_poll_reg2	2002Sep9
PASS		
0	cond_write_mem_poll_reg3	2002Sep9
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep9
PASS		
0	cp_bios_scratch	2002Sep9
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep9
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep9
PASS		
0	cp_ib_base000_bufszXXX	2002Sep9
PASS		
0	cp_ib_base001_bufszXXX	2002Sep9
PASS		
0	cp_ib_base010_bufszXXX	2002Sep9
PASS		
0	cp_ib_base011_bufszXXX	2002Sep9

PASS		
0	cp_ib_base100_bufszXXX	2002Sep9
PASS		
0	cp_ib_base101_bufszXXX	2002Sep9
PASS		
0	cp_ib_base110_bufszXXX	2002Sep9
PASS		
0	cp_ib_base111_bufszXXX	2002Sep9
PASS		
0	cp_interrupt	2002Sep9
PASS		
0	cp_interrupt_rt_events_16	2002Sep9
PASS		
0	cp_iq2_alignment_000	2002Sep9
PASS		
0	cp_iq2_alignment_001	2002Sep9
PASS		
0	cp_iq2_alignment_010	2002Sep9
PASS		
0	cp_iq2_alignment_011	2002Sep9
PASS		
0	cp_iq2_alignment_100	2002Sep9
PASS		
0	cp_iq2_alignment_101	2002Sep9
PASS		
0	cp_iq2_alignment_110	2002Sep9
PASS		
0	cp_iq2_alignment_111	2002Sep9
PASS		
0	cp_iq_alignment_000	2002Sep9
PASS		
0	cp_iq_alignment_001	2002Sep9
PASS		
0	cp_iq_alignment_010	2002Sep9
PASS		
0	cp_iq_alignment_011	2002Sep9
PASS		
0	cp_iq_alignment_100	2002Sep9
PASS		
0	cp_iq_alignment_101	2002Sep9
PASS		
0	cp_iq_alignment_110	2002Sep9
PASS		
0	cp_iq_alignment_111	2002Sep9
PASS		
0	cp_max_size_packet	2002Sep9
PASS		
0	cp_max_size_packet_random	2002Sep9

PASS		
0	cp_nrt_rt_protection_mode	2002Sep9
PASS		
0	cp_output_stress	2002Sep9
PASS		
0	cp_output_stress_random	2002Sep9
PASS		
0	cp_protection_mode	2002Sep9
PASS		
0	cp_rb_alignment_000	2002Sep9
PASS		
0	cp_rb_alignment_001	2002Sep9
PASS		
0	cp_rb_alignment_010	2002Sep9
PASS		
0	cp_rb_alignment_011	2002Sep9
PASS		
0	cp_rb_alignment_100	2002Sep9
PASS		
0	cp_rb_alignment_101	2002Sep9
PASS		
0	cp_rb_alignment_110	2002Sep9
PASS		
0	cp_rb_alignment_111	2002Sep9
PASS		
0	cp_rb_wptrXXX_base000	2002Sep9
PASS		
0	cp_rb_wptrXXX_base001	2002Sep9
PASS		
0	cp_rb_wptrXXX_base010	2002Sep9
PASS		
0	cp_rb_wptrXXX_base011	2002Sep9
PASS		
0	cp_rb_wptrXXX_base100	2002Sep9
PASS		
0	cp_rb_wptrXXX_base101	2002Sep9
PASS		
0	cp_rb_wptrXXX_base110	2002Sep9
PASS		
0	cp_rb_wptrXXX_base111	2002Sep9
PASS		
0	cp_reg_rw_test	2002Sep9
PASS		
0	cp_register_reads	2002Sep9
PASS		
0	cp_rt_protection_mode	2002Sep9
PASS		
0	cp_scratch	2002Sep9

PASS		
0	cp_scratch_multi	2002Sep9
PASS		
0	dma_cp_sync	2002Sep9
PASS		
0	dma_dscrstr_chckn_bit	2002Sep9
PASS		
0	dma_dscrstr_flush1	2002Sep9
PASS		
0	dma_dscrstr_flush2	2002Sep9
PASS		
0	dma_dscrstr_flush3	2002Sep9
PASS		
0	dma_m2m_src00_dstXX	2002Sep9
PASS		
0	dma_m2m_src01_dstXX	2002Sep9
PASS		
0	dma_m2m_src10_dstXX	2002Sep9
PASS		
0	dma_m2m_src11_dstXX	2002Sep9
PASS		
0	dma_mem2mem	2002Sep9
PASS		
0	dma_mem2mem_abort	2002Sep9
PASS		
0	dma_mem2mem_alignments	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep9
PASS		
0	dma_mem2mem_pio_abort	2002Sep9
PASS		
0	dma_mem2mem_random	2002Sep9
PASS		
0	dma_mem2reg	2002Sep9

PASS		
0	dma_mem2reg_abort	2002Sep9
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep9
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep9
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep9
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep9
PASS		
0	dma_mem2reg_sync	2002Sep9
PASS		
0	dma_r2r_m2m_r2r	2002Sep9
PASS		
0	dma_r2r_m2r_r2r	2002Sep9
PASS		
0	dma_r2r_r2m_r2r	2002Sep9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep9
PASS		
0	dma_reg2mem	2002Sep9
PASS		
0	dma_reg2mem_abort	2002Sep9
PASS		
0	dma_reg2reg	2002Sep9
PASS		
0	dma_reg2reg_2dw	2002Sep9
PASS		
0	dma_reg2reg_daic	2002Sep9
PASS		
0	dma_reg2reg_mult	2002Sep9
PASS		
0	dma_reg2reg_mult2	2002Sep9
PASS		
0	dma_reg2reg_mult3	2002Sep9
PASS		
0	dma_reg2reg_saic	2002Sep9
PASS		
0	dma_reg2reg_sdaic	2002Sep9
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep9
PASS		
0	dma_reg2reg_stress	2002Sep9

PASS		
0	draw_indx_a	2002Sep9
PASS		
0	draw_indx_b	2002Sep9
PASS		
0	draw_indx_c	2002Sep9
PASS		
0	draw_indx_indr1_a	2002Sep9
PASS		
0	draw_indx_indr1_b	2002Sep9
PASS		
0	draw_indx_indr1_short_a	2002Sep9
PASS		
0	draw_indx_indr1_short_b	2002Sep9
PASS		
0	draw_indx_indr1_stress	2002Sep9
PASS		
0	draw_indx_indr2_a	2002Sep9
PASS		
0	draw_indx_indr2_b	2002Sep9
PASS		
0	draw_indx_indr2_short_a	2002Sep9
PASS		
0	draw_indx_indr2_short_b	2002Sep9
PASS		
0	draw_indx_indr2_stress	2002Sep9
PASS		
0	draw_indx_ring1	2002Sep9
PASS		
0	draw_indx_ring2	2002Sep9
PASS		
0	draw_indx_ring_short1	2002Sep9
PASS		
0	draw_indx_ring_short2	2002Sep9
PASS		
0	draw_indx_ring_stress	2002Sep9
PASS		
0	draw_indx_short_a	2002Sep9
PASS		
0	draw_indx_short_b	2002Sep9
PASS		
0	draw_indx_short_c	2002Sep9
PASS		
0	draw_indx_stress_a	2002Sep9
PASS		
0	draw_indx_stress_b	2002Sep9
PASS		
0	draw_indx_stress_c	2002Sep9

PASS		
0	event_timestamp_write	2002Sep9
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep9
PASS		
0	ft_indr1_july22	2002Sep9
PASS		
0	ft_indr2_july22	2002Sep9
PASS		
0	ft_july22	2002Sep9
PASS		
0	ft_july22_random	2002Sep9
PASS		
0	header_dump	2002Sep9
PASS		
0	header_dump_rt	2002Sep9
PASS		
0	header_dump_small	2002Sep9
PASS		
198	hostdata_blt	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep9
PASS		
0	im_load_b	2002Sep9
PASS		
0	im_load_cid_mismatch	2002Sep9
PASS		
0	im_load_immediate	2002Sep9
PASS		
0	im_load_immediate_nrt_me	2002Sep9
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep9
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep9
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep9
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep9
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep9
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep9
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep9
PASS		
0	im_load_indr1_a	2002Sep9
PASS		
0	im_load_indr1_b	2002Sep9

PASS		
0	im_load_indr2_a	2002Sep9
PASS		
0	im_load_indr2_b	2002Sep9
PASS		
0	im_load_invalidate	2002Sep9
PASS		
0	im_load_match	2002Sep9
PASS		
0	im_load_match2	2002Sep9
PASS		
0	im_load_nrt_me	2002Sep9
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep9
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep9
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep9
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep9
PASS		
0	im_load_pfmatch_disable	2002Sep9
PASS		
0	im_load_ring_a	2002Sep9
PASS		
0	im_load_ring_b	2002Sep9
PASS		
0	im_load_rt	2002Sep9
PASS		
0	im_load_rt2	2002Sep9
PASS		
0	im_load_rt3	2002Sep9
PASS		
0	im_load_rt4	2002Sep9
PASS		
0	im_load_sz_mismatch	2002Sep9
PASS		
0	incremental_update_const	2002Sep9
PASS		
0	incremental_update_instr	2002Sep9
PASS		
0	incremental_update_state	2002Sep9
PASS		
0	indirect_buffer_a	2002Sep9
PASS		
0	indirect_buffer_b	2002Sep9
PASS		
0	indirect_buffer_c	2002Sep9

PASS		
0	indirect_buffer_d	2002Sep9
PASS		
0	indirect_buffer_e	2002Sep9
PASS		
0	indirect_buffer_f	2002Sep9
PASS		
0	indirect_buffer_g	2002Sep9
PASS		
0	indirect_buffer_h	2002Sep9
PASS		
0	indirect_buffer_i	2002Sep9
PASS		
0	indirect_buffer_j	2002Sep9
PASS		
0	indirect_buffer_k	2002Sep9
PASS		
0	indirect_buffer_l	2002Sep9
PASS		
0	indirect_buffer_mp_a	2002Sep9
PASS		
0	indirect_buffer_mp_b	2002Sep9
PASS		
0	indirect_buffer_mp_c	2002Sep9
PASS		
0	indirect_buffer_mp_d	2002Sep9
PASS		
0	indirect_buffer_mp_e	2002Sep9
PASS		
0	indirect_buffer_mp_f	2002Sep9
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0	invalid_opcode	2002Sep9
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0	invalidate_on_me_init	2002Sep9
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0	invalidate_state_constants	2002Sep9
PASS		
0	invalidate_state_shaders	2002Sep9
PASS		
0	invalidate_state_shaders2	2002Sep9
PASS		
0	invalidate_state_sublks	2002Sep9
PASS		
0	invalidate_state_sublks2	2002Sep9
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0	iq1_iq2_mult	2002Sep9
PASS		
0	iq1_iq2_push_wrap	2002Sep9

PASS		
0	iq2_csq_full	2002Sep9
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0	ld_cnst_cntx_coherency_cntrl	2002Sep9
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep9
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep9
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep9
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep9
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep9
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep9
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep9
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep9
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep9
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0	ld_cnst_cntx_match	2002Sep9
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0	ld_cnst_cntx_match_valid_preserved	2002Sep9
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep9
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0	ld_cnst_cntx_mismatch_base	2002Sep9
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0	ld_cnst_cntx_mismatch_force	2002Sep9
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep9
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep9
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep9
PASS		
0	ld_cnst_cntx_replicate8	2002Sep9
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep9
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep9
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0	ld_cnst_cntx_ring_replicate	2002Sep9
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep9

PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep9
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep9
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep9
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep9
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0	load_constant_context_rt	2002Sep9
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0	load_palette	2002Sep9
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0	mem_write	2002Sep9
PASS		
0	mem_write_rt	2002Sep9
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0	mpeg_index	2002Sep9
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0	mpeg_index_multi_context_max_context_0	2002Sep9
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0	mpeg_index_multi_context_max_context_1	2002Sep9
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0	mpeg_index_multi_context_max_context_2	2002Sep9
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0	mpeg_index_multi_context_max_context_3	2002Sep9
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep9
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0	mpeg_index_multi_context_max_context_5	2002Sep9
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep9
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0	mpeg_index_multi_context_max_context_7	2002Sep9
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0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep9

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep9
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0	paint_multi	2002Sep9
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0	pitch_clip	2002Sep9
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0	ply_nextscan	2002Sep9
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0	rbbm_WU_2D_idle_01	2002Sep9
PASS		
0	rbbm_WU_2D_idle_02	2002Sep9
PASS		
0	rbbm_WU_2D_idle_03	2002Sep9
PASS		
0	rbbm_WU_2D_idle_04	2002Sep9
PASS		
0	rbbm_WU_2D_idle_05	2002Sep9
PASS		
0	rbbm_WU_2D_idle_06	2002Sep9
PASS		
0	rbbm_WU_2D_idle_07	2002Sep9
PASS		
0	rbbm_WU_2D_idle_08	2002Sep9
PASS		
0	rbbm_WU_2D_idle_09	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep9

PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep9
PASS		
0	rbbm_WU_3D_idle_01	2002Sep9
PASS		
0	rbbm_WU_3D_idle_02	2002Sep9
PASS		
0	rbbm_WU_3D_idle_03	2002Sep9
PASS		
0	rbbm_WU_3D_idle_04	2002Sep9
PASS		
0	rbbm_WU_3D_idle_05	2002Sep9
PASS		
0	rbbm_WU_3D_idle_06	2002Sep9
PASS		
0	rbbm_WU_3D_idle_07	2002Sep9
PASS		
0	rbbm_WU_3D_idle_08	2002Sep9
PASS		
0	rbbm_WU_3D_idle_09	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep9
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep9

PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep9
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep9
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP update_pending_off	2002Sep9
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP update_pending_off	2002Sep9
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep9
PASS		
0	rbbm_WU_OV_FLIP_D1OVL update_pending_off	2002Sep9
PASS		
0	rbbm_WU_OV_FLIP_D2OVL update_pending_off	2002Sep9
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep9
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep9
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep9
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep9
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep9
PASS		
0	rbbm_WU_idct_semaphore	2002Sep9
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep9
PASS		
377	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep9
FAIL	cp_rbbm.cmp	
378	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep9
FAIL	cp_rbbm.cmp	
379	rbbm_WUrt_CP_dma_idle	2002Sep9
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
380	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep9

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**FAIL**      cp_rbbm.cmp
381  rbbm_WUrt_CRTC_D1MODE_vline_on                2002Sep9
**FAIL**      cp_rbbm.cmp
382  rbbm_WUrt_CRTC_D1MODE_vline_re                2002Sep9
**FAIL**      cp_rbbm.cmp
383  rbbm_WUrt_CRTC_D2MODE_vline_fe                2002Sep9
**FAIL**      cp_rbbm.cmp
384  rbbm_WUrt_CRTC_D2MODE_vline_on                2002Sep9
**FAIL**      cp_rbbm.cmp
385  rbbm_WUrt_CRTC_D2MODE_vline_re                2002Sep9
**FAIL**      cp_rbbm.cmp
386  rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep9
**FAIL**      cp_rbbm.cmp
387  rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep9
**FAIL**      cp_rbbm.cmp
388  rbbm_WUrt_MH_hdp_clean                          2002Sep9
**FAIL**      cp_rbbm.cmp
389  rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off     2002Sep9
**FAIL**      cp_rbbm.cmp
390  rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off     2002Sep9
**FAIL**      cp_rbbm.cmp
391  rbbm_WUrt_RT_idle_01                            2002Sep9
**FAIL**      cp_rbbm.cmp
392  rbbm_WUrt_RT_idle_02                            2002Sep9
**FAIL**      cp_rbbm.cmp
393  rbbm_WUrt_RT_idle_03                            2002Sep9
**FAIL**      cp_rbbm.cmp
394  rbbm_WUrt_RT_idle_04                            2002Sep9
**FAIL**      cp_rbbm.cmp
395  rbbm_WUrt_RT_idle_clean_01                       2002Sep9
**FAIL**      cp_rbbm.cmp
396  rbbm_WUrt_RT_idle_clean_02                       2002Sep9
**FAIL**      cp_rbbm.cmp
397  rbbm_WUrt_RT_idle_clean_03                       2002Sep9
**FAIL**      cp_rbbm.cmp
398  rbbm_WUrt_RT_idle_clean_04                       2002Sep9
**FAIL**      cp_rbbm.cmp
399  rbbm_WUrt_RT_idle_clean_05                       2002Sep9
**FAIL**      cp_rbbm.cmp
400  rbbm_WUrt_VIPH0_dma_idle                          2002Sep9
**FAIL**      cp_rbbm.cmp
401  rbbm_WUrt_VIPH1_dma_idle                          2002Sep9
**FAIL**      cp_rbbm.cmp
402  rbbm_WUrt_VIPH2_dma_idle                          2002Sep9
**FAIL**      cp_rbbm.cmp
403  rbbm_WUrt_VIPH3_dma_idle                          2002Sep9
**FAIL**      cp_rbbm.cmp
404  rbbm_WUrt_pad_ext_sig_rising_edge                2002Sep9

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**FAIL**          cp_rbbm.cmp
0   rbbm_cp_non_queued_rdwr_01          2002Sep9
PASS
0   rbbm_cp_non_queued_rdwr_02          2002Sep9
PASS
0   rbbm_cp_queued_holds_nonqueued      2002Sep9
PASS
0   rbbm_cp_queued_rdwr                  2002Sep9
PASS
0   rbbm_isync_cpscratch_idlegui        2002Sep9
PASS
0   rbbm_isync_wait_idlegui             2002Sep9
PASS
0   rbbm_soft_reset                      2002Sep9
PASS
0   reg_to_mem                           2002Sep9
PASS
0   reg_to_mem_rt                        2002Sep9
PASS
0   rop_cfd_wmd                          2002Sep9
PASS
0   rstr_ordr                            2002Sep9
PASS
0   rt_invalid_opcode                    2002Sep9
PASS
0   rtee_arm_wait_idle_trigger_events_16 2002Sep9
PASS
0   rtee_discretes_arm_rearm_events_32    2002Sep9
PASS
0   rtee_discretes_falling_00_events_16  2002Sep9
PASS
0   rtee_discretes_falling_01_events_16  2002Sep9
PASS
0   rtee_discretes_falling_02_events_16  2002Sep9
PASS
0   rtee_discretes_falling_03_events_16  2002Sep9
PASS
0   rtee_discretes_falling_04_events_16  2002Sep9
PASS
0   rtee_discretes_falling_05_events_16  2002Sep9
PASS
0   rtee_discretes_falling_06_events_16  2002Sep9
PASS
0   rtee_discretes_falling_07_events_16  2002Sep9
PASS
0   rtee_discretes_falling_08_events_16  2002Sep9
PASS
0   rtee_discretes_falling_09_events_16  2002Sep9

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PASS		
0	rtee_discretes_falling_10_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep9
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep9

PASS		
0	rtee_discretes_invert_02_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep9
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0	rtee_discretes_invert_09_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep9
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0	rtee_discretes_invert_11_events_16	2002Sep9
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0	rtee_discretes_invert_12_events_16	2002Sep9
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0	rtee_discretes_invert_13_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep9
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0	rtee_discretes_invert_16_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep9
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0	rtee_discretes_invert_19_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep9
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep9
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0	rtee_discretes_invert_30_events_16	2002Sep9
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0	rtee_discretes_invert_31_events_16	2002Sep9
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0	rtee_discretes_level_01_events_16	2002Sep9
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0	rtee_discretes_level_02_events_16	2002Sep9
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0	rtee_discretes_level_03_events_16	2002Sep9
PASS		
0	rtee_discretes_level_04_events_16	2002Sep9
PASS		
0	rtee_discretes_level_05_events_16	2002Sep9
PASS		
0	rtee_discretes_level_06_events_16	2002Sep9
PASS		
0	rtee_discretes_level_07_events_16	2002Sep9
PASS		
0	rtee_discretes_level_08_events_16	2002Sep9
PASS		
0	rtee_discretes_level_09_events_16	2002Sep9
PASS		
0	rtee_discretes_level_10_events_16	2002Sep9
PASS		
0	rtee_discretes_level_11_events_16	2002Sep9
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0	rtee_discretes_level_12_events_16	2002Sep9
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0	rtee_discretes_level_13_events_16	2002Sep9
PASS		
0	rtee_discretes_level_14_events_16	2002Sep9
PASS		
0	rtee_discretes_level_15_events_16	2002Sep9
PASS		
0	rtee_discretes_level_16_events_16	2002Sep9
PASS		
0	rtee_discretes_level_17_events_16	2002Sep9

PASS		
0	rtee_discretes_level_18_events_16	2002Sep9
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0	rtee_discretes_level_19_events_16	2002Sep9
PASS		
0	rtee_discretes_level_20_events_16	2002Sep9
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0	rtee_discretes_level_21_events_16	2002Sep9
PASS		
0	rtee_discretes_level_22_events_16	2002Sep9
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0	rtee_discretes_level_23_events_16	2002Sep9
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0	rtee_discretes_level_24_events_16	2002Sep9
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0	rtee_discretes_level_25_events_16	2002Sep9
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0	rtee_discretes_level_26_events_16	2002Sep9
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0	rtee_discretes_level_27_events_16	2002Sep9
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0	rtee_discretes_level_29_events_16	2002Sep9
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0	rtee_discretes_level_30_events_16	2002Sep9
PASS		
0	rtee_discretes_level_31_events_16	2002Sep9
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep9

PASS		
0	rtee_discretes_rising_09_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep9
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0	rtee_discretes_rising_18_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep9
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep9
PASS		
0	rtee_display1_comp_events_16	2002Sep9

PASS		
0	rtee_display2_comp_events_16	2002Sep9
PASS		
0	rtee_mem_linear_search_events_2	2002Sep9
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep9
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep9
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep9
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep9
PASS		
0	rtee_prefetch_parse_nops	2002Sep9
PASS		
0	rtee_reg_linear_search_events_2	2002Sep9
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep9
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep9

PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep9
PASS		
0	rtee_vip_comp_events_16	2002Sep9
PASS		
0	scratch_mem_read	2002Sep9
PASS		
0	scratch_mem_read2	2002Sep9
PASS		
0	set_constant	2002Sep9
PASS		
0	set_constant2	2002Sep9
PASS		
0	set_constant_alu_max1	2002Sep9
PASS		
0	set_constant_alu_max2	2002Sep9
PASS		
0	set_constant_bool_max1	2002Sep9
PASS		
0	set_constant_bool_max2	2002Sep9
PASS		
0	set_constant_indr1_short	2002Sep9
PASS		
0	set_constant_indr1_short2	2002Sep9
PASS		
0	set_constant_indr1_write_enable	2002Sep9
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep9
PASS		
0	set_constant_indr2_short	2002Sep9
PASS		
0	set_constant_indr2_short2	2002Sep9
PASS		
0	set_constant_indr2_write_enable	2002Sep9
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep9
PASS		
0	set_constant_loop_max1	2002Sep9
PASS		
0	set_constant_loop_max2	2002Sep9
PASS		
0	set_constant_ring_short	2002Sep9
PASS		
0	set_constant_ring_short2	2002Sep9
PASS		
0	set_constant_ring_write_enable	2002Sep9
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep9

PASS		
0	set_constant_short	2002Sep9
PASS		
0	set_constant_short2	2002Sep9
PASS		
0	set_constant_tex_max1	2002Sep9
PASS		
0	set_constant_tex_max2	2002Sep9
PASS		
0	set_constant_write_enable	2002Sep9
PASS		
0	set_constant_write_enable_lcc	2002Sep9
PASS		
0	set_constant_write_enable_lcc2	2002Sep9
PASS		
0	set_scissors	2002Sep9
PASS		
0	set_st_indrl_match	2002Sep9
PASS		
0	set_st_indrl_mismatch	2002Sep9
PASS		
0	set_st_indrl_mismatch2	2002Sep9
PASS		
0	set_st_indrl_mismatch3	2002Sep9
PASS		
0	set_st_indrl_mismatch4	2002Sep9
PASS		
0	set_st_indrl_ps	2002Sep9
PASS		
0	set_st_indrl_ps_only	2002Sep9
PASS		
0	set_st_indrl_sublk0	2002Sep9
PASS		
0	set_st_indrl_sublk1	2002Sep9
PASS		
0	set_st_indrl_sublk1_only	2002Sep9
PASS		
0	set_st_indrl_sublk2	2002Sep9
PASS		
0	set_st_indrl_sublk2_only	2002Sep9
PASS		
0	set_st_indrl_sublk3	2002Sep9
PASS		
0	set_st_indrl_sublk3_only	2002Sep9
PASS		
0	set_st_indrl_sublk4	2002Sep9
PASS		
0	set_st_indrl_sublk4_only	2002Sep9

PASS		
0	set_st_indr1_sublk5	2002Sep9
PASS		
0	set_st_indr1_sublk5_only	2002Sep9
PASS		
0	set_st_indr1_sublk6	2002Sep9
PASS		
0	set_st_indr1_sublk6_only	2002Sep9
PASS		
0	set_st_indr1_sublk7	2002Sep9
PASS		
0	set_st_indr1_sublk7_only	2002Sep9
PASS		
0	set_st_indr1_vs	2002Sep9
PASS		
0	set_st_indr1_vs_only	2002Sep9
PASS		
0	set_st_indr2_match	2002Sep9
PASS		
0	set_st_indr2_mismatch	2002Sep9
PASS		
0	set_st_indr2_mismatch2	2002Sep9
PASS		
0	set_st_indr2_mismatch3	2002Sep9
PASS		
0	set_st_indr2_mismatch4	2002Sep9
PASS		
0	set_st_indr2_ps	2002Sep9
PASS		
0	set_st_indr2_ps_only	2002Sep9
PASS		
0	set_st_indr2_sublk0	2002Sep9
PASS		
0	set_st_indr2_sublk1	2002Sep9
PASS		
0	set_st_indr2_sublk1_only	2002Sep9
PASS		
0	set_st_indr2_sublk2	2002Sep9
PASS		
0	set_st_indr2_sublk2_only	2002Sep9
PASS		
0	set_st_indr2_sublk3	2002Sep9
PASS		
0	set_st_indr2_sublk3_only	2002Sep9
PASS		
0	set_st_indr2_sublk4	2002Sep9
PASS		
0	set_st_indr2_sublk4_only	2002Sep9

PASS		
0	set_st_indr2_sublk5	2002Sep9
PASS		
0	set_st_indr2_sublk5_only	2002Sep9
PASS		
0	set_st_indr2_sublk6	2002Sep9
PASS		
0	set_st_indr2_sublk6_only	2002Sep9
PASS		
0	set_st_indr2_sublk7	2002Sep9
PASS		
0	set_st_indr2_sublk7_only	2002Sep9
PASS		
0	set_st_indr2_vs	2002Sep9
PASS		
0	set_st_indr2_vs_only	2002Sep9
PASS		
0	set_st_pfmatch_disabled	2002Sep9
PASS		
0	set_st_ring_match	2002Sep9
PASS		
0	set_st_ring_mismatch	2002Sep9
PASS		
0	set_st_ring_mismatch2	2002Sep9
PASS		
0	set_st_ring_mismatch3	2002Sep9
PASS		
0	set_st_ring_mismatch4	2002Sep9
PASS		
0	set_st_ring_ps	2002Sep9
PASS		
0	set_st_ring_ps_only	2002Sep9
PASS		
0	set_st_ring_sublk0	2002Sep9
PASS		
0	set_st_ring_sublk1	2002Sep9
PASS		
0	set_st_ring_sublk1_only	2002Sep9
PASS		
0	set_st_ring_sublk2	2002Sep9
PASS		
0	set_st_ring_sublk2_only	2002Sep9
PASS		
0	set_st_ring_sublk3	2002Sep9
PASS		
0	set_st_ring_sublk3_only	2002Sep9
PASS		
0	set_st_ring_sublk4	2002Sep9

PASS		
0	set_st_ring_sublk4_only	2002Sep9
PASS		
0	set_st_ring_sublk5	2002Sep9
PASS		
0	set_st_ring_sublk5_only	2002Sep9
PASS		
0	set_st_ring_sublk6	2002Sep9
PASS		
0	set_st_ring_sublk6_only	2002Sep9
PASS		
0	set_st_ring_sublk7	2002Sep9
PASS		
0	set_st_ring_sublk7_only	2002Sep9
PASS		
0	set_st_ring_vs	2002Sep9
PASS		
0	set_st_ring_vs_only	2002Sep9
PASS		
0	set_st_stress_a	2002Sep9
PASS		
0	set_st_stress_b	2002Sep9
PASS		
0	set_st_stress_c	2002Sep9
PASS		
0	set_st_stress_d	2002Sep9
PASS		
0	set_st_stress_e	2002Sep9
PASS		
0	set_st_stress_f	2002Sep9
PASS		
0	set_state_rt_a	2002Sep9
PASS		
0	set_state_rt_b	2002Sep9
PASS		
0	set_state_rt_c	2002Sep9
PASS		
0	set_state_rt_d	2002Sep9
PASS		
0	smoke_nrt	2002Sep9
PASS		
0	src_clip	2002Sep9
PASS		
0	srf_cohr	2002Sep9
PASS		
0	trans_bitblt	2002Sep9
PASS		
0	type0_2long_pckts	2002Sep9

PASS		
0	type0_3indr_buffs	2002Sep9
PASS		
0	type0_4pckts	2002Sep9
PASS		
0	type0_incr_updt_cnst	2002Sep9
PASS		
0	type0_incr_updt_instrmem	2002Sep9
PASS		
0	type0_incr_updt_st	2002Sep9
PASS		
0	type0_incr_updt_stress	2002Sep9
PASS		
0	type0_ind1_ind2_a	2002Sep9
PASS		
0	type0_ind1_ind2_b	2002Sep9
PASS		
0	type0_indr_buff	2002Sep9
PASS		
0	type1_4pckts	2002Sep9
PASS		
0	type1_indr_buff	2002Sep9
PASS		
0	type1_pckt	2002Sep9
PASS		
0	type_random	2002Sep9
PASS		
706	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
707	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep9
PASS		
0	uram_wrt_ucode	2002Sep9
PASS		
0	viz_query_a	2002Sep9
PASS		
0	viz_query_draw_indx	2002Sep9
PASS		
0	viz_query_draw_indx_indr1	2002Sep9
PASS		
0	viz_query_draw_indx_indr2	2002Sep9
PASS		
714	viz_query_draw_indx_multipass	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_draw_indx_ring	2002Sep9
PASS		
0	viz_query_indr1	2002Sep9

PASS
0 viz_query_indr1_simple 2002Sep9
PASS
0 viz_query_indr2 2002Sep9
PASS
0 viz_query_indr2_simple 2002Sep9
PASS
0 viz_query_ring 2002Sep9
PASS
0 viz_query_ring_simple 2002Sep9
PASS
0 wait_for_idle 2002Sep9
PASS
0 wait_reg_mem_poll_mem0 2002Sep9
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep9
PASS
0 wait_reg_mem_poll_mem1 2002Sep9
PASS
0 wait_reg_mem_poll_mem2 2002Sep9
PASS
0 wait_reg_mem_poll_mem3 2002Sep9
PASS
0 wait_reg_mem_poll_reg0 2002Sep9
PASS
0 wait_reg_mem_poll_reg1 2002Sep9
PASS
0 wait_reg_mem_poll_reg2 2002Sep9
PASS
0 wait_reg_mem_poll_reg3 2002Sep9
PASS

=====
Tests_Run_count = 731 Pass_count = 696 Fail_count = 35 Incomplete_count = 0
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep22
	PASS		
0	2d_indr_buffer_b		2002Sep22
	PASS		
0	2d_indr_buffer_c		2002Sep22
	PASS		
0	2d_indr_buffer_d		2002Sep22
	PASS		
0	2d_indr_buffer_e		2002Sep22
	PASS		
0	2d_indr_buffer_f		2002Sep22
	PASS		
0	2d_indr_buffer_g		2002Sep22
	PASS		
8	2d_indr_buffer_random		2002Sep22
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep22
	PASS		
0	3d_draw_indx_2_b		2002Sep22
	PASS		
0	IQ2_Mult		2002Sep22
	PASS		
0	IQ2_Wrap_Legacy		2002Sep22
	PASS		
0	IQ_Wrap		2002Sep22
	PASS		
0	Isync_Control		2002Sep22
	PASS		
0	Isync_Control_2D_3D		2002Sep22
	PASS		
0	Isync_Control_3D_2D		2002Sep22
	PASS		
0	RB_BUFSZ_0		2002Sep22
	PASS		
0	RB_BUFSZ_1		2002Sep22
	PASS		
0	RB_Wrap		2002Sep22
	PASS		
20	Small_Text_BM		2002Sep22

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type0_IQ2_BM                                     2002Sep22
PASS
0   Type0_IQ2_BM_IB_Packet                           2002Sep22
PASS
0   Type0_IQ_BM                                       2002Sep22
PASS
0   Type0_IQ_BM_IB_Packet                             2002Sep22
PASS
0   Type0_PQ_BM                                       2002Sep22
PASS
0   Type0_PQ_BM_Pre_Write_Timer                       2002Sep22
PASS
0   Type3_Bitblt                                       2002Sep22
PASS
0   Type3_Load_Palette                               2002Sep22
PASS
0   Type3_NOP                                         2002Sep22
PASS
0   Type3_Polyline                                    2002Sep22
PASS
0   Type3_Polyline_Large                             2002Sep22
PASS
0   bitblt_multi                                      2002Sep22
PASS
0   brush_32x1mono                                    2002Sep22
PASS
0   brush_8x8color_16                                2002Sep22
PASS
0   brush_8x8color_32                                2002Sep22
PASS
0   brush_8x8color_64                                2002Sep22
PASS
0   brush_8x8color_dst_0_1                           2002Sep22
PASS
0   brush_8x8mono                                     2002Sep22
PASS
0   brush_color_mono_stress                           2002Sep22
PASS
0   brush_flag                                        2002Sep22
PASS
41  brush_type0                                       2002Sep22
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
42  brush_type1                                       2002Sep22
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
43  brush_type10                                      2002Sep22
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
44  brush_type13                                      2002Sep22

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
45  brush_type14                                2002Sep22
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
46  brush_type6                                  2002Sep22
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
47  brush_type7                                  2002Sep22
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
48  brush_type_other                             2002Sep22
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   brush_types_13_14                           2002Sep22
PASS
0   clear_const_wr_en_on_me_init                 2002Sep22
PASS
0   clear_const_wr_en_with_lcc                  2002Sep22
PASS
0   cond_write_mem_poll_mem                     2002Sep22
PASS
0   cond_write_mem_poll_mem2                    2002Sep22
PASS
0   cond_write_mem_poll_mem3                    2002Sep22
PASS
0   cond_write_mem_poll_mem_rt                  2002Sep22
PASS
0   cond_write_mem_poll_reg                     2002Sep22
PASS
0   cond_write_mem_poll_reg2                    2002Sep22
PASS
0   cond_write_mem_poll_reg3                    2002Sep22
PASS
59  cp_acpi_recovery_soft_reset                 2002Sep22
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   cp_bios_scratch                             2002Sep22
PASS
0   cp_ib2_base000_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base001_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base010_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base011_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base100_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base101_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base110_bufszXXX                     2002Sep22
PASS
0   cp_ib2_base111_bufszXXX                     2002Sep22

```

PASS		
0	cp_ib_base000_bufszXXX	2002Sep22
PASS		
0	cp_ib_base001_bufszXXX	2002Sep22
PASS		
0	cp_ib_base010_bufszXXX	2002Sep22
PASS		
0	cp_ib_base011_bufszXXX	2002Sep22
PASS		
0	cp_ib_base100_bufszXXX	2002Sep22
PASS		
0	cp_ib_base101_bufszXXX	2002Sep22
PASS		
0	cp_ib_base110_bufszXXX	2002Sep22
PASS		
0	cp_ib_base111_bufszXXX	2002Sep22
PASS		
0	cp_interrupt	2002Sep22
PASS		
0	cp_interrupt_rt_events_16	2002Sep22
PASS		
0	cp_iq2_alignment_000	2002Sep22
PASS		
0	cp_iq2_alignment_001	2002Sep22
PASS		
0	cp_iq2_alignment_010	2002Sep22
PASS		
0	cp_iq2_alignment_011	2002Sep22
PASS		
0	cp_iq2_alignment_100	2002Sep22
PASS		
0	cp_iq2_alignment_101	2002Sep22
PASS		
0	cp_iq2_alignment_110	2002Sep22
PASS		
0	cp_iq2_alignment_111	2002Sep22
PASS		
0	cp_iq_alignment_000	2002Sep22
PASS		
0	cp_iq_alignment_001	2002Sep22
PASS		
0	cp_iq_alignment_010	2002Sep22
PASS		
0	cp_iq_alignment_011	2002Sep22
PASS		
0	cp_iq_alignment_100	2002Sep22
PASS		
0	cp_iq_alignment_101	2002Sep22

PASS		
0	cp_iq_alignment_110	2002Sep22
PASS		
0	cp_iq_alignment_111	2002Sep22
PASS		
0	cp_max_size_packet	2002Sep22
PASS		
96	cp_max_size_packet_random	2002Sep22
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep22
PASS		
0	cp_output_stress	2002Sep22
PASS		
0	cp_output_stress_random	2002Sep22
PASS		
0	cp_protection_mode	2002Sep22
PASS		
0	cp_rb_alignment_000	2002Sep22
PASS		
0	cp_rb_alignment_001	2002Sep22
PASS		
0	cp_rb_alignment_010	2002Sep22
PASS		
0	cp_rb_alignment_011	2002Sep22
PASS		
0	cp_rb_alignment_100	2002Sep22
PASS		
0	cp_rb_alignment_101	2002Sep22
PASS		
0	cp_rb_alignment_110	2002Sep22
PASS		
0	cp_rb_alignment_111	2002Sep22
PASS		
0	cp_rb_wptrXXX_base000	2002Sep22
PASS		
0	cp_rb_wptrXXX_base001	2002Sep22
PASS		
0	cp_rb_wptrXXX_base010	2002Sep22
PASS		
0	cp_rb_wptrXXX_base011	2002Sep22
PASS		
0	cp_rb_wptrXXX_base100	2002Sep22
PASS		
0	cp_rb_wptrXXX_base101	2002Sep22
PASS		
0	cp_rb_wptrXXX_base110	2002Sep22
PASS		
0	cp_rb_wptrXXX_base111	2002Sep22

PASS		
0	cp_reg_rw_test	2002Sep22
PASS		
0	cp_register_reads	2002Sep22
PASS		
0	cp_rt_protection_mode	2002Sep22
PASS		
0	cp_scratch	2002Sep22
PASS		
0	cp_scratch_multi	2002Sep22
PASS		
0	csf_reg_rd_default	2002Sep22
PASS		
0	csf_reg_wr_rd	2002Sep22
PASS		
0	dma_cp_sync	2002Sep22
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep22
PASS		
0	dma_dscrstr_flush1	2002Sep22
PASS		
0	dma_dscrstr_flush2	2002Sep22
PASS		
0	dma_dscrstr_flush3	2002Sep22
PASS		
0	dma_m2m_src00_dstXX	2002Sep22
PASS		
0	dma_m2m_src01_dstXX	2002Sep22
PASS		
0	dma_m2m_src10_dstXX	2002Sep22
PASS		
0	dma_m2m_src11_dstXX	2002Sep22
PASS		
0	dma_mem2mem	2002Sep22
PASS		
0	dma_mem2mem_abort	2002Sep22
PASS		
0	dma_mem2mem_alignments	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep22

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep22
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep22
PASS		
0	dma_mem2mem_pio_abort	2002Sep22
PASS		
0	dma_mem2mem_random	2002Sep22
PASS		
0	dma_mem2reg	2002Sep22
PASS		
0	dma_mem2reg_abort	2002Sep22
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep22
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep22
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep22
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep22
PASS		
0	dma_mem2reg_sync	2002Sep22
PASS		
0	dma_r2r_m2m_r2r	2002Sep22
PASS		
0	dma_r2r_m2r_r2r	2002Sep22
PASS		
0	dma_r2r_r2m_r2r	2002Sep22
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep22
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep22
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep22
PASS		
0	dma_reg2mem	2002Sep22
PASS		
0	dma_reg2mem_abort	2002Sep22
PASS		
0	dma_reg2reg	2002Sep22
PASS		
0	dma_reg2reg_2dw	2002Sep22
PASS		
0	dma_reg2reg_daic	2002Sep22
PASS		
0	dma_reg2reg_mult	2002Sep22

PASS		
0	dma_reg2reg_mult2	2002Sep22
PASS		
0	dma_reg2reg_mult3	2002Sep22
PASS		
0	dma_reg2reg_saic	2002Sep22
PASS		
0	dma_reg2reg_sdaic	2002Sep22
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep22
PASS		
0	dma_reg2reg_stress	2002Sep22
PASS		
0	draw_indx_a	2002Sep22
PASS		
0	draw_indx_b	2002Sep22
PASS		
0	draw_indx_c	2002Sep22
PASS		
0	draw_indx_indr1_a	2002Sep22
PASS		
0	draw_indx_indr1_b	2002Sep22
PASS		
0	draw_indx_indr1_short_a	2002Sep22
PASS		
0	draw_indx_indr1_short_b	2002Sep22
PASS		
0	draw_indx_indr1_stress	2002Sep22
PASS		
0	draw_indx_indr2_a	2002Sep22
PASS		
0	draw_indx_indr2_b	2002Sep22
PASS		
0	draw_indx_indr2_short_a	2002Sep22
PASS		
0	draw_indx_indr2_short_b	2002Sep22
PASS		
0	draw_indx_indr2_stress	2002Sep22
PASS		
0	draw_indx_ring1	2002Sep22
PASS		
0	draw_indx_ring2	2002Sep22
PASS		
0	draw_indx_ring_short1	2002Sep22
PASS		
0	draw_indx_ring_short2	2002Sep22
PASS		
0	draw_indx_ring_stress	2002Sep22

PASS		
0	draw_indx_short_a	2002Sep22
PASS		
0	draw_indx_short_b	2002Sep22
PASS		
0	draw_indx_short_c	2002Sep22
PASS		
0	draw_indx_stress_a	2002Sep22
PASS		
0	draw_indx_stress_b	2002Sep22
PASS		
0	draw_indx_stress_c	2002Sep22
PASS		
0	event_timestamp_write	2002Sep22
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep22
PASS		
0	ft_indr1_july22	2002Sep22
PASS		
0	ft_indr2_july22	2002Sep22
PASS		
0	ft_july22	2002Sep22
PASS		
0	ft_july22_random	2002Sep22
PASS		
0	header_dump	2002Sep22
PASS		
0	header_dump_rt	2002Sep22
PASS		
0	header_dump_small	2002Sep22
PASS		
204	hostdata_blt	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep22
PASS		
0	im_load_b	2002Sep22
PASS		
0	im_load_cid_mismatch	2002Sep22
PASS		
0	im_load_immediate	2002Sep22
PASS		
0	im_load_immediate_nrt_me	2002Sep22
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep22
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep22
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep22

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep22
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep22
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep22
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep22
PASS		
0	im_load_indr1_a	2002Sep22
PASS		
0	im_load_indr1_b	2002Sep22
PASS		
0	im_load_indr2_a	2002Sep22
PASS		
0	im_load_indr2_b	2002Sep22
PASS		
0	im_load_invalidate	2002Sep22
PASS		
0	im_load_match	2002Sep22
PASS		
0	im_load_match2	2002Sep22
PASS		
0	im_load_nrt_me	2002Sep22
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep22
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep22
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep22
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep22
PASS		
0	im_load_pfmacth_disable	2002Sep22
PASS		
0	im_load_ring_a	2002Sep22
PASS		
0	im_load_ring_b	2002Sep22
PASS		
0	im_load_rt	2002Sep22
PASS		
0	im_load_rt2	2002Sep22
PASS		
0	im_load_rt3	2002Sep22
PASS		
0	im_load_rt4	2002Sep22
PASS		
0	im_load_sz_mismatch	2002Sep22

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0	incremental_update_const	2002Sep22
PASS		
0	incremental_update_instr	2002Sep22
PASS		
0	incremental_update_state	2002Sep22
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0	indirect_buffer_a	2002Sep22
PASS		
0	indirect_buffer_b	2002Sep22
PASS		
0	indirect_buffer_c	2002Sep22
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0	indirect_buffer_d	2002Sep22
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0	indirect_buffer_e	2002Sep22
PASS		
0	indirect_buffer_f	2002Sep22
PASS		
0	indirect_buffer_g	2002Sep22
PASS		
0	indirect_buffer_h	2002Sep22
PASS		
0	indirect_buffer_i	2002Sep22
PASS		
0	indirect_buffer_j	2002Sep22
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0	indirect_buffer_k	2002Sep22
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0	indirect_buffer_l	2002Sep22
PASS		
0	indirect_buffer_mp_a	2002Sep22
PASS		
0	indirect_buffer_mp_b	2002Sep22
PASS		
0	indirect_buffer_mp_c	2002Sep22
PASS		
0	indirect_buffer_mp_d	2002Sep22
PASS		
0	indirect_buffer_mp_e	2002Sep22
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0	indirect_buffer_mp_f	2002Sep22
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258	invalid_opcode	2002Sep22
INCOMPLETE		
0	invalidate_on_me_init	2002Sep22
PASS		
0	invalidate_state_constants	2002Sep22

PASS		
0	invalidate_state_shaders	2002Sep22
PASS		
0	invalidate_state_shaders2	2002Sep22
PASS		
0	invalidate_state_sublks	2002Sep22
PASS		
0	invalidate_state_sublks2	2002Sep22
PASS		
0	iq1_iq2_mult	2002Sep22
PASS		
0	iq1_iq2_push_wrap	2002Sep22
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0	iq2_csq_full	2002Sep22
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0	ld_cnst_cntx_coherency_cntrl	2002Sep22
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep22
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0	ld_cnst_cntx_indr1_replicate	2002Sep22
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0	ld_cnst_cntx_indr1_replicate4	2002Sep22
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep22
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep22
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep22
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep22
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep22
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep22
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0	ld_cnst_cntx_match	2002Sep22
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0	ld_cnst_cntx_match_valid_preserved	2002Sep22
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0	ld_cnst_cntx_mismatch_alutex	2002Sep22
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep22
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0	ld_cnst_cntx_mismatch_force	2002Sep22
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0	ld_cnst_cntx_pfmacth_disable	2002Sep22
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep22

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep22
PASS		
0	ld_cnst_cntx_replicate8	2002Sep22
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep22
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep22
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep22
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep22
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep22
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep22
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep22
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep22
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0	load_constant_context_rt	2002Sep22
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0	load_palette	2002Sep22
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0	lp_2d3d	2002Sep22
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0	mem_write	2002Sep22
PASS		
0	mem_write_rt	2002Sep22
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0	mpeg_index	2002Sep22
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0	mpeg_index_multi_context_max_context_0	2002Sep22
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0	mpeg_index_multi_context_max_context_1	2002Sep22
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep22
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep22
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep22
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep22
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep22
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep22

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep22
PASS		
0	nop_eating_disabled	2002Sep22
PASS		
0	paint_multi	2002Sep22
PASS		
0	paint_packet	2002Sep22
PASS		
319	paint_packet_gui_control	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	pitch_clip	2002Sep22
PASS		
0	ply_nextscan	2002Sep22
PASS		
0	rbbm_WU_2D_idle_01	2002Sep22
PASS		
0	rbbm_WU_2D_idle_02	2002Sep22
PASS		
0	rbbm_WU_2D_idle_03	2002Sep22
PASS		
0	rbbm_WU_2D_idle_04	2002Sep22
PASS		
0	rbbm_WU_2D_idle_05	2002Sep22
PASS		
0	rbbm_WU_2D_idle_06	2002Sep22
PASS		
0	rbbm_WU_2D_idle_07	2002Sep22
PASS		
0	rbbm_WU_2D_idle_08	2002Sep22
PASS		
0	rbbm_WU_2D_idle_09	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep22

PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep22
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep22
PASS		
0	rbbm_WU_3D_idle_01	2002Sep22
PASS		
0	rbbm_WU_3D_idle_02	2002Sep22
PASS		
0	rbbm_WU_3D_idle_03	2002Sep22
PASS		
0	rbbm_WU_3D_idle_04	2002Sep22
PASS		
0	rbbm_WU_3D_idle_05	2002Sep22
PASS		
0	rbbm_WU_3D_idle_06	2002Sep22
PASS		
0	rbbm_WU_3D_idle_07	2002Sep22
PASS		
0	rbbm_WU_3D_idle_08	2002Sep22
PASS		
0	rbbm_WU_3D_idle_09	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep22

PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep22
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep22
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep22
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep22
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep22
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep22
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep22
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep22
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep22
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep22
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep22
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep22
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep22
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep22
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep22
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep22
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep22
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep22
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep22

PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep22
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep22
PASS		
0	rbbm_WU_idct_semaphore	2002Sep22
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep22
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep22
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep22
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep22
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep22
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep22
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep22
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep22
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep22
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep22
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep22
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep22
PASS		
403	rbbm_WUrt_RT_idle_clean_01	2002Sep22
FAIL		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep22

PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep22
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep22
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep22
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep22
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep22
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep22
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep22
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0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep22
PASS		
0	rbbm_clk_active_deassert_min_max	2002Sep22
PASS		
0	rbbm_clk_active_each_pipe	2002Sep22
PASS		
0	rbbm_clk_active_for_read_error	2002Sep22
PASS		
0	rbbm_clk_active_for_read_request	2002Sep22
PASS		
0	rbbm_clk_active_on_reset	2002Sep22
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep22
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep22
PASS		
420	rbbm_cp_queued_holds_nonqueued	2002Sep22
FAIL	cp_rbbm.cmp	
0	rbbm_cp_queued_rdwr	2002Sep22
PASS		
0	rbbm_int_ack	2002Sep22
PASS		
0	rbbm_int_each_client	2002Sep22
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Sep22
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep22
PASS		
0	rbbm_int_rderr	2002Sep22
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep22
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep22

PASS		
0	rbbm_isync_wait_idlegui	2002Sep22
PASS		
0	rbbm_soft_reset	2002Sep22
PASS		
0	reg_to_mem	2002Sep22
PASS		
0	reg_to_mem_rt	2002Sep22
PASS		
0	rop_cfd_wmd	2002Sep22
PASS		
0	rstr_ordr	2002Sep22
PASS		
0	rt_invalid_opcode	2002Sep22
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Sep22
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep22
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep22
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep22
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep22
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep22
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep22
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep22
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0	rtee_discretes_falling_06_events_16	2002Sep22
PASS		
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PASS		
0	rtee_discretes_falling_08_events_16	2002Sep22
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0	rtee_discretes_falling_10_events_16	2002Sep22
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0	rtee_discretes_falling_11_events_16	2002Sep22
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0	rtee_discretes_falling_12_events_16	2002Sep22
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0	rtee_discretes_falling_14_events_16	2002Sep22

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0	rtee_discretes_falling_22_events_16	2002Sep22
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0	rtee_discretes_falling_23_events_16	2002Sep22
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0	rtee_discretes_falling_25_events_16	2002Sep22
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0	rtee_discretes_falling_26_events_16	2002Sep22
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0	rtee_discretes_falling_27_events_16	2002Sep22
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0	rtee_discretes_falling_28_events_16	2002Sep22
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0	rtee_discretes_falling_29_events_16	2002Sep22
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0	rtee_discretes_falling_30_events_16	2002Sep22
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0	rtee_discretes_falling_31_events_16	2002Sep22
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0	rtee_discretes_invert_00_events_16	2002Sep22
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0	rtee_discretes_invert_01_events_16	2002Sep22
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0	rtee_discretes_invert_02_events_16	2002Sep22
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0	rtee_discretes_invert_03_events_16	2002Sep22
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0	rtee_discretes_invert_04_events_16	2002Sep22
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0	rtee_discretes_invert_05_events_16	2002Sep22
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0	rtee_discretes_invert_06_events_16	2002Sep22

PASS		
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0	rtee_discretes_invert_18_events_16	2002Sep22
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0	rtee_discretes_invert_19_events_16	2002Sep22
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0	rtee_discretes_invert_21_events_16	2002Sep22
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0	rtee_discretes_invert_22_events_16	2002Sep22
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0	rtee_discretes_invert_23_events_16	2002Sep22
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0	rtee_discretes_invert_24_events_16	2002Sep22
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0	rtee_discretes_invert_25_events_16	2002Sep22
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep22
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep22
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep22
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep22
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep22

PASS		
0	rtee_discretes_invert_31_events_16	2002Sep22
PASS		
0	rtee_discretes_level_00_events_16	2002Sep22
PASS		
0	rtee_discretes_level_01_events_16	2002Sep22
PASS		
0	rtee_discretes_level_02_events_16	2002Sep22
PASS		
0	rtee_discretes_level_03_events_16	2002Sep22
PASS		
0	rtee_discretes_level_04_events_16	2002Sep22
PASS		
0	rtee_discretes_level_05_events_16	2002Sep22
PASS		
0	rtee_discretes_level_06_events_16	2002Sep22
PASS		
0	rtee_discretes_level_07_events_16	2002Sep22
PASS		
0	rtee_discretes_level_08_events_16	2002Sep22
PASS		
0	rtee_discretes_level_09_events_16	2002Sep22
PASS		
0	rtee_discretes_level_10_events_16	2002Sep22
PASS		
0	rtee_discretes_level_11_events_16	2002Sep22
PASS		
0	rtee_discretes_level_12_events_16	2002Sep22
PASS		
0	rtee_discretes_level_13_events_16	2002Sep22
PASS		
0	rtee_discretes_level_14_events_16	2002Sep22
PASS		
0	rtee_discretes_level_15_events_16	2002Sep22
PASS		
0	rtee_discretes_level_16_events_16	2002Sep22
PASS		
0	rtee_discretes_level_17_events_16	2002Sep22
PASS		
0	rtee_discretes_level_18_events_16	2002Sep22
PASS		
0	rtee_discretes_level_19_events_16	2002Sep22
PASS		
0	rtee_discretes_level_20_events_16	2002Sep22
PASS		
0	rtee_discretes_level_21_events_16	2002Sep22
PASS		
0	rtee_discretes_level_22_events_16	2002Sep22

PASS		
0	rtee_discretes_level_23_events_16	2002Sep22
PASS		
0	rtee_discretes_level_24_events_16	2002Sep22
PASS		
0	rtee_discretes_level_25_events_16	2002Sep22
PASS		
0	rtee_discretes_level_26_events_16	2002Sep22
PASS		
0	rtee_discretes_level_27_events_16	2002Sep22
PASS		
0	rtee_discretes_level_28_events_16	2002Sep22
PASS		
0	rtee_discretes_level_29_events_16	2002Sep22
PASS		
0	rtee_discretes_level_30_events_16	2002Sep22
PASS		
0	rtee_discretes_level_31_events_16	2002Sep22
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep22

PASS		
0	rtee_discretes_rising_14_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep22
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep22
PASS		
0	rtee_display1_comp_events_16	2002Sep22
PASS		
0	rtee_display2_comp_events_16	2002Sep22
PASS		
0	rtee_mem_linear_search_events_2	2002Sep22
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep22

PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep22
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep22
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep22
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep22
PASS		
0	rtee_prefetch_parse_nops	2002Sep22
PASS		
0	rtee_reg_linear_search_events_2	2002Sep22
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep22
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep22
PASS		
0	rtee_vip_comp_events_16	2002Sep22
PASS		
0	scratch_mem_read	2002Sep22
PASS		
0	scratch_mem_read2	2002Sep22
PASS		
0	set_constant	2002Sep22

PASS		
0	set_constant2	2002Sep22
PASS		
0	set_constant_alu_max1	2002Sep22
PASS		
0	set_constant_alu_max2	2002Sep22
PASS		
0	set_constant_bool_max1	2002Sep22
PASS		
0	set_constant_bool_max2	2002Sep22
PASS		
0	set_constant_indr1_short	2002Sep22
PASS		
0	set_constant_indr1_short2	2002Sep22
PASS		
0	set_constant_indr1_write_enable	2002Sep22
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep22
PASS		
0	set_constant_indr2_short	2002Sep22
PASS		
0	set_constant_indr2_short2	2002Sep22
PASS		
0	set_constant_indr2_write_enable	2002Sep22
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep22
PASS		
0	set_constant_loop_max1	2002Sep22
PASS		
0	set_constant_loop_max2	2002Sep22
PASS		
0	set_constant_ring_short	2002Sep22
PASS		
0	set_constant_ring_short2	2002Sep22
PASS		
0	set_constant_ring_write_enable	2002Sep22
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep22
PASS		
0	set_constant_short	2002Sep22
PASS		
0	set_constant_short2	2002Sep22
PASS		
0	set_constant_tex_max1	2002Sep22
PASS		
0	set_constant_tex_max2	2002Sep22
PASS		
0	set_constant_write_enable	2002Sep22

PASS		
0	set_constant_write_enable_lcc	2002Sep22
PASS		
0	set_constant_write_enable_lcc2	2002Sep22
PASS		
0	set_scissors	2002Sep22
PASS		
0	set_st_indrl_match	2002Sep22
PASS		
0	set_st_indrl_mismatch	2002Sep22
PASS		
0	set_st_indrl_mismatch2	2002Sep22
PASS		
0	set_st_indrl_mismatch3	2002Sep22
PASS		
0	set_st_indrl_mismatch4	2002Sep22
PASS		
0	set_st_indrl_ps	2002Sep22
PASS		
0	set_st_indrl_ps_only	2002Sep22
PASS		
0	set_st_indrl_sublk0	2002Sep22
PASS		
0	set_st_indrl_sublk1	2002Sep22
PASS		
0	set_st_indrl_sublk1_only	2002Sep22
PASS		
0	set_st_indrl_sublk2	2002Sep22
PASS		
0	set_st_indrl_sublk2_only	2002Sep22
PASS		
0	set_st_indrl_sublk3	2002Sep22
PASS		
0	set_st_indrl_sublk3_only	2002Sep22
PASS		
0	set_st_indrl_sublk4	2002Sep22
PASS		
0	set_st_indrl_sublk4_only	2002Sep22
PASS		
0	set_st_indrl_sublk5	2002Sep22
PASS		
0	set_st_indrl_sublk5_only	2002Sep22
PASS		
0	set_st_indrl_sublk6	2002Sep22
PASS		
0	set_st_indrl_sublk6_only	2002Sep22
PASS		
0	set_st_indrl_sublk7	2002Sep22

PASS		
0	set_st_indr1_sublk7_only	2002Sep22
PASS		
0	set_st_indr1_vs	2002Sep22
PASS		
0	set_st_indr1_vs_only	2002Sep22
PASS		
0	set_st_indr2_match	2002Sep22
PASS		
0	set_st_indr2_mismatch	2002Sep22
PASS		
0	set_st_indr2_mismatch2	2002Sep22
PASS		
0	set_st_indr2_mismatch3	2002Sep22
PASS		
0	set_st_indr2_mismatch4	2002Sep22
PASS		
0	set_st_indr2_ps	2002Sep22
PASS		
0	set_st_indr2_ps_only	2002Sep22
PASS		
0	set_st_indr2_sublk0	2002Sep22
PASS		
0	set_st_indr2_sublk1	2002Sep22
PASS		
0	set_st_indr2_sublk1_only	2002Sep22
PASS		
0	set_st_indr2_sublk2	2002Sep22
PASS		
0	set_st_indr2_sublk2_only	2002Sep22
PASS		
0	set_st_indr2_sublk3	2002Sep22
PASS		
0	set_st_indr2_sublk3_only	2002Sep22
PASS		
0	set_st_indr2_sublk4	2002Sep22
PASS		
0	set_st_indr2_sublk4_only	2002Sep22
PASS		
0	set_st_indr2_sublk5	2002Sep22
PASS		
0	set_st_indr2_sublk5_only	2002Sep22
PASS		
0	set_st_indr2_sublk6	2002Sep22
PASS		
0	set_st_indr2_sublk6_only	2002Sep22
PASS		
0	set_st_indr2_sublk7	2002Sep22

PASS		
0	set_st_indr2_sublk7_only	2002Sep22
PASS		
0	set_st_indr2_vs	2002Sep22
PASS		
0	set_st_indr2_vs_only	2002Sep22
PASS		
0	set_st_pfmatch_disabled	2002Sep22
PASS		
0	set_st_ring_match	2002Sep22
PASS		
0	set_st_ring_mismatch	2002Sep22
PASS		
0	set_st_ring_mismatch2	2002Sep22
PASS		
0	set_st_ring_mismatch3	2002Sep22
PASS		
0	set_st_ring_mismatch4	2002Sep22
PASS		
0	set_st_ring_ps	2002Sep22
PASS		
0	set_st_ring_ps_only	2002Sep22
PASS		
0	set_st_ring_sublk0	2002Sep22
PASS		
0	set_st_ring_sublk1	2002Sep22
PASS		
0	set_st_ring_sublk1_only	2002Sep22
PASS		
0	set_st_ring_sublk2	2002Sep22
PASS		
0	set_st_ring_sublk2_only	2002Sep22
PASS		
0	set_st_ring_sublk3	2002Sep22
PASS		
0	set_st_ring_sublk3_only	2002Sep22
PASS		
0	set_st_ring_sublk4	2002Sep22
PASS		
0	set_st_ring_sublk4_only	2002Sep22
PASS		
0	set_st_ring_sublk5	2002Sep22
PASS		
0	set_st_ring_sublk5_only	2002Sep22
PASS		
0	set_st_ring_sublk6	2002Sep22
PASS		
0	set_st_ring_sublk6_only	2002Sep22

PASS		
0	set_st_ring_sublk7	2002Sep22
PASS		
0	set_st_ring_sublk7_only	2002Sep22
PASS		
0	set_st_ring_vs	2002Sep22
PASS		
0	set_st_ring_vs_only	2002Sep22
PASS		
0	set_st_stress_a	2002Sep22
PASS		
0	set_st_stress_b	2002Sep22
PASS		
0	set_st_stress_c	2002Sep22
PASS		
0	set_st_stress_d	2002Sep22
PASS		
0	set_st_stress_e	2002Sep22
PASS		
0	set_st_stress_f	2002Sep22
PASS		
703	set_state_rt_a	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
704	set_state_rt_b	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
705	set_state_rt_c	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
706	set_state_rt_d	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep22
PASS		
708	src_clip	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	srf_cohr	2002Sep22
PASS		
0	trans_bitblt	2002Sep22
PASS		
0	type0_2long_pckts	2002Sep22
PASS		
0	type0_3indr_buffs	2002Sep22
PASS		
0	type0_4pckts	2002Sep22
PASS		
0	type0_incr_updt_cnst	2002Sep22
PASS		
0	type0_incr_updt_instrmem	2002Sep22
PASS		
0	type0_incr_updt_st	2002Sep22

PASS		
0	type0_incr_updt_stress	2002Sep22
PASS		
0	type0_ind1_ind2_a	2002Sep22
PASS		
0	type0_ind1_ind2_b	2002Sep22
PASS		
0	type0_indr_buff	2002Sep22
PASS		
0	type1_4pckts	2002Sep22
PASS		
0	type1_indr_buff	2002Sep22
PASS		
0	type1_pckt	2002Sep22
PASS		
724	type_random	2002Sep22
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
725	uram_wrt_rt_ucode	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
726	uram_wrt_ucode	2002Sep22
FAIL	rbbmWr.cmp	
727	viz_query_a	2002Sep22
FAIL	rbbmWr.cmp	
728	viz_query_draw_indx	2002Sep22
FAIL	rbbmWr.cmp	
729	viz_query_draw_indx_indr1	2002Sep22
FAIL	rbbmWr.cmp	
730	viz_query_draw_indx_indr2	2002Sep22
FAIL	rbbmWr.cmp	
731	viz_query_draw_indx_multipass	2002Sep22
FAIL	cp_rbbm.cmp rbbmWr.cmp	
732	viz_query_draw_indx_ring	2002Sep22
FAIL	rbbmWr.cmp	
733	viz_query_indr1	2002Sep22
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep22
PASS		
735	viz_query_indr2	2002Sep22
FAIL	rbbmWr.cmp	
0	viz_query_indr2_simple	2002Sep22
PASS		
737	viz_query_ring	2002Sep22
FAIL	rbbmWr.cmp	
0	viz_query_ring_simple	2002Sep22
PASS		
0	wait_for_idle	2002Sep22
PASS		
0	wait_reg_mem_poll_mem0	2002Sep22

PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep22
PASS
0 wait_reg_mem_poll_mem1 2002Sep22
PASS
0 wait_reg_mem_poll_mem2 2002Sep22
PASS
0 wait_reg_mem_poll_mem3 2002Sep22
PASS
0 wait_reg_mem_poll_reg0 2002Sep22
PASS
0 wait_reg_mem_poll_reg1 2002Sep22
PASS
0 wait_reg_mem_poll_reg2 2002Sep22
PASS
0 wait_reg_mem_poll_reg3 2002Sep22
PASS

=====
Tests_Run_count = 748 Pass_count = 714 Fail_count = 33 Incomplete_count = 1
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep1
	PASS		
0	2d_indr_buffer_b		2002Sep1
	PASS		
0	2d_indr_buffer_c		2002Sep1
	PASS		
0	2d_indr_buffer_d		2002Sep1
	PASS		
0	2d_indr_buffer_e		2002Sep1
	PASS		
0	2d_indr_buffer_f		2002Sep1
	PASS		
0	2d_indr_buffer_g		2002Sep1
	PASS		
0	3d_draw_indx_2		2002Sep1
	PASS		
0	3d_draw_indx_2_b		2002Sep1
	PASS		
0	bitblt_multi		2002Sep1
	PASS		
0	brush_32x1mono		2002Sep1
	PASS		
0	brush_8x8color_16		2002Sep1
	PASS		
0	brush_8x8color_32		2002Sep1
	PASS		
0	brush_8x8color_64		2002Sep1
	PASS		
0	brush_8x8color_dst_0_1		2002Sep1
	PASS		
0	brush_8x8mono		2002Sep1
	PASS		
0	brush_color_mono_stress		2002Sep1
	PASS		
0	brush_flag		2002Sep1
	PASS		
0	brush_type0		2002Sep1
	PASS		
0	brush_type1		2002Sep1

PASS		
0	brush_type10	2002Sep1
PASS		
0	brush_type13	2002Sep1
PASS		
0	brush_type14	2002Sep1
PASS		
0	brush_type6	2002Sep1
PASS		
0	brush_type7	2002Sep1
PASS		
0	brush_type_other	2002Sep1
PASS		
0	brush_types_13_14	2002Sep1
PASS		
0	clear_const_wr_en_on_me_init	2002Sep1
PASS		
0	clear_const_wr_en_with_lcc	2002Sep1
PASS		
0	cond_write_mem_poll_mem	2002Sep1
PASS		
0	cond_write_mem_poll_mem2	2002Sep1
PASS		
0	cond_write_mem_poll_mem3	2002Sep1
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep1
PASS		
0	cond_write_mem_poll_reg	2002Sep1
PASS		
0	cond_write_mem_poll_reg2	2002Sep1
PASS		
0	cond_write_mem_poll_reg3	2002Sep1
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep1
PASS		
0	cp_bios_scratch	2002Sep1
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep1

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep1
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep1
PASS		
0	cp_ib_base000_bufszXXX	2002Sep1
PASS		
0	cp_ib_base001_bufszXXX	2002Sep1
PASS		
0	cp_ib_base010_bufszXXX	2002Sep1
PASS		
0	cp_ib_base011_bufszXXX	2002Sep1
PASS		
0	cp_ib_base100_bufszXXX	2002Sep1
PASS		
0	cp_ib_base101_bufszXXX	2002Sep1
PASS		
0	cp_ib_base110_bufszXXX	2002Sep1
PASS		
0	cp_ib_base111_bufszXXX	2002Sep1
PASS		
0	cp_interrupt	2002Sep1
PASS		
0	cp_interrupt_rt_events_16	2002Sep1
PASS		
0	cp_iq2_alignment_000	2002Sep1
PASS		
0	cp_iq2_alignment_001	2002Sep1
PASS		
0	cp_iq2_alignment_010	2002Sep1
PASS		
0	cp_iq2_alignment_011	2002Sep1
PASS		
0	cp_iq2_alignment_100	2002Sep1
PASS		
0	cp_iq2_alignment_101	2002Sep1
PASS		
0	cp_iq2_alignment_110	2002Sep1
PASS		
0	cp_iq2_alignment_111	2002Sep1
PASS		
0	cp_iq_alignment_000	2002Sep1
PASS		
0	cp_iq_alignment_001	2002Sep1
PASS		
0	cp_iq_alignment_010	2002Sep1
PASS		
0	cp_iq_alignment_011	2002Sep1

PASS		
0	cp_iq_alignment_100	2002Sep1
PASS		
0	cp_iq_alignment_101	2002Sep1
PASS		
0	cp_iq_alignment_110	2002Sep1
PASS		
0	cp_iq_alignment_111	2002Sep1
PASS		
0	cp_max_size_packet	2002Sep1
PASS		
74	cp_max_size_packet_random	2002Sep1
FAIL	rbbmWr.cmp	
0	cp_nrt_rt_protection_mode	2002Sep1
PASS		
0	cp_output_stress	2002Sep1
PASS		
0	cp_protection_mode	2002Sep1
PASS		
0	cp_rb_alignment_000	2002Sep1
PASS		
0	cp_rb_alignment_001	2002Sep1
PASS		
0	cp_rb_alignment_010	2002Sep1
PASS		
0	cp_rb_alignment_011	2002Sep1
PASS		
0	cp_rb_alignment_100	2002Sep1
PASS		
0	cp_rb_alignment_101	2002Sep1
PASS		
0	cp_rb_alignment_110	2002Sep1
PASS		
0	cp_rb_alignment_111	2002Sep1
PASS		
0	cp_rb_wptrXXX_base000	2002Sep1
PASS		
0	cp_rb_wptrXXX_base001	2002Sep1
PASS		
0	cp_rb_wptrXXX_base010	2002Sep1
PASS		
0	cp_rb_wptrXXX_base011	2002Sep1
PASS		
0	cp_rb_wptrXXX_base100	2002Sep1
PASS		
0	cp_rb_wptrXXX_base101	2002Sep1
PASS		
0	cp_rb_wptrXXX_base110	2002Sep1

PASS		
0	cp_rb_wptrXXX_base11	2002Sep1
PASS		
0	cp_register_reads	2002Sep1
PASS		
0	cp_reg_rw_test	2002Sep1
PASS		
0	cp_rt_protection_mode	2002Sep1
PASS		
0	cp_scratch	2002Sep1
PASS		
0	cp_scratch_multi	2002Sep1
PASS		
0	dma_cp_sync	2002Sep1
PASS		
0	dma_dscrstr_chckn_bit	2002Sep1
PASS		
0	dma_dscrstr_flush1	2002Sep1
PASS		
0	dma_dscrstr_flush2	2002Sep1
PASS		
0	dma_dscrstr_flush3	2002Sep1
PASS		
0	dma_m2m_src00_dstXX	2002Sep1
PASS		
0	dma_m2m_src01_dstXX	2002Sep1
PASS		
0	dma_m2m_src10_dstXX	2002Sep1
PASS		
0	dma_m2m_src11_dstXX	2002Sep1
PASS		
0	dma_mem2mem	2002Sep1
PASS		
0	dma_mem2mem_abort	2002Sep1
PASS		
0	dma_mem2mem_alignments	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep1

PASS		
0	dma_mem2mem_byte_alignment6	2002Sep1
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep1
PASS		
0	dma_mem2mem_pio_abort	2002Sep1
PASS		
0	dma_mem2reg	2002Sep1
PASS		
0	dma_mem2reg_abort	2002Sep1
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep1
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep1
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep1
PASS		
0	dma_mem2reg_sync	2002Sep1
PASS		
0	dma_r2r_m2m_r2r	2002Sep1
PASS		
0	dma_r2r_m2r_r2r	2002Sep1
PASS		
0	dma_r2r_r2m_r2r	2002Sep1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep1
PASS		
0	dma_reg2mem	2002Sep1
PASS		
0	dma_reg2mem_abort	2002Sep1
PASS		
0	dma_reg2reg	2002Sep1
PASS		
0	dma_reg2reg_2dw	2002Sep1
PASS		
0	dma_reg2reg_daic	2002Sep1
PASS		
0	dma_reg2reg_mult	2002Sep1
PASS		
0	dma_reg2reg_mult2	2002Sep1
PASS		
0	dma_reg2reg_mult3	2002Sep1
PASS		
0	dma_reg2reg_saic	2002Sep1

PASS		
0	dma_reg2reg_sdaic	2002Sep1
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep1
PASS		
0	dma_reg2reg_stress	2002Sep1
PASS		
0	draw_indx_a	2002Sep1
PASS		
0	draw_indx_b	2002Sep1
PASS		
0	draw_indx_c	2002Sep1
PASS		
0	draw_indx_indr1_a	2002Sep1
PASS		
0	draw_indx_indr1_b	2002Sep1
PASS		
0	draw_indx_indr1_short_a	2002Sep1
PASS		
0	draw_indx_indr1_short_b	2002Sep1
PASS		
0	draw_indx_indr1_stress	2002Sep1
PASS		
0	draw_indx_indr2_a	2002Sep1
PASS		
0	draw_indx_indr2_b	2002Sep1
PASS		
0	draw_indx_indr2_short_a	2002Sep1
PASS		
0	draw_indx_indr2_short_b	2002Sep1
PASS		
0	draw_indx_indr2_stress	2002Sep1
PASS		
0	draw_indx_ring1	2002Sep1
PASS		
0	draw_indx_ring2	2002Sep1
PASS		
0	draw_indx_ring_short1	2002Sep1
PASS		
0	draw_indx_ring_short2	2002Sep1
PASS		
0	draw_indx_ring_stress	2002Sep1
PASS		
0	draw_indx_short_a	2002Sep1
PASS		
0	draw_indx_short_b	2002Sep1
PASS		
0	draw_indx_short_c	2002Sep1

PASS		
0	draw_indx_stress_a	2002Sep1
PASS		
0	draw_indx_stress_b	2002Sep1
PASS		
0	draw_indx_stress_c	2002Sep1
PASS		
168	event_timestamp_write	2002Sep1
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep1
PASS		
0	ft_indr1_july22	2002Sep1
PASS		
0	ft_indr2_july22	2002Sep1
PASS		
0	ft_july22	2002Sep1
PASS		
0	ft_july22_random	2002Sep1
PASS		
0	header_dump	2002Sep1
PASS		
0	header_dump_rt	2002Sep1
PASS		
0	header_dump_small	2002Sep1
PASS		
0	hostdata_blt	2002Sep1
PASS		
0	im_load_a	2002Sep1
PASS		
0	im_load_b	2002Sep1
PASS		
0	im_load_cid_mismatch	2002Sep1
PASS		
0	im_load_immediate	2002Sep1
PASS		
0	im_load_immediate_nrt_me	2002Sep1
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep1
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep1
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep1
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep1
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep1
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep1

PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep1
PASS		
0	im_load_indr1_a	2002Sep1
PASS		
0	im_load_indr1_b	2002Sep1
PASS		
0	im_load_indr2_a	2002Sep1
PASS		
0	im_load_indr2_b	2002Sep1
PASS		
0	im_load_invalidate	2002Sep1
PASS		
0	im_load_match	2002Sep1
PASS		
0	im_load_match2	2002Sep1
PASS		
0	im_load_nrt_me	2002Sep1
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep1
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep1
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep1
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep1
PASS		
0	im_load_pfmatch_disable	2002Sep1
PASS		
0	im_load_ring_a	2002Sep1
PASS		
0	im_load_ring_b	2002Sep1
PASS		
0	im_load_rt	2002Sep1
PASS		
0	im_load_rt2	2002Sep1
PASS		
0	im_load_rt3	2002Sep1
PASS		
0	im_load_rt4	2002Sep1
PASS		
0	im_load_sz_mismatch	2002Sep1
PASS		
0	incremental_update_const	2002Sep1
PASS		
0	incremental_update_instr	2002Sep1
PASS		
0	incremental_update_state	2002Sep1

PASS		
0	indirect_buffer_a	2002Sep1
PASS		
0	indirect_buffer_b	2002Sep1
PASS		
0	indirect_buffer_c	2002Sep1
PASS		
0	indirect_buffer_d	2002Sep1
PASS		
0	indirect_buffer_e	2002Sep1
PASS		
0	indirect_buffer_f	2002Sep1
PASS		
0	indirect_buffer_g	2002Sep1
PASS		
0	indirect_buffer_h	2002Sep1
PASS		
0	indirect_buffer_i	2002Sep1
PASS		
0	indirect_buffer_j	2002Sep1
PASS		
0	indirect_buffer_k	2002Sep1
PASS		
0	indirect_buffer_l	2002Sep1
PASS		
0	indirect_buffer_mp_a	2002Sep1
PASS		
0	indirect_buffer_mp_b	2002Sep1
PASS		
0	indirect_buffer_mp_c	2002Sep1
PASS		
0	indirect_buffer_mp_d	2002Sep1
PASS		
0	indirect_buffer_mp_e	2002Sep1
PASS		
0	indirect_buffer_mp_f	2002Sep1
PASS		
0	invalidate_on_me_init	2002Sep1
PASS		
0	invalidate_state_constants	2002Sep1
PASS		
0	invalidate_state_shaders	2002Sep1
PASS		
0	invalidate_state_shaders2	2002Sep1
PASS		
0	invalidate_state_sublks	2002Sep1
PASS		
0	invalidate_state_sublks2	2002Sep1

PASS		
0	invalid_opcode	2002Sep1
PASS		
0	iq1_iq2_mult	2002Sep1
PASS		
0	iq1_iq2_push_wrap	2002Sep1
PASS		
0	iq2_csq_full	2002Sep1
PASS		
0	IQ2_Mult	2002Sep1
PASS		
0	IQ2_Wrap_Legacy	2002Sep1
PASS		
0	IQ_Wrap	2002Sep1
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep1
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep1
PASS		
0	ld_cnst_cntx_match	2002Sep1
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep1
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep1
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep1
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep1
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep1
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep1

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep1
PASS		
0	ld_cnst_cntx_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep1
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep1
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep1
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep1
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep1
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep1
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep1
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep1
PASS		
0	load_constant_context_rt	2002Sep1
PASS		
0	load_palette	2002Sep1
PASS		
0	lp_2d3d	2002Sep1
PASS		
0	mem_write	2002Sep1
PASS		
0	mem_write_rt	2002Sep1
PASS		
0	mpeg_index	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep1
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep1

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep1
PASS		
0	nop_eating_disabled	2002Sep1
PASS		
0	paint_multi	2002Sep1
PASS		
0	pitch_clip	2002Sep1
PASS		
0	ply_nextscan	2002Sep1
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep1
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep1
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep1
PASS		
0	rbbm_cp_queued_rdwr	2002Sep1
PASS		
0	rbbm_soft_reset	2002Sep1
PASS		
301	rbbm_WU_2D_idle_01	2002Sep1
FAIL		
302	rbbm_WU_2D_idle_02	2002Sep1
FAIL		
303	rbbm_WU_2D_idle_03	2002Sep1
FAIL		
304	rbbm_WU_2D_idle_04	2002Sep1
FAIL		
0	rbbm_WU_2D_idle_05	2002Sep1
PASS		
306	rbbm_WU_2D_idle_06	2002Sep1
FAIL		
307	rbbm_WU_2D_idle_07	2002Sep1
FAIL		
308	rbbm_WU_2D_idle_08	2002Sep1

FAIL		
309	rbbm_WU_2D_idle_09	2002Sep1
FAIL		
310	rbbm_WU_2D_idle_clean_01	2002Sep1
FAIL		
311	rbbm_WU_2D_idle_clean_02	2002Sep1
FAIL		
312	rbbm_WU_2D_idle_clean_03	2002Sep1
FAIL		
313	rbbm_WU_2D_idle_clean_04	2002Sep1
FAIL		
0	rbbm_WU_2D_idle_clean_05	2002Sep1
PASS		
315	rbbm_WU_2D_idle_clean_06	2002Sep1
FAIL		
316	rbbm_WU_2D_idle_clean_07	2002Sep1
FAIL		
317	rbbm_WU_2D_idle_clean_08	2002Sep1
FAIL		
318	rbbm_WU_2D_idle_clean_09	2002Sep1
FAIL		
319	rbbm_WU_2D_idle_clean_10	2002Sep1
FAIL		
320	rbbm_WU_2D_idle_clean_11	2002Sep1
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Sep1
PASS		
322	rbbm_WU_3D_idle_01	2002Sep1
FAIL		
323	rbbm_WU_3D_idle_02	2002Sep1
FAIL		
324	rbbm_WU_3D_idle_03	2002Sep1
FAIL		
325	rbbm_WU_3D_idle_04	2002Sep1
FAIL		
0	rbbm_WU_3D_idle_05	2002Sep1
PASS		
327	rbbm_WU_3D_idle_06	2002Sep1
FAIL		
328	rbbm_WU_3D_idle_07	2002Sep1
FAIL		
329	rbbm_WU_3D_idle_08	2002Sep1
FAIL		
330	rbbm_WU_3D_idle_09	2002Sep1
FAIL		
331	rbbm_WU_3D_idle_clean_01	2002Sep1
FAIL		
332	rbbm_WU_3D_idle_clean_02	2002Sep1

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**FAIL**
333  rbbm_WU_3D_idle_clean_03                2002Sep1
**FAIL**
334  rbbm_WU_3D_idle_clean_04                2002Sep1
**FAIL**
0    rbbm_WU_3D_idle_clean_05                2002Sep1
PASS
336  rbbm_WU_3D_idle_clean_06                2002Sep1
**FAIL**
337  rbbm_WU_3D_idle_clean_07                2002Sep1
**FAIL**
338  rbbm_WU_3D_idle_clean_08                2002Sep1
**FAIL**
339  rbbm_WU_3D_idle_clean_09                2002Sep1
**FAIL**
340  rbbm_WU_3D_idle_clean_10               2002Sep1
**FAIL**
341  rbbm_WU_3D_idle_clean_11               2002Sep1
**FAIL**
0    rbbm_WU_3D_idle_clean_12               2002Sep1
PASS
343  rbbm_WU_BIF_RBBM_agp_flush_off          2002Sep1
**FAIL**
344  rbbm_WU_BOTH_GRP update_pending_off    2002Sep1
**FAIL**
0    rbbm_WU_cmdfifo_entries                 2002Sep1
PASS
0    rbbm_WU_CP_dma_idle                     2002Sep1
PASS
347  rbbm_WU_CRTC_D1MODE_vline_fe           2002Sep1
**FAIL**
348  rbbm_WU_CRTC_D1MODE_vline_on           2002Sep1
**FAIL**
349  rbbm_WU_CRTC_D1MODE_vline_re           2002Sep1
**FAIL**
350  rbbm_WU_CRTC_D2MODE_vline_fe           2002Sep1
**FAIL**
351  rbbm_WU_CRTC_D2MODE_vline_on           2002Sep1
**FAIL**
352  rbbm_WU_CRTC_D2MODE_vline_re           2002Sep1
**FAIL**
353  rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep1
**FAIL**
354  rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep1
**FAIL**
355  rbbm_WU_idct_semaphore                  2002Sep1
**FAIL**
356  rbbm_WU_MH_hdp_clean                    2002Sep1

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**FAIL**
357 rbbm_WU_OV_FLIP_D1OVL_update_pending_off 2002Sep1
**FAIL**
358 rbbm_WU_OV_FLIP_D2OVL_update_pending_off 2002Sep1
**FAIL**
359 rbbm_WU_pad_ext_sig_rising_edge 2002Sep1
**FAIL**
0 rbbm_WUrt_BIF_RBBM_agp_flush_off 2002Sep1
PASS
0 rbbm_WUrt_BOTH_GRP_H_update_pending_off 2002Sep1
PASS
0 rbbm_WUrt_CP_dma_idle 2002Sep1
PASS
0 rbbm_WUrt_CRTC_D1MODE_vline_fe 2002Sep1
PASS
0 rbbm_WUrt_CRTC_D1MODE_vline_on 2002Sep1
PASS
0 rbbm_WUrt_CRTC_D1MODE_vline_re 2002Sep1
PASS
0 rbbm_WUrt_CRTC_D2MODE_vline_fe 2002Sep1
PASS
0 rbbm_WUrt_CRTC_D2MODE_vline_on 2002Sep1
PASS
0 rbbm_WUrt_CRTC_D2MODE_vline_re 2002Sep1
PASS
0 rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep1
PASS
0 rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep1
PASS
0 rbbm_WUrt_MH_hdp_clean 2002Sep1
PASS
0 rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off 2002Sep1
PASS
0 rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off 2002Sep1
PASS
0 rbbm_WUrt_pad_ext_sig_rising_edge 2002Sep1
PASS
0 rbbm_WUrt_RT_idle_01 2002Sep1
PASS
0 rbbm_WUrt_RT_idle_02 2002Sep1
PASS
0 rbbm_WUrt_RT_idle_03 2002Sep1
PASS
0 rbbm_WUrt_RT_idle_04 2002Sep1
PASS
0 rbbm_WUrt_RT_idle_clean_01 2002Sep1
PASS
0 rbbm_WUrt_RT_idle_clean_02 2002Sep1

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PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep1
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep1
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep1
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep1
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep1
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep1
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep1
PASS		
388	rbbm_WU_VIPH0_dma_idle	2002Sep1
FAIL		
389	rbbm_WU_VIPH1_dma_idle	2002Sep1
FAIL		
390	rbbm_WU_VIPH2_dma_idle	2002Sep1
FAIL		
391	rbbm_WU_VIPH3_dma_idle	2002Sep1
FAIL		
0	RB_BUFSZ_0	2002Sep1
PASS		
0	RB_BUFSZ_1	2002Sep1
PASS		
0	RB_Wrap	2002Sep1
PASS		
0	reg_to_mem	2002Sep1
PASS		
0	reg_to_mem_rt	2002Sep1
PASS		
0	rop_cfd_wmd	2002Sep1
PASS		
0	rstr_ordr	2002Sep1
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Sep1
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep1
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep1

PASS		
0	rtee_discretes_falling_04_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep1

PASS		
0	rtee_discretes_falling_28_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep1
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep1

PASS		
0	rtee_discretes_invert_20_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep1
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep1
PASS		
0	rtee_discretes_level_00_events_16	2002Sep1
PASS		
0	rtee_discretes_level_01_events_16	2002Sep1
PASS		
0	rtee_discretes_level_02_events_16	2002Sep1
PASS		
0	rtee_discretes_level_03_events_16	2002Sep1
PASS		
0	rtee_discretes_level_04_events_16	2002Sep1
PASS		
0	rtee_discretes_level_05_events_16	2002Sep1
PASS		
0	rtee_discretes_level_06_events_16	2002Sep1
PASS		
0	rtee_discretes_level_07_events_16	2002Sep1
PASS		
0	rtee_discretes_level_08_events_16	2002Sep1
PASS		
0	rtee_discretes_level_09_events_16	2002Sep1
PASS		
0	rtee_discretes_level_10_events_16	2002Sep1
PASS		
0	rtee_discretes_level_11_events_16	2002Sep1

PASS		
0	rtee_discretes_level_12_events_16	2002Sep1
PASS		
0	rtee_discretes_level_13_events_16	2002Sep1
PASS		
0	rtee_discretes_level_14_events_16	2002Sep1
PASS		
0	rtee_discretes_level_15_events_16	2002Sep1
PASS		
0	rtee_discretes_level_16_events_16	2002Sep1
PASS		
0	rtee_discretes_level_17_events_16	2002Sep1
PASS		
0	rtee_discretes_level_18_events_16	2002Sep1
PASS		
0	rtee_discretes_level_19_events_16	2002Sep1
PASS		
0	rtee_discretes_level_20_events_16	2002Sep1
PASS		
0	rtee_discretes_level_21_events_16	2002Sep1
PASS		
0	rtee_discretes_level_22_events_16	2002Sep1
PASS		
0	rtee_discretes_level_23_events_16	2002Sep1
PASS		
0	rtee_discretes_level_24_events_16	2002Sep1
PASS		
0	rtee_discretes_level_25_events_16	2002Sep1
PASS		
0	rtee_discretes_level_26_events_16	2002Sep1
PASS		
0	rtee_discretes_level_27_events_16	2002Sep1
PASS		
0	rtee_discretes_level_28_events_16	2002Sep1
PASS		
0	rtee_discretes_level_29_events_16	2002Sep1
PASS		
0	rtee_discretes_level_30_events_16	2002Sep1
PASS		
0	rtee_discretes_level_31_events_16	2002Sep1
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep1

PASS		
0	rtee_discretes_rising_03_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep1

PASS		
0	rtee_discretes_rising_27_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep1
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep1
PASS		
530	rtee_display1_comp_events_16	2002Sep1
FAIL	rbbmWr.cmp	
531	rtee_display2_comp_events_16	2002Sep1
FAIL	rbbmWr.cmp	
0	rtee_mem_linear_search_events_2	2002Sep1
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep1
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep1
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep1
PASS		
0	rtee_prefetch_parse_nops	2002Sep1
PASS		
0	rtee_reg_linear_search_events_2	2002Sep1
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep1

PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep1
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep1
PASS		
555	rtee_vip_comp_events_16	2002Sep1
FAIL	rbbmWr.cmp	
0	rt_invalid_opcode	2002Sep1
PASS		
0	scratch_mem_read	2002Sep1
PASS		
0	scratch_mem_read2	2002Sep1
PASS		
0	set_constant	2002Sep1
PASS		
0	set_constant2	2002Sep1
PASS		
0	set_constant_alu_max1	2002Sep1
PASS		
0	set_constant_alu_max2	2002Sep1
PASS		
0	set_constant_bool_max1	2002Sep1
PASS		
0	set_constant_bool_max2	2002Sep1
PASS		
0	set_constant_indr1_short	2002Sep1
PASS		
0	set_constant_indr1_short2	2002Sep1
PASS		
0	set_constant_indr1_write_enable	2002Sep1
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep1
PASS		
0	set_constant_indr2_short	2002Sep1
PASS		
0	set_constant_indr2_short2	2002Sep1
PASS		
0	set_constant_indr2_write_enable	2002Sep1
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep1

PASS		
0	set_constant_loop_max1	2002Sep1
PASS		
0	set_constant_loop_max2	2002Sep1
PASS		
0	set_constant_ring_short	2002Sep1
PASS		
0	set_constant_ring_short2	2002Sep1
PASS		
0	set_constant_ring_write_enable	2002Sep1
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep1
PASS		
0	set_constant_short	2002Sep1
PASS		
0	set_constant_short2	2002Sep1
PASS		
0	set_constant_tex_max1	2002Sep1
PASS		
0	set_constant_tex_max2	2002Sep1
PASS		
0	set_constant_write_enable	2002Sep1
PASS		
0	set_constant_write_enable_lcc	2002Sep1
PASS		
0	set_constant_write_enable_lcc2	2002Sep1
PASS		
0	set_scissors	2002Sep1
PASS		
0	set_state_rt_a	2002Sep1
PASS		
0	set_state_rt_b	2002Sep1
PASS		
0	set_state_rt_c	2002Sep1
PASS		
0	set_state_rt_d	2002Sep1
PASS		
0	set_st_indrl_match	2002Sep1
PASS		
0	set_st_indrl_mismatch	2002Sep1
PASS		
0	set_st_indrl_mismatch2	2002Sep1
PASS		
0	set_st_indrl_mismatch3	2002Sep1
PASS		
0	set_st_indrl_mismatch4	2002Sep1
PASS		
0	set_st_indrl_ps	2002Sep1

PASS		
0	set_st_indr1_ps_only	2002Sep1
PASS		
0	set_st_indr1_sublk0	2002Sep1
PASS		
0	set_st_indr1_sublk1	2002Sep1
PASS		
0	set_st_indr1_sublk1_only	2002Sep1
PASS		
0	set_st_indr1_sublk2	2002Sep1
PASS		
0	set_st_indr1_sublk2_only	2002Sep1
PASS		
0	set_st_indr1_sublk3	2002Sep1
PASS		
0	set_st_indr1_sublk3_only	2002Sep1
PASS		
0	set_st_indr1_sublk4	2002Sep1
PASS		
0	set_st_indr1_sublk4_only	2002Sep1
PASS		
0	set_st_indr1_sublk5	2002Sep1
PASS		
0	set_st_indr1_sublk5_only	2002Sep1
PASS		
0	set_st_indr1_sublk6	2002Sep1
PASS		
0	set_st_indr1_sublk6_only	2002Sep1
PASS		
0	set_st_indr1_sublk7	2002Sep1
PASS		
0	set_st_indr1_sublk7_only	2002Sep1
PASS		
0	set_st_indr1_vs	2002Sep1
PASS		
0	set_st_indr1_vs_only	2002Sep1
PASS		
0	set_st_indr2_match	2002Sep1
PASS		
0	set_st_indr2_mismatch	2002Sep1
PASS		
0	set_st_indr2_mismatch2	2002Sep1
PASS		
0	set_st_indr2_mismatch3	2002Sep1
PASS		
0	set_st_indr2_mismatch4	2002Sep1
PASS		
0	set_st_indr2_ps	2002Sep1

PASS		
0	set_st_indr2_ps_only	2002Sep1
PASS		
0	set_st_indr2_sublk0	2002Sep1
PASS		
0	set_st_indr2_sublk1	2002Sep1
PASS		
0	set_st_indr2_sublk1_only	2002Sep1
PASS		
0	set_st_indr2_sublk2	2002Sep1
PASS		
0	set_st_indr2_sublk2_only	2002Sep1
PASS		
0	set_st_indr2_sublk3	2002Sep1
PASS		
0	set_st_indr2_sublk3_only	2002Sep1
PASS		
0	set_st_indr2_sublk4	2002Sep1
PASS		
0	set_st_indr2_sublk4_only	2002Sep1
PASS		
0	set_st_indr2_sublk5	2002Sep1
PASS		
0	set_st_indr2_sublk5_only	2002Sep1
PASS		
0	set_st_indr2_sublk6	2002Sep1
PASS		
0	set_st_indr2_sublk6_only	2002Sep1
PASS		
0	set_st_indr2_sublk7	2002Sep1
PASS		
0	set_st_indr2_sublk7_only	2002Sep1
PASS		
0	set_st_indr2_vs	2002Sep1
PASS		
0	set_st_indr2_vs_only	2002Sep1
PASS		
0	set_st_pfmacth_disabled	2002Sep1
PASS		
0	set_st_ring_match	2002Sep1
PASS		
0	set_st_ring_mismatch	2002Sep1
PASS		
0	set_st_ring_mismatch2	2002Sep1
PASS		
0	set_st_ring_mismatch3	2002Sep1
PASS		
0	set_st_ring_mismatch4	2002Sep1

PASS		
0	set_st_ring_ps	2002Sep1
PASS		
0	set_st_ring_ps_only	2002Sep1
PASS		
0	set_st_ring_sublk0	2002Sep1
PASS		
0	set_st_ring_sublk1	2002Sep1
PASS		
0	set_st_ring_sublk1_only	2002Sep1
PASS		
0	set_st_ring_sublk2	2002Sep1
PASS		
0	set_st_ring_sublk2_only	2002Sep1
PASS		
0	set_st_ring_sublk3	2002Sep1
PASS		
0	set_st_ring_sublk3_only	2002Sep1
PASS		
0	set_st_ring_sublk4	2002Sep1
PASS		
0	set_st_ring_sublk4_only	2002Sep1
PASS		
0	set_st_ring_sublk5	2002Sep1
PASS		
0	set_st_ring_sublk5_only	2002Sep1
PASS		
0	set_st_ring_sublk6	2002Sep1
PASS		
0	set_st_ring_sublk6_only	2002Sep1
PASS		
0	set_st_ring_sublk7	2002Sep1
PASS		
0	set_st_ring_sublk7_only	2002Sep1
PASS		
0	set_st_ring_vs	2002Sep1
PASS		
0	set_st_ring_vs_only	2002Sep1
PASS		
0	set_st_stress_a	2002Sep1
PASS		
0	set_st_stress_b	2002Sep1
PASS		
0	set_st_stress_c	2002Sep1
PASS		
0	set_st_stress_d	2002Sep1
PASS		
0	set_st_stress_e	2002Sep1

PASS		
0	set_st_stress_f	2002Sep1
PASS		
0	Small_Text_BM	2002Sep1
PASS		
0	smoke_nrt	2002Sep1
PASS		
0	src_clip	2002Sep1
PASS		
0	srf_cohr	2002Sep1
PASS		
0	trans_bitblt	2002Sep1
PASS		
0	type0_2long_pckts	2002Sep1
PASS		
0	type0_3indr_buffs	2002Sep1
PASS		
0	type0_4pckts	2002Sep1
PASS		
0	type0_incr_updt_cnst	2002Sep1
PASS		
0	type0_incr_updt_instrmem	2002Sep1
PASS		
0	type0_incr_updt_st	2002Sep1
PASS		
0	type0_incr_updt_stress	2002Sep1
PASS		
0	type0_ind1_ind2_a	2002Sep1
PASS		
0	type0_ind1_ind2_b	2002Sep1
PASS		
0	type0_indr_buff	2002Sep1
PASS		
0	Type0_IQ2_BM	2002Sep1
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep1
PASS		
0	Type0_IQ_BM	2002Sep1
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep1
PASS		
0	Type0_PQ_BM	2002Sep1
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep1
PASS		
0	type1_4pckts	2002Sep1
PASS		
0	type1_indr_buff	2002Sep1

PASS		
0	type1_pckt	2002Sep1
PASS		
0	Type3_Bitblt	2002Sep1
PASS		
0	Type3_Load_Palette	2002Sep1
PASS		
0	Type3_NOP	2002Sep1
PASS		
697	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
698	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucose	2002Sep1
PASS		
0	uram_wrt_ucose	2002Sep1
PASS		
0	viz_query_a	2002Sep1
PASS		
0	viz_query_draw_indx	2002Sep1
PASS		
0	viz_query_draw_indx_indr1	2002Sep1
PASS		
0	viz_query_draw_indx_indr2	2002Sep1
PASS		
0	viz_query_draw_indx_multipass	2002Sep1
PASS		
0	viz_query_draw_indx_ring	2002Sep1
PASS		
0	viz_query_indr1	2002Sep1
PASS		
0	viz_query_indr1_simple	2002Sep1
PASS		
0	viz_query_indr2	2002Sep1
PASS		
0	viz_query_indr2_simple	2002Sep1
PASS		
0	viz_query_ring	2002Sep1
PASS		
0	viz_query_ring_simple	2002Sep1
PASS		
0	wait_for_idle	2002Sep1
PASS		
0	wait_reg_mem_poll_mem0	2002Sep1
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Sep1
PASS		
0	wait_reg_mem_poll_mem1	2002Sep1

PASS
0 wait_reg_mem_poll_mem2 2002Sep1
PASS
0 wait_reg_mem_poll_mem3 2002Sep1
PASS
0 wait_reg_mem_poll_reg0 2002Sep1
PASS
0 wait_reg_mem_poll_reg1 2002Sep1
PASS
0 wait_reg_mem_poll_reg2 2002Sep1
PASS
0 wait_reg_mem_poll_reg3 2002Sep1
PASS

=====
Tests_Run_count = 722 Pass_count = 660 Fail_count = 62 Incomplete_count = 0
=====

=====
Regression Report For CP - Project = r400 Created on Thu Aug 8 21:54:31 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

0	2d_indr_buffer_a		2002Aug8
PASS			
0	2d_indr_buffer_b		2002Aug8
PASS			
0	2d_indr_buffer_c		2002Aug8
PASS			
0	2d_indr_buffer_d		2002Aug8
PASS			
0	2d_indr_buffer_e		2002Aug8
PASS			
0	2d_indr_buffer_f		2002Aug8
PASS			
0	2d_indr_buffer_g		2002Aug8
PASS			
0	3d_draw_indx_2		2002Aug8
PASS			
0	3d_draw_indx_2_b		2002Aug8
PASS			
10	bitblt_multi		2002Aug8
FAIL	cp_rbbm.cmp rbbmWr.cmp		
0	brush_flag		2002Aug8
PASS			
0	cond_write_mem_poll_mem		2002Aug8
PASS			
0	cond_write_mem_poll_mem2		2002Aug8
PASS			
0	cond_write_mem_poll_mem3		2002Aug8
PASS			
0	cond_write_mem_poll_mem_rt		2002Aug8
PASS			
0	cond_write_mem_poll_reg		2002Aug8
PASS			
0	cond_write_mem_poll_reg2		2002Aug8
PASS			
0	cond_write_mem_poll_reg3		2002Aug8
PASS			
0	cp_acpi_recovery_soft_reset		2002Aug8
PASS			
0	cp_bios_scratch		2002Aug8

PASS		
0	cp_ib2_base000_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug8
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug8
PASS		
0	cp_ib_base000_bufszXXX	2002Aug8
PASS		
0	cp_ib_base001_bufszXXX	2002Aug8
PASS		
0	cp_ib_base010_bufszXXX	2002Aug8
PASS		
0	cp_ib_base011_bufszXXX	2002Aug8
PASS		
0	cp_ib_base100_bufszXXX	2002Aug8
PASS		
0	cp_ib_base101_bufszXXX	2002Aug8
PASS		
35	cp_ib_base110_bufszXXX	2002Aug8
FAIL	cp_mh_req.cmp	
36	cp_ib_base111_bufszXXX	2002Aug8
FAIL	cp_mh_req.cmp	
0	cp_interrupt	2002Aug8
PASS		
0	cp_interrupt_rt_events_16	2002Aug8
PASS		
0	cp_iq2_alignment_000	2002Aug8
PASS		
0	cp_iq2_alignment_001	2002Aug8
PASS		
0	cp_iq2_alignment_010	2002Aug8
PASS		
0	cp_iq2_alignment_011	2002Aug8
PASS		
0	cp_iq2_alignment_100	2002Aug8
PASS		
0	cp_iq2_alignment_101	2002Aug8

PASS		
0	cp_iq2_alignment_110	2002Aug8
PASS		
0	cp_iq2_alignment_111	2002Aug8
PASS		
0	cp_iq_alignment_000	2002Aug8
PASS		
0	cp_iq_alignment_001	2002Aug8
PASS		
0	cp_iq_alignment_010	2002Aug8
PASS		
0	cp_iq_alignment_011	2002Aug8
PASS		
0	cp_iq_alignment_100	2002Aug8
PASS		
0	cp_iq_alignment_101	2002Aug8
PASS		
0	cp_iq_alignment_110	2002Aug8
PASS		
0	cp_iq_alignment_111	2002Aug8
PASS		
0	cp_max_size_packet	2002Aug8
PASS		
0	cp_nrt_rt_protection_mode	2002Aug8
PASS		
0	cp_output_stress	2002Aug8
PASS		
0	cp_protection_mode	2002Aug8
PASS		
0	cp_rb_alignment_000	2002Aug8
PASS		
0	cp_rb_alignment_001	2002Aug8
PASS		
0	cp_rb_alignment_010	2002Aug8
PASS		
0	cp_rb_alignment_011	2002Aug8
PASS		
0	cp_rb_alignment_100	2002Aug8
PASS		
0	cp_rb_alignment_101	2002Aug8
PASS		
0	cp_rb_alignment_110	2002Aug8
PASS		
0	cp_rb_alignment_111	2002Aug8
PASS		
0	cp_register_reads	2002Aug8
PASS		
0	cp_reg_rw_test	2002Aug8

PASS		
0	cp_rt_protection_mode	2002Aug8
PASS		
0	cp_scratch	2002Aug8
PASS		
0	cp_scratch_multi	2002Aug8
PASS		
0	dma_cp_sync	2002Aug8
PASS		
0	dma_dscrstr_chckn_bit	2002Aug8
PASS		
0	dma_dscrstr_flush1	2002Aug8
PASS		
0	dma_dscrstr_flush2	2002Aug8
PASS		
0	dma_dscrstr_flush3	2002Aug8
PASS		
0	dma_m2m_src00_dstXX	2002Aug8
PASS		
0	dma_m2m_src01_dstXX	2002Aug8
PASS		
0	dma_m2m_src10_dstXX	2002Aug8
PASS		
0	dma_m2m_src11_dstXX	2002Aug8
PASS		
0	dma_mem2mem	2002Aug8
PASS		
0	dma_mem2mem_abort	2002Aug8
PASS		
0	dma_mem2mem_alignments	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug8
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug8
PASS		
0	dma_mem2mem_pio_abort	2002Aug8

PASS		
0	dma_mem2reg	2002Aug8
PASS		
0	dma_mem2reg_abort	2002Aug8
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug8
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug8
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug8
PASS		
0	dma_mem2reg_sync	2002Aug8
PASS		
0	dma_r2r_m2m_r2r	2002Aug8
PASS		
0	dma_r2r_m2r_r2r	2002Aug8
PASS		
0	dma_r2r_r2m_r2r	2002Aug8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug8
PASS		
0	dma_reg2mem	2002Aug8
PASS		
0	dma_reg2mem_abort	2002Aug8
PASS		
0	dma_reg2reg	2002Aug8
PASS		
0	dma_reg2reg_2dw	2002Aug8
PASS		
0	dma_reg2reg_daic	2002Aug8
PASS		
0	dma_reg2reg_mult	2002Aug8
PASS		
0	dma_reg2reg_mult2	2002Aug8
PASS		
0	dma_reg2reg_mult3	2002Aug8
PASS		
0	dma_reg2reg_saic	2002Aug8
PASS		
0	dma_reg2reg_sdaic	2002Aug8
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug8
PASS		
0	dma_reg2reg_stress	2002Aug8

PASS		
0	draw_indx_a	2002Aug8
PASS		
0	draw_indx_b	2002Aug8
PASS		
0	draw_indx_c	2002Aug8
PASS		
0	draw_indx_indr1_a	2002Aug8
PASS		
0	draw_indx_indr1_b	2002Aug8
PASS		
0	draw_indx_indr1_short_a	2002Aug8
PASS		
0	draw_indx_indr1_short_b	2002Aug8
PASS		
0	draw_indx_indr1_stress	2002Aug8
PASS		
0	draw_indx_indr2_a	2002Aug8
PASS		
0	draw_indx_indr2_b	2002Aug8
PASS		
0	draw_indx_indr2_short_a	2002Aug8
PASS		
0	draw_indx_indr2_short_b	2002Aug8
PASS		
0	draw_indx_indr2_stress	2002Aug8
PASS		
0	draw_indx_ring1	2002Aug8
PASS		
0	draw_indx_ring2	2002Aug8
PASS		
0	draw_indx_ring_short1	2002Aug8
PASS		
0	draw_indx_ring_short2	2002Aug8
PASS		
0	draw_indx_ring_stress	2002Aug8
PASS		
0	draw_indx_short_a	2002Aug8
PASS		
0	draw_indx_short_b	2002Aug8
PASS		
0	draw_indx_short_c	2002Aug8
PASS		
0	draw_indx_stress_a	2002Aug8
PASS		
0	draw_indx_stress_b	2002Aug8
PASS		
0	draw_indx_stress_c	2002Aug8

PASS		
141	event_timestamp_write	2002Aug8
INCOMPLETE		
0	ft_indr1_july22	2002Aug8
PASS		
0	ft_indr2_july22	2002Aug8
PASS		
0	ft_july22	2002Aug8
PASS		
0	header_dump	2002Aug8
PASS		
0	header_dump_rt	2002Aug8
PASS		
0	header_dump_small	2002Aug8
PASS		
0	im_load	2002Aug8
PASS		
0	im_load_immediate	2002Aug8
PASS		
0	im_load_immediate_nrt_me	2002Aug8
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug8
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug8
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug8
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug8
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug8
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug8
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug8
PASS		
0	im_load_nrt_me	2002Aug8
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug8
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug8
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug8
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug8
PASS		
0	incremental_update_const	2002Aug8
PASS		
164	incremental_update_instr	2002Aug8

```

**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_state      2002Aug8
PASS
0  indirect_buffer_a             2002Aug8
PASS
0  indirect_buffer_b             2002Aug8
PASS
0  indirect_buffer_c             2002Aug8
PASS
0  indirect_buffer_d             2002Aug8
PASS
0  indirect_buffer_e             2002Aug8
PASS
0  indirect_buffer_f             2002Aug8
PASS
0  indirect_buffer_g             2002Aug8
PASS
173 indirect_buffer_h            2002Aug8
**FAIL**      cp_rbbm.cmp
0  indirect_buffer_i             2002Aug8
PASS
175 indirect_buffer_j            2002Aug8
**FAIL**      cp_rbbm.cmp
0  indirect_buffer_k             2002Aug8
PASS
0  indirect_buffer_l             2002Aug8
PASS
178 indirect_buffer_mp_a         2002Aug8
**FAIL**      cp_rbbm.cmp
179 indirect_buffer_mp_b         2002Aug8
**FAIL**      cp_rbbm.cmp
180 indirect_buffer_mp_c         2002Aug8
**FAIL**      cp_rbbm.cmp
181 indirect_buffer_mp_d         2002Aug8
**FAIL**      cp_rbbm.cmp
182 indirect_buffer_mp_e         2002Aug8
**FAIL**      cp_rbbm.cmp
183 indirect_buffer_mp_f         2002Aug8
**FAIL**      cp_rbbm.cmp
0  invalidate_on_me_init         2002Aug8
PASS
0  invalidate_state_constants    2002Aug8
PASS
0  invalidate_state_shaders      2002Aug8
PASS
0  invalidate_state_shaders2     2002Aug8
PASS
0  invalidate_state_sublks       2002Aug8

```

PASS		
0	invalidate_state_sublks2	2002Aug8
PASS		
0	invalid_opcode	2002Aug8
PASS		
0	iq1_iq2_mult	2002Aug8
PASS		
192	iq1_iq2_push_wrap	2002Aug8
INCOMPLETE		
0	iq2_csq_full	2002Aug8
PASS		
0	IQ2_Mult	2002Aug8
PASS		
0	IQ2_Wrap_Legacy	2002Aug8
PASS		
0	IQ_Wrap	2002Aug8
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug8
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug8
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug8
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug8
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug8
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug8
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug8
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug8
PASS		
0	ld_cnst_cntx_match	2002Aug8
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug8
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug8
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug8
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug8
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug8
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug8
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug8

PASS		
0	ld_cnst_cntx_replicate8	2002Aug8
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug8
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug8
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug8
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug8
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug8
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug8
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug8
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug8
PASS		
0	load_constant_context_rt	2002Aug8
PASS		
0	mem_write	2002Aug8
PASS		
0	mem_write_rt	2002Aug8
PASS		
0	mpeg_index	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug8
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug8
PASS		
0	nop_eating_disabled	2002Aug8
PASS		
0	paint_multi	2002Aug8
PASS		
0	pitch_clip	2002Aug8

PASS		
0	RB_BUFSZ_0	2002Aug8
PASS		
0	RB_BUFSZ_1	2002Aug8
PASS		
0	RB_Wrap	2002Aug8
PASS		
0	reg_to_mem	2002Aug8
PASS		
0	reg_to_mem_rt	2002Aug8
PASS		
0	rop_cfd_wmd	2002Aug8
PASS		
0	rstr_ordr	2002Aug8
PASS		
0	rtee_discretes_falling_00_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug8

PASS		
0	rtee_discretes_falling_17_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug8
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug8

PASS		
0	rtee_discretes_invert_09_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_11_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug8
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug8
PASS		
0	rtee_discretes_level_00_events_16	2002Aug8

PASS		
0	rtee_discretes_level_01_events_16	2002Aug8
PASS		
0	rtee_discretes_level_02_events_16	2002Aug8
PASS		
0	rtee_discretes_level_03_events_16	2002Aug8
PASS		
0	rtee_discretes_level_04_events_16	2002Aug8
PASS		
0	rtee_discretes_level_05_events_16	2002Aug8
PASS		
0	rtee_discretes_level_06_events_16	2002Aug8
PASS		
0	rtee_discretes_level_07_events_16	2002Aug8
PASS		
0	rtee_discretes_level_08_events_16	2002Aug8
PASS		
0	rtee_discretes_level_09_events_16	2002Aug8
PASS		
0	rtee_discretes_level_10_events_16	2002Aug8
PASS		
0	rtee_discretes_level_11_events_16	2002Aug8
PASS		
0	rtee_discretes_level_12_events_16	2002Aug8
PASS		
0	rtee_discretes_level_13_events_16	2002Aug8
PASS		
0	rtee_discretes_level_14_events_16	2002Aug8
PASS		
0	rtee_discretes_level_15_events_16	2002Aug8
PASS		
0	rtee_discretes_level_16_events_16	2002Aug8
PASS		
0	rtee_discretes_level_17_events_16	2002Aug8
PASS		
0	rtee_discretes_level_18_events_16	2002Aug8
PASS		
0	rtee_discretes_level_19_events_16	2002Aug8
PASS		
0	rtee_discretes_level_20_events_16	2002Aug8
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0	rtee_discretes_level_21_events_16	2002Aug8
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0	rtee_discretes_level_22_events_16	2002Aug8
PASS		
0	rtee_discretes_level_23_events_16	2002Aug8
PASS		
0	rtee_discretes_level_24_events_16	2002Aug8

PASS		
0	rtee_discretes_level_25_events_16	2002Aug8
PASS		
0	rtee_discretes_level_26_events_16	2002Aug8
PASS		
0	rtee_discretes_level_27_events_16	2002Aug8
PASS		
0	rtee_discretes_level_28_events_16	2002Aug8
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0	rtee_discretes_level_29_events_16	2002Aug8
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0	rtee_discretes_level_30_events_16	2002Aug8
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0	rtee_discretes_level_31_events_16	2002Aug8
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug8
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0	rtee_discretes_rising_02_events_16	2002Aug8
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0	rtee_discretes_rising_03_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug8
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0	rtee_discretes_rising_08_events_16	2002Aug8
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0	rtee_discretes_rising_09_events_16	2002Aug8
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0	rtee_discretes_rising_10_events_16	2002Aug8
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0	rtee_discretes_rising_11_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug8
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0	rtee_discretes_rising_13_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug8

PASS		
0	rtee_discretes_rising_16_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug8
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0	rtee_discretes_rising_18_events_16	2002Aug8
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0	rtee_discretes_rising_19_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug8
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug8
PASS		
0	rtee_display1_comp_events_16	2002Aug8
PASS		
0	rtee_display2_comp_events_16	2002Aug8
PASS		
0	rtee_mem_linear_search_events_2	2002Aug8
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug8

PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug8
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug8
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug8
PASS		
0	rtee_prefetch_parse_nops	2002Aug8
PASS		
0	rtee_reg_linear_search_events_2	2002Aug8
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug8
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug8
PASS		
0	rtee_vip_comp_events_16	2002Aug8
PASS		
0	rt_invalid_opcode	2002Aug8
PASS		
0	scratch_mem_read	2002Aug8
PASS		
0	scratch_mem_read2	2002Aug8
PASS		
0	set_constant	2002Aug8
PASS		
0	set_constant2	2002Aug8
PASS		
0	set_constant_indr1_short	2002Aug8

PASS		
0	set_constant_indr1_short2	2002Aug8
PASS		
0	set_constant_indr2_short	2002Aug8
PASS		
0	set_constant_indr2_short2	2002Aug8
PASS		
0	set_constant_ring_short	2002Aug8
PASS		
0	set_constant_ring_short2	2002Aug8
PASS		
0	set_constant_short	2002Aug8
PASS		
0	set_scissors	2002Aug8
PASS		
0	set_state_rt_a	2002Aug8
PASS		
0	set_state_rt_b	2002Aug8
PASS		
0	set_state_rt_c	2002Aug8
PASS		
0	set_state_rt_d	2002Aug8
PASS		
0	set_st_indr1_match	2002Aug8
PASS		
0	set_st_indr1_mismatch	2002Aug8
PASS		
0	set_st_indr1_mismatch2	2002Aug8
PASS		
0	set_st_indr1_mismatch3	2002Aug8
PASS		
0	set_st_indr1_mismatch4	2002Aug8
PASS		
0	set_st_indr1_ps	2002Aug8
PASS		
0	set_st_indr1_ps_only	2002Aug8
PASS		
0	set_st_indr1_sublk0	2002Aug8
PASS		
0	set_st_indr1_sublk1	2002Aug8
PASS		
0	set_st_indr1_sublk1_only	2002Aug8
PASS		
0	set_st_indr1_sublk2	2002Aug8
PASS		
0	set_st_indr1_sublk2_only	2002Aug8
PASS		
0	set_st_indr1_sublk3	2002Aug8

PASS		
0	set_st_indr1_sublk3_only	2002Aug8
PASS		
0	set_st_indr1_sublk4	2002Aug8
PASS		
0	set_st_indr1_sublk4_only	2002Aug8
PASS		
0	set_st_indr1_sublk5	2002Aug8
PASS		
0	set_st_indr1_sublk5_only	2002Aug8
PASS		
0	set_st_indr1_sublk6	2002Aug8
PASS		
0	set_st_indr1_sublk6_only	2002Aug8
PASS		
0	set_st_indr1_sublk7	2002Aug8
PASS		
0	set_st_indr1_sublk7_only	2002Aug8
PASS		
0	set_st_indr1_vs	2002Aug8
PASS		
0	set_st_indr1_vs_only	2002Aug8
PASS		
0	set_st_indr2_match	2002Aug8
PASS		
0	set_st_indr2_mismatch	2002Aug8
PASS		
0	set_st_indr2_mismatch2	2002Aug8
PASS		
0	set_st_indr2_mismatch3	2002Aug8
PASS		
0	set_st_indr2_mismatch4	2002Aug8
PASS		
0	set_st_indr2_ps	2002Aug8
PASS		
0	set_st_indr2_ps_only	2002Aug8
PASS		
0	set_st_indr2_sublk0	2002Aug8
PASS		
0	set_st_indr2_sublk1	2002Aug8
PASS		
0	set_st_indr2_sublk1_only	2002Aug8
PASS		
0	set_st_indr2_sublk2	2002Aug8
PASS		
0	set_st_indr2_sublk2_only	2002Aug8
PASS		
0	set_st_indr2_sublk3	2002Aug8

PASS		
0	set_st_indr2_sublk3_only	2002Aug8
PASS		
0	set_st_indr2_sublk4	2002Aug8
PASS		
0	set_st_indr2_sublk4_only	2002Aug8
PASS		
0	set_st_indr2_sublk5	2002Aug8
PASS		
0	set_st_indr2_sublk5_only	2002Aug8
PASS		
0	set_st_indr2_sublk6	2002Aug8
PASS		
0	set_st_indr2_sublk6_only	2002Aug8
PASS		
0	set_st_indr2_sublk7	2002Aug8
PASS		
0	set_st_indr2_sublk7_only	2002Aug8
PASS		
0	set_st_indr2_vs	2002Aug8
PASS		
0	set_st_indr2_vs_only	2002Aug8
PASS		
0	set_st_pfmatch_disabled	2002Aug8
PASS		
0	set_st_ring_match	2002Aug8
PASS		
0	set_st_ring_mismatch	2002Aug8
PASS		
0	set_st_ring_mismatch2	2002Aug8
PASS		
0	set_st_ring_mismatch3	2002Aug8
PASS		
0	set_st_ring_mismatch4	2002Aug8
PASS		
0	set_st_ring_ps	2002Aug8
PASS		
0	set_st_ring_ps_only	2002Aug8
PASS		
0	set_st_ring_sublk0	2002Aug8
PASS		
0	set_st_ring_sublk1	2002Aug8
PASS		
0	set_st_ring_sublk1_only	2002Aug8
PASS		
0	set_st_ring_sublk2	2002Aug8
PASS		
0	set_st_ring_sublk2_only	2002Aug8

PASS		
0	set_st_ring_sublk3	2002Aug8
PASS		
0	set_st_ring_sublk3_only	2002Aug8
PASS		
0	set_st_ring_sublk4	2002Aug8
PASS		
0	set_st_ring_sublk4_only	2002Aug8
PASS		
0	set_st_ring_sublk5	2002Aug8
PASS		
0	set_st_ring_sublk5_only	2002Aug8
PASS		
0	set_st_ring_sublk6	2002Aug8
PASS		
0	set_st_ring_sublk6_only	2002Aug8
PASS		
0	set_st_ring_sublk7	2002Aug8
PASS		
0	set_st_ring_sublk7_only	2002Aug8
PASS		
0	set_st_ring_vs	2002Aug8
PASS		
0	set_st_ring_vs_only	2002Aug8
PASS		
0	set_st_stress_a	2002Aug8
PASS		
490	set_st_stress_b	2002Aug8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug8
PASS		
0	set_st_stress_d	2002Aug8
PASS		
0	set_st_stress_e	2002Aug8
PASS		
0	set_st_stress_f	2002Aug8
PASS		
0	smoke_nrt	2002Aug8
PASS		
496	src_clip	2002Aug8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	srf_cohr	2002Aug8
PASS		
498	trans_bitblt	2002Aug8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	type0_2long_pckts	2002Aug8
PASS		
0	type0_3indr_buffs	2002Aug8

PASS		
0	type0_4pckts	2002Aug8
PASS		
0	type0_incr_updt_cnst	2002Aug8
PASS		
0	type0_incr_updt_instrmem	2002Aug8
PASS		
0	type0_incr_updt_st	2002Aug8
PASS		
0	type0_incr_updt_stress	2002Aug8
PASS		
0	type0_ind1_ind2_a	2002Aug8
PASS		
0	type0_ind1_ind2_b	2002Aug8
PASS		
0	type0_indr_buff	2002Aug8
PASS		
0	Type0_IQ2_BM	2002Aug8
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug8
PASS		
0	Type0_IQ_BM	2002Aug8
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug8
PASS		
0	Type0_PQ_BM	2002Aug8
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug8
PASS		
0	type1_4pckts	2002Aug8
PASS		
0	type1_indr_buff	2002Aug8
PASS		
0	type1_pckt	2002Aug8
PASS		
0	Type3_Bitblt	2002Aug8
PASS		
519	Type3_Load_Palette	2002Aug8
FAIL	rbbmWr.cmp	
0	Type3_NOP	2002Aug8
PASS		
521	uram_rd_wrt	2002Aug8
INCOMPLETE		
522	uram_rd_wrt_rt	2002Aug8
INCOMPLETE		
523	uram_wrt_rt_ucode	2002Aug8
FAIL	rbbmWr.cmp	
0	uram_wrt_ucode	2002Aug8

PASS
0 wait_for_idle 2002Aug8
PASS
526 wait_reg_mem_poll_mem0 2002Aug8
FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
527 wait_reg_mem_poll_mem0_rt 2002Aug8
FAIL mh_cp_rd.cmp
528 wait_reg_mem_poll_mem1 2002Aug8
FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
529 wait_reg_mem_poll_mem2 2002Aug8
FAIL cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
530 wait_reg_mem_poll_mem3 2002Aug8
FAIL mh_cp_rd.cmp
531 wait_reg_mem_poll_reg0 2002Aug8
FAIL cp_rbbm.cmp
0 wait_reg_mem_poll_reg1 2002Aug8
PASS
0 wait_reg_mem_poll_reg2 2002Aug8
PASS
0 wait_reg_mem_poll_reg3 2002Aug8
PASS

=====
Tests_Run_count = 534 Pass_count = 507 Fail_count = 23 Incomplete_count = 4
=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep 26 19:23:55 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          647        647        580         89
              RBBM       109        109         77         70
+++++
              TOTAL       756        756        657         86
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct1
	PASS		
0	2d_indr_buffer_b		2002Oct1
	PASS		
0	2d_indr_buffer_c		2002Oct1
	PASS		
0	2d_indr_buffer_d		2002Oct1
	PASS		
0	2d_indr_buffer_e		2002Oct1
	PASS		
0	2d_indr_buffer_f		2002Oct1
	PASS		
0	2d_indr_buffer_g		2002Oct1
	PASS		
0	2d_indr_buffer_random		2002Oct1
	PASS		
9	2d_indr_buffer_random2		2002Sep29
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Oct1
	PASS		
0	3d_draw_indx_2_b		2002Oct1
	PASS		
0	IQ2_Mult		2002Oct1
	PASS		
0	IQ2_Wrap_Legacy		2002Oct1
	PASS		
0	IQ_Wrap		2002Oct1
	PASS		
15	Isync_Control		2002Oct1
	FAIL cp_rbbm.cmp rbbmWr.cmp		
16	Isync_Control_2D_3D		2002Oct1
	FAIL cp_rbbm.cmp rbbmWr.cmp		
17	Isync_Control_3D_2D		2002Oct1
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	RB_BUFSZ_0		2002Oct1
	PASS		
0	RB_BUFSZ_1		2002Oct1
	PASS		
0	RB_Wrap		2002Oct1

PASS		
0	Small_Text_BM	2002Oct1
PASS		
0	Type0_IQ2_BM	2002Oct1
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Oct1
PASS		
0	Type0_IQ_BM	2002Oct1
PASS		
0	Type0_IQ_BM_IB_Packet	2002Oct1
PASS		
0	Type0_PQ_BM	2002Oct1
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Oct1
PASS		
0	Type3_Bitblt	2002Oct1
PASS		
0	Type3_Load_Palette	2002Oct1
PASS		
0	Type3_NOP	2002Oct1
PASS		
0	Type3_Polyline	2002Oct1
PASS		
0	Type3_Polyline_Large	2002Oct1
PASS		
0	bitblt_multi	2002Oct1
PASS		
0	brush_32x1mono	2002Oct1
PASS		
0	brush_8x8color_16	2002Oct1
PASS		
0	brush_8x8color_32	2002Oct1
PASS		
0	brush_8x8color_64	2002Oct1
PASS		
0	brush_8x8color_dst_0_1	2002Oct1
PASS		
0	brush_8x8mono	2002Oct1
PASS		
0	brush_color_mono_stress	2002Oct1
PASS		
0	brush_flag	2002Oct1
PASS		
0	brush_type0	2002Oct1
PASS		
0	brush_type1	2002Oct1
PASS		
0	brush_type10	2002Oct1

PASS		
0	brush_type13	2002Oct1
PASS		
0	brush_type14	2002Oct1
PASS		
0	brush_type6	2002Oct1
PASS		
0	brush_type7	2002Oct1
PASS		
0	brush_type_other	2002Oct1
PASS		
0	brush_types_13_14	2002Oct1
PASS		
0	change_queue_sizes	2002Oct1
PASS		
0	clear_const_wr_en_on_me_init	2002Oct1
PASS		
0	clear_const_wr_en_with_lcc	2002Oct1
PASS		
0	cond_write_mem_poll_mem	2002Oct1
PASS		
0	cond_write_mem_poll_mem2	2002Oct1
PASS		
0	cond_write_mem_poll_mem3	2002Oct1
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct1
PASS		
0	cond_write_mem_poll_reg	2002Oct1
PASS		
0	cond_write_mem_poll_reg2	2002Oct1
PASS		
0	cond_write_mem_poll_reg3	2002Oct1
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct1
PASS		
0	cp_bios_scratch	2002Oct1
PASS		
0	cp_high_stress	2002Oct1
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct1
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct1
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct1
PASS		
0	cp_ib2_base011_bufszXXX	2002Oct1
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct1

PASS		
0	cp_ib2_base101_bufszXXX	2002Oct1
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct1
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct1
PASS		
0	cp_ib_base000_bufszXXX	2002Oct1
PASS		
0	cp_ib_base001_bufszXXX	2002Oct1
PASS		
0	cp_ib_base010_bufszXXX	2002Oct1
PASS		
0	cp_ib_base011_bufszXXX	2002Oct1
PASS		
0	cp_ib_base100_bufszXXX	2002Oct1
PASS		
0	cp_ib_base101_bufszXXX	2002Oct1
PASS		
0	cp_ib_base110_bufszXXX	2002Oct1
PASS		
0	cp_ib_base111_bufszXXX	2002Oct1
PASS		
0	cp_interrupt	2002Oct1
PASS		
0	cp_interrupt_rt_events_16	2002Oct1
PASS		
0	cp_iq2_alignment_000	2002Oct1
PASS		
0	cp_iq2_alignment_001	2002Oct1
PASS		
0	cp_iq2_alignment_010	2002Oct1
PASS		
0	cp_iq2_alignment_011	2002Oct1
PASS		
0	cp_iq2_alignment_100	2002Oct1
PASS		
0	cp_iq2_alignment_101	2002Oct1
PASS		
0	cp_iq2_alignment_110	2002Oct1
PASS		
0	cp_iq2_alignment_111	2002Oct1
PASS		
0	cp_iq_alignment_000	2002Oct1
PASS		
0	cp_iq_alignment_001	2002Oct1
PASS		
0	cp_iq_alignment_010	2002Oct1

PASS		
0	cp_iq_alignment_011	2002Oct1
PASS		
0	cp_iq_alignment_100	2002Oct1
PASS		
0	cp_iq_alignment_101	2002Oct1
PASS		
0	cp_iq_alignment_110	2002Oct1
PASS		
0	cp_iq_alignment_111	2002Oct1
PASS		
0	cp_max_size_packet	2002Oct1
PASS		
0	cp_max_size_packet_random	2002Oct1
PASS		
0	cp_nrt_rt_protection_mode	2002Oct1
PASS		
0	cp_output_stress	2002Oct1
PASS		
0	cp_output_stress_random	2002Oct1
PASS		
0	cp_protection_mode	2002Oct1
PASS		
0	cp_rb_alignment_000	2002Oct1
PASS		
0	cp_rb_alignment_001	2002Oct1
PASS		
0	cp_rb_alignment_010	2002Oct1
PASS		
0	cp_rb_alignment_011	2002Oct1
PASS		
0	cp_rb_alignment_100	2002Oct1
PASS		
0	cp_rb_alignment_101	2002Oct1
PASS		
0	cp_rb_alignment_110	2002Oct1
PASS		
0	cp_rb_alignment_111	2002Oct1
PASS		
0	cp_rb_wptrXXX_base000	2002Oct1
PASS		
0	cp_rb_wptrXXX_base001	2002Oct1
PASS		
0	cp_rb_wptrXXX_base010	2002Oct1
PASS		
0	cp_rb_wptrXXX_base011	2002Oct1
PASS		
0	cp_rb_wptrXXX_base100	2002Oct1

PASS		
0	cp_rb_wptrXXX_base101	2002Oct1
PASS		
0	cp_rb_wptrXXX_base110	2002Oct1
PASS		
0	cp_rb_wptrXXX_base111	2002Oct1
PASS		
0	cp_reg_rw_test	2002Oct1
PASS		
0	cp_register_reads	2002Oct1
PASS		
0	cp_rt_protection_mode	2002Oct1
PASS		
0	cp_scratch	2002Oct1
PASS		
0	cp_scratch_multi	2002Oct1
PASS		
0	csf_reg_rd_default	2002Oct1
PASS		
0	csf_reg_rd_only	2002Oct1
PASS		
0	csf_reg_wr_rd	2002Oct1
PASS		
0	dma_cp_sync	2002Oct1
PASS		
0	dma_dscrstr_chckn_bit	2002Oct1
PASS		
0	dma_dscrstr_flush1	2002Oct1
PASS		
0	dma_dscrstr_flush2	2002Oct1
PASS		
0	dma_dscrstr_flush3	2002Oct1
PASS		
0	dma_m2m_src00_dstXX	2002Oct1
PASS		
0	dma_m2m_src01_dstXX	2002Oct1
PASS		
0	dma_m2m_src10_dstXX	2002Oct1
PASS		
0	dma_m2m_src11_dstXX	2002Oct1
PASS		
0	dma_mem2mem	2002Oct1
PASS		
0	dma_mem2mem_abort	2002Oct1
PASS		
0	dma_mem2mem_alignments	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct1

PASS		
0	dma_mem2mem_byte_alignment1	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment2	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment3	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment4	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct1
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct1
PASS		
0	dma_mem2mem_large	2002Oct1
PASS		
0	dma_mem2mem_pio_abort	2002Oct1
PASS		
0	dma_mem2mem_random	2002Oct1
PASS		
0	dma_mem2reg	2002Oct1
PASS		
0	dma_mem2reg_abort	2002Oct1
PASS		
0	dma_mem2reg_dtaq_stress	2002Oct1
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Oct1
PASS		
0	dma_mem2reg_pio_stressbm	2002Oct1
PASS		
0	dma_mem2reg_pio_stresspio	2002Oct1
PASS		
0	dma_mem2reg_sync	2002Oct1
PASS		
0	dma_r2r_m2m_r2r	2002Oct1
PASS		
0	dma_r2r_m2r_r2r	2002Oct1
PASS		
0	dma_r2r_r2m_r2r	2002Oct1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct1
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct1
PASS		
0	dma_reg2mem	2002Oct1

PASS		
0	dma_reg2mem_abort	2002Oct1
PASS		
0	dma_reg2reg	2002Oct1
PASS		
0	dma_reg2reg_2dw	2002Oct1
PASS		
0	dma_reg2reg_daic	2002Oct1
PASS		
0	dma_reg2reg_mult	2002Oct1
PASS		
0	dma_reg2reg_mult2	2002Oct1
PASS		
0	dma_reg2reg_mult3	2002Oct1
PASS		
0	dma_reg2reg_saic	2002Oct1
PASS		
0	dma_reg2reg_sdaic	2002Oct1
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct1
PASS		
0	dma_reg2reg_stress	2002Oct1
PASS		
0	draw_indx_a	2002Oct1
PASS		
0	draw_indx_b	2002Oct1
PASS		
0	draw_indx_c	2002Oct1
PASS		
0	draw_indx_indr1_a	2002Oct1
PASS		
0	draw_indx_indr1_b	2002Oct1
PASS		
0	draw_indx_indr1_short_a	2002Oct1
PASS		
0	draw_indx_indr1_short_b	2002Oct1
PASS		
0	draw_indx_indr1_stress	2002Oct1
PASS		
0	draw_indx_indr2_a	2002Oct1
PASS		
0	draw_indx_indr2_b	2002Oct1
PASS		
0	draw_indx_indr2_short_a	2002Oct1
PASS		
0	draw_indx_indr2_short_b	2002Oct1
PASS		
0	draw_indx_indr2_stress	2002Oct1

PASS		
0	draw_indx_random_a	2002Oct1
PASS		
0	draw_indx_random_b	2002Oct1
PASS		
0	draw_indx_random_c	2002Oct1
PASS		
0	draw_indx_ring1	2002Oct1
PASS		
0	draw_indx_ring2	2002Oct1
PASS		
0	draw_indx_ring_short1	2002Oct1
PASS		
0	draw_indx_ring_short2	2002Oct1
PASS		
0	draw_indx_ring_stress	2002Oct1
PASS		
0	draw_indx_short_a	2002Oct1
PASS		
0	draw_indx_short_b	2002Oct1
PASS		
0	draw_indx_short_c	2002Oct1
PASS		
0	draw_indx_stress_a	2002Oct1
PASS		
0	draw_indx_stress_b	2002Oct1
PASS		
0	draw_indx_stress_c	2002Oct1
PASS		
0	event_timestamp_write	2002Oct1
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct1
PASS		
0	fix2flt_reg	2002Oct1
PASS		
0	ft_indr1_july22	2002Oct1
PASS		
0	ft_indr2_july22	2002Oct1
PASS		
0	ft_july22	2002Oct1
PASS		
0	ft_july22_random	2002Oct1
PASS		
0	header_dump	2002Oct1
PASS		
0	header_dump_rt	2002Oct1
PASS		
0	header_dump_small	2002Oct1

PASS		
0	hostdata_blt	2002Oct1
PASS		
0	im_load_a	2002Oct1
PASS		
0	im_load_b	2002Oct1
PASS		
0	im_load_cid_mismatch	2002Oct1
PASS		
0	im_load_immediate	2002Oct1
PASS		
0	im_load_immediate_nrt_me	2002Oct1
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct1
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct1
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct1
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct1
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct1
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct1
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct1
PASS		
0	im_load_indr1_a	2002Oct1
PASS		
0	im_load_indr1_b	2002Oct1
PASS		
0	im_load_indr2_a	2002Oct1
PASS		
0	im_load_indr2_b	2002Oct1
PASS		
0	im_load_invalidate	2002Oct1
PASS		
0	im_load_match	2002Oct1
PASS		
0	im_load_match2	2002Oct1
PASS		
0	im_load_nrt_me	2002Oct1
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct1
PASS		
0	im_load_nrt_me_ps_wrap	2002Oct1
PASS		
0	im_load_nrt_me_vs_nowrap	2002Oct1

PASS		
0	im_load_nrt_me_vs_wrap	2002Oct1
PASS		
0	im_load_pfmacth_disable	2002Oct1
PASS		
0	im_load_random_a	2002Oct1
PASS		
0	im_load_random_b	2002Oct1
PASS		
241	im_load_random_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Oct1
PASS		
0	im_load_ring_b	2002Oct1
PASS		
0	im_load_rt	2002Oct1
PASS		
0	im_load_rt2	2002Oct1
PASS		
0	im_load_rt3	2002Oct1
PASS		
0	im_load_rt4	2002Oct1
PASS		
0	im_load_sz_mismatch	2002Oct1
PASS		
0	incremental_update_const	2002Oct1
PASS		
0	incremental_update_instr	2002Oct1
PASS		
0	incremental_update_state	2002Oct1
PASS		
0	indirect_buffer_a	2002Oct1
PASS		
0	indirect_buffer_b	2002Oct1
PASS		
0	indirect_buffer_c	2002Oct1
PASS		
0	indirect_buffer_d	2002Oct1
PASS		
0	indirect_buffer_e	2002Oct1
PASS		
0	indirect_buffer_f	2002Oct1
PASS		
0	indirect_buffer_g	2002Oct1
PASS		
0	indirect_buffer_h	2002Oct1
PASS		
0	indirect_buffer_i	2002Oct1

PASS		
0	indirect_buffer_j	2002Oct1
PASS		
0	indirect_buffer_k	2002Oct1
PASS		
0	indirect_buffer_l	2002Oct1
PASS		
0	indirect_buffer_mp_a	2002Oct1
PASS		
0	indirect_buffer_mp_b	2002Oct1
PASS		
0	indirect_buffer_mp_c	2002Oct1
PASS		
0	indirect_buffer_mp_d	2002Oct1
PASS		
0	indirect_buffer_mp_e	2002Oct1
PASS		
0	indirect_buffer_mp_f	2002Oct1
PASS		
270	indirect_buffer_mp_random1	2002Oct1
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
271	indirect_buffer_mp_random2	2002Oct1
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
272	indirect_buffer_random1	2002Oct1
FAIL	mh_cp_rd.cmp	
273	indirect_buffer_random2	2002Oct1
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	2002Oct1
PASS		
0	invalidate_on_me_init	2002Oct1
PASS		
0	invalidate_state_constants	2002Oct1
PASS		
0	invalidate_state_shaders	2002Oct1
PASS		
0	invalidate_state_shaders2	2002Oct1
PASS		
0	invalidate_state_sublks	2002Oct1
PASS		
0	invalidate_state_sublks2	2002Oct1
PASS		
0	iq1_iq2_mult	2002Oct1
PASS		
0	iq1_iq2_push_wrap	2002Oct1
PASS		
0	iq2_csq_full	2002Oct1
PASS		
284	lcc_coherency_cntr_random	2002Sep29

```

**FAIL**          cp_mh_req.cmp cp_rbbm.cmp
0   lcc_match_random                2002Oct1
PASS
0   lcc_mismatch_alutex_random      2002Oct1
PASS
0   lcc_mismatch_base_random        2002Oct1
PASS
0   ld_cnst_cntx_coherency_cntrl    2002Oct1
PASS
0   ld_cnst_cntx_coherency_cntr2    2002Oct1
PASS
0   ld_cnst_cntx_indr1_replicate     2002Oct1
PASS
0   ld_cnst_cntx_indr1_replicate4    2002Oct1
PASS
0   ld_cnst_cntx_indr1_replicate8    2002Oct1
PASS
0   ld_cnst_cntx_indr1_simple        2002Oct1
PASS
0   ld_cnst_cntx_indr2_replicate     2002Oct1
PASS
0   ld_cnst_cntx_indr2_replicate4    2002Oct1
PASS
0   ld_cnst_cntx_indr2_replicate8    2002Oct1
PASS
0   ld_cnst_cntx_indr2_simple        2002Oct1
PASS
0   ld_cnst_cntx_match               2002Oct1
PASS
0   ld_cnst_cntx_match_valid_preserved 2002Oct1
PASS
0   ld_cnst_cntx_mismatch_alutex     2002Oct1
PASS
0   ld_cnst_cntx_mismatch_base       2002Oct1
PASS
0   ld_cnst_cntx_mismatch_force      2002Oct1
PASS
0   ld_cnst_cntx_pfmacth_disable     2002Oct1
PASS
0   ld_cnst_cntx_replicate4_alu      2002Oct1
PASS
0   ld_cnst_cntx_replicate4_tex      2002Oct1
PASS
0   ld_cnst_cntx_replicate8         2002Oct1
PASS
0   ld_cnst_cntx_replicate_alu       2002Oct1
PASS
0   ld_cnst_cntx_replicate_tex       2002Oct1

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PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct1
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct1
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct1
PASS		
0	ld_cnst_cntx_ring_simple	2002Oct1
PASS		
0	ld_cnst_cntx_simple_alu	2002Oct1
PASS		
0	ld_cnst_cntx_simple_tex	2002Oct1
PASS		
0	load_constant_context_rt	2002Oct1
PASS		
0	load_palette	2002Oct1
PASS		
0	lp_2d3d	2002Oct1
PASS		
0	me_reg_wr_rd	2002Oct1
PASS		
0	mem_write	2002Oct1
PASS		
0	mem_write_rt	2002Oct1
PASS		
0	mpeg_index	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_0	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_1	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_2	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_3	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_4	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_6	2002Oct1
PASS		
0	mpeg_index_multi_context_max_context_7	2002Oct1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Oct1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct1

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct1
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct1
PASS		
0	nop_eating_disabled	2002Oct1
PASS		
0	nrt_rt_interaction1	2002Oct1
PASS		
0	paint_multi	2002Oct1
PASS		
0	paint_packet	2002Oct1
PASS		
0	paint_packet_gui_control	2002Oct1
PASS		
0	pfp_reg_reads1	2002Oct1
PASS		
0	pfp_reg_reads2	2002Oct1
PASS		
0	pitch_clip	2002Oct1
PASS		
0	ply_nextscan	2002Oct1
PASS		
0	rbbm_WU_2D_idle_01	2002Oct1
PASS		
0	rbbm_WU_2D_idle_02	2002Oct1
PASS		
0	rbbm_WU_2D_idle_03	2002Oct1
PASS		
0	rbbm_WU_2D_idle_04	2002Oct1
PASS		
0	rbbm_WU_2D_idle_05	2002Oct1
PASS		
0	rbbm_WU_2D_idle_06	2002Oct1
PASS		
0	rbbm_WU_2D_idle_07	2002Oct1
PASS		
0	rbbm_WU_2D_idle_08	2002Oct1
PASS		
0	rbbm_WU_2D_idle_09	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct1

PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct1
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Oct1
PASS		
0	rbbm_WU_3D_idle_01	2002Oct1
PASS		
0	rbbm_WU_3D_idle_02	2002Oct1
PASS		
0	rbbm_WU_3D_idle_03	2002Oct1
PASS		
0	rbbm_WU_3D_idle_04	2002Oct1
PASS		
0	rbbm_WU_3D_idle_05	2002Oct1
PASS		
0	rbbm_WU_3D_idle_06	2002Oct1
PASS		
0	rbbm_WU_3D_idle_07	2002Oct1
PASS		
0	rbbm_WU_3D_idle_08	2002Oct1
PASS		
0	rbbm_WU_3D_idle_09	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct1

PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct1
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct1
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct1
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Oct1
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct1
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct1
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct1
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct1
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct1
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct1
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct1
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct1
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct1
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct1
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct1
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct1
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct1
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct1
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct1

PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct1
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct1
PASS		
0	rbbm_WU_idct_semaphore	2002Oct1
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct1
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct1
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Oct1
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct1
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct1
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct1
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct1
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_01	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_02	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_04	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Oct1

PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Oct1
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Oct1
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Oct1
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Oct1
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Oct1
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Oct1
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Oct1
PASS		
0	rbbm_clk_active_deassert_min_max	2002Oct1
PASS		
0	rbbm_clk_active_each_pipe	2002Oct1
PASS		
0	rbbm_clk_active_for_read_error	2002Oct1
PASS		
0	rbbm_clk_active_for_read_request	2002Oct1
PASS		
0	rbbm_clk_active_on_reset	2002Oct1
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Oct1
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Oct1
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Oct1
PASS		
0	rbbm_cp_queued_rdwr	2002Oct1
PASS		
0	rbbm_hi_data_and_be_swap	2002Oct1
PASS		
0	rbbm_hi_data_endian_swap	2002Oct1
PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Oct1
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Oct1
PASS		
0	rbbm_int_ack	2002Oct1
PASS		
0	rbbm_int_each_client	2002Oct1
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Oct1

PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Oct1
PASS		
0	rbbm_int_rderr	2002Oct1
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Oct1
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Oct1
PASS		
0	rbbm_isync_wait_idlegui	2002Oct1
PASS		
0	rbbm_soft_reset	2002Oct1
PASS		
0	reg_to_mem	2002Oct1
PASS		
0	reg_to_mem_rt	2002Oct1
PASS		
0	rop_cfd_wmd	2002Oct1
PASS		
0	roq_reg_reads	2002Oct1
PASS		
0	rstr_ordr	2002Oct1
PASS		
0	rt_invalid_opcode	2002Oct1
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct1
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct1
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct1

PASS		
0	rtee_discretes_falling_10_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct1
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct1

PASS		
0	rtee_discretes_invert_02_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_09_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_11_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct1

PASS		
0	rtee_discretes_invert_26_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct1
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct1
PASS		
0	rtee_discretes_level_00_events_16	2002Oct1
PASS		
0	rtee_discretes_level_01_events_16	2002Oct1
PASS		
0	rtee_discretes_level_02_events_16	2002Oct1
PASS		
0	rtee_discretes_level_03_events_16	2002Oct1
PASS		
0	rtee_discretes_level_04_events_16	2002Oct1
PASS		
0	rtee_discretes_level_05_events_16	2002Oct1
PASS		
0	rtee_discretes_level_06_events_16	2002Oct1
PASS		
0	rtee_discretes_level_07_events_16	2002Oct1
PASS		
0	rtee_discretes_level_08_events_16	2002Oct1
PASS		
0	rtee_discretes_level_09_events_16	2002Oct1
PASS		
0	rtee_discretes_level_10_events_16	2002Oct1
PASS		
0	rtee_discretes_level_11_events_16	2002Oct1
PASS		
0	rtee_discretes_level_12_events_16	2002Oct1
PASS		
0	rtee_discretes_level_13_events_16	2002Oct1
PASS		
0	rtee_discretes_level_14_events_16	2002Oct1
PASS		
0	rtee_discretes_level_15_events_16	2002Oct1
PASS		
0	rtee_discretes_level_16_events_16	2002Oct1
PASS		
0	rtee_discretes_level_17_events_16	2002Oct1

PASS		
0	rtee_discretes_level_18_events_16	2002Oct1
PASS		
0	rtee_discretes_level_19_events_16	2002Oct1
PASS		
0	rtee_discretes_level_20_events_16	2002Oct1
PASS		
0	rtee_discretes_level_21_events_16	2002Oct1
PASS		
0	rtee_discretes_level_22_events_16	2002Oct1
PASS		
0	rtee_discretes_level_23_events_16	2002Oct1
PASS		
0	rtee_discretes_level_24_events_16	2002Oct1
PASS		
0	rtee_discretes_level_25_events_16	2002Oct1
PASS		
0	rtee_discretes_level_26_events_16	2002Oct1
PASS		
0	rtee_discretes_level_27_events_16	2002Oct1
PASS		
0	rtee_discretes_level_28_events_16	2002Oct1
PASS		
0	rtee_discretes_level_29_events_16	2002Oct1
PASS		
0	rtee_discretes_level_30_events_16	2002Oct1
PASS		
0	rtee_discretes_level_31_events_16	2002Oct1
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct1

PASS		
0	rtee_discretes_rising_09_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_16_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_23_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct1
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct1
PASS		
0	rtee_display1_comp_events_16	2002Oct1

PASS		
0	rtee_display2_comp_events_16	2002Oct1
PASS		
0	rtee_mem_linear_search_events_2	2002Oct1
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct1
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct1
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct1
PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct1
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct1
PASS		
0	rtee_pio	2002Oct1
PASS		
0	rtee_prefetch_parse_nops	2002Oct1
PASS		
0	rtee_reg_linear_search_events_2	2002Oct1
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Oct1
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Oct1
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Oct1
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Oct1
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Oct1
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Oct1

PASS		
0	rtee_reg_poll_comp_110_events_16	20020ct1
PASS		
0	rtee_reg_poll_comp_111_events_16	20020ct1
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	20020ct1
PASS		
0	rtee_vip_comp_events_16	20020ct1
PASS		
0	scratch_mem_read	20020ct1
PASS		
0	scratch_mem_read2	20020ct1
PASS		
0	set_cnst_alu_max_random	20020ct1
PASS		
0	set_cnst_we_lcc_random	20020ct1
PASS		
0	set_cnst_we_random	20020ct1
PASS		
0	set_constant	20020ct1
PASS		
0	set_constant2	20020ct1
PASS		
0	set_constant_alu_max1	20020ct1
PASS		
0	set_constant_alu_max2	20020ct1
PASS		
0	set_constant_bool_max1	20020ct1
PASS		
0	set_constant_bool_max2	20020ct1
PASS		
0	set_constant_indr1_short	20020ct1
PASS		
0	set_constant_indr1_short2	20020ct1
PASS		
0	set_constant_indr1_write_enable	20020ct1
PASS		
0	set_constant_indr1_write_enable_lcc	20020ct1
PASS		
0	set_constant_indr2_short	20020ct1
PASS		
0	set_constant_indr2_short2	20020ct1
PASS		
0	set_constant_indr2_write_enable	20020ct1
PASS		
0	set_constant_indr2_write_enable_lcc	20020ct1
PASS		
0	set_constant_loop_max1	20020ct1

PASS		
0	set_constant_loop_max2	2002Oct1
PASS		
0	set_constant_ring_short	2002Oct1
PASS		
0	set_constant_ring_short2	2002Oct1
PASS		
0	set_constant_ring_write_enable	2002Oct1
PASS		
0	set_constant_ring_write_enable_lcc	2002Oct1
PASS		
0	set_constant_short	2002Oct1
PASS		
0	set_constant_short2	2002Oct1
PASS		
0	set_constant_tex_max1	2002Oct1
PASS		
0	set_constant_tex_max2	2002Oct1
PASS		
0	set_constant_write_enable	2002Oct1
PASS		
0	set_constant_write_enable_lcc	2002Oct1
PASS		
0	set_constant_write_enable_lcc2	2002Oct1
PASS		
0	set_scissors	2002Oct1
PASS		
0	set_st_indrl_match	2002Oct1
PASS		
0	set_st_indrl_mismatch	2002Oct1
PASS		
0	set_st_indrl_mismatch2	2002Oct1
PASS		
0	set_st_indrl_mismatch3	2002Oct1
PASS		
0	set_st_indrl_mismatch4	2002Oct1
PASS		
0	set_st_indrl_ps	2002Oct1
PASS		
0	set_st_indrl_ps_only	2002Oct1
PASS		
0	set_st_indrl_sublk0	2002Oct1
PASS		
0	set_st_indrl_sublk1	2002Oct1
PASS		
0	set_st_indrl_sublk1_only	2002Oct1
PASS		
0	set_st_indrl_sublk2	2002Oct1

PASS		
0	set_st_indr1_sublk2_only	2002Oct1
PASS		
0	set_st_indr1_sublk3	2002Oct1
PASS		
0	set_st_indr1_sublk3_only	2002Oct1
PASS		
0	set_st_indr1_sublk4	2002Oct1
PASS		
0	set_st_indr1_sublk4_only	2002Oct1
PASS		
0	set_st_indr1_sublk5	2002Oct1
PASS		
0	set_st_indr1_sublk5_only	2002Oct1
PASS		
0	set_st_indr1_sublk6	2002Oct1
PASS		
0	set_st_indr1_sublk6_only	2002Oct1
PASS		
0	set_st_indr1_sublk7	2002Oct1
PASS		
0	set_st_indr1_sublk7_only	2002Oct1
PASS		
0	set_st_indr1_vs	2002Oct1
PASS		
0	set_st_indr1_vs_only	2002Oct1
PASS		
0	set_st_indr2_match	2002Oct1
PASS		
0	set_st_indr2_mismatch	2002Oct1
PASS		
0	set_st_indr2_mismatch2	2002Oct1
PASS		
0	set_st_indr2_mismatch3	2002Oct1
PASS		
0	set_st_indr2_mismatch4	2002Oct1
PASS		
0	set_st_indr2_ps	2002Oct1
PASS		
0	set_st_indr2_ps_only	2002Oct1
PASS		
0	set_st_indr2_sublk0	2002Oct1
PASS		
0	set_st_indr2_sublk1	2002Oct1
PASS		
0	set_st_indr2_sublk1_only	2002Oct1
PASS		
0	set_st_indr2_sublk2	2002Oct1

PASS		
0	set_st_indr2_sublk2_only	2002Oct1
PASS		
0	set_st_indr2_sublk3	2002Oct1
PASS		
0	set_st_indr2_sublk3_only	2002Oct1
PASS		
0	set_st_indr2_sublk4	2002Oct1
PASS		
0	set_st_indr2_sublk4_only	2002Oct1
PASS		
0	set_st_indr2_sublk5	2002Oct1
PASS		
0	set_st_indr2_sublk5_only	2002Oct1
PASS		
0	set_st_indr2_sublk6	2002Oct1
PASS		
0	set_st_indr2_sublk6_only	2002Oct1
PASS		
0	set_st_indr2_sublk7	2002Oct1
PASS		
0	set_st_indr2_sublk7_only	2002Oct1
PASS		
0	set_st_indr2_vs	2002Oct1
PASS		
0	set_st_indr2_vs_only	2002Oct1
PASS		
0	set_st_pfmatch_disabled	2002Oct1
PASS		
0	set_st_ring_match	2002Oct1
PASS		
0	set_st_ring_mismatch	2002Oct1
PASS		
0	set_st_ring_mismatch2	2002Oct1
PASS		
0	set_st_ring_mismatch3	2002Oct1
PASS		
0	set_st_ring_mismatch4	2002Oct1
PASS		
0	set_st_ring_ps	2002Oct1
PASS		
0	set_st_ring_ps_only	2002Oct1
PASS		
0	set_st_ring_sublk0	2002Oct1
PASS		
0	set_st_ring_sublk1	2002Oct1
PASS		
0	set_st_ring_sublk1_only	2002Oct1

PASS		
0	set_st_ring_sublk2	2002Oct1
PASS		
0	set_st_ring_sublk2_only	2002Oct1
PASS		
0	set_st_ring_sublk3	2002Oct1
PASS		
0	set_st_ring_sublk3_only	2002Oct1
PASS		
0	set_st_ring_sublk4	2002Oct1
PASS		
0	set_st_ring_sublk4_only	2002Oct1
PASS		
0	set_st_ring_sublk5	2002Oct1
PASS		
0	set_st_ring_sublk5_only	2002Oct1
PASS		
0	set_st_ring_sublk6	2002Oct1
PASS		
0	set_st_ring_sublk6_only	2002Oct1
PASS		
0	set_st_ring_sublk7	2002Oct1
PASS		
0	set_st_ring_sublk7_only	2002Oct1
PASS		
0	set_st_ring_vs	2002Oct1
PASS		
0	set_st_ring_vs_only	2002Oct1
PASS		
0	set_st_stress_a	2002Oct1
PASS		
0	set_st_stress_b	2002Oct1
PASS		
0	set_st_stress_c	2002Oct1
PASS		
0	set_st_stress_d	2002Oct1
PASS		
0	set_st_stress_e	2002Oct1
PASS		
0	set_st_stress_f	2002Oct1
PASS		
737	set_state_rt_a	2002Oct1
FAIL	cp_rbbm.cmp rbbmWr.cmp	
738	set_state_rt_b	2002Oct1
FAIL	cp_rbbm.cmp rbbmWr.cmp	
739	set_state_rt_c	2002Oct1
FAIL	cp_rbbm.cmp rbbmWr.cmp	
740	set_state_rt_d	2002Oct1

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0  smoke_nrt                      2002Oct1
PASS
0  src_clip                        2002Oct1
PASS
0  srf_cohr                        2002Oct1
PASS
0  switch_pio_bm                  2002Oct1
PASS
0  trans_bitblt                   2002Oct1
PASS
0  type0_2long_pckts              2002Oct1
PASS
0  type0_3indr_buffs             2002Oct1
PASS
0  type0_4pckts                   2002Oct1
PASS
0  type0_incr_updt_cnst           2002Oct1
PASS
0  type0_incr_updt_instrmem       2002Oct1
PASS
0  type0_incr_updt_st             2002Oct1
PASS
0  type0_incr_updt_stress         2002Oct1
PASS
0  type0_ind1_ind2_a              2002Oct1
PASS
0  type0_ind1_ind2_b              2002Oct1
PASS
0  type0_indr_buff                2002Oct1
PASS
0  type1_4pckts                   2002Oct1
PASS
0  type1_indr_buff                2002Oct1
PASS
0  type1_pckt                     2002Oct1
PASS
0  type_random                    2002Oct1
PASS
760 uram_wrt_rt_ucode             2002Oct1
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
761 uram_wrt_ucode                2002Oct1
**FAIL**          rbbmWr.cmp
0  viz_query_a                    2002Oct1
PASS
0  viz_query_draw_indx            2002Oct1
PASS
0  viz_query_draw_indx_indr1      2002Oct1

```

```

PASS
0 viz_query_draw_indx_indr2 2002Oct1
PASS
766 viz_query_draw_indx_multipass 2002Oct1
**FAIL** rbbmWr.cmp
0 viz_query_draw_indx_ring 2002Oct1
PASS
0 viz_query_indr1 2002Oct1
PASS
0 viz_query_indr1_simple 2002Oct1
PASS
0 viz_query_indr2 2002Oct1
PASS
0 viz_query_indr2_simple 2002Oct1
PASS
0 viz_query_ring 2002Oct1
PASS
0 viz_query_ring_simple 2002Oct1
PASS
0 wait_for_idle 2002Oct1
PASS
0 wait_reg_mem_poll_mem0 2002Oct1
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct1
PASS
0 wait_reg_mem_poll_mem1 2002Oct1
PASS
0 wait_reg_mem_poll_mem2 2002Oct1
PASS
0 wait_reg_mem_poll_mem3 2002Oct1
PASS
0 wait_reg_mem_poll_reg0 2002Oct1
PASS
0 wait_reg_mem_poll_reg1 2002Oct1
PASS
0 wait_reg_mem_poll_reg2 2002Oct1
PASS
0 wait_reg_mem_poll_reg3 2002Oct1
PASS

```

```

=====
Tests_Run_count = 783 Pass_count = 766 Fail_count = 17 Incomplete_count = 0
=====

```


=====
Regression Report For CP - Project = r400 Created on Thu Aug 22 23:34:26 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug22
PASS			
0	2d_indr_buffer_b		2002Aug22
PASS			
0	2d_indr_buffer_c		2002Aug22
PASS			
0	2d_indr_buffer_d		2002Aug22
PASS			
0	2d_indr_buffer_e		2002Aug22
PASS			
0	2d_indr_buffer_f		2002Aug22
PASS			
0	2d_indr_buffer_g		2002Aug22
PASS			
0	3d_draw_indx_2		2002Aug22
PASS			
0	3d_draw_indx_2_b		2002Aug22
PASS			
0	bitblt_multi		2002Aug22
PASS			
0	brush_32x1mono		2002Aug22
PASS			
0	brush_8x8color_16		2002Aug22
PASS			
0	brush_8x8color_32		2002Aug22
PASS			
0	brush_8x8color_64		2002Aug22
PASS			
0	brush_8x8color_dst_0_1		2002Aug22
PASS			
0	brush_8x8mono		2002Aug22
PASS			
0	brush_color_mono_stress		2002Aug22
PASS			
18	brush_flag		2002Aug22
FAIL			
0	brush_types_13_14		2002Aug22
PASS			
0	clear_const_wr_en_on_me_init		2002Aug22

PASS		
0	clear_const_wr_en_with_lcc	2002Aug22
PASS		
0	cond_write_mem_poll_mem	2002Aug22
PASS		
0	cond_write_mem_poll_mem2	2002Aug22
PASS		
0	cond_write_mem_poll_mem3	2002Aug22
PASS		
0	cond_write_mem_poll_mem_rt	2002Aug22
PASS		
0	cond_write_mem_poll_reg	2002Aug22
PASS		
0	cond_write_mem_poll_reg2	2002Aug22
PASS		
0	cond_write_mem_poll_reg3	2002Aug22
PASS		
0	cp_acpi_recovery_soft_reset	2002Aug22
PASS		
0	cp_bios_scratch	2002Aug22
PASS		
0	cp_ib2_base000_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug22
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug22
PASS		
0	cp_ib_base000_bufszXXX	2002Aug22
PASS		
0	cp_ib_base001_bufszXXX	2002Aug22
PASS		
0	cp_ib_base010_bufszXXX	2002Aug22
PASS		
0	cp_ib_base011_bufszXXX	2002Aug22
PASS		
0	cp_ib_base100_bufszXXX	2002Aug22
PASS		
0	cp_ib_base101_bufszXXX	2002Aug22

PASS		
0	cp_ib_base110_bufszXXX	2002Aug22
PASS		
0	cp_ib_base111_bufszXXX	2002Aug22
PASS		
0	cp_interrupt	2002Aug22
PASS		
0	cp_interrupt_rt_events_16	2002Aug22
PASS		
0	cp_iq2_alignment_000	2002Aug22
PASS		
0	cp_iq2_alignment_001	2002Aug22
PASS		
0	cp_iq2_alignment_010	2002Aug22
PASS		
0	cp_iq2_alignment_011	2002Aug22
PASS		
0	cp_iq2_alignment_100	2002Aug22
PASS		
0	cp_iq2_alignment_101	2002Aug22
PASS		
0	cp_iq2_alignment_110	2002Aug22
PASS		
0	cp_iq2_alignment_111	2002Aug22
PASS		
0	cp_iq_alignment_000	2002Aug22
PASS		
0	cp_iq_alignment_001	2002Aug22
PASS		
0	cp_iq_alignment_010	2002Aug22
PASS		
0	cp_iq_alignment_011	2002Aug22
PASS		
0	cp_iq_alignment_100	2002Aug22
PASS		
0	cp_iq_alignment_101	2002Aug22
PASS		
0	cp_iq_alignment_110	2002Aug22
PASS		
0	cp_iq_alignment_111	2002Aug22
PASS		
0	cp_max_size_packet	2002Aug22
PASS		
0	cp_nrt_rt_protection_mode	2002Aug22
PASS		
0	cp_output_stress	2002Aug22
PASS		
0	cp_protection_mode	2002Aug22

PASS		
0	cp_rb_alignment_000	2002Aug22
PASS		
0	cp_rb_alignment_001	2002Aug22
PASS		
0	cp_rb_alignment_010	2002Aug22
PASS		
0	cp_rb_alignment_011	2002Aug22
PASS		
0	cp_rb_alignment_100	2002Aug22
PASS		
0	cp_rb_alignment_101	2002Aug22
PASS		
0	cp_rb_alignment_110	2002Aug22
PASS		
0	cp_rb_alignment_111	2002Aug22
PASS		
0	cp_rb_wptrXXX_base000	2002Aug22
PASS		
0	cp_rb_wptrXXX_base001	2002Aug22
PASS		
0	cp_rb_wptrXXX_base010	2002Aug22
PASS		
0	cp_rb_wptrXXX_base011	2002Aug22
PASS		
0	cp_rb_wptrXXX_base100	2002Aug22
PASS		
0	cp_rb_wptrXXX_base101	2002Aug22
PASS		
0	cp_rb_wptrXXX_base110	2002Aug22
PASS		
0	cp_rb_wptrXXX_base111	2002Aug22
PASS		
0	cp_register_reads	2002Aug22
PASS		
86	cp_reg_rw_test	2002Aug22
FAIL		
0	cp_rt_protection_mode	2002Aug22
PASS		
0	cp_scratch	2002Aug22
PASS		
0	cp_scratch_multi	2002Aug22
PASS		
0	dma_cp_sync	2002Aug22
PASS		
0	dma_dscrstr_chckn_bit	2002Aug22
PASS		
0	dma_dscrstr_flush1	2002Aug22

PASS		
0	dma_dscrstr_flush2	2002Aug22
PASS		
0	dma_dscrstr_flush3	2002Aug22
PASS		
0	dma_m2m_src00_dstXX	2002Aug22
PASS		
0	dma_m2m_src01_dstXX	2002Aug22
PASS		
0	dma_m2m_src10_dstXX	2002Aug22
PASS		
0	dma_m2m_src11_dstXX	2002Aug22
PASS		
0	dma_mem2mem	2002Aug22
PASS		
0	dma_mem2mem_abort	2002Aug22
PASS		
0	dma_mem2mem_alignments	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug22
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug22
PASS		
0	dma_mem2mem_pio_abort	2002Aug22
PASS		
0	dma_mem2reg	2002Aug22
PASS		
0	dma_mem2reg_abort	2002Aug22
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug22
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug22
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug22
PASS		
0	dma_mem2reg_sync	2002Aug22

PASS		
0	dma_r2r_m2m_r2r	2002Aug22
PASS		
0	dma_r2r_m2r_r2r	2002Aug22
PASS		
0	dma_r2r_r2m_r2r	2002Aug22
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug22
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug22
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug22
PASS		
0	dma_reg2mem	2002Aug22
PASS		
0	dma_reg2mem_abort	2002Aug22
PASS		
0	dma_reg2reg	2002Aug22
PASS		
0	dma_reg2reg_2dw	2002Aug22
PASS		
0	dma_reg2reg_daic	2002Aug22
PASS		
0	dma_reg2reg_mult	2002Aug22
PASS		
0	dma_reg2reg_mult2	2002Aug22
PASS		
0	dma_reg2reg_mult3	2002Aug22
PASS		
0	dma_reg2reg_saic	2002Aug22
PASS		
0	dma_reg2reg_sdaic	2002Aug22
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug22
PASS		
0	dma_reg2reg_stress	2002Aug22
PASS		
0	draw_indx_a	2002Aug22
PASS		
0	draw_indx_b	2002Aug22
PASS		
0	draw_indx_c	2002Aug22
PASS		
0	draw_indx_indr1_a	2002Aug22
PASS		
0	draw_indx_indr1_b	2002Aug22
PASS		
0	draw_indx_indr1_short_a	2002Aug22

PASS		
0	draw_indx_indr1_short_b	2002Aug22
PASS		
0	draw_indx_indr1_stress	2002Aug22
PASS		
0	draw_indx_indr2_a	2002Aug22
PASS		
0	draw_indx_indr2_b	2002Aug22
PASS		
0	draw_indx_indr2_short_a	2002Aug22
PASS		
0	draw_indx_indr2_short_b	2002Aug22
PASS		
0	draw_indx_indr2_stress	2002Aug22
PASS		
0	draw_indx_ring1	2002Aug22
PASS		
0	draw_indx_ring2	2002Aug22
PASS		
0	draw_indx_ring_short1	2002Aug22
PASS		
0	draw_indx_ring_short2	2002Aug22
PASS		
0	draw_indx_ring_stress	2002Aug22
PASS		
0	draw_indx_short_a	2002Aug22
PASS		
0	draw_indx_short_b	2002Aug22
PASS		
0	draw_indx_short_c	2002Aug22
PASS		
0	draw_indx_stress_a	2002Aug22
PASS		
0	draw_indx_stress_b	2002Aug22
PASS		
0	draw_indx_stress_c	2002Aug22
PASS		
0	event_timestamp_write	2002Aug22
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug22
PASS		
0	ft_indr1_july22	2002Aug22
PASS		
0	ft_indr2_july22	2002Aug22
PASS		
0	ft_july22	2002Aug22
PASS		
0	header_dump	2002Aug22

PASS		
0	header_dump_rt	2002Aug22
PASS		
0	header_dump_small	2002Aug22
PASS		
0	hostdata_blt	2002Aug22
PASS		
0	im_load_a	2002Aug22
PASS		
0	im_load_b	2002Aug22
PASS		
0	im_load_cid_mismatch	2002Aug22
PASS		
0	im_load_immediate	2002Aug22
PASS		
0	im_load_immediate_nrt_me	2002Aug22
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug22
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug22
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug22
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug22
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug22
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug22
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug22
PASS		
0	im_load_indr1_a	2002Aug22
PASS		
0	im_load_indr1_b	2002Aug22
PASS		
0	im_load_indr2_a	2002Aug22
PASS		
0	im_load_indr2_b	2002Aug22
PASS		
0	im_load_invalidate	2002Aug22
PASS		
0	im_load_match	2002Aug22
PASS		
0	im_load_match2	2002Aug22
PASS		
0	im_load_nrt_me	2002Aug22
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug22

PASS		
0	im_load_nrt_me_ps_wrap	2002Aug22
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug22
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug22
PASS		
0	im_load_pfmatch_disable	2002Aug22
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0	im_load_ring_a	2002Aug22
PASS		
0	im_load_ring_b	2002Aug22
PASS		
0	im_load_rt	2002Aug22
PASS		
0	im_load_rt2	2002Aug22
PASS		
0	im_load_rt3	2002Aug22
PASS		
0	im_load_rt4	2002Aug22
PASS		
0	im_load_sz_mismatch	2002Aug22
PASS		
0	incremental_update_const	2002Aug22
PASS		
0	incremental_update_instr	2002Aug22
PASS		
0	incremental_update_state	2002Aug22
PASS		
0	indirect_buffer_a	2002Aug22
PASS		
0	indirect_buffer_b	2002Aug22
PASS		
0	indirect_buffer_c	2002Aug22
PASS		
0	indirect_buffer_d	2002Aug22
PASS		
0	indirect_buffer_e	2002Aug22
PASS		
0	indirect_buffer_f	2002Aug22
PASS		
0	indirect_buffer_g	2002Aug22
PASS		
0	indirect_buffer_h	2002Aug22
PASS		
0	indirect_buffer_i	2002Aug22
PASS		
0	indirect_buffer_j	2002Aug22

PASS		
0	indirect_buffer_k	2002Aug22
PASS		
0	indirect_buffer_l	2002Aug22
PASS		
0	indirect_buffer_mp_a	2002Aug22
PASS		
0	indirect_buffer_mp_b	2002Aug22
PASS		
0	indirect_buffer_mp_c	2002Aug22
PASS		
0	indirect_buffer_mp_d	2002Aug22
PASS		
0	indirect_buffer_mp_e	2002Aug22
PASS		
0	indirect_buffer_mp_f	2002Aug22
PASS		
0	invalidate_on_me_init	2002Aug22
PASS		
0	invalidate_state_constants	2002Aug22
PASS		
0	invalidate_state_shaders	2002Aug22
PASS		
0	invalidate_state_shaders2	2002Aug22
PASS		
0	invalidate_state_sublks	2002Aug22
PASS		
0	invalidate_state_sublks2	2002Aug22
PASS		
0	invalid_opcode	2002Aug22
PASS		
0	iq1_iq2_mult	2002Aug22
PASS		
0	iq1_iq2_push_wrap	2002Aug22
PASS		
0	iq2_csq_full	2002Aug22
PASS		
0	IQ2_Mult	2002Aug22
PASS		
0	IQ2_Wrap_Legacy	2002Aug22
PASS		
0	IQ_Wrap	2002Aug22
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0	ld_cnst_cntx_coherency_cntrl	2002Aug22
PASS		
0	ld_cnst_cntx_coherency_cntrl2	2002Aug22
PASS		
0	ld_cnst_cntx_indrl_replicate	2002Aug22

PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug22
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug22
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug22
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug22
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug22
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug22
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug22
PASS		
0	ld_cnst_cntx_match	2002Aug22
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0	ld_cnst_cntx_match_valid_preserved	2002Aug22
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug22
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug22
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0	ld_cnst_cntx_mismatch_force	2002Aug22
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0	ld_cnst_cntx_pfmatch_disable	2002Aug22
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug22
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug22
PASS		
0	ld_cnst_cntx_replicate8	2002Aug22
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug22
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug22
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug22
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug22
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug22
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug22
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug22
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug22

PASS		
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PASS		
0	load_palette	2002Aug22
PASS		
0	mem_write	2002Aug22
PASS		
0	mem_write_rt	2002Aug22
PASS		
0	mpeg_index	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug22
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug22
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug22
PASS		
0	nop_eating_disabled	2002Aug22
PASS		
0	paint_multi	2002Aug22
PASS		
0	pitch_clip	2002Aug22
PASS		
0	ply_nextscan	2002Aug22

PASS		
0	rbbm_soft_reset	2002Aug22
PASS		
286	rbbm_WU_2D_idle_01	2002Aug22
FAIL		
287	rbbm_WU_2D_idle_02	2002Aug22
FAIL		
288	rbbm_WU_2D_idle_03	2002Aug22
FAIL		
289	rbbm_WU_2D_idle_04	2002Aug22
FAIL		
290	rbbm_WU_2D_idle_05	2002Aug22
FAIL		
291	rbbm_WU_2D_idle_06	2002Aug22
FAIL		
292	rbbm_WU_2D_idle_07	2002Aug22
FAIL		
293	rbbm_WU_2D_idle_08	2002Aug22
FAIL		
294	rbbm_WU_2D_idle_09	2002Aug22
FAIL		
295	rbbm_WU_2D_idle_clean_01	2002Aug22
FAIL		
296	rbbm_WU_2D_idle_clean_02	2002Aug22
FAIL		
297	rbbm_WU_2D_idle_clean_03	2002Aug22
FAIL		
298	rbbm_WU_2D_idle_clean_04	2002Aug22
FAIL		
299	rbbm_WU_2D_idle_clean_05	2002Aug22
FAIL		
300	rbbm_WU_2D_idle_clean_06	2002Aug22
FAIL		
301	rbbm_WU_2D_idle_clean_07	2002Aug22
FAIL		
302	rbbm_WU_2D_idle_clean_08	2002Aug22
FAIL		
303	rbbm_WU_2D_idle_clean_09	2002Aug22
FAIL		
304	rbbm_WU_2D_idle_clean_10	2002Aug22
FAIL		
305	rbbm_WU_2D_idle_clean_11	2002Aug22
FAIL		
306	rbbm_WU_2D_idle_clean_12	2002Aug22
FAIL		
307	rbbm_WU_3D_idle_01	2002Aug22
FAIL		
308	rbbm_WU_3D_idle_02	2002Aug22

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**FAIL**
309  rbbm_WU_3D_idle_03                2002Aug22
**FAIL**
310  rbbm_WU_3D_idle_04                2002Aug22
**FAIL**
311  rbbm_WU_3D_idle_05                2002Aug22
**FAIL**
312  rbbm_WU_3D_idle_06                2002Aug22
**FAIL**
313  rbbm_WU_3D_idle_07                2002Aug22
**FAIL**
314  rbbm_WU_3D_idle_08                2002Aug22
**FAIL**
315  rbbm_WU_3D_idle_09                2002Aug22
**FAIL**
316  rbbm_WU_3D_idle_clean_01          2002Aug22
**FAIL**
317  rbbm_WU_3D_idle_clean_02          2002Aug22
**FAIL**
318  rbbm_WU_3D_idle_clean_03          2002Aug22
**FAIL**
319  rbbm_WU_3D_idle_clean_04          2002Aug22
**FAIL**
320  rbbm_WU_3D_idle_clean_05          2002Aug22
**FAIL**
321  rbbm_WU_3D_idle_clean_06          2002Aug22
**FAIL**
322  rbbm_WU_3D_idle_clean_07          2002Aug22
**FAIL**
323  rbbm_WU_3D_idle_clean_08          2002Aug22
**FAIL**
324  rbbm_WU_3D_idle_clean_09          2002Aug22
**FAIL**
325  rbbm_WU_3D_idle_clean_10          2002Aug22
**FAIL**
326  rbbm_WU_3D_idle_clean_11          2002Aug22
**FAIL**
327  rbbm_WU_3D_idle_clean_12          2002Aug22
**FAIL**
328  rbbm_WU_BIF_RBBM_agp_flush_off    2002Aug22
**FAIL**
329  rbbm_WU_BOTH_GRPH_update_pending_off 2002Aug22
**FAIL**
330  rbbm_WU_cmdfifo_entries            2002Aug22
**FAIL**
331  rbbm_WU_CP_dma_idle                2002Aug22
**FAIL**
332  rbbm_WU_CRTC_D1MODE_vline_fe      2002Aug22

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**FAIL**
333  rbbm_WU_CRTC_D1MODE_vline_on                2002Aug22
**FAIL**
334  rbbm_WU_CRTC_D1MODE_vline_re                2002Aug22
**FAIL**
335  rbbm_WU_CRTC_D2MODE_vline_fe                2002Aug22
**FAIL**
336  rbbm_WU_CRTC_D2MODE_vline_on                2002Aug22
**FAIL**
337  rbbm_WU_CRTC_D2MODE_vline_re                2002Aug22
**FAIL**
338  rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off 2002Aug22
**FAIL**
339  rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off 2002Aug22
**FAIL**
340  rbbm_WU_idct_semaphore                        2002Aug22
**FAIL**
341  rbbm_WU_MH_hdp_clean                          2002Aug22
**FAIL**
342  rbbm_WU_OV_FLIP_D1OVL_update_pending_off     2002Aug22
**FAIL**
343  rbbm_WU_OV_FLIP_D2OVL_update_pending_off     2002Aug22
**FAIL**
344  rbbm_WU_pad_ext_sig_rising_edge              2002Aug22
**FAIL**
345  rbbm_WUrt_BIF_RBBM_agp_flush_off             2002Aug22
**FAIL**
346  rbbm_WUrt_BOTH_GRP_H_update_pending_off      2002Aug22
**FAIL**
347  rbbm_WUrt_CRTC_D1MODE_vline_fe              2002Aug22
**FAIL**
348  rbbm_WUrt_CRTC_D1MODE_vline_on              2002Aug22
**FAIL**
349  rbbm_WUrt_CRTC_D1MODE_vline_re              2002Aug22
**FAIL**
350  rbbm_WUrt_CRTC_D2MODE_vline_fe              2002Aug22
**FAIL**
351  rbbm_WUrt_CRTC_D2MODE_vline_on              2002Aug22
**FAIL**
352  rbbm_WUrt_CRTC_D2MODE_vline_re              2002Aug22
**FAIL**
353  rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Aug22
**FAIL**
354  rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Aug22
**FAIL**
355  rbbm_WUrt_MH_hdp_clean                          2002Aug22
**FAIL**
356  rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off   2002Aug22

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**FAIL**
357  rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off      2002Aug22
**FAIL**
358  rbbm_WUrt_pad_ext_sig_rising_edge              2002Aug22
**FAIL**
359  rbbm_WUrt_VIPH0_dma_idle                       2002Aug22
**FAIL**
360  rbbm_WUrt_VIPH1_dma_idle                       2002Aug22
**FAIL**
361  rbbm_WUrt_VIPH2_dma_idle                       2002Aug22
**FAIL**
362  rbbm_WUrt_VIPH3_dma_idle                       2002Aug22
**FAIL**
363  rbbm_WU_VIPH0_dma_idle                         2002Aug22
**FAIL**
364  rbbm_WU_VIPH1_dma_idle                         2002Aug22
**FAIL**
365  rbbm_WU_VIPH2_dma_idle                         2002Aug22
**FAIL**
366  rbbm_WU_VIPH3_dma_idle                         2002Aug22
**FAIL**
0    RB_BUFSZ_0                                     2002Aug22
PASS
0    RB_BUFSZ_1                                     2002Aug22
PASS
0    RB_Wrap                                         2002Aug22
PASS
0    reg_to_mem                                       2002Aug22
PASS
0    reg_to_mem_rt                                    2002Aug22
PASS
0    rop_cfd_wmd                                       2002Aug22
PASS
0    rstr_ordr                                         2002Aug22
PASS
0    rtee_discretes_falling_00_events_16            2002Aug22
PASS
0    rtee_discretes_falling_01_events_16            2002Aug22
PASS
0    rtee_discretes_falling_02_events_16            2002Aug22
PASS
0    rtee_discretes_falling_03_events_16            2002Aug22
PASS
0    rtee_discretes_falling_04_events_16            2002Aug22
PASS
0    rtee_discretes_falling_05_events_16            2002Aug22
PASS
0    rtee_discretes_falling_06_events_16            2002Aug22

```


PASS		
0	rtee_discretes_falling_07_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug22
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PASS		
0	rtee_discretes_falling_26_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug22
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PASS		
0	rtee_discretes_falling_29_events_16	2002Aug22
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug22

PASS		
0	rtee_discretes_falling_31_events_16	2002Aug22
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PASS		
0	rtee_discretes_invert_01_events_16	2002Aug22
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug22
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug22
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug22
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug22
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug22
PASS		
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PASS		
0	rtee_discretes_invert_08_events_16	2002Aug22
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug22
PASS		
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PASS		
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PASS		
0	rtee_discretes_invert_12_events_16	2002Aug22
PASS		
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0	rtee_discretes_invert_14_events_16	2002Aug22
PASS		
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PASS		
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0	rtee_discretes_invert_24_events_16	2002Aug22
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0	rtee_discretes_level_04_events_16	2002Aug22
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0	rtee_discretes_level_05_events_16	2002Aug22
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0	rtee_discretes_level_06_events_16	2002Aug22
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0	rtee_discretes_level_07_events_16	2002Aug22
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0	rtee_discretes_level_08_events_16	2002Aug22
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0	rtee_discretes_level_14_events_16	2002Aug22

PASS		
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0	rtee_discretes_level_16_events_16	2002Aug22
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0	rtee_discretes_level_22_events_16	2002Aug22
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0	rtee_discretes_level_23_events_16	2002Aug22
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0	rtee_discretes_level_24_events_16	2002Aug22
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0	rtee_discretes_level_25_events_16	2002Aug22
PASS		
0	rtee_discretes_level_26_events_16	2002Aug22
PASS		
0	rtee_discretes_level_27_events_16	2002Aug22
PASS		
0	rtee_discretes_level_28_events_16	2002Aug22
PASS		
0	rtee_discretes_level_29_events_16	2002Aug22
PASS		
0	rtee_discretes_level_30_events_16	2002Aug22
PASS		
0	rtee_discretes_level_31_events_16	2002Aug22
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug22

PASS		
0	rtee_discretes_rising_06_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug22

PASS		
0	rtee_discretes_rising_30_events_16	2002Aug22
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug22
PASS		
0	rtee_display1_comp_events_16	2002Aug22
PASS		
0	rtee_display2_comp_events_16	2002Aug22
PASS		
0	rtee_mem_linear_search_events_2	2002Aug22
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug22
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug22
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug22
PASS		
0	rtee_prefetch_parse_nops	2002Aug22
PASS		
0	rtee_reg_linear_search_events_2	2002Aug22
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug22

PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug22
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug22
PASS		
0	rtee_vip_comp_events_16	2002Aug22
PASS		
0	rt_invalid_opcode	2002Aug22
PASS		
0	scratch_mem_read	2002Aug22
PASS		
0	scratch_mem_read2	2002Aug22
PASS		
0	set_constant	2002Aug22
PASS		
0	set_constant2	2002Aug22
PASS		
0	set_constant_alu_max1	2002Aug22
PASS		
0	set_constant_alu_max2	2002Aug22
PASS		
0	set_constant_bool_max1	2002Aug22
PASS		
0	set_constant_bool_max2	2002Aug22
PASS		
0	set_constant_indr1_short	2002Aug22
PASS		
0	set_constant_indr1_short2	2002Aug22
PASS		
0	set_constant_indr1_write_enable	2002Aug22
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug22
PASS		
0	set_constant_indr2_short	2002Aug22
PASS		
0	set_constant_indr2_short2	2002Aug22
PASS		
0	set_constant_indr2_write_enable	2002Aug22
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug22
PASS		
0	set_constant_loop_max1	2002Aug22
PASS		
0	set_constant_loop_max2	2002Aug22
PASS		
0	set_constant_ring_short	2002Aug22

PASS		
0	set_constant_ring_short2	2002Aug22
PASS		
0	set_constant_ring_write_enable	2002Aug22
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug22
PASS		
0	set_constant_short	2002Aug22
PASS		
0	set_constant_short2	2002Aug22
PASS		
0	set_constant_tex_max1	2002Aug22
PASS		
0	set_constant_tex_max2	2002Aug22
PASS		
0	set_constant_write_enable	2002Aug22
PASS		
0	set_constant_write_enable_lcc	2002Aug22
PASS		
0	set_constant_write_enable_lcc2	2002Aug22
PASS		
0	set_scissors	2002Aug22
PASS		
0	set_state_rt_a	2002Aug22
PASS		
0	set_state_rt_b	2002Aug22
PASS		
0	set_state_rt_c	2002Aug22
PASS		
0	set_state_rt_d	2002Aug22
PASS		
0	set_st_indrl_match	2002Aug22
PASS		
0	set_st_indrl_mismatch	2002Aug22
PASS		
0	set_st_indrl_mismatch2	2002Aug22
PASS		
0	set_st_indrl_mismatch3	2002Aug22
PASS		
0	set_st_indrl_mismatch4	2002Aug22
PASS		
0	set_st_indrl_ps	2002Aug22
PASS		
0	set_st_indrl_ps_only	2002Aug22
PASS		
0	set_st_indrl_sublk0	2002Aug22
PASS		
0	set_st_indrl_sublk1	2002Aug22

PASS		
0	set_st_indr1_sublk1_only	2002Aug22
PASS		
0	set_st_indr1_sublk2	2002Aug22
PASS		
0	set_st_indr1_sublk2_only	2002Aug22
PASS		
0	set_st_indr1_sublk3	2002Aug22
PASS		
0	set_st_indr1_sublk3_only	2002Aug22
PASS		
0	set_st_indr1_sublk4	2002Aug22
PASS		
0	set_st_indr1_sublk4_only	2002Aug22
PASS		
0	set_st_indr1_sublk5	2002Aug22
PASS		
0	set_st_indr1_sublk5_only	2002Aug22
PASS		
0	set_st_indr1_sublk6	2002Aug22
PASS		
0	set_st_indr1_sublk6_only	2002Aug22
PASS		
0	set_st_indr1_sublk7	2002Aug22
PASS		
0	set_st_indr1_sublk7_only	2002Aug22
PASS		
0	set_st_indr1_vs	2002Aug22
PASS		
0	set_st_indr1_vs_only	2002Aug22
PASS		
0	set_st_indr2_match	2002Aug22
PASS		
0	set_st_indr2_mismatch	2002Aug22
PASS		
0	set_st_indr2_mismatch2	2002Aug22
PASS		
0	set_st_indr2_mismatch3	2002Aug22
PASS		
0	set_st_indr2_mismatch4	2002Aug22
PASS		
0	set_st_indr2_ps	2002Aug22
PASS		
0	set_st_indr2_ps_only	2002Aug22
PASS		
0	set_st_indr2_sublk0	2002Aug22
PASS		
0	set_st_indr2_sublk1	2002Aug22

PASS		
0	set_st_indr2_sublk1_only	2002Aug22
PASS		
0	set_st_indr2_sublk2	2002Aug22
PASS		
0	set_st_indr2_sublk2_only	2002Aug22
PASS		
0	set_st_indr2_sublk3	2002Aug22
PASS		
0	set_st_indr2_sublk3_only	2002Aug22
PASS		
0	set_st_indr2_sublk4	2002Aug22
PASS		
0	set_st_indr2_sublk4_only	2002Aug22
PASS		
0	set_st_indr2_sublk5	2002Aug22
PASS		
0	set_st_indr2_sublk5_only	2002Aug22
PASS		
0	set_st_indr2_sublk6	2002Aug22
PASS		
0	set_st_indr2_sublk6_only	2002Aug22
PASS		
0	set_st_indr2_sublk7	2002Aug22
PASS		
0	set_st_indr2_sublk7_only	2002Aug22
PASS		
0	set_st_indr2_vs	2002Aug22
PASS		
0	set_st_indr2_vs_only	2002Aug22
PASS		
0	set_st_pfmacth_disabled	2002Aug22
PASS		
0	set_st_ring_match	2002Aug22
PASS		
0	set_st_ring_mismatch	2002Aug22
PASS		
0	set_st_ring_mismatch2	2002Aug22
PASS		
0	set_st_ring_mismatch3	2002Aug22
PASS		
0	set_st_ring_mismatch4	2002Aug22
PASS		
0	set_st_ring_ps	2002Aug22
PASS		
0	set_st_ring_ps_only	2002Aug22
PASS		
0	set_st_ring_sublk0	2002Aug22

PASS		
0	set_st_ring_subblk1	2002Aug22
PASS		
0	set_st_ring_subblk1_only	2002Aug22
PASS		
0	set_st_ring_subblk2	2002Aug22
PASS		
0	set_st_ring_subblk2_only	2002Aug22
PASS		
0	set_st_ring_subblk3	2002Aug22
PASS		
0	set_st_ring_subblk3_only	2002Aug22
PASS		
0	set_st_ring_subblk4	2002Aug22
PASS		
0	set_st_ring_subblk4_only	2002Aug22
PASS		
0	set_st_ring_subblk5	2002Aug22
PASS		
0	set_st_ring_subblk5_only	2002Aug22
PASS		
0	set_st_ring_subblk6	2002Aug22
PASS		
0	set_st_ring_subblk6_only	2002Aug22
PASS		
0	set_st_ring_subblk7	2002Aug22
PASS		
0	set_st_ring_subblk7_only	2002Aug22
PASS		
0	set_st_ring_vs	2002Aug22
PASS		
0	set_st_ring_vs_only	2002Aug22
PASS		
0	set_st_stress_a	2002Aug22
PASS		
0	set_st_stress_b	2002Aug22
PASS		
0	set_st_stress_c	2002Aug22
PASS		
0	set_st_stress_d	2002Aug22
PASS		
0	set_st_stress_e	2002Aug22
PASS		
0	set_st_stress_f	2002Aug22
PASS		
0	Small_Text_BM	2002Aug22
PASS		
0	smoke_nrt	2002Aug22

PASS		
0	src_clip	2002Aug22
PASS		
0	srf_cohr	2002Aug22
PASS		
0	trans_bitblt	2002Aug22
PASS		
0	type0_2long_pckts	2002Aug22
PASS		
0	type0_3indr_buffs	2002Aug22
PASS		
0	type0_4pckts	2002Aug22
PASS		
0	type0_incr_updt_cnst	2002Aug22
PASS		
0	type0_incr_updt_instrmem	2002Aug22
PASS		
0	type0_incr_updt_st	2002Aug22
PASS		
0	type0_incr_updt_stress	2002Aug22
PASS		
0	type0_ind1_ind2_a	2002Aug22
PASS		
0	type0_ind1_ind2_b	2002Aug22
PASS		
0	type0_indr_buff	2002Aug22
PASS		
0	Type0_IQ2_BM	2002Aug22
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug22
PASS		
0	Type0_IQ_BM	2002Aug22
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug22
PASS		
0	Type0_PQ_BM	2002Aug22
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug22
PASS		
0	type1_4pckts	2002Aug22
PASS		
0	type1_indr_buff	2002Aug22
PASS		
0	type1_pckt	2002Aug22
PASS		
0	Type3_Bitblt	2002Aug22
PASS		
0	Type3_Load_Palette	2002Aug22

```

PASS
0   Type3_NOP                                     2002Aug22
PASS
670 uram_rd_wrt                                   2002Aug22
**FAIL**          rbbmWr.cmp
671 uram_rd_wrt_rt                               2002Aug22
**FAIL**          rbbmWr.cmp
672 uram_wrt_rt_ucode                             2002Aug22
**FAIL**          rbbmWr.cmp
0   uram_wrt_ucode                               2002Aug22
PASS
0   viz_query_a                                   2002Aug22
PASS
675 viz_query_draw_indx_ring                     2002Aug22
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   viz_query_indr1                               2002Aug22
PASS
0   viz_query_indr1_simple                         2002Aug22
PASS
0   viz_query_indr2                               2002Aug22
PASS
0   viz_query_indr2_simple                         2002Aug22
PASS
0   viz_query_ring                               2002Aug22
PASS
0   viz_query_ring_simple                         2002Aug22
PASS
0   wait_for_idle                                2002Aug22
PASS
683 wait_reg_mem_poll_mem0                       2002Aug22
**FAIL**          rbbmWr.cmp
0   wait_reg_mem_poll_mem0_rt                     2002Aug22
PASS
0   wait_reg_mem_poll_mem1                         2002Aug22
PASS
0   wait_reg_mem_poll_mem2                         2002Aug22
PASS
0   wait_reg_mem_poll_mem3                         2002Aug22
PASS
0   wait_reg_mem_poll_reg0                         2002Aug22
PASS
0   wait_reg_mem_poll_reg1                         2002Aug22
PASS
0   wait_reg_mem_poll_reg2                         2002Aug22
PASS
0   wait_reg_mem_poll_reg3                         2002Aug22
PASS
=====

```

Tests_Run_count = 691 Pass_count = 603 Fail_count = 88 Incomplete_count = 0

=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep  2  7:33:37 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             628         628         619         98
              RBBM           96          96          41          42
+++++
              TOTAL           724         724         660         91
+++++

```

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+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Sep 10  7:21:06 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          635         635         609         95
              RBBM        98          98          98         100
+++++
              TOTAL        733         733         707         96
+++++

```


No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct2
	PASS		
0	2d_indr_buffer_b		2002Oct2
	PASS		
0	2d_indr_buffer_c		2002Oct2
	PASS		
0	2d_indr_buffer_d		2002Oct2
	PASS		
0	2d_indr_buffer_e		2002Oct2
	PASS		
0	2d_indr_buffer_f		2002Oct2
	PASS		
0	2d_indr_buffer_g		2002Oct2
	PASS		
0	2d_indr_buffer_random		2002Oct2
	PASS		
9	2d_indr_buffer_random2		2002Sep29
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Oct2
	PASS		
0	3d_draw_indx_2_b		2002Oct2
	PASS		
0	IQ2_Mult		2002Oct2
	PASS		
13	IQ2_Wrap_Legacy		2002Oct2
	FAIL cp_rbbm.cmp rbbmWr.cmp		
14	IQ_Wrap		2002Oct2
	FAIL cp_rbbm.cmp rbbmWr.cmp		
15	Isync_Control		2002Oct2
	FAIL cp_rbbm.cmp rbbmWr.cmp		
16	Isync_Control_2D_3D		2002Oct2
	FAIL cp_rbbm.cmp rbbmWr.cmp		
17	Isync_Control_3D_2D		2002Oct2
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	RB_BUFSZ_0		2002Oct2
	PASS		
0	RB_BUFSZ_1		2002Oct2
	PASS		
0	RB_Wrap		2002Oct2

PASS		
0	Small_Text_BM	20020ct2
PASS		
0	Type0_IQ2_BM	20020ct2
PASS		
0	Type0_IQ2_BM_IB_Packet	20020ct2
PASS		
0	Type0_IQ_BM	20020ct2
PASS		
0	Type0_IQ_BM_IB_Packet	20020ct2
PASS		
0	Type0_PQ_BM	20020ct2
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	20020ct2
PASS		
0	Type3_Bitblt	20020ct2
PASS		
0	Type3_Hostdata_blt	20020ct2
PASS		
0	Type3_Load_Palette	20020ct2
PASS		
0	Type3_NOP	20020ct2
PASS		
0	Type3_Polyline	20020ct2
PASS		
0	Type3_Polyline_Large	20020ct2
PASS		
0	bitblt_multi	20020ct2
PASS		
0	brush_32x1mono	20020ct2
PASS		
0	brush_8x8color_16	20020ct2
PASS		
0	brush_8x8color_32	20020ct2
PASS		
0	brush_8x8color_64	20020ct2
PASS		
0	brush_8x8color_dst_0_1	20020ct2
PASS		
0	brush_8x8mono	20020ct2
PASS		
0	brush_color_mono_stress	20020ct2
PASS		
0	brush_flag	20020ct2
PASS		
0	brush_type0	20020ct2
PASS		
0	brush_type1	20020ct2

PASS		
0	brush_type10	20020ct2
PASS		
0	brush_type13	20020ct2
PASS		
0	brush_type14	20020ct2
PASS		
0	brush_type6	20020ct2
PASS		
0	brush_type7	20020ct2
PASS		
0	brush_type_other	20020ct2
PASS		
0	brush_types_13_14	20020ct2
PASS		
0	change_queue_sizes	20020ct2
PASS		
0	clear_const_wr_en_on_me_init	20020ct2
PASS		
0	clear_const_wr_en_with_lcc	20020ct2
PASS		
0	cond_write_mem_poll_mem	20020ct2
PASS		
0	cond_write_mem_poll_mem2	20020ct2
PASS		
0	cond_write_mem_poll_mem3	20020ct2
PASS		
0	cond_write_mem_poll_mem_rt	20020ct2
PASS		
0	cond_write_mem_poll_reg	20020ct2
PASS		
0	cond_write_mem_poll_reg2	20020ct2
PASS		
0	cond_write_mem_poll_reg3	20020ct2
PASS		
0	cp_acpi_recovery_soft_reset	20020ct2
PASS		
0	cp_bios_scratch	20020ct2
PASS		
0	cp_high_stress	20020ct2
PASS		
0	cp_ib2_base000_bufszXXX	20020ct2
PASS		
0	cp_ib2_base001_bufszXXX	20020ct2
PASS		
0	cp_ib2_base010_bufszXXX	20020ct2
PASS		
0	cp_ib2_base011_bufszXXX	20020ct2

PASS		
0	cp_ib2_base100_bufszXXX	2002Oct2
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct2
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct2
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct2
PASS		
0	cp_ib_base000_bufszXXX	2002Oct2
PASS		
0	cp_ib_base001_bufszXXX	2002Oct2
PASS		
0	cp_ib_base010_bufszXXX	2002Oct2
PASS		
0	cp_ib_base011_bufszXXX	2002Oct2
PASS		
0	cp_ib_base100_bufszXXX	2002Oct2
PASS		
0	cp_ib_base101_bufszXXX	2002Oct2
PASS		
0	cp_ib_base110_bufszXXX	2002Oct2
PASS		
0	cp_ib_base111_bufszXXX	2002Oct2
PASS		
0	cp_interrupt	2002Oct2
PASS		
0	cp_interrupt_rt_events_16	2002Oct2
PASS		
83	cp_iq2_alignment_000	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
84	cp_iq2_alignment_001	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
85	cp_iq2_alignment_010	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
86	cp_iq2_alignment_011	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
87	cp_iq2_alignment_100	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
88	cp_iq2_alignment_101	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
89	cp_iq2_alignment_110	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
90	cp_iq2_alignment_111	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
91	cp_iq_alignment_000	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
92	cp_iq_alignment_001	2002Oct2

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
93  cp_iq_alignment_010                                20020ct2
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
94  cp_iq_alignment_011                                20020ct2
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
95  cp_iq_alignment_100                                20020ct2
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
96  cp_iq_alignment_101                                20020ct2
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
97  cp_iq_alignment_110                                20020ct2
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
98  cp_iq_alignment_111                                20020ct2
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   cp_max_size_packet                                20020ct2
PASS
0   cp_max_size_packet_random                          20020ct2
PASS
0   cp_nrt_rt_protection_mode                          20020ct2
PASS
0   cp_output_stress                                  20020ct2
PASS
0   cp_output_stress_random                            20020ct2
PASS
0   cp_protection_mode                                20020ct2
PASS
0   cp_rb_alignment_000                                20020ct2
PASS
0   cp_rb_alignment_001                                20020ct2
PASS
0   cp_rb_alignment_010                                20020ct2
PASS
0   cp_rb_alignment_011                                20020ct2
PASS
0   cp_rb_alignment_100                                20020ct2
PASS
0   cp_rb_alignment_101                                20020ct2
PASS
0   cp_rb_alignment_110                                20020ct2
PASS
0   cp_rb_alignment_111                                20020ct2
PASS
0   cp_rb_wptrXXX_base000                              20020ct2
PASS
0   cp_rb_wptrXXX_base001                              20020ct2
PASS
0   cp_rb_wptrXXX_base010                              20020ct2
PASS
0   cp_rb_wptrXXX_base011                              20020ct2

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PASS		
0	cp_rb_wptrXXX_base100	20020ct2
PASS		
0	cp_rb_wptrXXX_base101	20020ct2
PASS		
0	cp_rb_wptrXXX_base110	20020ct2
PASS		
0	cp_rb_wptrXXX_base111	20020ct2
PASS		
0	cp_reg_rw_test	20020ct2
PASS		
0	cp_register_reads	20020ct2
PASS		
0	cp_rt_protection_mode	20020ct2
PASS		
0	cp_scratch	20020ct2
PASS		
0	cp_scratch_multi	20020ct2
PASS		
0	csf_reg_rd_default	20020ct2
PASS		
0	csf_reg_rd_only	20020ct2
PASS		
0	csf_reg_wr_rd	20020ct2
PASS		
0	dma_cp_sync	20020ct2
PASS		
0	dma_dscrstr_chckn_bit	20020ct2
PASS		
0	dma_dscrstr_flush1	20020ct2
PASS		
0	dma_dscrstr_flush2	20020ct2
PASS		
0	dma_dscrstr_flush3	20020ct2
PASS		
0	dma_m2m_src00_dstXX	20020ct2
PASS		
0	dma_m2m_src01_dstXX	20020ct2
PASS		
0	dma_m2m_src10_dstXX	20020ct2
PASS		
0	dma_m2m_src11_dstXX	20020ct2
PASS		
0	dma_mem2mem	20020ct2
PASS		
0	dma_mem2mem_abort	20020ct2
PASS		
0	dma_mem2mem_alignments	20020ct2

PASS		
0	dma_mem2mem_byte_alignment0	20020ct2
PASS		
0	dma_mem2mem_byte_alignment1	20020ct2
PASS		
0	dma_mem2mem_byte_alignment2	20020ct2
PASS		
0	dma_mem2mem_byte_alignment3	20020ct2
PASS		
0	dma_mem2mem_byte_alignment4	20020ct2
PASS		
0	dma_mem2mem_byte_alignment5	20020ct2
PASS		
0	dma_mem2mem_byte_alignment6	20020ct2
PASS		
0	dma_mem2mem_byte_alignment7	20020ct2
PASS		
0	dma_mem2mem_large	20020ct2
PASS		
0	dma_mem2mem_pio_abort	20020ct2
PASS		
0	dma_mem2mem_random	20020ct2
PASS		
0	dma_mem2reg	20020ct2
PASS		
0	dma_mem2reg_abort	20020ct2
PASS		
0	dma_mem2reg_dtaq_stress	20020ct2
PASS		
0	dma_mem2reg_dtaq_stress_random	20020ct2
PASS		
0	dma_mem2reg_pio_stressbm	20020ct2
PASS		
0	dma_mem2reg_pio_stresspio	20020ct2
PASS		
0	dma_mem2reg_sync	20020ct2
PASS		
0	dma_r2r_m2m_r2r	20020ct2
PASS		
0	dma_r2r_m2r_r2r	20020ct2
PASS		
0	dma_r2r_r2m_r2r	20020ct2
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	20020ct2
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	20020ct2
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	20020ct2

PASS		
0	dma_reg2mem	2002Oct2
PASS		
0	dma_reg2mem_abort	2002Oct2
PASS		
0	dma_reg2reg	2002Oct2
PASS		
0	dma_reg2reg_2dw	2002Oct2
PASS		
0	dma_reg2reg_daic	2002Oct2
PASS		
0	dma_reg2reg_mult	2002Oct2
PASS		
0	dma_reg2reg_mult2	2002Oct2
PASS		
0	dma_reg2reg_mult3	2002Oct2
PASS		
0	dma_reg2reg_saic	2002Oct2
PASS		
0	dma_reg2reg_sdaic	2002Oct2
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct2
PASS		
0	dma_reg2reg_stress	2002Oct2
PASS		
0	draw_indx_a	2002Oct2
PASS		
0	draw_indx_b	2002Oct2
PASS		
0	draw_indx_c	2002Oct2
PASS		
0	draw_indx_indr1_a	2002Oct2
PASS		
0	draw_indx_indr1_b	2002Oct2
PASS		
0	draw_indx_indr1_short_a	2002Oct2
PASS		
0	draw_indx_indr1_short_b	2002Oct2
PASS		
0	draw_indx_indr1_stress	2002Oct2
PASS		
0	draw_indx_indr2_a	2002Oct2
PASS		
0	draw_indx_indr2_b	2002Oct2
PASS		
0	draw_indx_indr2_short_a	2002Oct2
PASS		
0	draw_indx_indr2_short_b	2002Oct2

PASS		
0	draw_indx_indr2_stress	20020ct2
PASS		
0	draw_indx_random_a	20020ct2
PASS		
0	draw_indx_random_b	20020ct2
PASS		
0	draw_indx_random_c	20020ct2
PASS		
0	draw_indx_ring1	20020ct2
PASS		
0	draw_indx_ring2	20020ct2
PASS		
0	draw_indx_ring_short1	20020ct2
PASS		
0	draw_indx_ring_short2	20020ct2
PASS		
0	draw_indx_ring_stress	20020ct2
PASS		
0	draw_indx_short_a	20020ct2
PASS		
0	draw_indx_short_b	20020ct2
PASS		
0	draw_indx_short_c	20020ct2
PASS		
0	draw_indx_stress_a	20020ct2
PASS		
0	draw_indx_stress_b	20020ct2
PASS		
0	draw_indx_stress_c	20020ct2
PASS		
0	event_timestamp_write	20020ct2
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	20020ct2
PASS		
0	fix2flt_reg	20020ct2
PASS		
0	ft_indr1_july22	20020ct2
PASS		
0	ft_indr2_july22	20020ct2
PASS		
0	ft_july22	20020ct2
PASS		
0	ft_july22_random	20020ct2
PASS		
0	header_dump	20020ct2
PASS		
0	header_dump_rt	20020ct2

PASS		
0	header_dump_small	2002Oct2
PASS		
0	hostdata_blt	2002Oct2
PASS		
0	im_load_a	2002Oct2
PASS		
0	im_load_b	2002Oct2
PASS		
0	im_load_cid_mismatch	2002Oct2
PASS		
0	im_load_immediate	2002Oct2
PASS		
0	im_load_immediate_nrt_me	2002Oct2
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct2
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct2
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct2
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct2
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct2
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct2
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct2
PASS		
0	im_load_indr1_a	2002Oct2
PASS		
0	im_load_indr1_b	2002Oct2
PASS		
0	im_load_indr2_a	2002Oct2
PASS		
0	im_load_indr2_b	2002Oct2
PASS		
0	im_load_invalidate	2002Oct2
PASS		
0	im_load_match	2002Oct2
PASS		
0	im_load_match2	2002Oct2
PASS		
0	im_load_nrt_me	2002Oct2
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct2
PASS		
0	im_load_nrt_me_ps_wrap	2002Oct2

PASS		
0	im_load_nrt_me_vs_nowrap	2002Oct2
PASS		
0	im_load_nrt_me_vs_wrap	2002Oct2
PASS		
0	im_load_pfmatch_disable	2002Oct2
PASS		
0	im_load_random_a	2002Oct2
PASS		
0	im_load_random_b	2002Oct2
PASS		
242	im_load_random_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Oct2
PASS		
0	im_load_ring_b	2002Oct2
PASS		
0	im_load_rt	2002Oct2
PASS		
0	im_load_rt2	2002Oct2
PASS		
0	im_load_rt3	2002Oct2
PASS		
0	im_load_rt4	2002Oct2
PASS		
0	im_load_sz_mismatch	2002Oct2
PASS		
0	incremental_update_const	2002Oct2
PASS		
0	incremental_update_instr	2002Oct2
PASS		
0	incremental_update_state	2002Oct2
PASS		
0	indirect_buffer_a	2002Oct2
PASS		
0	indirect_buffer_b	2002Oct2
PASS		
0	indirect_buffer_c	2002Oct2
PASS		
0	indirect_buffer_d	2002Oct2
PASS		
0	indirect_buffer_e	2002Oct2
PASS		
0	indirect_buffer_f	2002Oct2
PASS		
0	indirect_buffer_g	2002Oct2
PASS		
0	indirect_buffer_h	2002Oct2

PASS		
0	indirect_buffer_i	20020ct2
PASS		
0	indirect_buffer_j	20020ct2
PASS		
0	indirect_buffer_k	20020ct2
PASS		
0	indirect_buffer_l	20020ct2
PASS		
0	indirect_buffer_mp_a	20020ct2
PASS		
0	indirect_buffer_mp_b	20020ct2
PASS		
0	indirect_buffer_mp_c	20020ct2
PASS		
0	indirect_buffer_mp_d	20020ct2
PASS		
0	indirect_buffer_mp_e	20020ct2
PASS		
0	indirect_buffer_mp_f	20020ct2
PASS		
271	indirect_buffer_mp_random1	20020ct2
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
272	indirect_buffer_mp_random2	20020ct2
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
273	indirect_buffer_random1	20020ct2
FAIL	mh_cp_rd.cmp	
274	indirect_buffer_random2	20020ct2
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	20020ct2
PASS		
0	invalidate_on_me_init	20020ct2
PASS		
0	invalidate_state_constants	20020ct2
PASS		
0	invalidate_state_shaders	20020ct2
PASS		
0	invalidate_state_shaders2	20020ct2
PASS		
0	invalidate_state_sublks	20020ct2
PASS		
0	invalidate_state_sublks2	20020ct2
PASS		
0	iq1_iq2_mult	20020ct2
PASS		
283	iq1_iq2_push_wrap	20020ct2
FAIL	rbbmWr.cmp	
0	iq2_csq_full	20020ct2

PASS		
285	lcc_coherency_cntr_random	2002Sep29
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Oct2
PASS		
0	lcc_mismatch_alutex_random	2002Oct2
PASS		
0	lcc_mismatch_base_random	2002Oct2
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct2
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Oct2
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct2
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct2
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct2
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct2
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct2
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct2
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct2
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct2
PASS		
0	ld_cnst_cntx_match	2002Oct2
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct2
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct2
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct2
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct2
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct2
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct2
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct2
PASS		
0	ld_cnst_cntx_replicate8	2002Oct2
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct2

PASS		
0	ld_cnst_cntx_replicate_tex	20020ct2
PASS		
0	ld_cnst_cntx_ring_replicate	20020ct2
PASS		
0	ld_cnst_cntx_ring_replicate4	20020ct2
PASS		
0	ld_cnst_cntx_ring_replicate8	20020ct2
PASS		
0	ld_cnst_cntx_ring_simple	20020ct2
PASS		
0	ld_cnst_cntx_simple_alu	20020ct2
PASS		
0	ld_cnst_cntx_simple_tex	20020ct2
PASS		
0	load_constant_context_rt	20020ct2
PASS		
0	load_palette	20020ct2
PASS		
0	lp_2d3d	20020ct2
PASS		
0	me_reg_wr_rd	20020ct2
PASS		
0	mem_write	20020ct2
PASS		
0	mem_write_rt	20020ct2
PASS		
0	mpeg_index	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_5	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_6	20020ct2
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct2
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct2
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	20020ct2

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	20020ct2
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	20020ct2
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	20020ct2
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	20020ct2
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	20020ct2
PASS		
0	nop_eating_disabled	20020ct2
PASS		
0	nrt_rt_interaction1	20020ct2
PASS		
0	paint_multi	20020ct2
PASS		
0	paint_packet	20020ct2
PASS		
0	paint_packet_gui_control	20020ct2
PASS		
0	pfp_reg_reads1	20020ct2
PASS		
0	pfp_reg_reads2	20020ct2
PASS		
0	pitch_clip	20020ct2
PASS		
0	ply_nextscan	20020ct2
PASS		
0	rbbm_WU_2D_idle_01	20020ct2
PASS		
0	rbbm_WU_2D_idle_02	20020ct2
PASS		
0	rbbm_WU_2D_idle_03	20020ct2
PASS		
0	rbbm_WU_2D_idle_04	20020ct2
PASS		
0	rbbm_WU_2D_idle_05	20020ct2
PASS		
0	rbbm_WU_2D_idle_06	20020ct2
PASS		
0	rbbm_WU_2D_idle_07	20020ct2
PASS		
0	rbbm_WU_2D_idle_08	20020ct2
PASS		
0	rbbm_WU_2D_idle_09	20020ct2
PASS		
0	rbbm_WU_2D_idle_clean_01	20020ct2

PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct2
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Oct2
PASS		
0	rbbm_WU_3D_idle_01	2002Oct2
PASS		
0	rbbm_WU_3D_idle_02	2002Oct2
PASS		
0	rbbm_WU_3D_idle_03	2002Oct2
PASS		
0	rbbm_WU_3D_idle_04	2002Oct2
PASS		
0	rbbm_WU_3D_idle_05	2002Oct2
PASS		
0	rbbm_WU_3D_idle_06	2002Oct2
PASS		
0	rbbm_WU_3D_idle_07	2002Oct2
PASS		
0	rbbm_WU_3D_idle_08	2002Oct2
PASS		
0	rbbm_WU_3D_idle_09	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct2

PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct2
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct2
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct2
PASS		
0	rbbm_WU_BOTH_GRPX_update_pending_off	2002Oct2
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct2
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct2
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct2
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct2
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct2
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct2
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct2
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPX_update_pending_off	2002Oct2
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPX_update_pending_off	2002Oct2
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct2
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct2
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct2
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct2
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct2

PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct2
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct2
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct2
PASS		
0	rbbm_WU_idct_semaphore	2002Oct2
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct2
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct2
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Oct2
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRP update_pending_off	2002Oct2
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP update_pending_off	2002Oct2
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct2
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL update_pending_off	2002Oct2
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL update_pending_off	2002Oct2
PASS		
0	rbbm_WUrt_RT_idle_01	2002Oct2
PASS		
0	rbbm_WUrt_RT_idle_02	2002Oct2
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct2
PASS		
0	rbbm_WUrt_RT_idle_04	2002Oct2
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Oct2

PASS		
0	rbbm_WUrt_RT_idle_clean_02	20020ct2
PASS		
0	rbbm_WUrt_RT_idle_clean_03	20020ct2
PASS		
0	rbbm_WUrt_RT_idle_clean_04	20020ct2
PASS		
0	rbbm_WUrt_RT_idle_clean_05	20020ct2
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	20020ct2
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	20020ct2
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	20020ct2
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	20020ct2
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	20020ct2
PASS		
0	rbbm_clk_active_deassert_min_max	20020ct2
PASS		
0	rbbm_clk_active_each_pipe	20020ct2
PASS		
0	rbbm_clk_active_for_read_error	20020ct2
PASS		
0	rbbm_clk_active_for_read_request	20020ct2
PASS		
0	rbbm_clk_active_on_reset	20020ct2
PASS		
0	rbbm_cp_non_queued_rdwr_01	20020ct2
PASS		
0	rbbm_cp_non_queued_rdwr_02	20020ct2
PASS		
0	rbbm_cp_queued_holds_nonqueued	20020ct2
PASS		
0	rbbm_cp_queued_rdwr	20020ct2
PASS		
447	rbbm_hi_data_and_be_swap	20020ct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_hi_data_endian_swap	20020ct2
PASS		
449	rbbm_hi_data_no_swap_for_cppush	20020ct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
450	rbbm_hi_data_swap_for_cppush	20020ct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_int_ack	20020ct2
PASS		
0	rbbm_int_each_client	20020ct2

PASS		
0	rbbm_int_nrt_gfxpipe_idle	20020ct2
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	20020ct2
PASS		
0	rbbm_int_rderr	20020ct2
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	20020ct2
PASS		
0	rbbm_isync_cpscratch_idlegui	20020ct2
PASS		
0	rbbm_isync_wait_idlegui	20020ct2
PASS		
0	rbbm_soft_reset	20020ct2
PASS		
0	reg_to_mem	20020ct2
PASS		
0	reg_to_mem_rt	20020ct2
PASS		
0	rop_cfd_wmd	20020ct2
PASS		
0	roq_reg_reads	20020ct2
PASS		
0	rstr_ordr	20020ct2
PASS		
465	rt_invalid_opcode	20020ct2
FAIL	rbbmWr.cmp	
0	rtee_arm_wait_idle_trigger_events_16	20020ct2
PASS		
0	rtee_discretes_arm_rearm_events_32	20020ct2
PASS		
0	rtee_discretes_falling_00_events_16	20020ct2
PASS		
0	rtee_discretes_falling_01_events_16	20020ct2
PASS		
0	rtee_discretes_falling_02_events_16	20020ct2
PASS		
0	rtee_discretes_falling_03_events_16	20020ct2
PASS		
0	rtee_discretes_falling_04_events_16	20020ct2
PASS		
0	rtee_discretes_falling_05_events_16	20020ct2
PASS		
0	rtee_discretes_falling_06_events_16	20020ct2
PASS		
0	rtee_discretes_falling_07_events_16	20020ct2
PASS		
0	rtee_discretes_falling_08_events_16	20020ct2

PASS		
0	rtee_discretes_falling_09_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct2
PASS		
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PASS		
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PASS		
0	rtee_discretes_falling_26_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct2
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct2

PASS		
0	rtee_discretes_invert_01_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct2
PASS		
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PASS		
0	rtee_discretes_invert_04_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct2
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0	rtee_discretes_invert_11_events_16	2002Oct2
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0	rtee_discretes_invert_12_events_16	2002Oct2
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0	rtee_discretes_invert_14_events_16	2002Oct2
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0	rtee_discretes_invert_15_events_16	2002Oct2
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PASS		
0	rtee_discretes_invert_17_events_16	2002Oct2
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0	rtee_discretes_invert_18_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct2
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct2
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0	rtee_discretes_invert_21_events_16	2002Oct2
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PASS		
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0	rtee_discretes_invert_24_events_16	2002Oct2

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PASS		
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PASS		
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0	rtee_discretes_level_05_events_16	2002Oct2
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PASS		
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PASS		
0	rtee_discretes_level_30_events_16	2002Oct2
PASS		
0	rtee_discretes_level_31_events_16	2002Oct2
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct2
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0	rtee_discretes_rising_00_events_16	2002Oct2
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0	rtee_discretes_rising_01_events_16	2002Oct2
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0	rtee_discretes_rising_02_events_16	2002Oct2
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PASS		
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0	rtee_discretes_rising_30_events_16	2002Oct2
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct2

PASS		
0	rtee_display1_comp_events_16	20020ct2
PASS		
0	rtee_display2_comp_events_16	20020ct2
PASS		
0	rtee_mem_linear_search_events_2	20020ct2
PASS		
0	rtee_mem_poll_comp_000_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_001_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_010_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_011_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_100_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_101_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_110_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_111_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_masks_events_16	20020ct2
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	20020ct2
PASS		
0	rtee_mem_poll_comp_swaps_events_16	20020ct2
PASS		
0	rtee_mem_poll_intervals_events_16	20020ct2
PASS		
0	rtee_mem_poll_short_intervals_events_16	20020ct2
PASS		
613	rtee_pio	20020ct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rtee_prefetch_parse_nops	20020ct2
PASS		
0	rtee_reg_linear_search_events_2	20020ct2
PASS		
0	rtee_reg_poll_comp_000_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_001_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_010_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_011_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_100_events_16	20020ct2

PASS		
0	rtee_reg_poll_comp_101_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_110_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_111_events_16	20020ct2
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	20020ct2
PASS		
0	rtee_vip_comp_events_16	20020ct2
PASS		
0	scratch_mem_read	20020ct2
PASS		
0	scratch_mem_read2	20020ct2
PASS		
0	set_cnst_alu_max_random	20020ct2
PASS		
0	set_cnst_we_lcc_random	20020ct2
PASS		
0	set_cnst_we_random	20020ct2
PASS		
0	set_constant	20020ct2
PASS		
0	set_constant2	20020ct2
PASS		
0	set_constant_alu_max1	20020ct2
PASS		
0	set_constant_alu_max2	20020ct2
PASS		
0	set_constant_bool_max1	20020ct2
PASS		
0	set_constant_bool_max2	20020ct2
PASS		
0	set_constant_indr1_short	20020ct2
PASS		
0	set_constant_indr1_short2	20020ct2
PASS		
0	set_constant_indr1_write_enable	20020ct2
PASS		
0	set_constant_indr1_write_enable_lcc	20020ct2
PASS		
0	set_constant_indr2_short	20020ct2
PASS		
0	set_constant_indr2_short2	20020ct2
PASS		
0	set_constant_indr2_write_enable	20020ct2
PASS		
0	set_constant_indr2_write_enable_lcc	20020ct2

PASS		
0	set_constant_loop_max1	20020ct2
PASS		
0	set_constant_loop_max2	20020ct2
PASS		
0	set_constant_ring_short	20020ct2
PASS		
0	set_constant_ring_short2	20020ct2
PASS		
0	set_constant_ring_write_enable	20020ct2
PASS		
0	set_constant_ring_write_enable_lcc	20020ct2
PASS		
0	set_constant_short	20020ct2
PASS		
0	set_constant_short2	20020ct2
PASS		
0	set_constant_tex_max1	20020ct2
PASS		
0	set_constant_tex_max2	20020ct2
PASS		
0	set_constant_write_enable	20020ct2
PASS		
0	set_constant_write_enable_lcc	20020ct2
PASS		
0	set_constant_write_enable_lcc2	20020ct2
PASS		
0	set_scissors	20020ct2
PASS		
0	set_st_indrl_match	20020ct2
PASS		
0	set_st_indrl_mismatch	20020ct2
PASS		
0	set_st_indrl_mismatch2	20020ct2
PASS		
0	set_st_indrl_mismatch3	20020ct2
PASS		
0	set_st_indrl_mismatch4	20020ct2
PASS		
0	set_st_indrl_ps	20020ct2
PASS		
0	set_st_indrl_ps_only	20020ct2
PASS		
0	set_st_indrl_sublk0	20020ct2
PASS		
0	set_st_indrl_sublk1	20020ct2
PASS		
0	set_st_indrl_sublk1_only	20020ct2

PASS		
0	set_st_indr1_sublk2	20020ct2
PASS		
0	set_st_indr1_sublk2_only	20020ct2
PASS		
0	set_st_indr1_sublk3	20020ct2
PASS		
0	set_st_indr1_sublk3_only	20020ct2
PASS		
0	set_st_indr1_sublk4	20020ct2
PASS		
0	set_st_indr1_sublk4_only	20020ct2
PASS		
0	set_st_indr1_sublk5	20020ct2
PASS		
0	set_st_indr1_sublk5_only	20020ct2
PASS		
0	set_st_indr1_sublk6	20020ct2
PASS		
0	set_st_indr1_sublk6_only	20020ct2
PASS		
0	set_st_indr1_sublk7	20020ct2
PASS		
0	set_st_indr1_sublk7_only	20020ct2
PASS		
0	set_st_indr1_vs	20020ct2
PASS		
0	set_st_indr1_vs_only	20020ct2
PASS		
0	set_st_indr2_match	20020ct2
PASS		
0	set_st_indr2_mismatch	20020ct2
PASS		
0	set_st_indr2_mismatch2	20020ct2
PASS		
0	set_st_indr2_mismatch3	20020ct2
PASS		
0	set_st_indr2_mismatch4	20020ct2
PASS		
0	set_st_indr2_ps	20020ct2
PASS		
0	set_st_indr2_ps_only	20020ct2
PASS		
0	set_st_indr2_sublk0	20020ct2
PASS		
0	set_st_indr2_sublk1	20020ct2
PASS		
0	set_st_indr2_sublk1_only	20020ct2

PASS		
0	set_st_indr2_sublk2	20020ct2
PASS		
0	set_st_indr2_sublk2_only	20020ct2
PASS		
0	set_st_indr2_sublk3	20020ct2
PASS		
0	set_st_indr2_sublk3_only	20020ct2
PASS		
0	set_st_indr2_sublk4	20020ct2
PASS		
0	set_st_indr2_sublk4_only	20020ct2
PASS		
0	set_st_indr2_sublk5	20020ct2
PASS		
0	set_st_indr2_sublk5_only	20020ct2
PASS		
0	set_st_indr2_sublk6	20020ct2
PASS		
0	set_st_indr2_sublk6_only	20020ct2
PASS		
0	set_st_indr2_sublk7	20020ct2
PASS		
0	set_st_indr2_sublk7_only	20020ct2
PASS		
0	set_st_indr2_vs	20020ct2
PASS		
0	set_st_indr2_vs_only	20020ct2
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0	set_st_pfmatch_disabled	20020ct2
PASS		
0	set_st_ring_match	20020ct2
PASS		
0	set_st_ring_mismatch	20020ct2
PASS		
0	set_st_ring_mismatch2	20020ct2
PASS		
0	set_st_ring_mismatch3	20020ct2
PASS		
0	set_st_ring_mismatch4	20020ct2
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0	set_st_ring_ps	20020ct2
PASS		
0	set_st_ring_ps_only	20020ct2
PASS		
0	set_st_ring_sublk0	20020ct2
PASS		
0	set_st_ring_sublk1	20020ct2

PASS		
0	set_st_ring_subblk1_only	20020ct2
PASS		
0	set_st_ring_subblk2	20020ct2
PASS		
0	set_st_ring_subblk2_only	20020ct2
PASS		
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PASS		
0	set_st_ring_subblk3_only	20020ct2
PASS		
0	set_st_ring_subblk4	20020ct2
PASS		
0	set_st_ring_subblk4_only	20020ct2
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0	set_st_ring_subblk5	20020ct2
PASS		
0	set_st_ring_subblk5_only	20020ct2
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0	set_st_ring_subblk6	20020ct2
PASS		
0	set_st_ring_subblk6_only	20020ct2
PASS		
0	set_st_ring_subblk7	20020ct2
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0	set_st_ring_subblk7_only	20020ct2
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0	set_st_ring_vs	20020ct2
PASS		
0	set_st_ring_vs_only	20020ct2
PASS		
0	set_st_stress_a	20020ct2
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0	set_st_stress_b	20020ct2
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0	set_st_stress_c	20020ct2
PASS		
0	set_st_stress_d	20020ct2
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0	set_st_stress_e	20020ct2
PASS		
0	set_st_stress_f	20020ct2
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0	set_state_rt_a	20020ct2
PASS		
0	set_state_rt_b	20020ct2
PASS		
0	set_state_rt_c	20020ct2

PASS		
0	set_state_rt_d	2002Oct2
PASS		
0	smoke_nrt	2002Oct2
PASS		
0	src_clip	2002Oct2
PASS		
0	srf_cohr	2002Oct2
PASS		
0	switch_pio_bm	2002Oct2
PASS		
0	trans_bitblt	2002Oct2
PASS		
0	type0_2long_pckts	2002Oct2
PASS		
0	type0_3indr_buffs	2002Oct2
PASS		
0	type0_4pckts	2002Oct2
PASS		
0	type0_incr_updt_cnst	2002Oct2
PASS		
0	type0_incr_updt_instrmem	2002Oct2
PASS		
0	type0_incr_updt_st	2002Oct2
PASS		
0	type0_incr_updt_stress	2002Oct2
PASS		
0	type0_ind1_ind2_a	2002Oct2
PASS		
0	type0_ind1_ind2_b	2002Oct2
PASS		
0	type0_indr_buff	2002Oct2
PASS		
0	type1_4pckts	2002Oct2
PASS		
0	type1_indr_buff	2002Oct2
PASS		
0	type1_pckt	2002Oct2
PASS		
0	type_random	2002Oct2
PASS		
761	uram_wrt_rt_ucose	2002Oct2
FAIL	cp_rbbm.cmp rbbmWr.cmp	
762	uram_wrt_ucose	2002Oct2
FAIL	rbbmWr.cmp	
0	viz_query_a	2002Oct2
PASS		
0	viz_query_draw_indx	2002Oct2


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PASS
0 viz_query_draw_indx_indr1 20020ct2
PASS
0 viz_query_draw_indx_indr2 20020ct2
PASS
0 viz_query_draw_indx_multipass 20020ct2
PASS
0 viz_query_draw_indx_ring 20020ct2
PASS
0 viz_query_indr1 20020ct2
PASS
0 viz_query_indr1_simple 20020ct2
PASS
0 viz_query_indr2 20020ct2
PASS
0 viz_query_indr2_simple 20020ct2
PASS
0 viz_query_ring 20020ct2
PASS
0 viz_query_ring_simple 20020ct2
PASS
0 wait_for_idle 20020ct2
PASS
0 wait_reg_mem_poll_mem0 20020ct2
PASS
0 wait_reg_mem_poll_mem0_rt 20020ct2
PASS
0 wait_reg_mem_poll_mem1 20020ct2
PASS
0 wait_reg_mem_poll_mem2 20020ct2
PASS
0 wait_reg_mem_poll_mem3 20020ct2
PASS
0 wait_reg_mem_poll_reg0 20020ct2
PASS
0 wait_reg_mem_poll_reg1 20020ct2
PASS
0 wait_reg_mem_poll_reg2 20020ct2
PASS
0 wait_reg_mem_poll_reg3 20020ct2
PASS

```

```

=====
Tests_Run_count = 784 Pass_count = 748 Fail_count = 36 Incomplete_count = 0
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug28
	PASS		
0	2d_indr_buffer_b		2002Aug28
	PASS		
0	2d_indr_buffer_c		2002Aug28
	PASS		
0	2d_indr_buffer_d		2002Aug28
	PASS		
0	2d_indr_buffer_e		2002Aug28
	PASS		
0	2d_indr_buffer_f		2002Aug28
	PASS		
0	2d_indr_buffer_g		2002Aug28
	PASS		
0	3d_draw_indx_2		2002Aug28
	PASS		
0	3d_draw_indx_2_b		2002Aug28
	PASS		
0	bitblt_multi		2002Aug28
	PASS		
11	brush_32x1mono		2002Aug28
	FAIL cp_rbbm.cmp rbbmWr.cmp		
12	brush_8x8color_16		2002Aug28
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
13	brush_8x8color_32		2002Aug28
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
14	brush_8x8color_64		2002Aug28
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	brush_8x8color_dst_0_1		2002Aug28
	PASS		
16	brush_8x8mono		2002Aug28
	FAIL cp_rbbm.cmp rbbmWr.cmp		
17	brush_color_mono_stress		2002Aug28
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
0	brush_flag		2002Aug28
	PASS		
19	brush_type0		2002Aug28
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
20	brush_type1		2002Aug28

```

**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
21  brush_type10                                2002Aug28
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
0   brush_type13                                2002Aug28
PASS
0   brush_type14                                2002Aug28
PASS
24  brush_type6                                  2002Aug28
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
25  brush_type7                                  2002Aug28
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
0   brush_type_other                            2002Aug28
PASS
0   brush_types_13_14                          2002Aug28
PASS
0   clear_const_wr_en_on_me_init                2002Aug28
PASS
0   clear_const_wr_en_with_lcc                 2002Aug28
PASS
0   cond_write_mem_poll_mem                    2002Aug28
PASS
0   cond_write_mem_poll_mem2                   2002Aug28
PASS
0   cond_write_mem_poll_mem3                   2002Aug28
PASS
0   cond_write_mem_poll_mem_rt                 2002Aug28
PASS
0   cond_write_mem_poll_reg                    2002Aug28
PASS
0   cond_write_mem_poll_reg2                   2002Aug28
PASS
0   cond_write_mem_poll_reg3                   2002Aug28
PASS
0   cp_acpi_recovery_soft_reset                2002Aug28
PASS
0   cp_bios_scratch                            2002Aug28
PASS
0   cp_ib2_base000_bufszXXX                     2002Aug28
PASS
0   cp_ib2_base001_bufszXXX                     2002Aug28
PASS
0   cp_ib2_base010_bufszXXX                     2002Aug28
PASS
0   cp_ib2_base011_bufszXXX                     2002Aug28
PASS
0   cp_ib2_base100_bufszXXX                     2002Aug28
PASS
0   cp_ib2_base101_bufszXXX                     2002Aug28

```

PASS		
0	cp_ib2_base110_bufszXXX	2002Aug28
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug28
PASS		
0	cp_ib_base000_bufszXXX	2002Aug28
PASS		
0	cp_ib_base001_bufszXXX	2002Aug28
PASS		
0	cp_ib_base010_bufszXXX	2002Aug28
PASS		
0	cp_ib_base011_bufszXXX	2002Aug28
PASS		
0	cp_ib_base100_bufszXXX	2002Aug28
PASS		
0	cp_ib_base101_bufszXXX	2002Aug28
PASS		
0	cp_ib_base110_bufszXXX	2002Aug28
PASS		
0	cp_ib_base111_bufszXXX	2002Aug28
PASS		
0	cp_interrupt	2002Aug28
PASS		
0	cp_interrupt_rt_events_16	2002Aug28
PASS		
0	cp_iq2_alignment_000	2002Aug28
PASS		
0	cp_iq2_alignment_001	2002Aug28
PASS		
0	cp_iq2_alignment_010	2002Aug28
PASS		
0	cp_iq2_alignment_011	2002Aug28
PASS		
0	cp_iq2_alignment_100	2002Aug28
PASS		
0	cp_iq2_alignment_101	2002Aug28
PASS		
0	cp_iq2_alignment_110	2002Aug28
PASS		
0	cp_iq2_alignment_111	2002Aug28
PASS		
0	cp_iq_alignment_000	2002Aug28
PASS		
0	cp_iq_alignment_001	2002Aug28
PASS		
0	cp_iq_alignment_010	2002Aug28
PASS		
0	cp_iq_alignment_011	2002Aug28

PASS		
0	cp_iq_alignment_100	2002Aug28
PASS		
0	cp_iq_alignment_101	2002Aug28
PASS		
0	cp_iq_alignment_110	2002Aug28
PASS		
0	cp_iq_alignment_111	2002Aug28
PASS		
0	cp_max_size_packet	2002Aug28
PASS		
0	cp_nrt_rt_protection_mode	2002Aug28
PASS		
0	cp_output_stress	2002Aug28
PASS		
0	cp_protection_mode	2002Aug28
PASS		
0	cp_rb_alignment_000	2002Aug28
PASS		
0	cp_rb_alignment_001	2002Aug28
PASS		
0	cp_rb_alignment_010	2002Aug28
PASS		
0	cp_rb_alignment_011	2002Aug28
PASS		
0	cp_rb_alignment_100	2002Aug28
PASS		
0	cp_rb_alignment_101	2002Aug28
PASS		
0	cp_rb_alignment_110	2002Aug28
PASS		
0	cp_rb_alignment_111	2002Aug28
PASS		
0	cp_rb_wptrXXX_base000	2002Aug28
PASS		
0	cp_rb_wptrXXX_base001	2002Aug28
PASS		
0	cp_rb_wptrXXX_base010	2002Aug28
PASS		
0	cp_rb_wptrXXX_base011	2002Aug28
PASS		
0	cp_rb_wptrXXX_base100	2002Aug28
PASS		
0	cp_rb_wptrXXX_base101	2002Aug28
PASS		
0	cp_rb_wptrXXX_base110	2002Aug28
PASS		
0	cp_rb_wptrXXX_base111	2002Aug28

PASS		
0	cp_register_reads	2002Aug28
PASS		
0	cp_reg_rw_test	2002Aug28
PASS		
0	cp_rt_protection_mode	2002Aug28
PASS		
0	cp_scratch	2002Aug28
PASS		
0	cp_scratch_multi	2002Aug28
PASS		
0	dma_cp_sync	2002Aug28
PASS		
0	dma_dscrstr_chckn_bit	2002Aug28
PASS		
0	dma_dscrstr_flush1	2002Aug28
PASS		
0	dma_dscrstr_flush2	2002Aug28
PASS		
0	dma_dscrstr_flush3	2002Aug28
PASS		
0	dma_m2m_src00_dstXX	2002Aug28
PASS		
0	dma_m2m_src01_dstXX	2002Aug28
PASS		
0	dma_m2m_src10_dstXX	2002Aug28
PASS		
0	dma_m2m_src11_dstXX	2002Aug28
PASS		
0	dma_mem2mem	2002Aug28
PASS		
0	dma_mem2mem_abort	2002Aug28
PASS		
0	dma_mem2mem_alignments	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug28
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug28

PASS		
0	dma_mem2mem_byte_alignment7	2002Aug28
PASS		
0	dma_mem2mem_pio_abort	2002Aug28
PASS		
0	dma_mem2reg	2002Aug28
PASS		
0	dma_mem2reg_abort	2002Aug28
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug28
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug28
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug28
PASS		
0	dma_mem2reg_sync	2002Aug28
PASS		
0	dma_r2r_m2m_r2r	2002Aug28
PASS		
0	dma_r2r_m2r_r2r	2002Aug28
PASS		
0	dma_r2r_r2m_r2r	2002Aug28
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug28
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug28
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug28
PASS		
0	dma_reg2mem	2002Aug28
PASS		
0	dma_reg2mem_abort	2002Aug28
PASS		
0	dma_reg2reg	2002Aug28
PASS		
0	dma_reg2reg_2dw	2002Aug28
PASS		
0	dma_reg2reg_daic	2002Aug28
PASS		
0	dma_reg2reg_mult	2002Aug28
PASS		
0	dma_reg2reg_mult2	2002Aug28
PASS		
0	dma_reg2reg_mult3	2002Aug28
PASS		
0	dma_reg2reg_saic	2002Aug28
PASS		
0	dma_reg2reg_sdaic	2002Aug28

PASS		
0	dma_reg2reg_sdaic_stress	2002Aug28
PASS		
0	dma_reg2reg_stress	2002Aug28
PASS		
0	draw_indx_a	2002Aug28
PASS		
0	draw_indx_b	2002Aug28
PASS		
0	draw_indx_c	2002Aug28
PASS		
0	draw_indx_indr1_a	2002Aug28
PASS		
0	draw_indx_indr1_b	2002Aug28
PASS		
0	draw_indx_indr1_short_a	2002Aug28
PASS		
0	draw_indx_indr1_short_b	2002Aug28
PASS		
0	draw_indx_indr1_stress	2002Aug28
PASS		
0	draw_indx_indr2_a	2002Aug28
PASS		
0	draw_indx_indr2_b	2002Aug28
PASS		
0	draw_indx_indr2_short_a	2002Aug28
PASS		
0	draw_indx_indr2_short_b	2002Aug28
PASS		
0	draw_indx_indr2_stress	2002Aug28
PASS		
0	draw_indx_ring1	2002Aug28
PASS		
0	draw_indx_ring2	2002Aug28
PASS		
0	draw_indx_ring_short1	2002Aug28
PASS		
0	draw_indx_ring_short2	2002Aug28
PASS		
0	draw_indx_ring_stress	2002Aug28
PASS		
0	draw_indx_short_a	2002Aug28
PASS		
0	draw_indx_short_b	2002Aug28
PASS		
0	draw_indx_short_c	2002Aug28
PASS		
0	draw_indx_stress_a	2002Aug28

PASS		
0	draw_indx_stress_b	2002Aug28
PASS		
0	draw_indx_stress_c	2002Aug28
PASS		
0	event_timestamp_write	2002Aug28
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug28
PASS		
0	ft_indr1_july22	2002Aug28
PASS		
0	ft_indr2_july22	2002Aug28
PASS		
0	ft_july22	2002Aug28
PASS		
0	ft_july22_random	2002Aug28
PASS		
0	header_dump	2002Aug28
PASS		
0	header_dump_rt	2002Aug28
PASS		
0	header_dump_small	2002Aug28
PASS		
176	hostdata_blt	2002Aug28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Aug28
PASS		
0	im_load_b	2002Aug28
PASS		
0	im_load_cid_mismatch	2002Aug28
PASS		
0	im_load_immediate	2002Aug28
PASS		
0	im_load_immediate_nrt_me	2002Aug28
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug28
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug28
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug28
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug28
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug28
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug28
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug28

PASS		
0	im_load_indr1_a	2002Aug28
PASS		
0	im_load_indr1_b	2002Aug28
PASS		
0	im_load_indr2_a	2002Aug28
PASS		
0	im_load_indr2_b	2002Aug28
PASS		
0	im_load_invalidate	2002Aug28
PASS		
0	im_load_match	2002Aug28
PASS		
0	im_load_match2	2002Aug28
PASS		
0	im_load_nrt_me	2002Aug28
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug28
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug28
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug28
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug28
PASS		
0	im_load_pfmatch_disable	2002Aug28
PASS		
0	im_load_ring_a	2002Aug28
PASS		
0	im_load_ring_b	2002Aug28
PASS		
0	im_load_rt	2002Aug28
PASS		
0	im_load_rt2	2002Aug28
PASS		
0	im_load_rt3	2002Aug28
PASS		
0	im_load_rt4	2002Aug28
PASS		
0	im_load_sz_mismatch	2002Aug28
PASS		
0	incremental_update_const	2002Aug28
PASS		
0	incremental_update_instr	2002Aug28
PASS		
0	incremental_update_state	2002Aug28
PASS		
0	indirect_buffer_a	2002Aug28

PASS		
0	indirect_buffer_b	2002Aug28
PASS		
0	indirect_buffer_c	2002Aug28
PASS		
0	indirect_buffer_d	2002Aug28
PASS		
0	indirect_buffer_e	2002Aug28
PASS		
0	indirect_buffer_f	2002Aug28
PASS		
0	indirect_buffer_g	2002Aug28
PASS		
0	indirect_buffer_h	2002Aug28
PASS		
0	indirect_buffer_i	2002Aug28
PASS		
0	indirect_buffer_j	2002Aug28
PASS		
0	indirect_buffer_k	2002Aug28
PASS		
0	indirect_buffer_l	2002Aug28
PASS		
0	indirect_buffer_mp_a	2002Aug28
PASS		
0	indirect_buffer_mp_b	2002Aug28
PASS		
0	indirect_buffer_mp_c	2002Aug28
PASS		
0	indirect_buffer_mp_d	2002Aug28
PASS		
0	indirect_buffer_mp_e	2002Aug28
PASS		
0	indirect_buffer_mp_f	2002Aug28
PASS		
0	invalidate_on_me_init	2002Aug28
PASS		
0	invalidate_state_constants	2002Aug28
PASS		
0	invalidate_state_shaders	2002Aug28
PASS		
0	invalidate_state_shaders2	2002Aug28
PASS		
0	invalidate_state_sublks	2002Aug28
PASS		
0	invalidate_state_sublks2	2002Aug28
PASS		
0	invalid_opcode	2002Aug28

PASS		
0	iq1_iq2_mult	2002Aug28
PASS		
0	iq1_iq2_push_wrap	2002Aug28
PASS		
0	iq2_csq_full	2002Aug28
PASS		
0	IQ2_Mult	2002Aug28
PASS		
0	IQ2_Wrap_Legacy	2002Aug28
PASS		
0	IQ_Wrap	2002Aug28
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Aug28
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Aug28
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug28
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug28
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug28
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug28
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug28
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug28
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug28
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug28
PASS		
0	ld_cnst_cntx_match	2002Aug28
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug28
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug28
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug28
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug28
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug28
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug28
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug28

PASS		
0	ld_cnst_cntx_replicate8	2002Aug28
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug28
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug28
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug28
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug28
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug28
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug28
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug28
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug28
PASS		
0	load_constant_context_rt	2002Aug28
PASS		
0	load_palette	2002Aug28
PASS		
0	lp_2d3d	2002Aug28
PASS		
0	mem_write	2002Aug28
PASS		
0	mem_write_rt	2002Aug28
PASS		
0	mpeg_index	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug28
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug28

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug28
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug28
PASS		
0	nop_eating_disabled	2002Aug28
PASS		
0	paint_multi	2002Aug28
PASS		
0	pitch_clip	2002Aug28
PASS		
0	ply_nextscan	2002Aug28
PASS		
0	rbbm_soft_reset	2002Aug28
PASS		
0	rbbm_WU_2D_idle_01	2002Aug28
PASS		
0	rbbm_WU_2D_idle_02	2002Aug28
PASS		
0	rbbm_WU_2D_idle_03	2002Aug28
PASS		
0	rbbm_WU_2D_idle_04	2002Aug28
PASS		
0	rbbm_WU_2D_idle_05	2002Aug28
PASS		
0	rbbm_WU_2D_idle_06	2002Aug28
PASS		
0	rbbm_WU_2D_idle_07	2002Aug28
PASS		
0	rbbm_WU_2D_idle_08	2002Aug28
PASS		
0	rbbm_WU_2D_idle_09	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Aug28

PASS		
0	rbbm_WU_2D_idle_clean_05	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Aug28
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Aug28
PASS		
0	rbbm_WU_3D_idle_01	2002Aug28
PASS		
0	rbbm_WU_3D_idle_02	2002Aug28
PASS		
0	rbbm_WU_3D_idle_03	2002Aug28
PASS		
0	rbbm_WU_3D_idle_04	2002Aug28
PASS		
0	rbbm_WU_3D_idle_05	2002Aug28
PASS		
0	rbbm_WU_3D_idle_06	2002Aug28
PASS		
0	rbbm_WU_3D_idle_07	2002Aug28
PASS		
0	rbbm_WU_3D_idle_08	2002Aug28
PASS		
0	rbbm_WU_3D_idle_09	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Aug28

PASS		
0	rbbm_WU_3D_idle_clean_08	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Aug28
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Aug28
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug28
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Aug28
PASS		
0	rbbm_WU_cmdfifo_entries	2002Aug28
PASS		
0	rbbm_WU_CP_dma_idle	2002Aug28
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug28
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug28
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug28
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug28
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug28
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug28
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP update_pending_off	2002Aug28
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP update_pending_off	2002Aug28
PASS		
0	rbbm_WU_idct_semaphore	2002Aug28
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug28
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug28
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug28
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug28
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Aug28
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Aug28

PASS		
0	rbbm_WUrt_CP_dma_idle	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug28
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug28
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Aug28
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Aug28
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Aug28
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0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Aug28
PASS		
0	rbbm_WUrt_RT_idle_01	2002Aug28
PASS		
0	rbbm_WUrt_RT_idle_02	2002Aug28
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0	rbbm_WUrt_RT_idle_03	2002Aug28
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0	rbbm_WUrt_RT_idle_04	2002Aug28
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0	rbbm_WUrt_RT_idle_clean_01	2002Aug28
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0	rbbm_WUrt_RT_idle_clean_02	2002Aug28
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Aug28
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0	rbbm_WUrt_RT_idle_clean_04	2002Aug28
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Aug28
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0	rbbm_WUrt_VIPH0_dma_idle	2002Aug28
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0	rbbm_WUrt_VIPH1_dma_idle	2002Aug28

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0	rbbm_WUrt_VIPH2_dma_idle	2002Aug28
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0	rbbm_WUrt_VIPH3_dma_idle	2002Aug28
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0	rbbm_WU_VIPH0_dma_idle	2002Aug28
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0	rbbm_WU_VIPH1_dma_idle	2002Aug28
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0	rbbm_WU_VIPH2_dma_idle	2002Aug28
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0	rbbm_WU_VIPH3_dma_idle	2002Aug28
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PASS		
0	RB_BUFSZ_1	2002Aug28
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0	RB_Wrap	2002Aug28
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0	reg_to_mem_rt	2002Aug28
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0	rop_cfd_wmd	2002Aug28
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0	rstr_ordr	2002Aug28
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Aug28
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0	rtee_discretes_arm_rearm_events_32	2002Aug28
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PASS		
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0	rtee_discretes_rising_01_events_16	2002Aug28
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0	rtee_discretes_rising_07_events_16	2002Aug28

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0	rtee_discretes_rising_31_events_16	2002Aug28

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0	rtee_display2_comp_events_16	2002Aug28
PASS		
0	rtee_mem_linear_search_events_2	2002Aug28
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug28
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug28
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug28
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0	rtee_prefetch_parse_nops	2002Aug28
PASS		
0	rtee_reg_linear_search_events_2	2002Aug28
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug28
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug28
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0	rtee_reg_poll_comp_010_events_16	2002Aug28
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug28
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0	rtee_reg_poll_comp_100_events_16	2002Aug28
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug28
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug28
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug28

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PASS		
0	scratch_mem_read2	2002Aug28
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PASS		
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PASS		
0	set_constant_alu_max2	2002Aug28
PASS		
0	set_constant_bool_max1	2002Aug28
PASS		
0	set_constant_bool_max2	2002Aug28
PASS		
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PASS		
0	set_constant_indr1_short2	2002Aug28
PASS		
0	set_constant_indr1_write_enable	2002Aug28
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug28
PASS		
0	set_constant_indr2_short	2002Aug28
PASS		
0	set_constant_indr2_short2	2002Aug28
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PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug28
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PASS		
0	set_constant_loop_max2	2002Aug28
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PASS		
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PASS		
0	set_constant_ring_write_enable	2002Aug28

PASS		
0	set_constant_ring_write_enable_lcc	2002Aug28
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PASS		
0	set_constant_short2	2002Aug28
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PASS		
0	set_constant_tex_max2	2002Aug28
PASS		
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PASS		
0	set_constant_write_enable_lcc	2002Aug28
PASS		
0	set_constant_write_enable_lcc2	2002Aug28
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PASS		
0	set_state_rt_b	2002Aug28
PASS		
0	set_state_rt_c	2002Aug28
PASS		
0	set_state_rt_d	2002Aug28
PASS		
0	set_st_indrl_match	2002Aug28
PASS		
0	set_st_indrl_mismatch	2002Aug28
PASS		
0	set_st_indrl_mismatch2	2002Aug28
PASS		
0	set_st_indrl_mismatch3	2002Aug28
PASS		
0	set_st_indrl_mismatch4	2002Aug28
PASS		
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PASS		
0	set_st_indrl_sublk0	2002Aug28
PASS		
0	set_st_indrl_sublk1	2002Aug28
PASS		
0	set_st_indrl_sublk1_only	2002Aug28
PASS		
0	set_st_indrl_sublk2	2002Aug28

PASS		
0	set_st_indr1_sublk2_only	2002Aug28
PASS		
0	set_st_indr1_sublk3	2002Aug28
PASS		
0	set_st_indr1_sublk3_only	2002Aug28
PASS		
0	set_st_indr1_sublk4	2002Aug28
PASS		
0	set_st_indr1_sublk4_only	2002Aug28
PASS		
0	set_st_indr1_sublk5	2002Aug28
PASS		
0	set_st_indr1_sublk5_only	2002Aug28
PASS		
0	set_st_indr1_sublk6	2002Aug28
PASS		
0	set_st_indr1_sublk6_only	2002Aug28
PASS		
0	set_st_indr1_sublk7	2002Aug28
PASS		
0	set_st_indr1_sublk7_only	2002Aug28
PASS		
0	set_st_indr1_vs	2002Aug28
PASS		
0	set_st_indr1_vs_only	2002Aug28
PASS		
0	set_st_indr2_match	2002Aug28
PASS		
0	set_st_indr2_mismatch	2002Aug28
PASS		
0	set_st_indr2_mismatch2	2002Aug28
PASS		
0	set_st_indr2_mismatch3	2002Aug28
PASS		
0	set_st_indr2_mismatch4	2002Aug28
PASS		
0	set_st_indr2_ps	2002Aug28
PASS		
0	set_st_indr2_ps_only	2002Aug28
PASS		
0	set_st_indr2_sublk0	2002Aug28
PASS		
0	set_st_indr2_sublk1	2002Aug28
PASS		
0	set_st_indr2_sublk1_only	2002Aug28
PASS		
0	set_st_indr2_sublk2	2002Aug28

PASS		
0	set_st_indr2_sublk2_only	2002Aug28
PASS		
0	set_st_indr2_sublk3	2002Aug28
PASS		
0	set_st_indr2_sublk3_only	2002Aug28
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0	set_st_indr2_sublk4	2002Aug28
PASS		
0	set_st_indr2_sublk4_only	2002Aug28
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0	set_st_indr2_sublk5	2002Aug28
PASS		
0	set_st_indr2_sublk5_only	2002Aug28
PASS		
0	set_st_indr2_sublk6	2002Aug28
PASS		
0	set_st_indr2_sublk6_only	2002Aug28
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0	set_st_indr2_sublk7	2002Aug28
PASS		
0	set_st_indr2_sublk7_only	2002Aug28
PASS		
0	set_st_indr2_vs	2002Aug28
PASS		
0	set_st_indr2_vs_only	2002Aug28
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0	set_st_ring_match	2002Aug28
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0	set_st_ring_mismatch	2002Aug28
PASS		
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PASS		
0	set_st_ring_mismatch3	2002Aug28
PASS		
0	set_st_ring_mismatch4	2002Aug28
PASS		
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PASS		
0	set_st_ring_ps_only	2002Aug28
PASS		
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PASS		
0	set_st_ring_sublk1	2002Aug28
PASS		
0	set_st_ring_sublk1_only	2002Aug28

PASS		
0	set_st_ring_sublk2	2002Aug28
PASS		
0	set_st_ring_sublk2_only	2002Aug28
PASS		
0	set_st_ring_sublk3	2002Aug28
PASS		
0	set_st_ring_sublk3_only	2002Aug28
PASS		
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PASS		
0	set_st_ring_sublk4_only	2002Aug28
PASS		
0	set_st_ring_sublk5	2002Aug28
PASS		
0	set_st_ring_sublk5_only	2002Aug28
PASS		
0	set_st_ring_sublk6	2002Aug28
PASS		
0	set_st_ring_sublk6_only	2002Aug28
PASS		
0	set_st_ring_sublk7	2002Aug28
PASS		
0	set_st_ring_sublk7_only	2002Aug28
PASS		
0	set_st_ring_vs	2002Aug28
PASS		
0	set_st_ring_vs_only	2002Aug28
PASS		
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PASS		
0	set_st_stress_b	2002Aug28
PASS		
0	set_st_stress_c	2002Aug28
PASS		
0	set_st_stress_d	2002Aug28
PASS		
0	set_st_stress_e	2002Aug28
PASS		
0	set_st_stress_f	2002Aug28
PASS		
665	Small_Text_BM	2002Aug28
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Aug28
PASS		
0	src_clip	2002Aug28
PASS		
0	srf_cohr	2002Aug28

PASS		
0	trans_bitblt	2002Aug28
PASS		
0	type0_2long_pckts	2002Aug28
PASS		
0	type0_3indr_buffs	2002Aug28
PASS		
0	type0_4pckts	2002Aug28
PASS		
0	type0_incr_updt_cnst	2002Aug28
PASS		
0	type0_incr_updt_instrmem	2002Aug28
PASS		
0	type0_incr_updt_st	2002Aug28
PASS		
0	type0_incr_updt_stress	2002Aug28
PASS		
0	type0_ind1_ind2_a	2002Aug28
PASS		
0	type0_ind1_ind2_b	2002Aug28
PASS		
0	type0_indr_buff	2002Aug28
PASS		
0	Type0_IQ2_BM	2002Aug28
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug28
PASS		
0	Type0_IQ_BM	2002Aug28
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug28
PASS		
0	Type0_PQ_BM	2002Aug28
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug28
PASS		
0	type1_4pckts	2002Aug28
PASS		
0	type1_indr_buff	2002Aug28
PASS		
0	type1_pckt	2002Aug28
PASS		
0	Type3_Bitblt	2002Aug28
PASS		
0	Type3_Load_Palette	2002Aug28
PASS		
0	Type3_NOP	2002Aug28
PASS		
692	uram_rd_wrt	2002Aug23

```

**FAIL**          rbbmWr.cmp
693  uram_rd_wrt_rt          2002Aug23
**FAIL**          rbbmWr.cmp
0    uram_wrt_rt_ucode      2002Aug28
PASS
0    uram_wrt_ucode         2002Aug28
PASS
0    viz_query_a            2002Aug28
PASS
0    viz_query_draw_indx    2002Aug28
PASS
0    viz_query_draw_indx_indr1 2002Aug28
PASS
0    viz_query_draw_indx_indr2 2002Aug28
PASS
0    viz_query_draw_indx_multipass 2002Aug28
PASS
0    viz_query_draw_indx_ring 2002Aug28
PASS
0    viz_query_indr1        2002Aug28
PASS
0    viz_query_indr1_simple 2002Aug28
PASS
0    viz_query_indr2        2002Aug28
PASS
0    viz_query_indr2_simple 2002Aug28
PASS
0    viz_query_ring         2002Aug28
PASS
0    viz_query_ring_simple  2002Aug28
PASS
0    wait_for_idle          2002Aug28
PASS
0    wait_reg_mem_poll_mem0 2002Aug28
PASS
0    wait_reg_mem_poll_mem0_rt 2002Aug28
PASS
0    wait_reg_mem_poll_mem1 2002Aug28
PASS
0    wait_reg_mem_poll_mem2 2002Aug28
PASS
0    wait_reg_mem_poll_mem3 2002Aug28
PASS
0    wait_reg_mem_poll_reg0 2002Aug28
PASS
0    wait_reg_mem_poll_reg1 2002Aug28
PASS
0    wait_reg_mem_poll_reg2 2002Aug28

```

PASS

0 wait_reg_mem_poll_reg3

2002Aug28

PASS

=====
Tests_Run_count = 717 Pass_count = 702 Fail_count = 15 Incomplete_count = 0
=====


```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Aug 20 10:27:50 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             583         583         569         97
              RBBM           21          21          17          80
+++++
              TOTAL           604         604         586         97
+++++

```

=====
Regression Report For CP - Project = r400 Created on Tue Aug 20 10:27:50 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug20
PASS			
0	2d_indr_buffer_b		2002Aug20
PASS			
0	2d_indr_buffer_c		2002Aug20
PASS			
0	2d_indr_buffer_d		2002Aug20
PASS			
0	2d_indr_buffer_e		2002Aug20
PASS			
0	2d_indr_buffer_f		2002Aug20
PASS			
0	2d_indr_buffer_g		2002Aug20
PASS			
0	3d_draw_indx_2		2002Aug20
PASS			
0	3d_draw_indx_2_b		2002Aug20
PASS			
0	bitblt_multi		2002Aug20
PASS			
0	brush_flag		2002Aug20
PASS			
0	cond_write_mem_poll_mem		2002Aug20
PASS			
0	cond_write_mem_poll_mem2		2002Aug20
PASS			
0	cond_write_mem_poll_mem3		2002Aug20
PASS			
0	cond_write_mem_poll_mem_rt		2002Aug20
PASS			
0	cond_write_mem_poll_reg		2002Aug20
PASS			
0	cond_write_mem_poll_reg2		2002Aug20
PASS			
0	cond_write_mem_poll_reg3		2002Aug20
PASS			
0	cp_acpi_recovery_soft_reset		2002Aug20
PASS			
0	cp_bios_scratch		2002Aug20

PASS		
0	cp_ib2_base000_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug20
PASS		
0	cp_ib_base000_bufszXXX	2002Aug20
PASS		
0	cp_ib_base001_bufszXXX	2002Aug20
PASS		
0	cp_ib_base010_bufszXXX	2002Aug20
PASS		
0	cp_ib_base011_bufszXXX	2002Aug20
PASS		
0	cp_ib_base100_bufszXXX	2002Aug20
PASS		
0	cp_ib_base101_bufszXXX	2002Aug20
PASS		
0	cp_ib_base110_bufszXXX	2002Aug20
PASS		
0	cp_ib_base111_bufszXXX	2002Aug20
PASS		
0	cp_interrupt	2002Aug20
PASS		
0	cp_interrupt_rt_events_16	2002Aug20
PASS		
0	cp_iq2_alignment_000	2002Aug20
PASS		
0	cp_iq2_alignment_001	2002Aug20
PASS		
0	cp_iq2_alignment_010	2002Aug20
PASS		
0	cp_iq2_alignment_011	2002Aug20
PASS		
0	cp_iq2_alignment_100	2002Aug20
PASS		
0	cp_iq2_alignment_101	2002Aug20

PASS		
0	cp_iq2_alignment_110	2002Aug20
PASS		
0	cp_iq2_alignment_111	2002Aug20
PASS		
0	cp_iq_alignment_000	2002Aug20
PASS		
0	cp_iq_alignment_001	2002Aug20
PASS		
0	cp_iq_alignment_010	2002Aug20
PASS		
0	cp_iq_alignment_011	2002Aug20
PASS		
0	cp_iq_alignment_100	2002Aug20
PASS		
0	cp_iq_alignment_101	2002Aug20
PASS		
0	cp_iq_alignment_110	2002Aug20
PASS		
0	cp_iq_alignment_111	2002Aug20
PASS		
0	cp_max_size_packet	2002Aug20
PASS		
0	cp_nrt_rt_protection_mode	2002Aug20
PASS		
0	cp_output_stress	2002Aug20
PASS		
0	cp_protection_mode	2002Aug20
PASS		
0	cp_rb_alignment_000	2002Aug20
PASS		
0	cp_rb_alignment_001	2002Aug20
PASS		
0	cp_rb_alignment_010	2002Aug20
PASS		
0	cp_rb_alignment_011	2002Aug20
PASS		
0	cp_rb_alignment_100	2002Aug20
PASS		
0	cp_rb_alignment_101	2002Aug20
PASS		
0	cp_rb_alignment_110	2002Aug20
PASS		
0	cp_rb_alignment_111	2002Aug20
PASS		
0	cp_register_reads	2002Aug20
PASS		
0	cp_reg_rw_test	2002Aug20

PASS		
0	cp_rt_protection_mode	2002Aug20
PASS		
0	cp_scratch	2002Aug20
PASS		
0	cp_scratch_multi	2002Aug20
PASS		
0	dma_cp_sync	2002Aug20
PASS		
0	dma_dscrstr_chckn_bit	2002Aug20
PASS		
0	dma_dscrstr_flush1	2002Aug20
PASS		
0	dma_dscrstr_flush2	2002Aug20
PASS		
0	dma_dscrstr_flush3	2002Aug20
PASS		
0	dma_m2m_src00_dstXX	2002Aug20
PASS		
0	dma_m2m_src01_dstXX	2002Aug20
PASS		
0	dma_m2m_src10_dstXX	2002Aug20
PASS		
0	dma_m2m_src11_dstXX	2002Aug20
PASS		
0	dma_mem2mem	2002Aug20
PASS		
0	dma_mem2mem_abort	2002Aug20
PASS		
0	dma_mem2mem_alignments	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug20
PASS		
0	dma_mem2mem_pio_abort	2002Aug20

PASS		
0	dma_mem2reg	2002Aug20
PASS		
0	dma_mem2reg_abort	2002Aug20
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug20
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug20
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug20
PASS		
0	dma_mem2reg_sync	2002Aug20
PASS		
0	dma_r2r_m2m_r2r	2002Aug20
PASS		
0	dma_r2r_m2r_r2r	2002Aug20
PASS		
0	dma_r2r_r2m_r2r	2002Aug20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug20
PASS		
0	dma_reg2mem	2002Aug20
PASS		
0	dma_reg2mem_abort	2002Aug20
PASS		
0	dma_reg2reg	2002Aug20
PASS		
0	dma_reg2reg_2dw	2002Aug20
PASS		
0	dma_reg2reg_daic	2002Aug20
PASS		
0	dma_reg2reg_mult	2002Aug20
PASS		
0	dma_reg2reg_mult2	2002Aug20
PASS		
0	dma_reg2reg_mult3	2002Aug20
PASS		
0	dma_reg2reg_saic	2002Aug20
PASS		
0	dma_reg2reg_sdaic	2002Aug20
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug20
PASS		
0	dma_reg2reg_stress	2002Aug20

PASS		
0	draw_indx_a	2002Aug20
PASS		
0	draw_indx_b	2002Aug20
PASS		
0	draw_indx_c	2002Aug20
PASS		
0	draw_indx_indr1_a	2002Aug20
PASS		
0	draw_indx_indr1_b	2002Aug20
PASS		
0	draw_indx_indr1_short_a	2002Aug20
PASS		
0	draw_indx_indr1_short_b	2002Aug20
PASS		
0	draw_indx_indr1_stress	2002Aug20
PASS		
0	draw_indx_indr2_a	2002Aug20
PASS		
0	draw_indx_indr2_b	2002Aug20
PASS		
0	draw_indx_indr2_short_a	2002Aug20
PASS		
0	draw_indx_indr2_short_b	2002Aug20
PASS		
0	draw_indx_indr2_stress	2002Aug20
PASS		
0	draw_indx_ring1	2002Aug20
PASS		
0	draw_indx_ring2	2002Aug20
PASS		
0	draw_indx_ring_short1	2002Aug20
PASS		
0	draw_indx_ring_short2	2002Aug20
PASS		
0	draw_indx_ring_stress	2002Aug20
PASS		
0	draw_indx_short_a	2002Aug20
PASS		
0	draw_indx_short_b	2002Aug20
PASS		
0	draw_indx_short_c	2002Aug20
PASS		
0	draw_indx_stress_a	2002Aug20
PASS		
0	draw_indx_stress_b	2002Aug20
PASS		
0	draw_indx_stress_c	2002Aug20

PASS		
0	event_timestamp_write	2002Aug20
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug20
PASS		
0	ft_indr1_july22	2002Aug20
PASS		
0	ft_indr2_july22	2002Aug20
PASS		
0	ft_july22	2002Aug20
PASS		
0	header_dump	2002Aug20
PASS		
0	header_dump_rt	2002Aug20
PASS		
0	header_dump_small	2002Aug20
PASS		
0	hostdata_blt	2002Aug20
PASS		
0	im_load_a	2002Aug20
PASS		
0	im_load_b	2002Aug20
PASS		
0	im_load_cid_mismatch	2002Aug20
PASS		
0	im_load_immediate	2002Aug20
PASS		
0	im_load_immediate_nrt_me	2002Aug20
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug20
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug20
PASS		
0	im_load_indr1_a	2002Aug20
PASS		
0	im_load_indr1_b	2002Aug20
PASS		
0	im_load_indr2_a	2002Aug20

PASS		
0	im_load_indr2_b	2002Aug20
PASS		
0	im_load_invalidate	2002Aug20
PASS		
0	im_load_match	2002Aug20
PASS		
0	im_load_match2	2002Aug20
PASS		
0	im_load_nrt_me	2002Aug20
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug20
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug20
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug20
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug20
PASS		
0	im_load_pfmatch_disable	2002Aug20
PASS		
0	im_load_ring_a	2002Aug20
PASS		
0	im_load_ring_b	2002Aug20
PASS		
0	im_load_rt	2002Aug20
PASS		
0	im_load_rt2	2002Aug20
PASS		
0	im_load_rt3	2002Aug20
PASS		
0	im_load_rt4	2002Aug20
PASS		
0	im_load_sz_mismatch	2002Aug20
PASS		
0	incremental_update_const	2002Aug20
PASS		
0	incremental_update_instr	2002Aug20
PASS		
0	incremental_update_state	2002Aug20
PASS		
0	indirect_buffer_a	2002Aug20
PASS		
0	indirect_buffer_b	2002Aug20
PASS		
0	indirect_buffer_c	2002Aug20
PASS		
0	indirect_buffer_d	2002Aug20

PASS		
0	indirect_buffer_e	2002Aug20
PASS		
0	indirect_buffer_f	2002Aug20
PASS		
0	indirect_buffer_g	2002Aug20
PASS		
0	indirect_buffer_h	2002Aug20
PASS		
0	indirect_buffer_i	2002Aug20
PASS		
0	indirect_buffer_j	2002Aug20
PASS		
0	indirect_buffer_k	2002Aug20
PASS		
0	indirect_buffer_l	2002Aug20
PASS		
0	indirect_buffer_mp_a	2002Aug20
PASS		
0	indirect_buffer_mp_b	2002Aug20
PASS		
0	indirect_buffer_mp_c	2002Aug20
PASS		
0	indirect_buffer_mp_d	2002Aug20
PASS		
0	indirect_buffer_mp_e	2002Aug20
PASS		
0	indirect_buffer_mp_f	2002Aug20
PASS		
0	invalidate_on_me_init	2002Aug20
PASS		
0	invalidate_state_constants	2002Aug20
PASS		
0	invalidate_state_shaders	2002Aug20
PASS		
0	invalidate_state_shaders2	2002Aug20
PASS		
0	invalidate_state_sublks	2002Aug20
PASS		
0	invalidate_state_sublks2	2002Aug20
PASS		
0	invalid_opcode	2002Aug20
PASS		
210	iq1_iq2_mult	2002Aug20
INCOMPLETE		
211	iq1_iq2_push_wrap	2002Aug20
INCOMPLETE		
0	iq2_csq_full	2002Aug20

PASS		
0	IQ2_Mult	2002Aug20
PASS		
0	IQ2_Wrap_Legacy	2002Aug20
PASS		
0	IQ_Wrap	2002Aug20
PASS		
216	ld_cnst_cntx_coherency_cntrl	2002Aug20
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
217	ld_cnst_cntx_coherency_cntrl2	2002Aug20
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug20
PASS		
0	ld_cnst_cntx_match	2002Aug20
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug20
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug20
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug20
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug20
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug20
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug20
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug20
PASS		
0	ld_cnst_cntx_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug20
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug20

PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug20
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug20
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug20
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug20
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug20
PASS		
0	load_constant_context_rt	2002Aug20
PASS		
0	load_palette	2002Aug20
PASS		
0	mem_write	2002Aug20
PASS		
0	mem_write_rt	2002Aug20
PASS		
0	mpeg_index	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug20

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug20
PASS		
0	nop_eating_disabled	2002Aug20
PASS		
0	paint_multi	2002Aug20
PASS		
265	pitch_clip	2002Aug20
FAIL	cp_rbbm.cmp	
0	ply_nextscan	2002Aug20
PASS		
267	rbbm_soft_reset	2002Aug20
FAIL	rbbmWr.cmp	
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug20
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Aug20
PASS		
270	rbbm_WU_CP_dma_idle	2002Aug20
FAIL		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug20
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug20
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug20
PASS		
0	rbbm_WU_idct_semaphore	2002Aug20
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug20
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug20
PASS		
282	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug20
FAIL		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug20
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Aug20

PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Aug20
PASS		
286	rbbm_WU_VIPH2_dma_idle	2002Aug20
FAIL		
0	rbbm_WU_VIPH3_dma_idle	2002Aug20
PASS		
0	RB_BUFSZ_0	2002Aug20
PASS		
0	RB_BUFSZ_1	2002Aug20
PASS		
0	RB_Wrap	2002Aug20
PASS		
0	reg_to_mem	2002Aug20
PASS		
0	reg_to_mem_rt	2002Aug20
PASS		
0	rop_cfd_wmd	2002Aug20
PASS		
0	rstr_ordr	2002Aug20
PASS		
0	rtee_discretes_falling_00_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug20

PASS		
0	rtee_discretes_falling_14_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug20

PASS		
0	rtee_discretes_invert_06_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_11_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug20

PASS		
0	rtee_discretes_invert_30_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug20
PASS		
0	rtee_discretes_level_00_events_16	2002Aug20
PASS		
0	rtee_discretes_level_01_events_16	2002Aug20
PASS		
0	rtee_discretes_level_02_events_16	2002Aug20
PASS		
0	rtee_discretes_level_03_events_16	2002Aug20
PASS		
0	rtee_discretes_level_04_events_16	2002Aug20
PASS		
0	rtee_discretes_level_05_events_16	2002Aug20
PASS		
0	rtee_discretes_level_06_events_16	2002Aug20
PASS		
0	rtee_discretes_level_07_events_16	2002Aug20
PASS		
0	rtee_discretes_level_08_events_16	2002Aug20
PASS		
0	rtee_discretes_level_09_events_16	2002Aug20
PASS		
0	rtee_discretes_level_10_events_16	2002Aug20
PASS		
0	rtee_discretes_level_11_events_16	2002Aug20
PASS		
0	rtee_discretes_level_12_events_16	2002Aug20
PASS		
0	rtee_discretes_level_13_events_16	2002Aug20
PASS		
0	rtee_discretes_level_14_events_16	2002Aug20
PASS		
0	rtee_discretes_level_15_events_16	2002Aug20
PASS		
0	rtee_discretes_level_16_events_16	2002Aug20
PASS		
0	rtee_discretes_level_17_events_16	2002Aug20
PASS		
0	rtee_discretes_level_18_events_16	2002Aug20
PASS		
0	rtee_discretes_level_19_events_16	2002Aug20
PASS		
0	rtee_discretes_level_20_events_16	2002Aug20
PASS		
0	rtee_discretes_level_21_events_16	2002Aug20

PASS		
0	rtee_discretes_level_22_events_16	2002Aug20
PASS		
0	rtee_discretes_level_23_events_16	2002Aug20
PASS		
0	rtee_discretes_level_24_events_16	2002Aug20
PASS		
0	rtee_discretes_level_25_events_16	2002Aug20
PASS		
0	rtee_discretes_level_26_events_16	2002Aug20
PASS		
0	rtee_discretes_level_27_events_16	2002Aug20
PASS		
0	rtee_discretes_level_28_events_16	2002Aug20
PASS		
0	rtee_discretes_level_29_events_16	2002Aug20
PASS		
0	rtee_discretes_level_30_events_16	2002Aug20
PASS		
0	rtee_discretes_level_31_events_16	2002Aug20
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug20

PASS		
0	rtee_discretes_rising_13_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug20
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug20
PASS		
0	rtee_display1_comp_events_16	2002Aug20
PASS		
0	rtee_display2_comp_events_16	2002Aug20
PASS		
0	rtee_mem_linear_search_events_2	2002Aug20
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug20

PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug20
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug20
PASS		
0	rtee_prefetch_parse_nops	2002Aug20
PASS		
0	rtee_reg_linear_search_events_2	2002Aug20
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug20
PASS		
0	rtee_vip_comp_events_16	2002Aug20
PASS		
0	rt_invalid_opcode	2002Aug20
PASS		
0	scratch_mem_read	2002Aug20
PASS		
0	scratch_mem_read2	2002Aug20

PASS		
0	set_constant	2002Aug20
PASS		
0	set_constant2	2002Aug20
PASS		
0	set_constant_alu_max1	2002Aug20
PASS		
0	set_constant_alu_max2	2002Aug20
PASS		
0	set_constant_bool_max1	2002Aug20
PASS		
0	set_constant_bool_max2	2002Aug20
PASS		
0	set_constant_indr1_short	2002Aug20
PASS		
0	set_constant_indr1_short2	2002Aug20
PASS		
0	set_constant_indr1_write_enable	2002Aug20
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug20
PASS		
0	set_constant_indr2_short	2002Aug20
PASS		
0	set_constant_indr2_short2	2002Aug20
PASS		
0	set_constant_indr2_write_enable	2002Aug20
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug20
PASS		
0	set_constant_loop_max1	2002Aug20
PASS		
0	set_constant_loop_max2	2002Aug20
PASS		
0	set_constant_ring_short	2002Aug20
PASS		
0	set_constant_ring_short2	2002Aug20
PASS		
0	set_constant_ring_write_enable	2002Aug20
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug20
PASS		
0	set_constant_short	2002Aug20
PASS		
0	set_constant_short2	2002Aug20
PASS		
0	set_constant_tex_max1	2002Aug20
PASS		
0	set_constant_tex_max2	2002Aug20

PASS		
0	set_constant_write_enable	2002Aug20
PASS		
478	set_constant_write_enable_lcc	2002Aug20
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	set_constant_write_enable_lcc2	2002Aug20
PASS		
0	set_scissors	2002Aug20
PASS		
0	set_state_rt_a	2002Aug20
PASS		
482	set_state_rt_b	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
483	set_state_rt_c	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
484	set_state_rt_d	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_indrl_match	2002Aug20
PASS		
0	set_st_indrl_mismatch	2002Aug20
PASS		
0	set_st_indrl_mismatch2	2002Aug20
PASS		
0	set_st_indrl_mismatch3	2002Aug20
PASS		
0	set_st_indrl_mismatch4	2002Aug20
PASS		
0	set_st_indrl_ps	2002Aug20
PASS		
0	set_st_indrl_ps_only	2002Aug20
PASS		
0	set_st_indrl_sublk0	2002Aug20
PASS		
0	set_st_indrl_sublk1	2002Aug20
PASS		
0	set_st_indrl_sublk1_only	2002Aug20
PASS		
0	set_st_indrl_sublk2	2002Aug20
PASS		
0	set_st_indrl_sublk2_only	2002Aug20
PASS		
0	set_st_indrl_sublk3	2002Aug20
PASS		
0	set_st_indrl_sublk3_only	2002Aug20
PASS		
0	set_st_indrl_sublk4	2002Aug20
PASS		
0	set_st_indrl_sublk4_only	2002Aug20

PASS		
0	set_st_indr1_sublk5	2002Aug20
PASS		
0	set_st_indr1_sublk5_only	2002Aug20
PASS		
0	set_st_indr1_sublk6	2002Aug20
PASS		
0	set_st_indr1_sublk6_only	2002Aug20
PASS		
0	set_st_indr1_sublk7	2002Aug20
PASS		
0	set_st_indr1_sublk7_only	2002Aug20
PASS		
0	set_st_indr1_vs	2002Aug20
PASS		
0	set_st_indr1_vs_only	2002Aug20
PASS		
0	set_st_indr2_match	2002Aug20
PASS		
0	set_st_indr2_mismatch	2002Aug20
PASS		
0	set_st_indr2_mismatch2	2002Aug20
PASS		
0	set_st_indr2_mismatch3	2002Aug20
PASS		
0	set_st_indr2_mismatch4	2002Aug20
PASS		
0	set_st_indr2_ps	2002Aug20
PASS		
0	set_st_indr2_ps_only	2002Aug20
PASS		
0	set_st_indr2_sublk0	2002Aug20
PASS		
0	set_st_indr2_sublk1	2002Aug20
PASS		
0	set_st_indr2_sublk1_only	2002Aug20
PASS		
0	set_st_indr2_sublk2	2002Aug20
PASS		
0	set_st_indr2_sublk2_only	2002Aug20
PASS		
0	set_st_indr2_sublk3	2002Aug20
PASS		
0	set_st_indr2_sublk3_only	2002Aug20
PASS		
0	set_st_indr2_sublk4	2002Aug20
PASS		
0	set_st_indr2_sublk4_only	2002Aug20

PASS		
0	set_st_indr2_sublk5	2002Aug20
PASS		
0	set_st_indr2_sublk5_only	2002Aug20
PASS		
0	set_st_indr2_sublk6	2002Aug20
PASS		
0	set_st_indr2_sublk6_only	2002Aug20
PASS		
0	set_st_indr2_sublk7	2002Aug20
PASS		
0	set_st_indr2_sublk7_only	2002Aug20
PASS		
0	set_st_indr2_vs	2002Aug20
PASS		
0	set_st_indr2_vs_only	2002Aug20
PASS		
0	set_st_pfmatch_disabled	2002Aug20
PASS		
0	set_st_ring_match	2002Aug20
PASS		
0	set_st_ring_mismatch	2002Aug20
PASS		
0	set_st_ring_mismatch2	2002Aug20
PASS		
0	set_st_ring_mismatch3	2002Aug20
PASS		
0	set_st_ring_mismatch4	2002Aug20
PASS		
0	set_st_ring_ps	2002Aug20
PASS		
0	set_st_ring_ps_only	2002Aug20
PASS		
0	set_st_ring_sublk0	2002Aug20
PASS		
0	set_st_ring_sublk1	2002Aug20
PASS		
0	set_st_ring_sublk1_only	2002Aug20
PASS		
0	set_st_ring_sublk2	2002Aug20
PASS		
0	set_st_ring_sublk2_only	2002Aug20
PASS		
0	set_st_ring_sublk3	2002Aug20
PASS		
0	set_st_ring_sublk3_only	2002Aug20
PASS		
0	set_st_ring_sublk4	2002Aug20

PASS		
0	set_st_ring_subblk4_only	2002Aug20
PASS		
0	set_st_ring_subblk5	2002Aug20
PASS		
0	set_st_ring_subblk5_only	2002Aug20
PASS		
0	set_st_ring_subblk6	2002Aug20
PASS		
0	set_st_ring_subblk6_only	2002Aug20
PASS		
0	set_st_ring_subblk7	2002Aug20
PASS		
0	set_st_ring_subblk7_only	2002Aug20
PASS		
0	set_st_ring_vs	2002Aug20
PASS		
0	set_st_ring_vs_only	2002Aug20
PASS		
0	set_st_stress_a	2002Aug20
PASS		
559	set_st_stress_b	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug20
PASS		
0	set_st_stress_d	2002Aug20
PASS		
0	set_st_stress_e	2002Aug20
PASS		
0	set_st_stress_f	2002Aug20
PASS		
0	smoke_nrt	2002Aug20
PASS		
0	src_clip	2002Aug20
PASS		
0	srf_cohr	2002Aug20
PASS		
0	trans_bitblt	2002Aug20
PASS		
0	type0_2long_pckts	2002Aug20
PASS		
0	type0_3indr_buffs	2002Aug20
PASS		
0	type0_4pckts	2002Aug20
PASS		
0	type0_incr_updt_cnst	2002Aug20
PASS		
0	type0_incr_updt_instrmem	2002Aug20

PASS		
0	type0_incr_updt_st	2002Aug20
PASS		
0	type0_incr_updt_stress	2002Aug20
PASS		
0	type0_ind1_ind2_a	2002Aug20
PASS		
0	type0_ind1_ind2_b	2002Aug20
PASS		
0	type0_indr_buff	2002Aug20
PASS		
0	Type0_IQ2_BM	2002Aug20
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug20
PASS		
0	Type0_IQ_BM	2002Aug20
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug20
PASS		
0	Type0_PQ_BM	2002Aug20
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug20
PASS		
0	type1_4pckts	2002Aug20
PASS		
0	type1_indr_buff	2002Aug20
PASS		
0	type1_pckt	2002Aug20
PASS		
0	Type3_Bitblt	2002Aug20
PASS		
0	Type3_Load_Palette	2002Aug20
PASS		
0	Type3_NOP	2002Aug20
PASS		
590	uram_rd_wrt	2002Aug20
FAIL		
591	uram_rd_wrt_rt	2002Aug20
FAIL		
592	uram_wrt_rt_ucose	2002Aug20
FAIL	rbbmWr.cmp	
0	uram_wrt_ucose	2002Aug20
PASS		
0	wait_for_idle	2002Aug20
PASS		
0	wait_reg_mem_poll_mem0	2002Aug20
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Aug20

```
PASS
0 wait_reg_mem_poll_mem1 2002Aug20
PASS
0 wait_reg_mem_poll_mem2 2002Aug20
PASS
0 wait_reg_mem_poll_mem3 2002Aug20
PASS
0 wait_reg_mem_poll_reg0 2002Aug20
PASS
0 wait_reg_mem_poll_reg1 2002Aug20
PASS
0 wait_reg_mem_poll_reg2 2002Aug20
PASS
0 wait_reg_mem_poll_reg3 2002Aug20
PASS
```

```
=====
Tests_Run_count = 603 Pass_count = 586 Fail_count = 15 Incomplete_count = 2
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Oct  3  0:44:52 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU              0          0          0          0
              CP             673         673         638         94
              RBBM           113         113         110         97
+++++
              TOTAL           786         786         748         95
+++++

```

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+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Sep 28  2:28:22 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          667        667        611         91
              RBBM        113        113        113        100
+++++
              TOTAL        780        780        724         92
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buf_d_sm_meq		2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
2	2d_indr_buf_g_sm_meq		2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	2d_indr_buffer_a		2002Oct10
PASS			
0	2d_indr_buffer_b		2002Oct10
PASS			
0	2d_indr_buffer_c		2002Oct10
PASS			
0	2d_indr_buffer_d		2002Oct10
PASS			
0	2d_indr_buffer_e		2002Oct10
PASS			
0	2d_indr_buffer_f		2002Oct10
PASS			
0	2d_indr_buffer_f_sm_roq		2002Oct10
PASS			
0	2d_indr_buffer_f_sm_roq2		2002Oct10
PASS			
0	2d_indr_buffer_g		2002Oct10
PASS			
12	2d_indr_buffer_random		2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
13	2d_indr_buffer_random2		2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	3d_draw_indx_2		2002Oct10
PASS			
0	3d_draw_indx_2_b		2002Oct10
PASS			
0	IQ2_Mult		2002Oct10
PASS			
0	IQ2_Wrap_Legacy		2002Oct10
PASS			
0	IQ_Wrap		2002Oct10
PASS			
19	Isync_Control		2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp		
20	Isync_Control_2D_3D		2002Oct10

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
21  Isync_Control_3D_2D                                2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   RB_BUFSZ_0                                          2002Oct10
PASS
0   RB_BUFSZ_1                                          2002Oct10
PASS
0   RB_Wrap                                             2002Oct10
PASS
25  Small_Text_BM                                       2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type0_IQ2_BM                                        2002Oct10
PASS
0   Type0_IQ2_BM_IB_Packet                             2002Oct10
PASS
0   Type0_IQ_BM                                         2002Oct10
PASS
0   Type0_IQ_BM_IB_Packet                             2002Oct10
PASS
0   Type0_PQ_BM                                         2002Oct10
PASS
0   Type0_PQ_BM_Pre_Write_Timer                       2002Oct10
PASS
0   Type3_Bitblt                                       2002Oct10
PASS
33  Type3_Hostdata_blt                                  2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
34  Type3_Hostdata_blt_PIO                             2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type3_Load_Palette                                 2002Oct10
PASS
0   Type3_NOP                                          2002Oct10
PASS
37  Type3_Polyline                                      2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
38  Type3_Polyline_Large                               2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
39  bitblt_multi                                       2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
40  brush_32x1mono                                     2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
41  brush_8x8color_16                                  2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
42  brush_8x8color_32                                  2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
43  brush_8x8color_64                                  2002Oct10
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
44  brush_8x8color_dst_0_1                             2002Oct10

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
45  brush_8x8mono                                2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
46  brush_color_mono_stress                      2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
47  brush_flag                                    2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
48  brush_type0                                   2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
49  brush_type1                                   2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
50  brush_type10                                  2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
51  brush_type13                                  2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
52  brush_type14                                  2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
53  brush_type6                                    2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
54  brush_type7                                    2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
55  brush_type_other                              2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
56  brush_types_13_14                            2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
57  change_queue_sizes                           2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   clear_const_wr_en_on_me_init                 2002Oct10
PASS
0   clear_const_wr_en_with_lcc                   2002Oct10
PASS
0   cond_write_mem_poll_mem                      2002Oct10
PASS
0   cond_write_mem_poll_mem2                     2002Oct10
PASS
0   cond_write_mem_poll_mem3                     2002Oct10
PASS
0   cond_write_mem_poll_mem_rt                   2002Oct10
PASS
0   cond_write_mem_poll_reg                      2002Oct10
PASS
0   cond_write_mem_poll_reg2                     2002Oct10
PASS
0   cond_write_mem_poll_reg3                     2002Oct10
PASS
0   cp_acpi_recovery_soft_reset                  2002Oct10
PASS
0   cp_bios_scratch                              2002Oct10

```


PASS		
0	cp_high_stress	2002Oct10
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base010_bufszXXX_sm_roq	2002Oct10
PASS		
0	cp_ib2_base011_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct10
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct10
PASS		
0	cp_ib_base000_bufszXXX	2002Oct10
PASS		
0	cp_ib_base001_bufszXXX	2002Oct10
PASS		
0	cp_ib_base010_bufszXXX	2002Oct10
PASS		
0	cp_ib_base011_bufszXXX	2002Oct10
PASS		
0	cp_ib_base100_bufszXXX	2002Oct10
PASS		
0	cp_ib_base101_bufszXXX	2002Oct10
PASS		
0	cp_ib_base101_bufszXXX_sm_roq	2002Oct10
PASS		
0	cp_ib_base110_bufszXXX	2002Oct10
PASS		
0	cp_ib_base111_bufszXXX	2002Oct10
PASS		
0	cp_interrupt	2002Oct10
PASS		
0	cp_interrupt_rt_events_16	2002Oct10
PASS		
0	cp_iq2_alignment_000	2002Oct10
PASS		
0	cp_iq2_alignment_001	2002Oct10
PASS		
0	cp_iq2_alignment_010	2002Oct10

PASS		
0	cp_iq2_alignment_011	2002Oct10
PASS		
0	cp_iq2_alignment_100	2002Oct10
PASS		
0	cp_iq2_alignment_101	2002Oct10
PASS		
0	cp_iq2_alignment_110	2002Oct10
PASS		
0	cp_iq2_alignment_111	2002Oct10
PASS		
0	cp_iq_alignment_000	2002Oct10
PASS		
0	cp_iq_alignment_001	2002Oct10
PASS		
0	cp_iq_alignment_010	2002Oct10
PASS		
0	cp_iq_alignment_011	2002Oct10
PASS		
0	cp_iq_alignment_100	2002Oct10
PASS		
0	cp_iq_alignment_101	2002Oct10
PASS		
0	cp_iq_alignment_110	2002Oct10
PASS		
0	cp_iq_alignment_111	2002Oct10
PASS		
0	cp_max_size_packet	2002Oct10
PASS		
0	cp_max_size_packet_random	2002Oct10
PASS		
0	cp_nrt_rt_protection_mode	2002Oct10
PASS		
0	cp_output_stress	2002Oct10
PASS		
0	cp_output_stress_random	2002Oct10
PASS		
0	cp_protection_mode	2002Oct10
PASS		
0	cp_rb_alignment_000	2002Oct10
PASS		
0	cp_rb_alignment_001	2002Oct10
PASS		
0	cp_rb_alignment_010	2002Oct10
PASS		
0	cp_rb_alignment_011	2002Oct10
PASS		
0	cp_rb_alignment_100	2002Oct10

PASS		
0	cp_rb_alignment_101	2002Oct10
PASS		
0	cp_rb_alignment_110	2002Oct10
PASS		
0	cp_rb_alignment_111	2002Oct10
PASS		
0	cp_rb_wptrXXX_base000	2002Oct10
PASS		
0	cp_rb_wptrXXX_base001	2002Oct10
PASS		
0	cp_rb_wptrXXX_base010	2002Oct10
PASS		
0	cp_rb_wptrXXX_base011	2002Oct10
PASS		
0	cp_rb_wptrXXX_base100	2002Oct10
PASS		
0	cp_rb_wptrXXX_base101	2002Oct10
PASS		
0	cp_rb_wptrXXX_base101_sm_roq	2002Oct10
PASS		
0	cp_rb_wptrXXX_base110	2002Oct10
PASS		
0	cp_rb_wptrXXX_base111	2002Oct10
PASS		
0	cp_reg_rw_test	2002Oct10
PASS		
0	cp_register_reads	2002Oct10
PASS		
0	cp_rt_protection_mode	2002Oct10
PASS		
0	cp_scratch	2002Oct10
PASS		
0	cp_scratch_multi	2002Oct10
PASS		
0	csf_reg_rd_default	2002Oct10
PASS		
135	csf_reg_rd_only	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	csf_reg_wr_rd	2002Oct10
PASS		
0	dma_cp_sync	2002Oct10
PASS		
0	dma_dscrstr_chckn_bit	2002Oct10
PASS		
0	dma_dscrstr_flush1	2002Oct10
PASS		
0	dma_dscrstr_flush2	2002Oct10

PASS		
0	dma_dscrstr_flush3	2002Oct10
PASS		
0	dma_m2m_src00_dstXX	2002Oct10
PASS		
0	dma_m2m_src01_dstXX	2002Oct10
PASS		
0	dma_m2m_src10_dstXX	2002Oct10
PASS		
0	dma_m2m_src11_dstXX	2002Oct10
PASS		
0	dma_mem2mem	2002Oct10
PASS		
0	dma_mem2mem_abort	2002Oct10
PASS		
0	dma_mem2mem_alignments	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment1	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment2	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment3	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment4	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct10
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct10
PASS		
0	dma_mem2mem_large	2002Oct10
PASS		
0	dma_mem2mem_pio_abort	2002Oct10
PASS		
0	dma_mem2mem_random	2002Oct10
PASS		
0	dma_mem2reg	2002Oct10
PASS		
0	dma_mem2reg_abort	2002Oct10
PASS		
0	dma_mem2reg_dtaq_stress	2002Oct10
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Oct10
PASS		
0	dma_mem2reg_pio_stressbm	2002Oct10

PASS		
0	dma_mem2reg_pio_stresspio	2002Oct10
PASS		
0	dma_mem2reg_sync	2002Oct10
PASS		
0	dma_r2r_m2m_r2r	2002Oct10
PASS		
0	dma_r2r_m2r_r2r	2002Oct10
PASS		
0	dma_r2r_r2m_r2r	2002Oct10
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct10
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct10
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct10
PASS		
0	dma_reg2mem	2002Oct10
PASS		
0	dma_reg2mem_abort	2002Oct10
PASS		
0	dma_reg2reg	2002Oct10
PASS		
0	dma_reg2reg_2dw	2002Oct10
PASS		
0	dma_reg2reg_daic	2002Oct10
PASS		
0	dma_reg2reg_mult	2002Oct10
PASS		
0	dma_reg2reg_mult2	2002Oct10
PASS		
0	dma_reg2reg_mult3	2002Oct10
PASS		
0	dma_reg2reg_saic	2002Oct10
PASS		
0	dma_reg2reg_sdaic	2002Oct10
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct10
PASS		
0	dma_reg2reg_stress	2002Oct10
PASS		
0	draw_indx_a	2002Oct10
PASS		
0	draw_indx_b	2002Oct10
PASS		
0	draw_indx_c	2002Oct10
PASS		
0	draw_indx_indr1_a	2002Oct10

PASS		
0	draw_indx_indr1_b	2002Oct10
PASS		
0	draw_indx_indr1_short_a	2002Oct10
PASS		
0	draw_indx_indr1_short_b	2002Oct10
PASS		
0	draw_indx_indr1_stress	2002Oct10
PASS		
0	draw_indx_indr2_a	2002Oct10
PASS		
0	draw_indx_indr2_b	2002Oct10
PASS		
0	draw_indx_indr2_short_a	2002Oct10
PASS		
0	draw_indx_indr2_short_b	2002Oct10
PASS		
0	draw_indx_indr2_stress	2002Oct10
PASS		
198	draw_indx_random_a	2002Oct10
FAIL	mh_cp_rd.cmp	
199	draw_indx_random_b	2002Oct10
FAIL	mh_cp_rd.cmp	
200	draw_indx_random_c	2002Oct10
FAIL	mh_cp_rd.cmp	
0	draw_indx_ring1	2002Oct10
PASS		
0	draw_indx_ring2	2002Oct10
PASS		
0	draw_indx_ring_short1	2002Oct10
PASS		
0	draw_indx_ring_short2	2002Oct10
PASS		
0	draw_indx_ring_stress	2002Oct10
PASS		
0	draw_indx_short_a	2002Oct10
PASS		
0	draw_indx_short_b	2002Oct10
PASS		
0	draw_indx_short_c	2002Oct10
PASS		
0	draw_indx_stress_a	2002Oct10
PASS		
0	draw_indx_stress_b	2002Oct10
PASS		
211	draw_indx_stress_b_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
0	draw_indx_stress_b_sm_roq	2002Oct10

PASS		
0	draw_indx_stress_b_sm_roq2	2002Oct10
PASS		
0	draw_indx_stress_c	2002Oct10
PASS		
215	draw_indx_stress_c_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
0	event_timestamp_write	2002Oct10
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct10
PASS		
0	fix2flt_reg	2002Oct10
PASS		
0	ft_indr1_july22	2002Oct10
PASS		
0	ft_indr2_july22	2002Oct10
PASS		
0	ft_july22	2002Oct10
PASS		
0	ft_july22_random	2002Oct10
PASS		
0	header_dump	2002Oct10
PASS		
0	header_dump_rt	2002Oct10
PASS		
0	header_dump_small	2002Oct10
PASS		
226	hostdata_blt	2002Oct10
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
227	im_ld_cid_mismatch_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
228	im_ld_invalidate_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
229	im_ld_match2_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
230	im_ld_match_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
231	im_ld_sz_mismatch_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
0	im_load_a	2002Oct10
PASS		
0	im_load_b	2002Oct10
PASS		
0	im_load_cid_mismatch	2002Oct10
PASS		
0	im_load_immediate	2002Oct10
PASS		
0	im_load_immediate_nrt_me	2002Oct10

PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct10
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct10
PASS		
239	im_load_immediate_nrt_me_ps_wrap	2002Oct10
FAIL	mh_cp_rd.cmp	
240	im_load_immediate_nrt_me_vs_nowrap	2002Oct10
FAIL	mh_cp_rd.cmp	
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct10
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct10
PASS		
243	im_load_immediate_nrt_me_vs_wrap	2002Oct10
FAIL	mh_cp_rd.cmp	
0	im_load_indr1_a	2002Oct10
PASS		
0	im_load_indr1_b	2002Oct10
PASS		
0	im_load_indr2_a	2002Oct10
PASS		
0	im_load_indr2_b	2002Oct10
PASS		
0	im_load_invalidate	2002Oct10
PASS		
249	im_load_invalidate_sm_roq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	im_load_match	2002Oct10
PASS		
0	im_load_match2	2002Oct10
PASS		
0	im_load_nrt_me	2002Oct10
PASS		
253	im_load_nrt_me_ps_nowrap	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
254	im_load_nrt_me_ps_wrap	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
255	im_load_nrt_me_vs_nowrap	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
256	im_load_nrt_me_vs_wrap	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_pfmatch_disable	2002Oct10
PASS		
258	im_load_random_a	2002Oct10
FAIL	mh_cp_rd.cmp	
259	im_load_random_b	2002Oct10
FAIL	mh_cp_rd.cmp	
260	im_load_random_c	2002Oct10


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**FAIL**      mh_cp_rd.cmp
0  im_load_ring_a      2002Oct10
PASS
0  im_load_ring_b      2002Oct10
PASS
0  im_load_rt          2002Oct10
PASS
0  im_load_rt2         2002Oct10
PASS
0  im_load_rt3         2002Oct10
PASS
0  im_load_rt4         2002Oct10
PASS
0  im_load_sz_mismatch 2002Oct10
PASS
268 incr_update_cnst_random 2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
269 incr_update_const_sm_meq 2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
270 incr_update_instr_sm_meq 2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
271 incr_update_state_sm_meq 2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_const 2002Oct10
PASS
0  incremental_update_instr 2002Oct10
PASS
0  incremental_update_state 2002Oct10
PASS
0  incremental_update_state_sm_roq 2002Oct10
PASS
0  indirect_buffer_a     2002Oct10
PASS
0  indirect_buffer_b     2002Oct10
PASS
0  indirect_buffer_c     2002Oct10
PASS
0  indirect_buffer_d     2002Oct10
PASS
280 indirect_buffer_e     2002Oct10
**FAIL**      rbbmWr.cmp
0  indirect_buffer_f     2002Oct10
PASS
0  indirect_buffer_f_sm_roq 2002Oct10
PASS
0  indirect_buffer_g     2002Oct10
PASS
0  indirect_buffer_h     2002Oct10

```

PASS		
0	indirect_buffer_i	2002Oct10
PASS		
0	indirect_buffer_j	2002Oct10
PASS		
0	indirect_buffer_k	2002Oct10
PASS		
0	indirect_buffer_l	2002Oct10
PASS		
0	indirect_buffer_mp_a	2002Oct10
PASS		
0	indirect_buffer_mp_b	2002Oct10
PASS		
0	indirect_buffer_mp_c	2002Oct10
PASS		
0	indirect_buffer_mp_d	2002Oct10
PASS		
0	indirect_buffer_mp_e	2002Oct10
PASS		
0	indirect_buffer_mp_f	2002Oct10
PASS		
0	indirect_buffer_mp_f_sm_roq	2002Oct10
PASS		
296	indirect_buffer_mp_random1	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
297	indirect_buffer_mp_random2	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
298	indirect_buffer_random1	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
299	indirect_buffer_random2	2002Oct10
FAIL	mh_cp_rd.cmp	
300	indr_buf_e_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
301	indr_buf_k_sm_meq	2002Oct10
FAIL	mh_cp_rd.cmp	
302	indr_buf_mp_c_sm_meq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
303	indr_buf_mp_f_sm_meq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalid_opcode	2002Oct10
PASS		
0	invalidate_on_me_init	2002Oct10
PASS		
306	invalidate_on_me_init_random	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
307	invalidate_on_me_init_sm_meq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalidate_state_constants	2002Oct10

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PASS
0   invalidate_state_shaders                2002Oct10
PASS
0   invalidate_state_shaders2              2002Oct10
PASS
311 invalidate_state_shaders_random        2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
312 invalidate_state_shaders_sm_meq        2002Oct10
**FAIL**      mh_cp_rd.cmp
0   invalidate_state_sublks                2002Oct10
PASS
0   invalidate_state_sublks2              2002Oct10
PASS
315 invalidate_state_sublks2_sm_meq        2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
316 invalidate_state_sublks_random        2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   invalidate_state_sublks_sm_roq        2002Oct10
PASS
0   iq1_iq2_mult                          2002Oct10
PASS
0   iq1_iq2_push_wrap                    2002Oct10
PASS
0   iq2_csq_full                         2002Oct10
PASS
321 lcc_coherency_cntrl_sm_meq            2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
322 lcc_coherency_cntr2_sm_meq            2002Oct10
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
323 lcc_coherency_cntr_random             2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
324 lcc_match_random                     2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
325 lcc_match_sm_meq                     2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
326 lcc_match_valid_preserved_sm_meq      2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
327 lcc_mismatch_alutex_random            2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
328 lcc_mismatch_alutex_sm_meq            2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
329 lcc_mismatch_base_random              2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
330 lcc_mismatch_base_sm_meq              2002Oct10
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   lcc_rt1                              2002Oct10
PASS
0   lcc_rt2                              2002Oct10

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PASS		
0	lcc_rt3	2002Oct10
PASS		
0	lcc_rt4	2002Oct10
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct10
PASS		
0	ld_cnst_cntx_coherency_cntrl_sm_roq	2002Oct10
PASS		
0	ld_cnst_cntx_coherency_cntrl2	2002Oct10
PASS		
0	ld_cnst_cntx_coherency_cntrl2_sm_roq	2002Oct10
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct10
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct10
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct10
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct10
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct10
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct10
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct10
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct10
PASS		
0	ld_cnst_cntx_match	2002Oct10
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct10
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct10
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct10
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct10
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct10
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct10
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct10
PASS		
0	ld_cnst_cntx_replicate8	2002Oct10
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct10

PASS		
0	ld_cnst_cntx_replicate_tex	2002Oct10
PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct10
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct10
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct10
PASS		
0	ld_cnst_cntx_ring_simple	2002Oct10
PASS		
0	ld_cnst_cntx_simple_alu	2002Oct10
PASS		
0	ld_cnst_cntx_simple_tex	2002Oct10
PASS		
0	load_constant_context_rt	2002Oct10
PASS		
0	load_palette	2002Oct10
PASS		
366	lp_2d3d	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
367	me_reg_wr_rd	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	2002Oct10
PASS		
0	mem_write_rt	2002Oct10
PASS		
0	mpeg_index	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_0	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_1	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_2	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_3	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_4	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_6	2002Oct10
PASS		
0	mpeg_index_multi_context_max_context_7	2002Oct10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Oct10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct10

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct10
PASS		
0	nop_eating_disabled	2002Oct10
PASS		
0	nrt_rt_interaction1	2002Oct10
PASS		
388	paint_multi	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
389	paint_packet	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
390	paint_packet_gui_control	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
391	pfpr_reg_reads1	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
392	pfpr_reg_reads2	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
393	pitch_clip	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
394	ply_nextscan	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_NQWU_BIF_01	2002Oct10
PASS		
0	rbbm_NQWU_BIF_02	2002Oct10
PASS		
397	rbbm_NQWU_CF_01	2002Oct10
FAIL		
398	rbbm_NQWU_CPnq_01	2002Oct10
FAIL		
0	rbbm_WU_2D_idle_01	2002Oct10
PASS		
0	rbbm_WU_2D_idle_02	2002Oct10
PASS		
0	rbbm_WU_2D_idle_03	2002Oct10
PASS		
0	rbbm_WU_2D_idle_04	2002Oct10
PASS		
403	rbbm_WU_2D_idle_05	2002Oct10
FAIL	rbbmWr.cmp	
0	rbbm_WU_2D_idle_06	2002Oct10

PASS		
0	rbbm_WU_2D_idle_07	2002Oct10
PASS		
0	rbbm_WU_2D_idle_08	2002Oct10
PASS		
0	rbbm_WU_2D_idle_09	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct10
PASS		
412	rbbm_WU_2D_idle_clean_05	2002Oct10
FAIL	rbbmWr.cmp	
0	rbbm_WU_2D_idle_clean_06	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct10
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct10
PASS		
419	rbbm_WU_2D_idle_clean_12	2002Oct10
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_01	2002Oct10
PASS		
0	rbbm_WU_3D_idle_02	2002Oct10
PASS		
0	rbbm_WU_3D_idle_03	2002Oct10
PASS		
0	rbbm_WU_3D_idle_04	2002Oct10
PASS		
424	rbbm_WU_3D_idle_05	2002Oct10
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_06	2002Oct10
PASS		
0	rbbm_WU_3D_idle_07	2002Oct10
PASS		
0	rbbm_WU_3D_idle_08	2002Oct10
PASS		
0	rbbm_WU_3D_idle_09	2002Oct10

PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct10
PASS		
433	rbbm_WU_3D_idle_clean_05	2002Oct10
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_clean_06	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct10
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct10
PASS		
440	rbbm_WU_3D_idle_clean_12	2002Oct10
FAIL	rbbmWr.cmp	
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct10
PASS		
0	rbbm_WU_BOTH_GRPX_update_pending_off	2002Oct10
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct10
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct10
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct10
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct10
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct10
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct10
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct10
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPX_update_pending_off	2002Oct10
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPX_update_pending_off	2002Oct10
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct10

PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct10
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct10
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct10
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct10
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct10
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct10
PASS		
459	rbbm_WU_cmdfifo_entries	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_WU_idct_semaphore	2002Oct10
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct10
PASS		
462	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct10
FAIL	cp_rbbm.cmp	
463	rbbm_WUrt_BOTH_GRP update_pending_off	2002Oct10
FAIL	cp_rbbm.cmp	
464	rbbm_WUrt_CP_dma_idle	2002Oct10
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
465	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct10
FAIL	cp_rbbm.cmp	
466	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct10
FAIL	cp_rbbm.cmp	
467	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct10
FAIL	cp_rbbm.cmp	
468	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct10
FAIL	cp_rbbm.cmp	
469	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct10
FAIL	cp_rbbm.cmp	
470	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct10
FAIL	cp_rbbm.cmp	
471	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct10
FAIL	cp_rbbm.cmp	
472	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct10
FAIL	cp_rbbm.cmp	
473	rbbm_WUrt_MH_hdp_clean	2002Oct10
FAIL	cp_rbbm.cmp	
474	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct10
FAIL	cp_rbbm.cmp	
475	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct10
FAIL	cp_rbbm.cmp	
0	rbbm_WUrt_RT_idle_01	2002Oct10

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PASS
0   rbbm_WUrt_RT_idle_02                2002Oct10
PASS
0   rbbm_WUrt_RT_idle_03                2002Oct10
PASS
0   rbbm_WUrt_RT_idle_04                2002Oct10
PASS
480 rbbm_WUrt_RT_idle_clean_01          2002Oct10
**FAIL**          cp_rbbm.cmp
481 rbbm_WUrt_RT_idle_clean_02          2002Oct10
**FAIL**          cp_rbbm.cmp
482 rbbm_WUrt_RT_idle_clean_03          2002Oct10
**FAIL**          cp_rbbm.cmp
483 rbbm_WUrt_RT_idle_clean_04          2002Oct10
**FAIL**          cp_rbbm.cmp
0   rbbm_WUrt_RT_idle_clean_05          2002Oct10
PASS
485 rbbm_WUrt_VIPH0_dma_idle            2002Oct10
**FAIL**          cp_rbbm.cmp
486 rbbm_WUrt_VIPH1_dma_idle            2002Oct10
**FAIL**          cp_rbbm.cmp
487 rbbm_WUrt_VIPH2_dma_idle            2002Oct10
**FAIL**          cp_rbbm.cmp
488 rbbm_WUrt_VIPH3_dma_idle            2002Oct10
**FAIL**          cp_rbbm.cmp
489 rbbm_WUrt_pad_ext_sig_rising_edge   2002Oct10
**FAIL**          cp_rbbm.cmp
0   rbbm_clk_active_deassert_min_max     2002Oct10
PASS
0   rbbm_clk_active_each_pipe            2002Oct10
PASS
0   rbbm_clk_active_for_read_error       2002Oct10
PASS
0   rbbm_clk_active_for_read_request     2002Oct10
PASS
0   rbbm_clk_active_on_reset             2002Oct10
PASS
0   rbbm_cp_non_queued_rdwr_01           2002Oct10
PASS
496 rbbm_cp_non_queued_rdwr_02           2002Oct10
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
0   rbbm_cp_queued_holds_nonqueued       2002Oct10
PASS
498 rbbm_cp_queued_rdwr                  2002Oct10
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
0   rbbm_hi_data_and_be_swap             2002Oct10
PASS
0   rbbm_hi_data_endian_swap             2002Oct10

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PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Oct10
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Oct10
PASS		
0	rbbm_int_ack	2002Oct10
PASS		
504	rbbm_int_each_client	2002Oct10
FAIL		
0	rbbm_int_nrt_gfxpipe_idle	2002Oct10
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Oct10
PASS		
0	rbbm_int_rderr	2002Oct10
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Oct10
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Oct10
PASS		
0	rbbm_isync_wait_idlegui	2002Oct10
PASS		
0	rbbm_soft_reset	2002Oct10
PASS		
0	rbbm_vgt_default_req	2002Oct10
PASS		
513	rbbm_vgt_max_req	2002Oct10
FAIL		
514	rbbm_vgt_min_req	2002Oct10
FAIL		
0	reg_to_mem	2002Oct10
PASS		
0	reg_to_mem_rt	2002Oct10
PASS		
0	ring_il_pio_i2_bm	2002Oct10
PASS		
518	ring_pio_ibs_bm_stress	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
519	rop_cfd_wmd	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
520	roq_reg_reads	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
521	rstr_ordr	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rt_invalid_opcode	2002Oct10
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct10
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct10

PASS		
0	rtee_discretes_falling_00_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct10

PASS		
0	rtee_discretes_falling_24_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct10
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_09_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_11_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct10

PASS		
0	rtee_discretes_invert_16_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct10
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct10
PASS		
0	rtee_discretes_level_00_events_16	2002Oct10
PASS		
0	rtee_discretes_level_01_events_16	2002Oct10
PASS		
0	rtee_discretes_level_02_events_16	2002Oct10
PASS		
0	rtee_discretes_level_03_events_16	2002Oct10
PASS		
0	rtee_discretes_level_04_events_16	2002Oct10
PASS		
0	rtee_discretes_level_05_events_16	2002Oct10
PASS		
0	rtee_discretes_level_06_events_16	2002Oct10
PASS		
0	rtee_discretes_level_07_events_16	2002Oct10

PASS		
0	rtee_discretes_level_08_events_16	2002Oct10
PASS		
0	rtee_discretes_level_09_events_16	2002Oct10
PASS		
0	rtee_discretes_level_10_events_16	2002Oct10
PASS		
0	rtee_discretes_level_11_events_16	2002Oct10
PASS		
0	rtee_discretes_level_12_events_16	2002Oct10
PASS		
0	rtee_discretes_level_13_events_16	2002Oct10
PASS		
0	rtee_discretes_level_14_events_16	2002Oct10
PASS		
0	rtee_discretes_level_15_events_16	2002Oct10
PASS		
0	rtee_discretes_level_16_events_16	2002Oct10
PASS		
0	rtee_discretes_level_17_events_16	2002Oct10
PASS		
0	rtee_discretes_level_18_events_16	2002Oct10
PASS		
0	rtee_discretes_level_19_events_16	2002Oct10
PASS		
0	rtee_discretes_level_20_events_16	2002Oct10
PASS		
0	rtee_discretes_level_21_events_16	2002Oct10
PASS		
0	rtee_discretes_level_22_events_16	2002Oct10
PASS		
0	rtee_discretes_level_23_events_16	2002Oct10
PASS		
0	rtee_discretes_level_24_events_16	2002Oct10
PASS		
0	rtee_discretes_level_25_events_16	2002Oct10
PASS		
0	rtee_discretes_level_26_events_16	2002Oct10
PASS		
0	rtee_discretes_level_27_events_16	2002Oct10
PASS		
0	rtee_discretes_level_28_events_16	2002Oct10
PASS		
0	rtee_discretes_level_29_events_16	2002Oct10
PASS		
0	rtee_discretes_level_30_events_16	2002Oct10
PASS		
0	rtee_discretes_level_31_events_16	2002Oct10

PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_16_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct10

PASS		
0	rtee_discretes_rising_23_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct10
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct10
PASS		
0	rtee_display1_comp_events_16	2002Oct10
PASS		
0	rtee_display2_comp_events_16	2002Oct10
PASS		
0	rtee_mem_linear_search_events_2	2002Oct10
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct10
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct10
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct10
PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct10

```

PASS
0   rtee_mem_poll_short_intervals_events_16           2002Oct10
PASS
0   rtee_pio                                           2002Oct10
PASS
0   rtee_prefetch_parse_nops                          2002Oct10
PASS
0   rtee_reg_linear_search_events_2                   2002Oct10
PASS
0   rtee_reg_poll_comp_000_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_001_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_010_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_011_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_100_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_101_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_110_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_111_events_16                  2002Oct10
PASS
0   rtee_reg_poll_comp_queued_nonqueued_events_16    2002Oct10
PASS
0   rtee_vip_comp_events_16                           2002Oct10
PASS
0   scratch_mem_read                                  2002Oct10
PASS
0   scratch_mem_read2                                 2002Oct10
PASS
685 set_cnst2_sm_meq                                  2002Oct10
**FAIL**      mh_cp_rd.cmp
686 set_cnst_alu_max1_sm_meq                          2002Oct10
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
687 set_cnst_alu_max_random                           2002Oct10
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   set_cnst_bool_max2_sm_meq                          2002Oct10
PASS
689 set_cnst_loop_max2_sm_meq                         2002Oct10
**FAIL**      mh_cp_rd.cmp
690 set_cnst_sm_meq                                   2002Oct10
**FAIL**      mh_cp_rd.cmp
691 set_cnst_tex_max2_sm_meq                          2002Oct10
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
692 set_cnst_we_lcc_random                            2002Oct10

```

```

**FAIL**      mh_cp_rd.cmp
693 set_cnst_we_random                2002Oct10
**FAIL**      mh_cp_rd.cmp
694 set_cnst_write_enable_lcc2_sm_meq 2002Oct10
**FAIL**      mh_cp_rd.cmp
695 set_cnst_write_enable_sm_meq      2002Oct10
**FAIL**      mh_cp_rd.cmp
0 set_constant                        2002Oct10
PASS
0 set_constant2                       2002Oct10
PASS
698 set_constant_alu_max1             2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
699 set_constant_alu_max2             2002Oct10
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0 set_constant_bool_max1              2002Oct10
PASS
0 set_constant_bool_max2              2002Oct10
PASS
0 set_constant_indr1_short            2002Oct10
PASS
0 set_constant_indr1_short2           2002Oct10
PASS
0 set_constant_indr1_write_enable     2002Oct10
PASS
0 set_constant_indr1_write_enable_lcc 2002Oct10
PASS
0 set_constant_indr2_short            2002Oct10
PASS
0 set_constant_indr2_short2           2002Oct10
PASS
0 set_constant_indr2_write_enable     2002Oct10
PASS
0 set_constant_indr2_write_enable_lcc 2002Oct10
PASS
0 set_constant_loop_max1              2002Oct10
PASS
0 set_constant_loop_max2              2002Oct10
PASS
0 set_constant_ring_short             2002Oct10
PASS
0 set_constant_ring_short2            2002Oct10
PASS
0 set_constant_ring_write_enable      2002Oct10
PASS
0 set_constant_ring_write_enable_lcc  2002Oct10
PASS
0 set_constant_short                  2002Oct10

```

PASS		
0	set_constant_short2	2002Oct10
PASS		
718	set_constant_tex_max1	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
719	set_constant_tex_max2	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
720	set_constant_tex_max2_sm_roq	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	set_constant_write_enable	2002Oct10
PASS		
0	set_constant_write_enable_lcc	2002Oct10
PASS		
0	set_constant_write_enable_lcc2	2002Oct10
PASS		
0	set_constant_write_enable_lcc2_sm_roq	2002Oct10
PASS		
0	set_scissors	2002Oct10
PASS		
0	set_st_indrl_match	2002Oct10
PASS		
0	set_st_indrl_mismatch	2002Oct10
PASS		
0	set_st_indrl_mismatch2	2002Oct10
PASS		
0	set_st_indrl_mismatch3	2002Oct10
PASS		
0	set_st_indrl_mismatch4	2002Oct10
PASS		
0	set_st_indrl_ps	2002Oct10
PASS		
0	set_st_indrl_ps_only	2002Oct10
PASS		
0	set_st_indrl_sublk0	2002Oct10
PASS		
0	set_st_indrl_sublk1	2002Oct10
PASS		
0	set_st_indrl_sublk1_only	2002Oct10
PASS		
0	set_st_indrl_sublk2	2002Oct10
PASS		
0	set_st_indrl_sublk2_only	2002Oct10
PASS		
0	set_st_indrl_sublk3	2002Oct10
PASS		
0	set_st_indrl_sublk3_only	2002Oct10
PASS		
0	set_st_indrl_sublk4	2002Oct10

PASS		
0	set_st_indr1_sublk4_only	2002Oct10
PASS		
0	set_st_indr1_sublk5	2002Oct10
PASS		
0	set_st_indr1_sublk5_only	2002Oct10
PASS		
0	set_st_indr1_sublk6	2002Oct10
PASS		
0	set_st_indr1_sublk6_only	2002Oct10
PASS		
0	set_st_indr1_sublk7	2002Oct10
PASS		
0	set_st_indr1_sublk7_only	2002Oct10
PASS		
0	set_st_indr1_vs	2002Oct10
PASS		
0	set_st_indr1_vs_only	2002Oct10
PASS		
0	set_st_indr2_match	2002Oct10
PASS		
0	set_st_indr2_mismatch	2002Oct10
PASS		
0	set_st_indr2_mismatch2	2002Oct10
PASS		
0	set_st_indr2_mismatch3	2002Oct10
PASS		
0	set_st_indr2_mismatch4	2002Oct10
PASS		
0	set_st_indr2_ps	2002Oct10
PASS		
0	set_st_indr2_ps_only	2002Oct10
PASS		
0	set_st_indr2_sublk0	2002Oct10
PASS		
0	set_st_indr2_sublk1	2002Oct10
PASS		
0	set_st_indr2_sublk1_only	2002Oct10
PASS		
0	set_st_indr2_sublk2	2002Oct10
PASS		
0	set_st_indr2_sublk2_only	2002Oct10
PASS		
0	set_st_indr2_sublk3	2002Oct10
PASS		
0	set_st_indr2_sublk3_only	2002Oct10
PASS		
0	set_st_indr2_sublk4	2002Oct10

PASS		
0	set_st_indr2_sublk4_only	2002Oct10
PASS		
0	set_st_indr2_sublk5	2002Oct10
PASS		
0	set_st_indr2_sublk5_only	2002Oct10
PASS		
0	set_st_indr2_sublk6	2002Oct10
PASS		
0	set_st_indr2_sublk6_only	2002Oct10
PASS		
0	set_st_indr2_sublk7	2002Oct10
PASS		
0	set_st_indr2_sublk7_only	2002Oct10
PASS		
0	set_st_indr2_vs	2002Oct10
PASS		
0	set_st_indr2_vs_only	2002Oct10
PASS		
0	set_st_pfmatch_disabled	2002Oct10
PASS		
0	set_st_ring_match	2002Oct10
PASS		
0	set_st_ring_mismatch	2002Oct10
PASS		
0	set_st_ring_mismatch2	2002Oct10
PASS		
0	set_st_ring_mismatch3	2002Oct10
PASS		
0	set_st_ring_mismatch4	2002Oct10
PASS		
0	set_st_ring_ps	2002Oct10
PASS		
0	set_st_ring_ps_only	2002Oct10
PASS		
0	set_st_ring_sublk0	2002Oct10
PASS		
0	set_st_ring_sublk1	2002Oct10
PASS		
0	set_st_ring_sublk1_only	2002Oct10
PASS		
0	set_st_ring_sublk2	2002Oct10
PASS		
0	set_st_ring_sublk2_only	2002Oct10
PASS		
0	set_st_ring_sublk3	2002Oct10
PASS		
0	set_st_ring_sublk3_only	2002Oct10

PASS		
0	set_st_ring_sublk4	2002Oct10
PASS		
0	set_st_ring_sublk4_only	2002Oct10
PASS		
0	set_st_ring_sublk5	2002Oct10
PASS		
0	set_st_ring_sublk5_only	2002Oct10
PASS		
0	set_st_ring_sublk6	2002Oct10
PASS		
0	set_st_ring_sublk6_only	2002Oct10
PASS		
0	set_st_ring_sublk7	2002Oct10
PASS		
0	set_st_ring_sublk7_only	2002Oct10
PASS		
0	set_st_ring_vs	2002Oct10
PASS		
0	set_st_ring_vs_only	2002Oct10
PASS		
0	set_st_stress_a	2002Oct10
PASS		
800	set_st_stress_b	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
801	set_st_stress_b_random	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
802	set_st_stress_b_sm_meq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Oct10
PASS		
0	set_st_stress_c_sm_roq	2002Oct10
PASS		
0	set_st_stress_d	2002Oct10
PASS		
806	set_st_stress_d_random	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
807	set_st_stress_d_sm_meq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_e	2002Oct10
PASS		
809	set_st_stress_e_sm_meq	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_f	2002Oct10
PASS		
811	set_st_stress_f_random	2002Oct10
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_state_rt_a	2002Oct10

PASS		
0	set_state_rt_b	2002Oct10
PASS		
0	set_state_rt_c	2002Oct10
PASS		
0	set_state_rt_d	2002Oct10
PASS		
0	smoke_nrt	2002Oct10
PASS		
817	src_clip	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
818	srf_cohr	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
819	switch_pio_bm	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
820	trans_bitblt	2002Oct10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	type0_2long_pckts	2002Oct10
PASS		
0	type0_3indr_buffs	2002Oct10
PASS		
0	type0_4pckts	2002Oct10
PASS		
0	type0_incr_updt_cnst	2002Oct10
PASS		
0	type0_incr_updt_instrmem	2002Oct10
PASS		
0	type0_incr_updt_st	2002Oct10
PASS		
0	type0_incr_updt_stress	2002Oct10
PASS		
0	type0_ind1_ind2_a	2002Oct10
PASS		
0	type0_ind1_ind2_b	2002Oct10
PASS		
0	type0_indr_buff	2002Oct10
PASS		
0	type1_4pckts	2002Oct10
PASS		
0	type1_indr_buff	2002Oct10
PASS		
0	type1_pckt	2002Oct10
PASS		
834	type_random	2002Oct10
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Oct10
PASS		
0	uram_wrt_ucode	2002Oct10


```

PASS
0 viz_query_a 2002Oct10
PASS
0 viz_query_draw_indx 2002Oct10
PASS
0 viz_query_draw_indx_indr1 2002Oct10
PASS
0 viz_query_draw_indx_indr2 2002Oct10
PASS
0 viz_query_draw_indx_multipass 2002Oct10
PASS
0 viz_query_draw_indx_ring 2002Oct10
PASS
0 viz_query_indr1 2002Oct10
PASS
0 viz_query_indr1_simple 2002Oct10
PASS
0 viz_query_indr2 2002Oct10
PASS
0 viz_query_indr2_simple 2002Oct10
PASS
0 viz_query_ring 2002Oct10
PASS
0 viz_query_ring_simple 2002Oct10
PASS
0 wait_for_idle 2002Oct10
PASS
0 wait_reg_mem_poll_mem0 2002Oct10
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct10
PASS
0 wait_reg_mem_poll_mem1 2002Oct10
PASS
0 wait_reg_mem_poll_mem2 2002Oct10
PASS
0 wait_reg_mem_poll_mem3 2002Oct10
PASS
0 wait_reg_mem_poll_reg0 2002Oct10
PASS
0 wait_reg_mem_poll_reg1 2002Oct10
PASS
0 wait_reg_mem_poll_reg2 2002Oct10
PASS
0 wait_reg_mem_poll_reg3 2002Oct10
PASS

```

```

=====
Tests_Run_count = 858 Pass_count = 698 Fail_count = 160 Incomplete_count = 0
=====

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Oct  8 23:42:26 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          683         683         617         90
              RBBM        120         120          84          70
+++++
              TOTAL        803         803         701         87
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Aug 27  3:32:06 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          625        625        573         91
              RBBM        92         92         92        100
+++++
              TOTAL        717        717        665         92
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep 23  2:22:08 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          641         641         607         94
              RBBM        109         109         107         98
+++++
              TOTAL        750         750         714         95
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Aug 31  5:26:43 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          628         628         619         98
              RBBM        92          92          37          40
+++++
              TOTAL        720         720         656         91
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sun Sep 22  2:52:56 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             641         641         607         94
              RBBM           109         109         107         98
+++++
              TOTAL           750         750         714         95
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep 25  2:09:28 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          645        645        625        96
              RBBM        109        109        107        98
+++++
              TOTAL        754        754        732        97
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Aug 22 23:34:26 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             610         610         602         98
              RBBM           82          82          1          1
+++++
              TOTAL           692         692         603         87
+++++

```



```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Fri Aug 16  5:29:34 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC                0          0          0          0
              CL                0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU                0          0          0          0
              CP             571        571        549         96
              RBBM           21         21         21        100
+++++
              TOTAL           592        592        570         96
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct7
	PASS		
0	2d_indr_buffer_b		2002Oct7
	PASS		
0	2d_indr_buffer_c		2002Oct7
	PASS		
0	2d_indr_buffer_d		2002Oct7
	PASS		
0	2d_indr_buffer_e		2002Oct7
	PASS		
0	2d_indr_buffer_f		2002Oct7
	PASS		
0	2d_indr_buffer_g		2002Oct7
	PASS		
0	2d_indr_buffer_random		2002Oct7
	PASS		
0	2d_indr_buffer_random2		2002Oct7
	PASS		
0	3d_draw_indx_2		2002Oct7
	PASS		
0	3d_draw_indx_2_b		2002Oct7
	PASS		
0	IQ2_Mult		2002Oct7
	PASS		
0	IQ2_Wrap_Legacy		2002Oct7
	PASS		
0	IQ_Wrap		2002Oct7
	PASS		
0	Isync_Control		2002Oct7
	PASS		
0	Isync_Control_2D_3D		2002Oct7
	PASS		
0	Isync_Control_3D_2D		2002Oct7
	PASS		
0	RB_BUFSZ_0		2002Oct7
	PASS		
0	RB_BUFSZ_1		2002Oct7
	PASS		
0	RB_Wrap		2002Oct7

PASS		
0	Small_Text_BM	2002Oct7
PASS		
0	Type0_IQ2_BM	2002Oct7
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Oct7
PASS		
0	Type0_IQ_BM	2002Oct7
PASS		
0	Type0_IQ_BM_IB_Packet	2002Oct7
PASS		
0	Type0_PQ_BM	2002Oct7
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Oct7
PASS		
0	Type3_Bitblt	2002Oct7
PASS		
29	Type3_Hostdata_blt	2002Oct7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
30	Type3_Hostdata_blt_PIO	2002Oct7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	Type3_Load_Palette	2002Oct7
PASS		
0	Type3_NOP	2002Oct7
PASS		
0	Type3_Polyline	2002Oct7
PASS		
0	Type3_Polyline_Large	2002Oct7
PASS		
0	bitblt_multi	2002Oct7
PASS		
0	brush_32x1mono	2002Oct7
PASS		
0	brush_8x8color_16	2002Oct7
PASS		
0	brush_8x8color_32	2002Oct7
PASS		
0	brush_8x8color_64	2002Oct7
PASS		
0	brush_8x8color_dst_0_1	2002Oct7
PASS		
0	brush_8x8mono	2002Oct7
PASS		
0	brush_color_mono_stress	2002Oct7
PASS		
0	brush_flag	2002Oct7
PASS		
0	brush_type0	2002Oct7

PASS		
0	brush_type1	20020ct7
PASS		
0	brush_type10	20020ct7
PASS		
0	brush_type13	20020ct7
PASS		
0	brush_type14	20020ct7
PASS		
0	brush_type6	20020ct7
PASS		
0	brush_type7	20020ct7
PASS		
0	brush_type_other	20020ct7
PASS		
0	brush_types_13_14	20020ct7
PASS		
0	change_queue_sizes	20020ct7
PASS		
0	clear_const_wr_en_on_me_init	20020ct7
PASS		
0	clear_const_wr_en_with_lcc	20020ct7
PASS		
0	cond_write_mem_poll_mem	20020ct7
PASS		
0	cond_write_mem_poll_mem2	20020ct7
PASS		
0	cond_write_mem_poll_mem3	20020ct7
PASS		
0	cond_write_mem_poll_mem_rt	20020ct7
PASS		
0	cond_write_mem_poll_reg	20020ct7
PASS		
0	cond_write_mem_poll_reg2	20020ct7
PASS		
0	cond_write_mem_poll_reg3	20020ct7
PASS		
0	cp_acpi_recovery_soft_reset	20020ct7
PASS		
0	cp_bios_scratch	20020ct7
PASS		
65	cp_high_stress	20020ct7
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_ib2_base000_bufszXXX	20020ct7
PASS		
0	cp_ib2_base001_bufszXXX	20020ct7
PASS		
0	cp_ib2_base010_bufszXXX	20020ct7

PASS		
0	cp_ib2_base011_bufszXXX	20020ct7
PASS		
0	cp_ib2_base100_bufszXXX	20020ct7
PASS		
0	cp_ib2_base101_bufszXXX	20020ct7
PASS		
0	cp_ib2_base110_bufszXXX	20020ct7
PASS		
0	cp_ib2_base111_bufszXXX	20020ct7
PASS		
0	cp_ib_base000_bufszXXX	20020ct7
PASS		
0	cp_ib_base001_bufszXXX	20020ct7
PASS		
0	cp_ib_base010_bufszXXX	20020ct7
PASS		
0	cp_ib_base011_bufszXXX	20020ct7
PASS		
0	cp_ib_base100_bufszXXX	20020ct7
PASS		
0	cp_ib_base101_bufszXXX	20020ct7
PASS		
0	cp_ib_base110_bufszXXX	20020ct7
PASS		
0	cp_ib_base111_bufszXXX	20020ct7
PASS		
0	cp_interrupt	20020ct7
PASS		
0	cp_interrupt_rt_events_16	20020ct7
PASS		
0	cp_iq2_alignment_000	20020ct7
PASS		
0	cp_iq2_alignment_001	20020ct7
PASS		
0	cp_iq2_alignment_010	20020ct7
PASS		
0	cp_iq2_alignment_011	20020ct7
PASS		
0	cp_iq2_alignment_100	20020ct7
PASS		
0	cp_iq2_alignment_101	20020ct7
PASS		
0	cp_iq2_alignment_110	20020ct7
PASS		
0	cp_iq2_alignment_111	20020ct7
PASS		
0	cp_iq_alignment_000	20020ct7

PASS		
0	cp_iq_alignment_001	20020ct7
PASS		
0	cp_iq_alignment_010	20020ct7
PASS		
0	cp_iq_alignment_011	20020ct7
PASS		
0	cp_iq_alignment_100	20020ct7
PASS		
0	cp_iq_alignment_101	20020ct7
PASS		
0	cp_iq_alignment_110	20020ct7
PASS		
0	cp_iq_alignment_111	20020ct7
PASS		
0	cp_max_size_packet	20020ct7
PASS		
0	cp_max_size_packet_random	20020ct7
PASS		
0	cp_nrt_rt_protection_mode	20020ct7
PASS		
103	cp_output_stress	20020ct7
FAIL	cp_mh_req.cmp	
104	cp_output_stress_random	20020ct7
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_protection_mode	20020ct7
PASS		
0	cp_rb_alignment_000	20020ct7
PASS		
0	cp_rb_alignment_001	20020ct7
PASS		
0	cp_rb_alignment_010	20020ct7
PASS		
0	cp_rb_alignment_011	20020ct7
PASS		
0	cp_rb_alignment_100	20020ct7
PASS		
0	cp_rb_alignment_101	20020ct7
PASS		
0	cp_rb_alignment_110	20020ct7
PASS		
0	cp_rb_alignment_111	20020ct7
PASS		
0	cp_rb_wptrXXX_base000	20020ct7
PASS		
0	cp_rb_wptrXXX_base001	20020ct7
PASS		
0	cp_rb_wptrXXX_base010	20020ct7

PASS		
0	cp_rb_wptrXXX_base011	20020ct7
PASS		
0	cp_rb_wptrXXX_base100	20020ct7
PASS		
0	cp_rb_wptrXXX_base101	20020ct7
PASS		
0	cp_rb_wptrXXX_base110	20020ct7
PASS		
0	cp_rb_wptrXXX_base111	20020ct7
PASS		
0	cp_reg_rw_test	20020ct7
PASS		
0	cp_register_reads	20020ct7
PASS		
0	cp_rt_protection_mode	20020ct7
PASS		
0	cp_scratch	20020ct7
PASS		
0	cp_scratch_multi	20020ct7
PASS		
0	csf_reg_rd_default	20020ct7
PASS		
0	csf_reg_rd_only	20020ct7
PASS		
0	csf_reg_wr_rd	20020ct7
PASS		
0	dma_cp_sync	20020ct7
PASS		
0	dma_dscrstr_chckn_bit	20020ct7
PASS		
0	dma_dscrstr_flush1	20020ct7
PASS		
0	dma_dscrstr_flush2	20020ct7
PASS		
0	dma_dscrstr_flush3	20020ct7
PASS		
0	dma_m2m_src00_dstXX	20020ct7
PASS		
0	dma_m2m_src01_dstXX	20020ct7
PASS		
0	dma_m2m_src10_dstXX	20020ct7
PASS		
0	dma_m2m_src11_dstXX	20020ct7
PASS		
0	dma_mem2mem	20020ct7
PASS		
0	dma_mem2mem_abort	20020ct7

PASS		
0	dma_mem2mem_alignments	20020ct7
PASS		
0	dma_mem2mem_byte_alignment0	20020ct7
PASS		
0	dma_mem2mem_byte_alignment1	20020ct7
PASS		
0	dma_mem2mem_byte_alignment2	20020ct7
PASS		
0	dma_mem2mem_byte_alignment3	20020ct7
PASS		
146	dma_mem2mem_byte_alignment4	20020ct7
FAIL	cp_mh_req.cmp	
147	dma_mem2mem_byte_alignment5	20020ct7
FAIL	cp_mh_req.cmp	
148	dma_mem2mem_byte_alignment6	20020ct7
FAIL	cp_mh_req.cmp	
149	dma_mem2mem_byte_alignment7	20020ct7
FAIL	cp_mh_req.cmp	
150	dma_mem2mem_large	20020ct7
FAIL	cp_mh_req.cmp	
151	dma_mem2mem_pio_abort	20020ct7
FAIL		
152	dma_mem2mem_random	20020ct7
FAIL	cp_mh_req.cmp	
0	dma_mem2reg	20020ct7
PASS		
0	dma_mem2reg_abort	20020ct7
PASS		
0	dma_mem2reg_dtaq_stress	20020ct7
PASS		
0	dma_mem2reg_dtaq_stress_random	20020ct7
PASS		
0	dma_mem2reg_pio_stressbm	20020ct7
PASS		
0	dma_mem2reg_pio_stresspio	20020ct7
PASS		
0	dma_mem2reg_sync	20020ct7
PASS		
160	dma_r2r_m2m_r2r	20020ct7
FAIL	cp_mh_req.cmp	
0	dma_r2r_m2r_r2r	20020ct7
PASS		
0	dma_r2r_r2m_r2r	20020ct7
PASS		
163	dma_r2r_r2m_r2r_m2r_m2m	20020ct7
FAIL	cp_mh_req.cmp	
164	dma_r2r_r2m_r2r_m2r_m2m_queued	20020ct7


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**FAIL**          cp_mh_req.cmp
165 dma_r2r_r2m_r2r_m2r_m2m_table          20020ct7
**FAIL**          cp_mh_req.cmp
0 dma_reg2mem                                20020ct7
PASS
0 dma_reg2mem_abort                          20020ct7
PASS
0 dma_reg2reg                                20020ct7
PASS
0 dma_reg2reg_2dw                            20020ct7
PASS
0 dma_reg2reg_daic                          20020ct7
PASS
0 dma_reg2reg_mult                          20020ct7
PASS
0 dma_reg2reg_mult2                         20020ct7
PASS
0 dma_reg2reg_mult3                         20020ct7
PASS
0 dma_reg2reg_saic                          20020ct7
PASS
0 dma_reg2reg_sdaic                         20020ct7
PASS
0 dma_reg2reg_sdaic_stress                  20020ct7
PASS
0 dma_reg2reg_stress                        20020ct7
PASS
0 draw_indx_a                               20020ct7
PASS
0 draw_indx_b                               20020ct7
PASS
0 draw_indx_c                               20020ct7
PASS
0 draw_indx_indr1_a                         20020ct7
PASS
0 draw_indx_indr1_b                         20020ct7
PASS
0 draw_indx_indr1_short_a                   20020ct7
PASS
0 draw_indx_indr1_short_b                   20020ct7
PASS
0 draw_indx_indr1_stress                    20020ct7
PASS
0 draw_indx_indr2_a                         20020ct7
PASS
0 draw_indx_indr2_b                         20020ct7
PASS
0 draw_indx_indr2_short_a                   20020ct7

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PASS		
0	draw_indx_indr2_short_b	20020ct7
PASS		
0	draw_indx_indr2_stress	20020ct7
PASS		
0	draw_indx_random_a	20020ct7
PASS		
0	draw_indx_random_b	20020ct7
PASS		
0	draw_indx_random_c	20020ct7
PASS		
0	draw_indx_ring1	20020ct7
PASS		
0	draw_indx_ring2	20020ct7
PASS		
0	draw_indx_ring_short1	20020ct7
PASS		
0	draw_indx_ring_short2	20020ct7
PASS		
0	draw_indx_ring_stress	20020ct7
PASS		
0	draw_indx_short_a	20020ct7
PASS		
0	draw_indx_short_b	20020ct7
PASS		
0	draw_indx_short_c	20020ct7
PASS		
0	draw_indx_stress_a	20020ct7
PASS		
0	draw_indx_stress_b	20020ct7
PASS		
0	draw_indx_stress_c	20020ct7
PASS		
0	event_timestamp_write	20020ct7
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	20020ct7
PASS		
0	fix2flt_reg	20020ct7
PASS		
0	ft_indr1_july22	20020ct7
PASS		
0	ft_indr2_july22	20020ct7
PASS		
0	ft_july22	20020ct7
PASS		
0	ft_july22_random	20020ct7
PASS		
0	header_dump	20020ct7

PASS		
0	header_dump_rt	2002Oct7
PASS		
0	header_dump_small	2002Oct7
PASS		
0	hostdata_blt	2002Oct7
PASS		
0	im_load_a	2002Oct7
PASS		
0	im_load_b	2002Oct7
PASS		
0	im_load_cid_mismatch	2002Oct7
PASS		
0	im_load_immediate	2002Oct7
PASS		
0	im_load_immediate_nrt_me	2002Oct7
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct7
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct7
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct7
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct7
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct7
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct7
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct7
PASS		
0	im_load_indr1_a	2002Oct7
PASS		
0	im_load_indr1_b	2002Oct7
PASS		
0	im_load_indr2_a	2002Oct7
PASS		
0	im_load_indr2_b	2002Oct7
PASS		
0	im_load_invalidate	2002Oct7
PASS		
0	im_load_match	2002Oct7
PASS		
0	im_load_match2	2002Oct7
PASS		
0	im_load_nrt_me	2002Oct7
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct7

PASS		
0	im_load_nrt_me_ps_wrap	20020ct7
PASS		
0	im_load_nrt_me_vs_nowrap	20020ct7
PASS		
0	im_load_nrt_me_vs_wrap	20020ct7
PASS		
0	im_load_pfmatch_disable	20020ct7
PASS		
0	im_load_random_a	20020ct7
PASS		
0	im_load_random_b	20020ct7
PASS		
243	im_load_random_c	20020ct7
FAIL	cp_rbbm.cmp mh_cp_rd.cmp	
0	im_load_ring_a	20020ct7
PASS		
0	im_load_ring_b	20020ct7
PASS		
0	im_load_rt	20020ct7
PASS		
0	im_load_rt2	20020ct7
PASS		
0	im_load_rt3	20020ct7
PASS		
0	im_load_rt4	20020ct7
PASS		
0	im_load_sz_mismatch	20020ct7
PASS		
0	incremental_update_const	20020ct7
PASS		
0	incremental_update_instr	20020ct7
PASS		
0	incremental_update_state	20020ct7
PASS		
0	indirect_buffer_a	20020ct7
PASS		
0	indirect_buffer_b	20020ct7
PASS		
0	indirect_buffer_c	20020ct7
PASS		
0	indirect_buffer_d	20020ct7
PASS		
0	indirect_buffer_e	20020ct7
PASS		
0	indirect_buffer_f	20020ct7
PASS		
0	indirect_buffer_g	20020ct7

PASS		
0	indirect_buffer_h	20020ct7
PASS		
0	indirect_buffer_i	20020ct7
PASS		
0	indirect_buffer_j	20020ct7
PASS		
0	indirect_buffer_k	20020ct7
PASS		
0	indirect_buffer_l	20020ct7
PASS		
0	indirect_buffer_mp_a	20020ct7
PASS		
0	indirect_buffer_mp_b	20020ct7
PASS		
0	indirect_buffer_mp_c	20020ct7
PASS		
0	indirect_buffer_mp_d	20020ct7
PASS		
0	indirect_buffer_mp_e	20020ct7
PASS		
0	indirect_buffer_mp_f	20020ct7
PASS		
0	indirect_buffer_mp_random1	20020ct7
PASS		
0	indirect_buffer_mp_random2	20020ct7
PASS		
0	indirect_buffer_random1	20020ct7
PASS		
0	indirect_buffer_random2	20020ct7
PASS		
0	invalid_opcode	20020ct7
PASS		
0	invalidate_on_me_init	20020ct7
PASS		
0	invalidate_state_constants	20020ct7
PASS		
0	invalidate_state_shaders	20020ct7
PASS		
0	invalidate_state_shaders2	20020ct7
PASS		
0	invalidate_state_sublks	20020ct7
PASS		
0	invalidate_state_sublks2	20020ct7
PASS		
0	iq1_iq2_mult	20020ct7
PASS		
0	iq1_iq2_push_wrap	20020ct7

PASS		
0	iq2_csq_full	2002Oct7
PASS		
286	lcc_coherency_cntr_random	2002Oct7
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Oct7
PASS		
0	lcc_mismatch_alutex_random	2002Oct7
PASS		
0	lcc_mismatch_base_random	2002Oct7
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct7
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Oct7
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct7
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct7
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct7
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct7
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct7
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct7
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct7
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct7
PASS		
0	ld_cnst_cntx_match	2002Oct7
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct7
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct7
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct7
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct7
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct7
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct7
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct7
PASS		
0	ld_cnst_cntx_replicate8	2002Oct7

PASS		
0	ld_cnst_cntx_replicate_alu	20020ct7
PASS		
0	ld_cnst_cntx_replicate_tex	20020ct7
PASS		
0	ld_cnst_cntx_ring_replicate	20020ct7
PASS		
0	ld_cnst_cntx_ring_replicate4	20020ct7
PASS		
0	ld_cnst_cntx_ring_replicate8	20020ct7
PASS		
0	ld_cnst_cntx_ring_simple	20020ct7
PASS		
0	ld_cnst_cntx_simple_alu	20020ct7
PASS		
0	ld_cnst_cntx_simple_tex	20020ct7
PASS		
0	load_constant_context_rt	20020ct7
PASS		
0	load_palette	20020ct7
PASS		
0	lp_2d3d	20020ct7
PASS		
0	me_reg_wr_rd	20020ct7
PASS		
0	mem_write	20020ct7
PASS		
0	mem_write_rt	20020ct7
PASS		
0	mpeg_index	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_5	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_6	20020ct7
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct7

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	20020ct7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	20020ct7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	20020ct7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	20020ct7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	20020ct7
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	20020ct7
PASS		
0	nop_eating_disabled	20020ct7
PASS		
0	nrt_rt_interaction1	20020ct7
PASS		
0	paint_multi	20020ct7
PASS		
0	paint_packet	20020ct7
PASS		
0	paint_packet_gui_control	20020ct7
PASS		
0	pfpr_reg_reads1	20020ct7
PASS		
0	pfpr_reg_reads2	20020ct7
PASS		
0	pitch_clip	20020ct7
PASS		
0	ply_nextscan	20020ct7
PASS		
0	rbbm_NQWU_BIF_01	20020ct7
PASS		
0	rbbm_NQWU_BIF_02	20020ct7
PASS		
0	rbbm_NQWU_CF_01	20020ct7
PASS		
0	rbbm_NQWU_CPnq_01	20020ct7
PASS		
0	rbbm_WU_2D_idle_01	20020ct7
PASS		
0	rbbm_WU_2D_idle_02	20020ct7
PASS		
0	rbbm_WU_2D_idle_03	20020ct7
PASS		
0	rbbm_WU_2D_idle_04	20020ct7
PASS		
0	rbbm_WU_2D_idle_05	20020ct7

PASS		
0	rbbm_WU_2D_idle_06	20020ct7
PASS		
0	rbbm_WU_2D_idle_07	20020ct7
PASS		
0	rbbm_WU_2D_idle_08	20020ct7
PASS		
0	rbbm_WU_2D_idle_09	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_01	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_02	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_03	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_04	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_05	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_06	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_07	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_08	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_09	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_10	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_11	20020ct7
PASS		
0	rbbm_WU_2D_idle_clean_12	20020ct7
PASS		
0	rbbm_WU_3D_idle_01	20020ct7
PASS		
0	rbbm_WU_3D_idle_02	20020ct7
PASS		
0	rbbm_WU_3D_idle_03	20020ct7
PASS		
0	rbbm_WU_3D_idle_04	20020ct7
PASS		
0	rbbm_WU_3D_idle_05	20020ct7
PASS		
0	rbbm_WU_3D_idle_06	20020ct7
PASS		
0	rbbm_WU_3D_idle_07	20020ct7
PASS		
0	rbbm_WU_3D_idle_08	20020ct7

PASS		
0	rbbm_WU_3D_idle_09	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct7
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct7
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct7
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Oct7
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct7
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct7
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct7
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct7
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct7
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct7
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct7
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP update_pending_off	2002Oct7
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP update_pending_off	2002Oct7

PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct7
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct7
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct7
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct7
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct7
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct7
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct7
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct7
PASS		
0	rbbm_WU_idct_semaphore	2002Oct7
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct7
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct7
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Oct7
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Oct7
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Oct7
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct7
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct7
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct7

PASS		
0	rbbm_WUrt_RT_idle_01	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_02	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_03	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_04	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_clean_01	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_clean_02	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_clean_03	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_clean_04	20020ct7
PASS		
0	rbbm_WUrt_RT_idle_clean_05	20020ct7
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	20020ct7
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	20020ct7
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	20020ct7
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	20020ct7
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	20020ct7
PASS		
0	rbbm_clk_active_deassert_min_max	20020ct7
PASS		
0	rbbm_clk_active_each_pipe	20020ct7
PASS		
0	rbbm_clk_active_for_read_error	20020ct7
PASS		
0	rbbm_clk_active_for_read_request	20020ct7
PASS		
0	rbbm_clk_active_on_reset	20020ct7
PASS		
0	rbbm_cp_non_queued_rdwr_01	20020ct7
PASS		
0	rbbm_cp_non_queued_rdwr_02	20020ct7
PASS		
0	rbbm_cp_queued_holds_nonqueued	20020ct7
PASS		
0	rbbm_cp_queued_rdwr	20020ct7
PASS		
0	rbbm_hi_data_and_be_swap	20020ct7

PASS		
0	rbbm_hi_data_endian_swap	20020ct7
PASS		
0	rbbm_hi_data_no_swap_for_cppush	20020ct7
PASS		
0	rbbm_hi_data_swap_for_cppush	20020ct7
PASS		
0	rbbm_int_ack	20020ct7
PASS		
0	rbbm_int_each_client	20020ct7
PASS		
0	rbbm_int_nrt_gfxpipe_idle	20020ct7
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	20020ct7
PASS		
0	rbbm_int_rderr	20020ct7
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	20020ct7
PASS		
0	rbbm_isync_cpscratch_idlegui	20020ct7
PASS		
0	rbbm_isync_wait_idlegui	20020ct7
PASS		
0	rbbm_soft_reset	20020ct7
PASS		
0	rbbm_vgt_default_req	20020ct7
PASS		
0	rbbm_vgt_max_req	20020ct7
PASS		
0	rbbm_vgt_min_req	20020ct7
PASS		
0	reg_to_mem	20020ct7
PASS		
0	reg_to_mem_rt	20020ct7
PASS		
0	ring_il_pio_i2_bm	20020ct7
PASS		
471	ring_pio_ibs_bm_stress	20020ct7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
472	rop_cfd_wmd	20020ct7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	roq_reg_reads	20020ct7
PASS		
0	rstr_ordr	20020ct7
PASS		
0	rt_invalid_opcode	20020ct7
PASS		
0	rtee_arm_wait_idle_trigger_events_16	20020ct7

PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct7
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct7
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0	rtee_discretes_falling_10_events_16	2002Oct7
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0	rtee_discretes_falling_11_events_16	2002Oct7
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0	rtee_discretes_falling_12_events_16	2002Oct7
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0	rtee_discretes_falling_13_events_16	2002Oct7
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0	rtee_discretes_falling_14_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct7
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0	rtee_discretes_falling_16_events_16	2002Oct7
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0	rtee_discretes_falling_17_events_16	2002Oct7
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0	rtee_discretes_falling_18_events_16	2002Oct7
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0	rtee_discretes_falling_19_events_16	2002Oct7
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0	rtee_discretes_falling_20_events_16	2002Oct7
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0	rtee_discretes_falling_21_events_16	2002Oct7
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0	rtee_discretes_falling_22_events_16	2002Oct7

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0	rtee_discretes_falling_23_events_16	2002Oct7
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0	rtee_discretes_falling_24_events_16	2002Oct7
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0	rtee_discretes_falling_25_events_16	2002Oct7
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0	rtee_discretes_falling_27_events_16	2002Oct7
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0	rtee_discretes_falling_28_events_16	2002Oct7
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0	rtee_discretes_falling_29_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct7
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct7
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct7
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct7
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct7
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0	rtee_discretes_invert_03_events_16	2002Oct7
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0	rtee_discretes_invert_04_events_16	2002Oct7
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0	rtee_discretes_invert_05_events_16	2002Oct7
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0	rtee_discretes_invert_06_events_16	2002Oct7
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0	rtee_discretes_invert_07_events_16	2002Oct7
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0	rtee_discretes_invert_08_events_16	2002Oct7
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0	rtee_discretes_invert_09_events_16	2002Oct7
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0	rtee_discretes_invert_13_events_16	2002Oct7
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0	rtee_discretes_invert_14_events_16	2002Oct7

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0	rtee_discretes_invert_15_events_16	2002Oct7
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0	rtee_discretes_invert_16_events_16	2002Oct7
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0	rtee_discretes_invert_17_events_16	2002Oct7
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0	rtee_discretes_invert_18_events_16	2002Oct7
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0	rtee_discretes_invert_19_events_16	2002Oct7
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0	rtee_discretes_invert_20_events_16	2002Oct7
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0	rtee_discretes_invert_21_events_16	2002Oct7
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0	rtee_discretes_invert_22_events_16	2002Oct7
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0	rtee_discretes_invert_23_events_16	2002Oct7
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0	rtee_discretes_invert_31_events_16	2002Oct7
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0	rtee_discretes_level_00_events_16	2002Oct7
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0	rtee_discretes_level_01_events_16	2002Oct7
PASS		
0	rtee_discretes_level_02_events_16	2002Oct7
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0	rtee_discretes_level_03_events_16	2002Oct7
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0	rtee_discretes_level_04_events_16	2002Oct7
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0	rtee_discretes_level_05_events_16	2002Oct7
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0	rtee_discretes_level_06_events_16	2002Oct7

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0	rtee_discretes_level_07_events_16	2002Oct7
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0	rtee_discretes_level_09_events_16	2002Oct7
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0	rtee_discretes_level_28_events_16	2002Oct7
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0	rtee_discretes_level_29_events_16	2002Oct7
PASS		
0	rtee_discretes_level_30_events_16	2002Oct7

PASS		
0	rtee_discretes_level_31_events_16	2002Oct7
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct7
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct7
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct7
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct7
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0	rtee_discretes_rising_03_events_16	2002Oct7
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0	rtee_discretes_rising_04_events_16	2002Oct7
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0	rtee_discretes_rising_05_events_16	2002Oct7
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0	rtee_discretes_rising_07_events_16	2002Oct7
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PASS		
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PASS		
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PASS		
0	rtee_discretes_rising_30_events_16	2002Oct7
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct7
PASS		
0	rtee_display1_comp_events_16	2002Oct7
PASS		
0	rtee_display2_comp_events_16	2002Oct7
PASS		
0	rtee_mem_linear_search_events_2	2002Oct7
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct7
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct7
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct7

PASS		
0	rtee_mem_poll_intervals_events_16	20020ct7
PASS		
0	rtee_mem_poll_short_intervals_events_16	20020ct7
PASS		
0	rtee_pio	20020ct7
PASS		
0	rtee_prefetch_parse_nops	20020ct7
PASS		
0	rtee_reg_linear_search_events_2	20020ct7
PASS		
0	rtee_reg_poll_comp_000_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_001_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_010_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_011_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_100_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_101_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_110_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_111_events_16	20020ct7
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	20020ct7
PASS		
0	rtee_vip_comp_events_16	20020ct7
PASS		
0	scratch_mem_read	20020ct7
PASS		
0	scratch_mem_read2	20020ct7
PASS		
0	set_cnst_alu_max_random	20020ct7
PASS		
0	set_cnst_we_lcc_random	20020ct7
PASS		
0	set_cnst_we_random	20020ct7
PASS		
0	set_constant	20020ct7
PASS		
0	set_constant2	20020ct7
PASS		
0	set_constant_alu_max1	20020ct7
PASS		
0	set_constant_alu_max2	20020ct7

PASS		
0	set_constant_bool_max1	20020ct7
PASS		
0	set_constant_bool_max2	20020ct7
PASS		
0	set_constant_indr1_short	20020ct7
PASS		
0	set_constant_indr1_short2	20020ct7
PASS		
0	set_constant_indr1_write_enable	20020ct7
PASS		
0	set_constant_indr1_write_enable_lcc	20020ct7
PASS		
0	set_constant_indr2_short	20020ct7
PASS		
0	set_constant_indr2_short2	20020ct7
PASS		
0	set_constant_indr2_write_enable	20020ct7
PASS		
0	set_constant_indr2_write_enable_lcc	20020ct7
PASS		
0	set_constant_loop_max1	20020ct7
PASS		
0	set_constant_loop_max2	20020ct7
PASS		
0	set_constant_ring_short	20020ct7
PASS		
0	set_constant_ring_short2	20020ct7
PASS		
0	set_constant_ring_write_enable	20020ct7
PASS		
0	set_constant_ring_write_enable_lcc	20020ct7
PASS		
0	set_constant_short	20020ct7
PASS		
0	set_constant_short2	20020ct7
PASS		
0	set_constant_tex_max1	20020ct7
PASS		
0	set_constant_tex_max2	20020ct7
PASS		
0	set_constant_write_enable	20020ct7
PASS		
0	set_constant_write_enable_lcc	20020ct7
PASS		
0	set_constant_write_enable_lcc2	20020ct7
PASS		
0	set_scissors	20020ct7

PASS		
0	set_st_indrl_match	20020ct7
PASS		
0	set_st_indrl_mismatch	20020ct7
PASS		
0	set_st_indrl_mismatch2	20020ct7
PASS		
0	set_st_indrl_mismatch3	20020ct7
PASS		
0	set_st_indrl_mismatch4	20020ct7
PASS		
0	set_st_indrl_ps	20020ct7
PASS		
0	set_st_indrl_ps_only	20020ct7
PASS		
0	set_st_indrl_sublk0	20020ct7
PASS		
0	set_st_indrl_sublk1	20020ct7
PASS		
0	set_st_indrl_sublk1_only	20020ct7
PASS		
0	set_st_indrl_sublk2	20020ct7
PASS		
0	set_st_indrl_sublk2_only	20020ct7
PASS		
0	set_st_indrl_sublk3	20020ct7
PASS		
0	set_st_indrl_sublk3_only	20020ct7
PASS		
0	set_st_indrl_sublk4	20020ct7
PASS		
0	set_st_indrl_sublk4_only	20020ct7
PASS		
0	set_st_indrl_sublk5	20020ct7
PASS		
0	set_st_indrl_sublk5_only	20020ct7
PASS		
0	set_st_indrl_sublk6	20020ct7
PASS		
0	set_st_indrl_sublk6_only	20020ct7
PASS		
0	set_st_indrl_sublk7	20020ct7
PASS		
0	set_st_indrl_sublk7_only	20020ct7
PASS		
0	set_st_indrl_vs	20020ct7
PASS		
0	set_st_indrl_vs_only	20020ct7

PASS		
0	set_st_indr2_match	20020ct7
PASS		
0	set_st_indr2_mismatch	20020ct7
PASS		
0	set_st_indr2_mismatch2	20020ct7
PASS		
0	set_st_indr2_mismatch3	20020ct7
PASS		
0	set_st_indr2_mismatch4	20020ct7
PASS		
0	set_st_indr2_ps	20020ct7
PASS		
0	set_st_indr2_ps_only	20020ct7
PASS		
0	set_st_indr2_sublk0	20020ct7
PASS		
0	set_st_indr2_sublk1	20020ct7
PASS		
0	set_st_indr2_sublk1_only	20020ct7
PASS		
0	set_st_indr2_sublk2	20020ct7
PASS		
0	set_st_indr2_sublk2_only	20020ct7
PASS		
0	set_st_indr2_sublk3	20020ct7
PASS		
0	set_st_indr2_sublk3_only	20020ct7
PASS		
0	set_st_indr2_sublk4	20020ct7
PASS		
0	set_st_indr2_sublk4_only	20020ct7
PASS		
0	set_st_indr2_sublk5	20020ct7
PASS		
0	set_st_indr2_sublk5_only	20020ct7
PASS		
0	set_st_indr2_sublk6	20020ct7
PASS		
0	set_st_indr2_sublk6_only	20020ct7
PASS		
0	set_st_indr2_sublk7	20020ct7
PASS		
0	set_st_indr2_sublk7_only	20020ct7
PASS		
0	set_st_indr2_vs	20020ct7
PASS		
0	set_st_indr2_vs_only	20020ct7

PASS		
0	set_st_pfmatch_disabled	20020ct7
PASS		
0	set_st_ring_match	20020ct7
PASS		
0	set_st_ring_mismatch	20020ct7
PASS		
0	set_st_ring_mismatch2	20020ct7
PASS		
0	set_st_ring_mismatch3	20020ct7
PASS		
0	set_st_ring_mismatch4	20020ct7
PASS		
0	set_st_ring_ps	20020ct7
PASS		
0	set_st_ring_ps_only	20020ct7
PASS		
0	set_st_ring_subblk0	20020ct7
PASS		
0	set_st_ring_subblk1	20020ct7
PASS		
0	set_st_ring_subblk1_only	20020ct7
PASS		
0	set_st_ring_subblk2	20020ct7
PASS		
0	set_st_ring_subblk2_only	20020ct7
PASS		
0	set_st_ring_subblk3	20020ct7
PASS		
0	set_st_ring_subblk3_only	20020ct7
PASS		
0	set_st_ring_subblk4	20020ct7
PASS		
0	set_st_ring_subblk4_only	20020ct7
PASS		
0	set_st_ring_subblk5	20020ct7
PASS		
0	set_st_ring_subblk5_only	20020ct7
PASS		
0	set_st_ring_subblk6	20020ct7
PASS		
0	set_st_ring_subblk6_only	20020ct7
PASS		
0	set_st_ring_subblk7	20020ct7
PASS		
0	set_st_ring_subblk7_only	20020ct7
PASS		
0	set_st_ring_vs	20020ct7

PASS		
0	set_st_ring_vs_only	2002Oct7
PASS		
0	set_st_stress_a	2002Oct7
PASS		
0	set_st_stress_b	2002Oct7
PASS		
0	set_st_stress_c	2002Oct7
PASS		
0	set_st_stress_d	2002Oct7
PASS		
0	set_st_stress_e	2002Oct7
PASS		
0	set_st_stress_f	2002Oct7
PASS		
0	set_state_rt_a	2002Oct7
PASS		
0	set_state_rt_b	2002Oct7
PASS		
0	set_state_rt_c	2002Oct7
PASS		
0	set_state_rt_d	2002Oct7
PASS		
0	smoke_nrt	2002Oct7
PASS		
0	src_clip	2002Oct7
PASS		
0	srf_cohr	2002Oct7
PASS		
0	switch_pio_bm	2002Oct7
PASS		
756	trans_bitblt	2002Oct7
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	type0_2long_pckts	2002Oct7
PASS		
0	type0_3indr_buffs	2002Oct7
PASS		
0	type0_4pckts	2002Oct7
PASS		
0	type0_incr_updt_cnst	2002Oct7
PASS		
0	type0_incr_updt_instrmem	2002Oct7
PASS		
0	type0_incr_updt_st	2002Oct7
PASS		
0	type0_incr_updt_stress	2002Oct7
PASS		
0	type0_ind1_ind2_a	2002Oct7

PASS		
0	type0_ind1_ind2_b	2002Oct7
PASS		
0	type0_indr_buff	2002Oct7
PASS		
0	type1_4pckts	2002Oct7
PASS		
0	type1_indr_buff	2002Oct7
PASS		
0	type1_pckt	2002Oct7
PASS		
0	type_random	2002Oct7
PASS		
771	uram_wrt_rt_ucose	2002Oct7
FAIL		
772	uram_wrt_ucose	2002Oct7
FAIL		
0	viz_query_a	2002Oct7
PASS		
0	viz_query_draw_indx	2002Oct7
PASS		
0	viz_query_draw_indx_indr1	2002Oct7
PASS		
0	viz_query_draw_indx_indr2	2002Oct7
PASS		
0	viz_query_draw_indx_multipass	2002Oct7
PASS		
0	viz_query_draw_indx_ring	2002Oct7
PASS		
0	viz_query_indr1	2002Oct7
PASS		
0	viz_query_indr1_simple	2002Oct7
PASS		
0	viz_query_indr2	2002Oct7
PASS		
0	viz_query_indr2_simple	2002Oct7
PASS		
0	viz_query_ring	2002Oct7
PASS		
0	viz_query_ring_simple	2002Oct7
PASS		
0	wait_for_idle	2002Oct7
PASS		
0	wait_reg_mem_poll_mem0	2002Oct7
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Oct7
PASS		
0	wait_reg_mem_poll_mem1	2002Oct7

```
PASS
0 wait_reg_mem_poll_mem2 2002Oct7
PASS
0 wait_reg_mem_poll_mem3 2002Oct7
PASS
0 wait_reg_mem_poll_reg0 2002Oct7
PASS
0 wait_reg_mem_poll_reg1 2002Oct7
PASS
0 wait_reg_mem_poll_reg2 2002Oct7
PASS
0 wait_reg_mem_poll_reg3 2002Oct7
PASS
```

```
=====
Tests_Run_count = 794 Pass_count = 771 Fail_count = 23 Incomplete_count = 0
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Sep 24  2:26:26 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             641         641         617         96
              RBBM           109         109         107         98
+++++
              TOTAL           750         750         724         96
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep12
	PASS		
0	2d_indr_buffer_b		2002Sep12
	PASS		
0	2d_indr_buffer_c		2002Sep12
	PASS		
0	2d_indr_buffer_d		2002Sep12
	PASS		
0	2d_indr_buffer_e		2002Sep12
	PASS		
0	2d_indr_buffer_f		2002Sep12
	PASS		
0	2d_indr_buffer_g		2002Sep12
	PASS		
0	3d_draw_indx_2		2002Sep12
	PASS		
0	3d_draw_indx_2_b		2002Sep12
	PASS		
10	bitblt_multi		2002Sep12
	FAIL cp_rbbm.cmp rbbmWr.cmp		
11	brush_32x1mono		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
12	brush_8x8color_16		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
13	brush_8x8color_32		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
14	brush_8x8color_64		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
15	brush_8x8color_dst_0_1		2002Sep12
	FAIL cp_rbbm.cmp rbbmWr.cmp		
16	brush_8x8mono		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
17	brush_color_mono_stress		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
18	brush_flag		2002Sep12
	FAIL cp_rbbm.cmp rbbmWr.cmp		
19	brush_type0		2002Sep12
	FAIL cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp		
20	brush_type1		2002Sep12

```

**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
21  brush_type10                                2002Sep12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
22  brush_type13                                2002Sep12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
23  brush_type14                                2002Sep12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
24  brush_type6                                 2002Sep12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
25  brush_type7                                 2002Sep12
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp
26  brush_type_other                            2002Sep12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
27  brush_types_13_14                          2002Sep12
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   clear_const_wr_en_on_me_init                2002Sep12
PASS
0   clear_const_wr_en_with_lcc                  2002Sep12
PASS
0   cond_write_mem_poll_mem                     2002Sep12
PASS
0   cond_write_mem_poll_mem2                    2002Sep12
PASS
0   cond_write_mem_poll_mem3                    2002Sep12
PASS
0   cond_write_mem_poll_mem_rt                  2002Sep12
PASS
0   cond_write_mem_poll_reg                     2002Sep12
PASS
0   cond_write_mem_poll_reg2                    2002Sep12
PASS
0   cond_write_mem_poll_reg3                    2002Sep12
PASS
0   cp_acpi_recovery_soft_reset                 2002Sep12
PASS
0   cp_bios_scratch                             2002Sep12
PASS
0   cp_ib2_base000_bufszXXX                     2002Sep12
PASS
0   cp_ib2_base001_bufszXXX                     2002Sep12
PASS
0   cp_ib2_base010_bufszXXX                     2002Sep12
PASS
0   cp_ib2_base011_bufszXXX                     2002Sep12
PASS
0   cp_ib2_base100_bufszXXX                     2002Sep12
PASS
0   cp_ib2_base101_bufszXXX                     2002Sep12

```

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep12
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep12
PASS		
0	cp_ib_base000_bufszXXX	2002Sep12
PASS		
0	cp_ib_base001_bufszXXX	2002Sep12
PASS		
0	cp_ib_base010_bufszXXX	2002Sep12
PASS		
0	cp_ib_base011_bufszXXX	2002Sep12
PASS		
0	cp_ib_base100_bufszXXX	2002Sep12
PASS		
0	cp_ib_base101_bufszXXX	2002Sep12
PASS		
0	cp_ib_base110_bufszXXX	2002Sep12
PASS		
0	cp_ib_base111_bufszXXX	2002Sep12
PASS		
0	cp_interrupt	2002Sep12
PASS		
0	cp_interrupt_rt_events_16	2002Sep12
PASS		
0	cp_iq2_alignment_000	2002Sep12
PASS		
0	cp_iq2_alignment_001	2002Sep12
PASS		
0	cp_iq2_alignment_010	2002Sep12
PASS		
0	cp_iq2_alignment_011	2002Sep12
PASS		
0	cp_iq2_alignment_100	2002Sep12
PASS		
0	cp_iq2_alignment_101	2002Sep12
PASS		
0	cp_iq2_alignment_110	2002Sep12
PASS		
0	cp_iq2_alignment_111	2002Sep12
PASS		
0	cp_iq_alignment_000	2002Sep12
PASS		
0	cp_iq_alignment_001	2002Sep12
PASS		
0	cp_iq_alignment_010	2002Sep12
PASS		
0	cp_iq_alignment_011	2002Sep12

PASS		
0	cp_iq_alignment_100	2002Sep12
PASS		
0	cp_iq_alignment_101	2002Sep12
PASS		
0	cp_iq_alignment_110	2002Sep12
PASS		
0	cp_iq_alignment_111	2002Sep12
PASS		
0	cp_max_size_packet	2002Sep12
PASS		
0	cp_max_size_packet_random	2002Sep12
PASS		
0	cp_nrt_rt_protection_mode	2002Sep12
PASS		
0	cp_output_stress	2002Sep12
PASS		
0	cp_output_stress_random	2002Sep12
PASS		
0	cp_protection_mode	2002Sep12
PASS		
0	cp_rb_alignment_000	2002Sep12
PASS		
0	cp_rb_alignment_001	2002Sep12
PASS		
0	cp_rb_alignment_010	2002Sep12
PASS		
0	cp_rb_alignment_011	2002Sep12
PASS		
0	cp_rb_alignment_100	2002Sep12
PASS		
0	cp_rb_alignment_101	2002Sep12
PASS		
0	cp_rb_alignment_110	2002Sep12
PASS		
0	cp_rb_alignment_111	2002Sep12
PASS		
0	cp_rb_wptrXXX_base000	2002Sep12
PASS		
0	cp_rb_wptrXXX_base001	2002Sep12
PASS		
0	cp_rb_wptrXXX_base010	2002Sep12
PASS		
0	cp_rb_wptrXXX_base011	2002Sep12
PASS		
0	cp_rb_wptrXXX_base100	2002Sep12
PASS		
0	cp_rb_wptrXXX_base101	2002Sep12

PASS		
0	cp_rb_wptrXXX_base10	2002Sep12
PASS		
0	cp_rb_wptrXXX_base11	2002Sep12
PASS		
0	cp_register_reads	2002Sep12
PASS		
0	cp_reg_rw_test	2002Sep12
PASS		
0	cp_rt_protection_mode	2002Sep12
PASS		
0	cp_scratch	2002Sep12
PASS		
0	cp_scratch_multi	2002Sep12
PASS		
0	dma_cp_sync	2002Sep12
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep12
PASS		
0	dma_dscrstr_flush1	2002Sep12
PASS		
0	dma_dscrstr_flush2	2002Sep12
PASS		
0	dma_dscrstr_flush3	2002Sep12
PASS		
0	dma_m2m_src00_dstXX	2002Sep12
PASS		
0	dma_m2m_src01_dstXX	2002Sep12
PASS		
0	dma_m2m_src10_dstXX	2002Sep12
PASS		
0	dma_m2m_src11_dstXX	2002Sep12
PASS		
0	dma_mem2mem	2002Sep12
PASS		
0	dma_mem2mem_abort	2002Sep12
PASS		
0	dma_mem2mem_alignments	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep12

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep12
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep12
PASS		
0	dma_mem2mem_pio_abort	2002Sep12
PASS		
0	dma_mem2mem_random	2002Sep12
PASS		
0	dma_mem2reg	2002Sep12
PASS		
0	dma_mem2reg_abort	2002Sep12
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep12
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep12
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep12
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep12
PASS		
0	dma_mem2reg_sync	2002Sep12
PASS		
0	dma_r2r_m2m_r2r	2002Sep12
PASS		
0	dma_r2r_m2r_r2r	2002Sep12
PASS		
0	dma_r2r_r2m_r2r	2002Sep12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep12
PASS		
0	dma_reg2mem	2002Sep12
PASS		
0	dma_reg2mem_abort	2002Sep12
PASS		
0	dma_reg2reg	2002Sep12
PASS		
0	dma_reg2reg_2dw	2002Sep12
PASS		
0	dma_reg2reg_daic	2002Sep12
PASS		
0	dma_reg2reg_mult	2002Sep12

PASS		
0	dma_reg2reg_mult2	2002Sep12
PASS		
0	dma_reg2reg_mult3	2002Sep12
PASS		
0	dma_reg2reg_saic	2002Sep12
PASS		
0	dma_reg2reg_sdaic	2002Sep12
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep12
PASS		
0	dma_reg2reg_stress	2002Sep12
PASS		
0	draw_indx_a	2002Sep12
PASS		
0	draw_indx_b	2002Sep12
PASS		
0	draw_indx_c	2002Sep12
PASS		
0	draw_indx_indr1_a	2002Sep12
PASS		
0	draw_indx_indr1_b	2002Sep12
PASS		
0	draw_indx_indr1_short_a	2002Sep12
PASS		
0	draw_indx_indr1_short_b	2002Sep12
PASS		
0	draw_indx_indr1_stress	2002Sep12
PASS		
0	draw_indx_indr2_a	2002Sep12
PASS		
0	draw_indx_indr2_b	2002Sep12
PASS		
0	draw_indx_indr2_short_a	2002Sep12
PASS		
0	draw_indx_indr2_short_b	2002Sep12
PASS		
0	draw_indx_indr2_stress	2002Sep12
PASS		
0	draw_indx_ring1	2002Sep12
PASS		
0	draw_indx_ring2	2002Sep12
PASS		
0	draw_indx_ring_short1	2002Sep12
PASS		
0	draw_indx_ring_short2	2002Sep12
PASS		
0	draw_indx_ring_stress	2002Sep12

PASS		
0	draw_indx_short_a	2002Sep12
PASS		
0	draw_indx_short_b	2002Sep12
PASS		
0	draw_indx_short_c	2002Sep12
PASS		
0	draw_indx_stress_a	2002Sep12
PASS		
0	draw_indx_stress_b	2002Sep12
PASS		
0	draw_indx_stress_c	2002Sep12
PASS		
0	event_timestamp_write	2002Sep12
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep12
PASS		
0	ft_indr1_july22	2002Sep12
PASS		
0	ft_indr2_july22	2002Sep12
PASS		
0	ft_july22	2002Sep12
PASS		
176	ft_july22_random	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	header_dump	2002Sep12
PASS		
0	header_dump_rt	2002Sep12
PASS		
0	header_dump_small	2002Sep12
PASS		
180	hostdata_blt	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep12
PASS		
0	im_load_b	2002Sep12
PASS		
0	im_load_cid_mismatch	2002Sep12
PASS		
0	im_load_immediate	2002Sep12
PASS		
0	im_load_immediate_nrt_me	2002Sep12
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep12
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep12
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep12

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep12
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep12
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep12
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep12
PASS		
0	im_load_indr1_a	2002Sep12
PASS		
0	im_load_indr1_b	2002Sep12
PASS		
0	im_load_indr2_a	2002Sep12
PASS		
0	im_load_indr2_b	2002Sep12
PASS		
0	im_load_invalidate	2002Sep12
PASS		
0	im_load_match	2002Sep12
PASS		
0	im_load_match2	2002Sep12
PASS		
0	im_load_nrt_me	2002Sep12
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep12
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep12
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep12
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep12
PASS		
0	im_load_pfmacth_disable	2002Sep12
PASS		
0	im_load_ring_a	2002Sep12
PASS		
0	im_load_ring_b	2002Sep12
PASS		
0	im_load_rt	2002Sep12
PASS		
0	im_load_rt2	2002Sep12
PASS		
0	im_load_rt3	2002Sep12
PASS		
0	im_load_rt4	2002Sep12
PASS		
0	im_load_sz_mismatch	2002Sep12

PASS		
0	incremental_update_const	2002Sep12
PASS		
214	incremental_update_instr	2002Sep12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	incremental_update_state	2002Sep12
PASS		
0	indirect_buffer_a	2002Sep12
PASS		
0	indirect_buffer_b	2002Sep12
PASS		
0	indirect_buffer_c	2002Sep12
PASS		
0	indirect_buffer_d	2002Sep12
PASS		
0	indirect_buffer_e	2002Sep12
PASS		
0	indirect_buffer_f	2002Sep12
PASS		
0	indirect_buffer_g	2002Sep12
PASS		
0	indirect_buffer_h	2002Sep12
PASS		
0	indirect_buffer_i	2002Sep12
PASS		
0	indirect_buffer_j	2002Sep12
PASS		
0	indirect_buffer_k	2002Sep12
PASS		
0	indirect_buffer_l	2002Sep12
PASS		
0	indirect_buffer_mp_a	2002Sep12
PASS		
0	indirect_buffer_mp_b	2002Sep12
PASS		
0	indirect_buffer_mp_c	2002Sep12
PASS		
0	indirect_buffer_mp_d	2002Sep12
PASS		
0	indirect_buffer_mp_e	2002Sep12
PASS		
0	indirect_buffer_mp_f	2002Sep12
PASS		
0	invalidate_on_me_init	2002Sep12
PASS		
0	invalidate_state_constants	2002Sep12
PASS		
0	invalidate_state_shaders	2002Sep12

PASS		
0	invalidate_state_shaders2	2002Sep12
PASS		
0	invalidate_state_sublks	2002Sep12
PASS		
0	invalidate_state_sublks2	2002Sep12
PASS		
0	invalid_opcode	2002Sep12
PASS		
0	iq1_iq2_mult	2002Sep12
PASS		
0	iq1_iq2_push_wrap	2002Sep12
PASS		
0	iq2_csq_full	2002Sep12
PASS		
0	IQ2_Mult	2002Sep12
PASS		
0	IQ2_Wrap_Legacy	2002Sep12
PASS		
0	IQ_Wrap	2002Sep12
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep12
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep12
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep12
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep12
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep12
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep12
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep12
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep12
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep12
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep12
PASS		
0	ld_cnst_cntx_match	2002Sep12
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep12
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep12
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep12

PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep12
PASS		
0	ld_cnst_cntx_pfmatch_disable	2002Sep12
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep12
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep12
PASS		
0	ld_cnst_cntx_replicate8	2002Sep12
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep12
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep12
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep12
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep12
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep12
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep12
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep12
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep12
PASS		
0	load_constant_context_rt	2002Sep12
PASS		
275	load_palette	2002Sep12
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
276	lp_2d3d	2002Sep12
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	2002Sep12
PASS		
0	mem_write_rt	2002Sep12
PASS		
0	mpeg_index	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep12

PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep12
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep12
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep12
PASS		
0	nop_eating_disabled	2002Sep12
PASS		
296	paint_multi	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
297	paint_packet	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
298	paint_packet_gui_control	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
299	pitch_clip	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
300	ply_nextscan	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_cp_non_queued_rdwr_01	2002Sep12
PASS		
302	rbbm_cp_non_queued_rdwr_02	2002Sep12
FAIL	cp_rbbm.cmp	
303	rbbm_cp_queued_holds_nonqueued	2002Sep12
FAIL	cp_rbbm.cmp	
304	rbbm_cp_queued_rdwr	2002Sep12
FAIL	cp_rbbm.cmp	
0	rbbm_isync_cpscratch_idlegui	2002Sep12
PASS		
0	rbbm_isync_wait_idlegui	2002Sep12
PASS		
0	rbbm_soft_reset	2002Sep12
PASS		
0	rbbm_WU_2D_idle_01	2002Sep12

PASS		
0	rbbm_WU_2D_idle_02	2002Sep12
PASS		
0	rbbm_WU_2D_idle_03	2002Sep12
PASS		
0	rbbm_WU_2D_idle_04	2002Sep12
PASS		
0	rbbm_WU_2D_idle_05	2002Sep12
PASS		
0	rbbm_WU_2D_idle_06	2002Sep12
PASS		
0	rbbm_WU_2D_idle_07	2002Sep12
PASS		
0	rbbm_WU_2D_idle_08	2002Sep12
PASS		
0	rbbm_WU_2D_idle_09	2002Sep12
PASS		
317	rbbm_WU_2D_idle_clean_01	2002Sep12
FAIL		
318	rbbm_WU_2D_idle_clean_02	2002Sep12
FAIL		
319	rbbm_WU_2D_idle_clean_03	2002Sep12
FAIL		
320	rbbm_WU_2D_idle_clean_04	2002Sep12
FAIL		
0	rbbm_WU_2D_idle_clean_05	2002Sep12
PASS		
322	rbbm_WU_2D_idle_clean_06	2002Sep12
FAIL		
323	rbbm_WU_2D_idle_clean_07	2002Sep12
FAIL		
324	rbbm_WU_2D_idle_clean_08	2002Sep12
FAIL		
325	rbbm_WU_2D_idle_clean_09	2002Sep12
FAIL		
326	rbbm_WU_2D_idle_clean_10	2002Sep12
FAIL		
327	rbbm_WU_2D_idle_clean_11	2002Sep12
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Sep12
PASS		
0	rbbm_WU_3D_idle_01	2002Sep12
PASS		
0	rbbm_WU_3D_idle_02	2002Sep12
PASS		
0	rbbm_WU_3D_idle_03	2002Sep12
PASS		
0	rbbm_WU_3D_idle_04	2002Sep12

PASS		
0	rbbm_WU_3D_idle_05	2002Sep12
PASS		
0	rbbm_WU_3D_idle_06	2002Sep12
PASS		
0	rbbm_WU_3D_idle_07	2002Sep12
PASS		
0	rbbm_WU_3D_idle_08	2002Sep12
PASS		
0	rbbm_WU_3D_idle_09	2002Sep12
PASS		
338	rbbm_WU_3D_idle_clean_01	2002Sep12
FAIL		
339	rbbm_WU_3D_idle_clean_02	2002Sep12
FAIL		
340	rbbm_WU_3D_idle_clean_03	2002Sep12
FAIL		
341	rbbm_WU_3D_idle_clean_04	2002Sep12
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Sep12
PASS		
343	rbbm_WU_3D_idle_clean_06	2002Sep12
FAIL		
344	rbbm_WU_3D_idle_clean_07	2002Sep12
FAIL		
345	rbbm_WU_3D_idle_clean_08	2002Sep12
FAIL		
346	rbbm_WU_3D_idle_clean_09	2002Sep12
FAIL		
347	rbbm_WU_3D_idle_clean_10	2002Sep12
FAIL		
348	rbbm_WU_3D_idle_clean_11	2002Sep12
FAIL		
0	rbbm_WU_3D_idle_clean_12	2002Sep12
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep12
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Sep12
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep12
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep12

PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep12
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep12
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep12
PASS		
0	rbbm_WU_idct_semaphore	2002Sep12
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep12
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep12
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep12
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep12
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep12
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep12
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep12
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep12
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep12
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep12
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep12

PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep12
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep12
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep12
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep12
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep12
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep12
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep12
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep12
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep12
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep12
PASS		
0	RB_BUFSZ_0	2002Sep12
PASS		
0	RB_BUFSZ_1	2002Sep12
PASS		
0	RB_Wrap	2002Sep12
PASS		
0	reg_to_mem	2002Sep12
PASS		
0	reg_to_mem_rt	2002Sep12
PASS		
404	rop_cfd_wmd	2002Sep12

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
405  rstr_ordr                                          2002Sep12
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    rtee_arm_wait_idle_trigger_events_16            2002Sep12
PASS
0    rtee_discretes_arm_rearm_events_32              2002Sep12
PASS
0    rtee_discretes_falling_00_events_16             2002Sep12
PASS
0    rtee_discretes_falling_01_events_16            2002Sep12
PASS
0    rtee_discretes_falling_02_events_16            2002Sep12
PASS
0    rtee_discretes_falling_03_events_16            2002Sep12
PASS
0    rtee_discretes_falling_04_events_16            2002Sep12
PASS
0    rtee_discretes_falling_05_events_16            2002Sep12
PASS
0    rtee_discretes_falling_06_events_16            2002Sep12
PASS
0    rtee_discretes_falling_07_events_16            2002Sep12
PASS
0    rtee_discretes_falling_08_events_16            2002Sep12
PASS
0    rtee_discretes_falling_09_events_16            2002Sep12
PASS
0    rtee_discretes_falling_10_events_16            2002Sep12
PASS
0    rtee_discretes_falling_11_events_16            2002Sep12
PASS
0    rtee_discretes_falling_12_events_16            2002Sep12
PASS
0    rtee_discretes_falling_13_events_16            2002Sep12
PASS
0    rtee_discretes_falling_14_events_16            2002Sep12
PASS
0    rtee_discretes_falling_15_events_16            2002Sep12
PASS
0    rtee_discretes_falling_16_events_16            2002Sep12
PASS
0    rtee_discretes_falling_17_events_16            2002Sep12
PASS
0    rtee_discretes_falling_18_events_16            2002Sep12
PASS
0    rtee_discretes_falling_19_events_16            2002Sep12
PASS
0    rtee_discretes_falling_20_events_16            2002Sep12

```

PASS		
0	rtee_discretes_falling_21_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep12
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep12

PASS		
0	rtee_discretes_invert_13_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep12
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep12
PASS		
0	rtee_discretes_level_00_events_16	2002Sep12
PASS		
0	rtee_discretes_level_01_events_16	2002Sep12
PASS		
0	rtee_discretes_level_02_events_16	2002Sep12
PASS		
0	rtee_discretes_level_03_events_16	2002Sep12
PASS		
0	rtee_discretes_level_04_events_16	2002Sep12

PASS		
0	rtee_discretes_level_05_events_16	2002Sep12
PASS		
0	rtee_discretes_level_06_events_16	2002Sep12
PASS		
0	rtee_discretes_level_07_events_16	2002Sep12
PASS		
0	rtee_discretes_level_08_events_16	2002Sep12
PASS		
0	rtee_discretes_level_09_events_16	2002Sep12
PASS		
0	rtee_discretes_level_10_events_16	2002Sep12
PASS		
0	rtee_discretes_level_11_events_16	2002Sep12
PASS		
0	rtee_discretes_level_12_events_16	2002Sep12
PASS		
0	rtee_discretes_level_13_events_16	2002Sep12
PASS		
0	rtee_discretes_level_14_events_16	2002Sep12
PASS		
0	rtee_discretes_level_15_events_16	2002Sep12
PASS		
0	rtee_discretes_level_16_events_16	2002Sep12
PASS		
0	rtee_discretes_level_17_events_16	2002Sep12
PASS		
0	rtee_discretes_level_18_events_16	2002Sep12
PASS		
0	rtee_discretes_level_19_events_16	2002Sep12
PASS		
0	rtee_discretes_level_20_events_16	2002Sep12
PASS		
0	rtee_discretes_level_21_events_16	2002Sep12
PASS		
0	rtee_discretes_level_22_events_16	2002Sep12
PASS		
0	rtee_discretes_level_23_events_16	2002Sep12
PASS		
0	rtee_discretes_level_24_events_16	2002Sep12
PASS		
0	rtee_discretes_level_25_events_16	2002Sep12
PASS		
0	rtee_discretes_level_26_events_16	2002Sep12
PASS		
0	rtee_discretes_level_27_events_16	2002Sep12
PASS		
0	rtee_discretes_level_28_events_16	2002Sep12

PASS		
0	rtee_discretes_level_29_events_16	2002Sep12
PASS		
0	rtee_discretes_level_30_events_16	2002Sep12
PASS		
0	rtee_discretes_level_31_events_16	2002Sep12
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep12

PASS		
0	rtee_discretes_rising_20_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep12
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep12
PASS		
0	rtee_display1_comp_events_16	2002Sep12
PASS		
0	rtee_display2_comp_events_16	2002Sep12
PASS		
0	rtee_mem_linear_search_events_2	2002Sep12
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep12
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep12

PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep12
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep12
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep12
PASS		
0	rtee_prefetch_parse_nops	2002Sep12
PASS		
0	rtee_reg_linear_search_events_2	2002Sep12
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep12
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep12
PASS		
0	rtee_vip_comp_events_16	2002Sep12
PASS		
0	rt_invalid_opcode	2002Sep12
PASS		
0	scratch_mem_read	2002Sep12
PASS		
0	scratch_mem_read2	2002Sep12
PASS		
0	set_constant	2002Sep12
PASS		
0	set_constant2	2002Sep12
PASS		
0	set_constant_alu_max1	2002Sep12
PASS		
0	set_constant_alu_max2	2002Sep12
PASS		
0	set_constant_bool_max1	2002Sep12
PASS		
0	set_constant_bool_max2	2002Sep12

PASS		
0	set_constant_indr1_short	2002Sep12
PASS		
0	set_constant_indr1_short2	2002Sep12
PASS		
0	set_constant_indr1_write_enable	2002Sep12
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep12
PASS		
0	set_constant_indr2_short	2002Sep12
PASS		
0	set_constant_indr2_short2	2002Sep12
PASS		
0	set_constant_indr2_write_enable	2002Sep12
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep12
PASS		
0	set_constant_loop_max1	2002Sep12
PASS		
0	set_constant_loop_max2	2002Sep12
PASS		
0	set_constant_ring_short	2002Sep12
PASS		
0	set_constant_ring_short2	2002Sep12
PASS		
0	set_constant_ring_write_enable	2002Sep12
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep12
PASS		
0	set_constant_short	2002Sep12
PASS		
0	set_constant_short2	2002Sep12
PASS		
0	set_constant_tex_max1	2002Sep12
PASS		
0	set_constant_tex_max2	2002Sep12
PASS		
0	set_constant_write_enable	2002Sep12
PASS		
0	set_constant_write_enable_lcc	2002Sep12
PASS		
0	set_constant_write_enable_lcc2	2002Sep12
PASS		
0	set_scissors	2002Sep12
PASS		
0	set_state_rt_a	2002Sep12
PASS		
0	set_state_rt_b	2002Sep12

PASS		
0	set_state_rt_c	2002Sep12
PASS		
0	set_state_rt_d	2002Sep12
PASS		
0	set_st_indrl_match	2002Sep12
PASS		
0	set_st_indrl_mismatch	2002Sep12
PASS		
0	set_st_indrl_mismatch2	2002Sep12
PASS		
0	set_st_indrl_mismatch3	2002Sep12
PASS		
0	set_st_indrl_mismatch4	2002Sep12
PASS		
0	set_st_indrl_ps	2002Sep12
PASS		
0	set_st_indrl_ps_only	2002Sep12
PASS		
0	set_st_indrl_sublk0	2002Sep12
PASS		
0	set_st_indrl_sublk1	2002Sep12
PASS		
0	set_st_indrl_sublk1_only	2002Sep12
PASS		
0	set_st_indrl_sublk2	2002Sep12
PASS		
0	set_st_indrl_sublk2_only	2002Sep12
PASS		
0	set_st_indrl_sublk3	2002Sep12
PASS		
0	set_st_indrl_sublk3_only	2002Sep12
PASS		
0	set_st_indrl_sublk4	2002Sep12
PASS		
0	set_st_indrl_sublk4_only	2002Sep12
PASS		
0	set_st_indrl_sublk5	2002Sep12
PASS		
0	set_st_indrl_sublk5_only	2002Sep12
PASS		
0	set_st_indrl_sublk6	2002Sep12
PASS		
0	set_st_indrl_sublk6_only	2002Sep12
PASS		
0	set_st_indrl_sublk7	2002Sep12
PASS		
0	set_st_indrl_sublk7_only	2002Sep12

PASS		
0	set_st_indr1_vs	2002Sep12
PASS		
0	set_st_indr1_vs_only	2002Sep12
PASS		
0	set_st_indr2_match	2002Sep12
PASS		
0	set_st_indr2_mismatch	2002Sep12
PASS		
0	set_st_indr2_mismatch2	2002Sep12
PASS		
0	set_st_indr2_mismatch3	2002Sep12
PASS		
0	set_st_indr2_mismatch4	2002Sep12
PASS		
0	set_st_indr2_ps	2002Sep12
PASS		
0	set_st_indr2_ps_only	2002Sep12
PASS		
0	set_st_indr2_sublk0	2002Sep12
PASS		
0	set_st_indr2_sublk1	2002Sep12
PASS		
0	set_st_indr2_sublk1_only	2002Sep12
PASS		
0	set_st_indr2_sublk2	2002Sep12
PASS		
0	set_st_indr2_sublk2_only	2002Sep12
PASS		
0	set_st_indr2_sublk3	2002Sep12
PASS		
0	set_st_indr2_sublk3_only	2002Sep12
PASS		
0	set_st_indr2_sublk4	2002Sep12
PASS		
0	set_st_indr2_sublk4_only	2002Sep12
PASS		
0	set_st_indr2_sublk5	2002Sep12
PASS		
0	set_st_indr2_sublk5_only	2002Sep12
PASS		
0	set_st_indr2_sublk6	2002Sep12
PASS		
0	set_st_indr2_sublk6_only	2002Sep12
PASS		
0	set_st_indr2_sublk7	2002Sep12
PASS		
0	set_st_indr2_sublk7_only	2002Sep12

PASS		
0	set_st_indr2_vs	2002Sep12
PASS		
0	set_st_indr2_vs_only	2002Sep12
PASS		
0	set_st_pfmatch_disabled	2002Sep12
PASS		
0	set_st_ring_match	2002Sep12
PASS		
0	set_st_ring_mismatch	2002Sep12
PASS		
0	set_st_ring_mismatch2	2002Sep12
PASS		
0	set_st_ring_mismatch3	2002Sep12
PASS		
0	set_st_ring_mismatch4	2002Sep12
PASS		
0	set_st_ring_ps	2002Sep12
PASS		
0	set_st_ring_ps_only	2002Sep12
PASS		
0	set_st_ring_subblk0	2002Sep12
PASS		
0	set_st_ring_subblk1	2002Sep12
PASS		
0	set_st_ring_subblk1_only	2002Sep12
PASS		
0	set_st_ring_subblk2	2002Sep12
PASS		
0	set_st_ring_subblk2_only	2002Sep12
PASS		
0	set_st_ring_subblk3	2002Sep12
PASS		
0	set_st_ring_subblk3_only	2002Sep12
PASS		
0	set_st_ring_subblk4	2002Sep12
PASS		
0	set_st_ring_subblk4_only	2002Sep12
PASS		
0	set_st_ring_subblk5	2002Sep12
PASS		
0	set_st_ring_subblk5_only	2002Sep12
PASS		
0	set_st_ring_subblk6	2002Sep12
PASS		
0	set_st_ring_subblk6_only	2002Sep12
PASS		
0	set_st_ring_subblk7	2002Sep12

PASS		
0	set_st_ring_sublk7_only	2002Sep12
PASS		
0	set_st_ring_vs	2002Sep12
PASS		
0	set_st_ring_vs_only	2002Sep12
PASS		
0	set_st_stress_a	2002Sep12
PASS		
673	set_st_stress_b	2002Sep12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Sep12
PASS		
0	set_st_stress_d	2002Sep12
PASS		
0	set_st_stress_e	2002Sep12
PASS		
0	set_st_stress_f	2002Sep12
PASS		
678	Small_Text_BM	2002Sep12
FAIL	cp_mh_req.cmp cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep12
PASS		
680	src_clip	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
681	srf_cohr	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
682	trans_bitblt	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
683	type0_2long_pckts	2002Sep12
FAIL	cp_mh_req.cmp	
0	type0_3indr_buffs	2002Sep12
PASS		
0	type0_4pckts	2002Sep12
PASS		
0	type0_incr_updt_cnst	2002Sep12
PASS		
0	type0_incr_updt_instrmem	2002Sep12
PASS		
0	type0_incr_updt_st	2002Sep12
PASS		
0	type0_incr_updt_stress	2002Sep12
PASS		
0	type0_ind1_ind2_a	2002Sep12
PASS		
0	type0_ind1_ind2_b	2002Sep12
PASS		
0	type0_indr_buff	2002Sep12

PASS		
0	Type0_IQ2_BM	2002Sep12
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep12
PASS		
0	Type0_IQ_BM	2002Sep12
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep12
PASS		
0	Type0_PQ_BM	2002Sep12
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep12
PASS		
0	type1_4pckts	2002Sep12
PASS		
0	type1_indr_buff	2002Sep12
PASS		
0	type1_pckt	2002Sep12
PASS		
702	Type3_Bitblt	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
703	Type3_Load_Palette	2002Sep12
FAIL	cp_mh_req.cmp rbbmWr.cmp	
0	Type3_NOP	2002Sep12
PASS		
705	Type3_Polyline	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
706	Type3_Polyline_Large	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
707	type_random	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep12
PASS		
709	uram_wrt_ucode	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_a	2002Sep12
PASS		
0	viz_query_draw_indx	2002Sep12
PASS		
0	viz_query_draw_indx_indr1	2002Sep12
PASS		
0	viz_query_draw_indx_indr2	2002Sep12
PASS		
714	viz_query_draw_indx_multipass	2002Sep12
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_draw_indx_ring	2002Sep12
PASS		
0	viz_query_indr1	2002Sep12

```

PASS
0 viz_query_indr1_simple 2002Sep12
PASS
0 viz_query_indr2 2002Sep12
PASS
0 viz_query_indr2_simple 2002Sep12
PASS
0 viz_query_ring 2002Sep12
PASS
0 viz_query_ring_simple 2002Sep12
PASS
0 wait_for_idle 2002Sep12
PASS
0 wait_reg_mem_poll_mem0 2002Sep12
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep12
PASS
725 wait_reg_mem_poll_mem1 2002Sep12
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
726 wait_reg_mem_poll_mem2 2002Sep12
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
727 wait_reg_mem_poll_mem3 2002Sep12
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_reg0 2002Sep12
PASS
729 wait_reg_mem_poll_reg1 2002Sep12
**FAIL** cp_rbbm.cmp
730 wait_reg_mem_poll_reg2 2002Sep12
**FAIL** cp_rbbm.cmp
731 wait_reg_mem_poll_reg3 2002Sep12
**FAIL** cp_rbbm.cmp
=====
Tests_Run_count = 731 Pass_count = 659 Fail_count = 72 Incomplete_count = 0
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct8
	PASS		
0	2d_indr_buffer_b		2002Oct8
	PASS		
0	2d_indr_buffer_c		2002Oct8
	PASS		
0	2d_indr_buffer_d		2002Oct8
	PASS		
0	2d_indr_buffer_e		2002Oct8
	PASS		
0	2d_indr_buffer_f		2002Oct8
	PASS		
0	2d_indr_buffer_g		2002Oct8
	PASS		
8	2d_indr_buffer_random		2002Oct8
	FAIL cp_mh_req.cmp mh_cp_rd.cmp		
9	2d_indr_buffer_random2		2002Oct8
	FAIL cp_mh_req.cmp mh_cp_rd.cmp		
0	3d_draw_indx_2		2002Oct8
	PASS		
0	3d_draw_indx_2_b		2002Oct8
	PASS		
0	IQ2_Mult		2002Oct8
	PASS		
0	IQ2_Wrap_Legacy		2002Oct8
	PASS		
0	IQ_Wrap		2002Oct8
	PASS		
0	Isync_Control		2002Oct8
	PASS		
0	Isync_Control_2D_3D		2002Oct8
	PASS		
0	Isync_Control_3D_2D		2002Oct8
	PASS		
0	RB_BUFSZ_0		2002Oct8
	PASS		
0	RB_BUFSZ_1		2002Oct8
	PASS		
0	RB_Wrap		2002Oct8

PASS		
0	Small_Text_BM	2002Oct8
PASS		
0	Type0_IQ2_BM	2002Oct8
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Oct8
PASS		
0	Type0_IQ_BM	2002Oct8
PASS		
0	Type0_IQ_BM_IB_Packet	2002Oct8
PASS		
0	Type0_PQ_BM	2002Oct8
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Oct8
PASS		
0	Type3_Bitblt	2002Oct8
PASS		
0	Type3_Hostdata_blt	2002Oct8
PASS		
0	Type3_Hostdata_blt_PIO	2002Oct8
PASS		
0	Type3_Load_Palette	2002Oct8
PASS		
0	Type3_NOP	2002Oct8
PASS		
0	Type3_Polyline	2002Oct8
PASS		
0	Type3_Polyline_Large	2002Oct8
PASS		
0	bitblt_multi	2002Oct8
PASS		
0	brush_32x1mono	2002Oct8
PASS		
0	brush_8x8color_16	2002Oct8
PASS		
0	brush_8x8color_32	2002Oct8
PASS		
0	brush_8x8color_64	2002Oct8
PASS		
0	brush_8x8color_dst_0_1	2002Oct8
PASS		
0	brush_8x8mono	2002Oct8
PASS		
0	brush_color_mono_stress	2002Oct8
PASS		
0	brush_flag	2002Oct8
PASS		
0	brush_type0	2002Oct8

PASS		
0	brush_type1	2002Oct8
PASS		
0	brush_type10	2002Oct8
PASS		
0	brush_type13	2002Oct8
PASS		
0	brush_type14	2002Oct8
PASS		
0	brush_type6	2002Oct8
PASS		
0	brush_type7	2002Oct8
PASS		
0	brush_type_other	2002Oct8
PASS		
0	brush_types_13_14	2002Oct8
PASS		
53	change_queue_sizes	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	clear_const_wr_en_on_me_init	2002Oct8
PASS		
0	clear_const_wr_en_with_lcc	2002Oct8
PASS		
0	cond_write_mem_poll_mem	2002Oct8
PASS		
0	cond_write_mem_poll_mem2	2002Oct8
PASS		
0	cond_write_mem_poll_mem3	2002Oct8
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct8
PASS		
0	cond_write_mem_poll_reg	2002Oct8
PASS		
0	cond_write_mem_poll_reg2	2002Oct8
PASS		
0	cond_write_mem_poll_reg3	2002Oct8
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct8
PASS		
0	cp_bios_scratch	2002Oct8
PASS		
65	cp_high_stress	2002Oct8
FAIL	mh_cp_rd.cmp	
0	cp_ib2_base000_bufszXXX	2002Oct8
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct8
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct8

PASS		
0	cp_ib2_base011_bufszXXX	20020ct8
PASS		
0	cp_ib2_base100_bufszXXX	20020ct8
PASS		
0	cp_ib2_base101_bufszXXX	20020ct8
PASS		
0	cp_ib2_base110_bufszXXX	20020ct8
PASS		
0	cp_ib2_base111_bufszXXX	20020ct8
PASS		
0	cp_ib_base000_bufszXXX	20020ct8
PASS		
0	cp_ib_base001_bufszXXX	20020ct8
PASS		
0	cp_ib_base010_bufszXXX	20020ct8
PASS		
0	cp_ib_base011_bufszXXX	20020ct8
PASS		
0	cp_ib_base100_bufszXXX	20020ct8
PASS		
0	cp_ib_base101_bufszXXX	20020ct8
PASS		
0	cp_ib_base110_bufszXXX	20020ct8
PASS		
0	cp_ib_base111_bufszXXX	20020ct8
PASS		
0	cp_interrupt	20020ct8
PASS		
0	cp_interrupt_rt_events_16	20020ct8
PASS		
0	cp_iq2_alignment_000	20020ct8
PASS		
0	cp_iq2_alignment_001	20020ct8
PASS		
0	cp_iq2_alignment_010	20020ct8
PASS		
0	cp_iq2_alignment_011	20020ct8
PASS		
0	cp_iq2_alignment_100	20020ct8
PASS		
0	cp_iq2_alignment_101	20020ct8
PASS		
0	cp_iq2_alignment_110	20020ct8
PASS		
0	cp_iq2_alignment_111	20020ct8
PASS		
0	cp_iq_alignment_000	20020ct8

PASS		
0	cp_iq_alignment_001	20020ct8
PASS		
0	cp_iq_alignment_010	20020ct8
PASS		
0	cp_iq_alignment_011	20020ct8
PASS		
0	cp_iq_alignment_100	20020ct8
PASS		
0	cp_iq_alignment_101	20020ct8
PASS		
0	cp_iq_alignment_110	20020ct8
PASS		
0	cp_iq_alignment_111	20020ct8
PASS		
0	cp_max_size_packet	20020ct8
PASS		
101	cp_max_size_packet_random	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	20020ct8
PASS		
0	cp_output_stress	20020ct8
PASS		
104	cp_output_stress_random	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_protection_mode	20020ct8
PASS		
0	cp_rb_alignment_000	20020ct8
PASS		
0	cp_rb_alignment_001	20020ct8
PASS		
0	cp_rb_alignment_010	20020ct8
PASS		
0	cp_rb_alignment_011	20020ct8
PASS		
0	cp_rb_alignment_100	20020ct8
PASS		
0	cp_rb_alignment_101	20020ct8
PASS		
0	cp_rb_alignment_110	20020ct8
PASS		
0	cp_rb_alignment_111	20020ct8
PASS		
0	cp_rb_wptrXXX_base000	20020ct8
PASS		
0	cp_rb_wptrXXX_base001	20020ct8
PASS		
0	cp_rb_wptrXXX_base010	20020ct8

PASS		
0	cp_rb_wptrXXX_base011	20020ct8
PASS		
0	cp_rb_wptrXXX_base100	20020ct8
PASS		
0	cp_rb_wptrXXX_base101	20020ct8
PASS		
0	cp_rb_wptrXXX_base110	20020ct8
PASS		
0	cp_rb_wptrXXX_base111	20020ct8
PASS		
0	cp_reg_rw_test	20020ct8
PASS		
0	cp_register_reads	20020ct8
PASS		
0	cp_rt_protection_mode	20020ct8
PASS		
0	cp_scratch	20020ct8
PASS		
0	cp_scratch_multi	20020ct8
PASS		
0	csf_reg_rd_default	20020ct8
PASS		
128	csf_reg_rd_only	20020ct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	csf_reg_wr_rd	20020ct8
PASS		
0	dma_cp_sync	20020ct8
PASS		
0	dma_dscrstr_chckn_bit	20020ct8
PASS		
0	dma_dscrstr_flush1	20020ct8
PASS		
0	dma_dscrstr_flush2	20020ct8
PASS		
0	dma_dscrstr_flush3	20020ct8
PASS		
0	dma_m2m_src00_dstXX	20020ct8
PASS		
0	dma_m2m_src01_dstXX	20020ct8
PASS		
0	dma_m2m_src10_dstXX	20020ct8
PASS		
0	dma_m2m_src11_dstXX	20020ct8
PASS		
0	dma_mem2mem	20020ct8
PASS		
0	dma_mem2mem_abort	20020ct8

PASS		
0	dma_mem2mem_alignments	20020ct8
PASS		
0	dma_mem2mem_byte_alignment0	20020ct8
PASS		
0	dma_mem2mem_byte_alignment1	20020ct8
PASS		
0	dma_mem2mem_byte_alignment2	20020ct8
PASS		
0	dma_mem2mem_byte_alignment3	20020ct8
PASS		
0	dma_mem2mem_byte_alignment4	20020ct8
PASS		
0	dma_mem2mem_byte_alignment5	20020ct8
PASS		
0	dma_mem2mem_byte_alignment6	20020ct8
PASS		
0	dma_mem2mem_byte_alignment7	20020ct8
PASS		
0	dma_mem2mem_large	20020ct8
PASS		
151	dma_mem2mem_pio_abort	20020ct8
FAIL		
152	dma_mem2mem_random	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	dma_mem2reg	20020ct8
PASS		
154	dma_mem2reg_abort	20020ct8
FAIL	rbbmWr.cmp	
0	dma_mem2reg_dtaq_stress	20020ct8
PASS		
156	dma_mem2reg_dtaq_stress_random	20020ct8
FAIL	mh_cp_rd.cmp	
0	dma_mem2reg_pio_stressbm	20020ct8
PASS		
0	dma_mem2reg_pio_stresspio	20020ct8
PASS		
0	dma_mem2reg_sync	20020ct8
PASS		
0	dma_r2r_m2m_r2r	20020ct8
PASS		
0	dma_r2r_m2r_r2r	20020ct8
PASS		
0	dma_r2r_r2m_r2r	20020ct8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	20020ct8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	20020ct8

PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct8
PASS		
0	dma_reg2mem	2002Oct8
PASS		
0	dma_reg2mem_abort	2002Oct8
PASS		
0	dma_reg2reg	2002Oct8
PASS		
0	dma_reg2reg_2dw	2002Oct8
PASS		
0	dma_reg2reg_daic	2002Oct8
PASS		
0	dma_reg2reg_mult	2002Oct8
PASS		
0	dma_reg2reg_mult2	2002Oct8
PASS		
0	dma_reg2reg_mult3	2002Oct8
PASS		
0	dma_reg2reg_saic	2002Oct8
PASS		
0	dma_reg2reg_sdaic	2002Oct8
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct8
PASS		
0	dma_reg2reg_stress	2002Oct8
PASS		
0	draw_indx_a	2002Oct8
PASS		
0	draw_indx_b	2002Oct8
PASS		
0	draw_indx_c	2002Oct8
PASS		
0	draw_indx_indr1_a	2002Oct8
PASS		
0	draw_indx_indr1_b	2002Oct8
PASS		
0	draw_indx_indr1_short_a	2002Oct8
PASS		
0	draw_indx_indr1_short_b	2002Oct8
PASS		
0	draw_indx_indr1_stress	2002Oct8
PASS		
0	draw_indx_indr2_a	2002Oct8
PASS		
0	draw_indx_indr2_b	2002Oct8
PASS		
0	draw_indx_indr2_short_a	2002Oct8

PASS		
0	draw_indx_indr2_short_b	20020ct8
PASS		
0	draw_indx_indr2_stress	20020ct8
PASS		
191	draw_indx_random_a	20020ct8
FAIL	mh_cp_rd.cmp	
192	draw_indx_random_b	20020ct8
FAIL	mh_cp_rd.cmp	
193	draw_indx_random_c	20020ct8
FAIL	mh_cp_rd.cmp	
0	draw_indx_ring1	20020ct8
PASS		
0	draw_indx_ring2	20020ct8
PASS		
0	draw_indx_ring_short1	20020ct8
PASS		
0	draw_indx_ring_short2	20020ct8
PASS		
0	draw_indx_ring_stress	20020ct8
PASS		
0	draw_indx_short_a	20020ct8
PASS		
0	draw_indx_short_b	20020ct8
PASS		
0	draw_indx_short_c	20020ct8
PASS		
0	draw_indx_stress_a	20020ct8
PASS		
0	draw_indx_stress_b	20020ct8
PASS		
0	draw_indx_stress_c	20020ct8
PASS		
0	event_timestamp_write	20020ct8
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	20020ct8
PASS		
0	fix2flt_reg	20020ct8
PASS		
0	ft_indr1_july22	20020ct8
PASS		
0	ft_indr2_july22	20020ct8
PASS		
0	ft_july22	20020ct8
PASS		
0	ft_july22_random	20020ct8
PASS		
0	header_dump	20020ct8

PASS		
0	header_dump_rt	2002Oct8
PASS		
0	header_dump_small	2002Oct8
PASS		
0	hostdata_blt	2002Oct8
PASS		
0	im_load_a	2002Oct8
PASS		
0	im_load_b	2002Oct8
PASS		
0	im_load_cid_mismatch	2002Oct8
PASS		
219	im_load_immediate	2002Oct8
FAIL	rbbmWr.cmp	
220	im_load_immediate_nrt_me	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
221	im_load_immediate_nrt_me_ps_nowrap	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
222	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
223	im_load_immediate_nrt_me_ps_wrap	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
224	im_load_immediate_nrt_me_vs_nowrap	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
225	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
226	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
227	im_load_immediate_nrt_me_vs_wrap	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_indr1_a	2002Oct8
PASS		
0	im_load_indr1_b	2002Oct8
PASS		
0	im_load_indr2_a	2002Oct8
PASS		
0	im_load_indr2_b	2002Oct8
PASS		
0	im_load_invalidate	2002Oct8
PASS		
0	im_load_match	2002Oct8
PASS		
0	im_load_match2	2002Oct8
PASS		
235	im_load_nrt_me	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
236	im_load_nrt_me_ps_nowrap	2002Oct8

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**FAIL**          cp_rbbm.cmp rbbmWr.cmp
237 im_load_nrt_me_ps_wrap                20020ct8
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
238 im_load_nrt_me_vs_nowrap              20020ct8
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
239 im_load_nrt_me_vs_wrap                20020ct8
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   im_load_pfmatch_disable               20020ct8
PASS
241 im_load_random_a                      20020ct8
**FAIL**          mh_cp_rd.cmp
242 im_load_random_b                      20020ct8
**FAIL**          mh_cp_rd.cmp
243 im_load_random_c                      20020ct8
**FAIL**          mh_cp_rd.cmp
0   im_load_ring_a                       20020ct8
PASS
0   im_load_ring_b                       20020ct8
PASS
0   im_load_rt                            20020ct8
PASS
0   im_load_rt2                           20020ct8
PASS
0   im_load_rt3                           20020ct8
PASS
0   im_load_rt4                           20020ct8
PASS
0   im_load_sz_mismatch                   20020ct8
PASS
251 incr_update_cnst_random               20020ct8
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0   incremental_update_const              20020ct8
PASS
0   incremental_update_instr              20020ct8
PASS
0   incremental_update_state              20020ct8
PASS
0   indirect_buffer_a                     20020ct8
PASS
0   indirect_buffer_b                     20020ct8
PASS
0   indirect_buffer_c                     20020ct8
PASS
0   indirect_buffer_d                     20020ct8
PASS
0   indirect_buffer_e                     20020ct8
PASS
0   indirect_buffer_f                     20020ct8

```

PASS		
0	indirect_buffer_g	20020ct8
PASS		
0	indirect_buffer_h	20020ct8
PASS		
0	indirect_buffer_i	20020ct8
PASS		
0	indirect_buffer_j	20020ct8
PASS		
0	indirect_buffer_k	20020ct8
PASS		
0	indirect_buffer_l	20020ct8
PASS		
0	indirect_buffer_mp_a	20020ct8
PASS		
0	indirect_buffer_mp_b	20020ct8
PASS		
0	indirect_buffer_mp_c	20020ct8
PASS		
0	indirect_buffer_mp_d	20020ct8
PASS		
0	indirect_buffer_mp_e	20020ct8
PASS		
0	indirect_buffer_mp_f	20020ct8
PASS		
273	indirect_buffer_mp_random1	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
274	indirect_buffer_mp_random2	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
275	indirect_buffer_random1	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
276	indirect_buffer_random2	20020ct8
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	20020ct8
PASS		
0	invalidate_on_me_init	20020ct8
PASS		
279	invalidate_on_me_init_random	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalidate_state_constants	20020ct8
PASS		
0	invalidate_state_shaders	20020ct8
PASS		
0	invalidate_state_shaders2	20020ct8
PASS		
283	invalidate_state_shaders_random	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalidate_state_sublks	20020ct8

PASS		
0	invalidate_state_sublks2	2002Oct8
PASS		
286	invalidate_state_sublks_random	2002Oct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	iq1_iq2_mult	2002Oct8
PASS		
0	iq1_iq2_push_wrap	2002Oct8
PASS		
0	iq2_csq_full	2002Oct8
PASS		
290	lcc_coherency_cntr_random	2002Oct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
291	lcc_match_random	2002Oct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
292	lcc_mismatch_alutex_random	2002Oct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
293	lcc_mismatch_base_random	2002Oct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_coherency_cntrl	2002Oct8
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Oct8
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct8
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct8
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct8
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct8
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct8
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct8
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct8
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct8
PASS		
0	ld_cnst_cntx_match	2002Oct8
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct8
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct8
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct8
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct8

PASS		
0	ld_cnst_cntx_pfmatch_disable	20020ct8
PASS		
0	ld_cnst_cntx_replicate4_alu	20020ct8
PASS		
0	ld_cnst_cntx_replicate4_tex	20020ct8
PASS		
0	ld_cnst_cntx_replicate8	20020ct8
PASS		
0	ld_cnst_cntx_replicate_alu	20020ct8
PASS		
0	ld_cnst_cntx_replicate_tex	20020ct8
PASS		
0	ld_cnst_cntx_ring_replicate	20020ct8
PASS		
0	ld_cnst_cntx_ring_replicate4	20020ct8
PASS		
0	ld_cnst_cntx_ring_replicate8	20020ct8
PASS		
0	ld_cnst_cntx_ring_simple	20020ct8
PASS		
0	ld_cnst_cntx_simple_alu	20020ct8
PASS		
0	ld_cnst_cntx_simple_tex	20020ct8
PASS		
0	load_constant_context_rt	20020ct8
PASS		
0	load_palette	20020ct8
PASS		
0	lp_2d3d	20020ct8
PASS		
324	me_reg_wr_rd	20020ct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	20020ct8
PASS		
0	mem_write_rt	20020ct8
PASS		
0	mpeg_index	20020ct8
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct8
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct8
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct8
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct8
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct8

PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct8
PASS		
0	mpeg_index_multi_context_max_context_6	2002Oct8
PASS		
0	mpeg_index_multi_context_max_context_7	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct8
PASS		
0	nop_eating_disabled	2002Oct8
PASS		
0	nrt_rt_interaction1	2002Oct8
PASS		
0	paint_multi	2002Oct8
PASS		
0	paint_packet	2002Oct8
PASS		
0	paint_packet_gui_control	2002Oct8
PASS		
348	pfpr_reg_reads1	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
349	pfpr_reg_reads2	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	pitch_clip	2002Oct8
PASS		
0	ply_nextscan	2002Oct8
PASS		
0	rbbm_NQWU_BIF_01	2002Oct8
PASS		
0	rbbm_NQWU_BIF_02	2002Oct8
PASS		
0	rbbm_NQWU_CF_01	2002Oct8
PASS		
0	rbbm_NQWU_CPnq_01	2002Oct8
PASS		
0	rbbm_WU_2D_idle_01	2002Oct8

PASS		
0	rbbm_WU_2D_idle_02	2002Oct8
PASS		
0	rbbm_WU_2D_idle_03	2002Oct8
PASS		
0	rbbm_WU_2D_idle_04	2002Oct8
PASS		
360	rbbm_WU_2D_idle_05	2002Oct8
FAIL	rbbmWr.cmp	
0	rbbm_WU_2D_idle_06	2002Oct8
PASS		
0	rbbm_WU_2D_idle_07	2002Oct8
PASS		
0	rbbm_WU_2D_idle_08	2002Oct8
PASS		
0	rbbm_WU_2D_idle_09	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct8
PASS		
369	rbbm_WU_2D_idle_clean_05	2002Oct8
FAIL	rbbmWr.cmp	
0	rbbm_WU_2D_idle_clean_06	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct8
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct8
PASS		
376	rbbm_WU_2D_idle_clean_12	2002Oct8
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_01	2002Oct8
PASS		
0	rbbm_WU_3D_idle_02	2002Oct8
PASS		
0	rbbm_WU_3D_idle_03	2002Oct8
PASS		
0	rbbm_WU_3D_idle_04	2002Oct8

PASS		
381	rbbm_WU_3D_idle_05	2002Oct8
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_06	2002Oct8
PASS		
0	rbbm_WU_3D_idle_07	2002Oct8
PASS		
0	rbbm_WU_3D_idle_08	2002Oct8
PASS		
0	rbbm_WU_3D_idle_09	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct8
PASS		
390	rbbm_WU_3D_idle_clean_05	2002Oct8
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_clean_06	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct8
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct8
PASS		
397	rbbm_WU_3D_idle_clean_12	2002Oct8
FAIL	rbbmWr.cmp	
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct8
PASS		
0	rbbm_WU_BOTH_GRPX_update_pending_off	2002Oct8
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct8
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct8
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct8
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct8
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct8

PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct8
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct8
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct8
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct8
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct8
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct8
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct8
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct8
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct8
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct8
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct8
PASS		
416	rbbm_WU_cmdfifo_entries	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_WU_idct_semaphore	2002Oct8
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct8
PASS		
419	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct8
FAIL	cp_rbbm.cmp	
420	rbbm_WUrt_BOTH_GRP update_pending_off	2002Oct8
FAIL	cp_rbbm.cmp	
421	rbbm_WUrt_CP_dma_idle	2002Oct8
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
422	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct8
FAIL	cp_rbbm.cmp	
423	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct8
FAIL	cp_rbbm.cmp	
424	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct8
FAIL	cp_rbbm.cmp	
425	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct8
FAIL	cp_rbbm.cmp	
426	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct8
FAIL	cp_rbbm.cmp	
427	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct8
FAIL	cp_rbbm.cmp	
428	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct8

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**FAIL**      cp_rbbm.cmp
429  rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off      2002Oct8
**FAIL**      cp_rbbm.cmp
430  rbbm_WUrt_MH_hdp_clean                              2002Oct8
**FAIL**      cp_rbbm.cmp
431  rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off          2002Oct8
**FAIL**      cp_rbbm.cmp
432  rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off          2002Oct8
**FAIL**      cp_rbbm.cmp
0    rbbm_WUrt_RT_idle_01                                2002Oct8
PASS
0    rbbm_WUrt_RT_idle_02                                2002Oct8
PASS
0    rbbm_WUrt_RT_idle_03                                2002Oct8
PASS
0    rbbm_WUrt_RT_idle_04                                2002Oct8
PASS
437  rbbm_WUrt_RT_idle_clean_01                          2002Oct8
**FAIL**      cp_rbbm.cmp
438  rbbm_WUrt_RT_idle_clean_02                          2002Oct8
**FAIL**      cp_rbbm.cmp
439  rbbm_WUrt_RT_idle_clean_03                          2002Oct8
**FAIL**      cp_rbbm.cmp
440  rbbm_WUrt_RT_idle_clean_04                          2002Oct8
**FAIL**      cp_rbbm.cmp
0    rbbm_WUrt_RT_idle_clean_05                          2002Oct8
PASS
442  rbbm_WUrt_VIPH0_dma_idle                             2002Oct8
**FAIL**      cp_rbbm.cmp
443  rbbm_WUrt_VIPH1_dma_idle                             2002Oct8
**FAIL**      cp_rbbm.cmp
444  rbbm_WUrt_VIPH2_dma_idle                             2002Oct8
**FAIL**      cp_rbbm.cmp
445  rbbm_WUrt_VIPH3_dma_idle                             2002Oct8
**FAIL**      cp_rbbm.cmp
446  rbbm_WUrt_pad_ext_sig_rising_edge                   2002Oct8
**FAIL**      cp_rbbm.cmp
0    rbbm_clk_active_deassert_min_max                    2002Oct8
PASS
0    rbbm_clk_active_each_pipe                           2002Oct8
PASS
0    rbbm_clk_active_for_read_error                       2002Oct8
PASS
0    rbbm_clk_active_for_read_request                     2002Oct8
PASS
0    rbbm_clk_active_on_reset                             2002Oct8
PASS
0    rbbm_cp_non_queued_rdwr_01                           2002Oct8

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PASS
453  rbbm_cp_non_queued_rdwr_02                                2002Oct8
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
454  rbbm_cp_queued_holds_nonqueued                          2002Oct8
**FAIL**          cp_rbbm.cmp
455  rbbm_cp_queued_rdwr                                      2002Oct8
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
0    rbbm_hi_data_and_be_swap                                2002Oct8
PASS
0    rbbm_hi_data_endian_swap                                2002Oct8
PASS
0    rbbm_hi_data_no_swap_for_cppush                         2002Oct8
PASS
0    rbbm_hi_data_swap_for_cppush                            2002Oct8
PASS
0    rbbm_int_ack                                             2002Oct8
PASS
461  rbbm_int_each_client                                    2002Oct8
**FAIL**
0    rbbm_int_nrt_gfxpipe_idle                               2002Oct8
PASS
0    rbbm_int_nrt_gfxpipe_idle_rderr                         2002Oct8
PASS
0    rbbm_int_rderr                                          2002Oct8
PASS
0    rbbm_int_st_restore_nrt_gfxpipe_idle_rderr             2002Oct8
PASS
0    rbbm_isync_cpscratch_idlegui                           2002Oct8
PASS
0    rbbm_isync_wait_idlegui                                 2002Oct8
PASS
0    rbbm_soft_reset                                         2002Oct8
PASS
0    rbbm_vgt_default_req                                    2002Oct8
PASS
470  rbbm_vgt_max_req                                        2002Oct8
**FAIL**
471  rbbm_vgt_min_req                                        2002Oct8
**FAIL**
0    reg_to_mem                                              2002Oct8
PASS
0    reg_to_mem_rt                                           2002Oct8
PASS
0    ring_il_pio_i2_bm                                        2002Oct8
PASS
475  ring_pio_ibs_bm_stress                                  2002Oct8
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    rop_cfd_wmd                                             2002Oct8

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PASS		
477	roq_reg_reads	2002Oct8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rstr_ordr	2002Oct8
PASS		
0	rt_invalid_opcode	2002Oct8
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct8
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct8
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct8

PASS		
0	rtee_discretes_falling_19_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct8
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_09_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct8

PASS		
0	rtee_discretes_invert_11_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct8
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct8
PASS		
0	rtee_discretes_level_00_events_16	2002Oct8
PASS		
0	rtee_discretes_level_01_events_16	2002Oct8
PASS		
0	rtee_discretes_level_02_events_16	2002Oct8

PASS		
0	rtee_discretes_level_03_events_16	2002Oct8
PASS		
0	rtee_discretes_level_04_events_16	2002Oct8
PASS		
0	rtee_discretes_level_05_events_16	2002Oct8
PASS		
0	rtee_discretes_level_06_events_16	2002Oct8
PASS		
0	rtee_discretes_level_07_events_16	2002Oct8
PASS		
0	rtee_discretes_level_08_events_16	2002Oct8
PASS		
0	rtee_discretes_level_09_events_16	2002Oct8
PASS		
0	rtee_discretes_level_10_events_16	2002Oct8
PASS		
0	rtee_discretes_level_11_events_16	2002Oct8
PASS		
0	rtee_discretes_level_12_events_16	2002Oct8
PASS		
0	rtee_discretes_level_13_events_16	2002Oct8
PASS		
0	rtee_discretes_level_14_events_16	2002Oct8
PASS		
0	rtee_discretes_level_15_events_16	2002Oct8
PASS		
0	rtee_discretes_level_16_events_16	2002Oct8
PASS		
0	rtee_discretes_level_17_events_16	2002Oct8
PASS		
0	rtee_discretes_level_18_events_16	2002Oct8
PASS		
0	rtee_discretes_level_19_events_16	2002Oct8
PASS		
0	rtee_discretes_level_20_events_16	2002Oct8
PASS		
0	rtee_discretes_level_21_events_16	2002Oct8
PASS		
0	rtee_discretes_level_22_events_16	2002Oct8
PASS		
0	rtee_discretes_level_23_events_16	2002Oct8
PASS		
0	rtee_discretes_level_24_events_16	2002Oct8
PASS		
0	rtee_discretes_level_25_events_16	2002Oct8
PASS		
0	rtee_discretes_level_26_events_16	2002Oct8

PASS		
0	rtee_discretes_level_27_events_16	2002Oct8
PASS		
0	rtee_discretes_level_28_events_16	2002Oct8
PASS		
0	rtee_discretes_level_29_events_16	2002Oct8
PASS		
0	rtee_discretes_level_30_events_16	2002Oct8
PASS		
0	rtee_discretes_level_31_events_16	2002Oct8
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_16_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct8

PASS		
0	rtee_discretes_rising_18_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_23_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct8
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct8
PASS		
0	rtee_display1_comp_events_16	2002Oct8
PASS		
0	rtee_display2_comp_events_16	2002Oct8
PASS		
0	rtee_mem_linear_search_events_2	2002Oct8
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct8
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct8
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct8
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct8
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct8
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct8
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct8

```

PASS
0   rtee_mem_poll_comp_111_events_16           2002Oct8
PASS
0   rtee_mem_poll_comp_masks_events_16         2002Oct8
PASS
623 rtee_mem_poll_comp_masks_events_16_random  2002Oct8
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0   rtee_mem_poll_comp_swaps_events_16         2002Oct8
PASS
0   rtee_mem_poll_intervals_events_16          2002Oct8
PASS
0   rtee_mem_poll_short_intervals_events_16    2002Oct8
PASS
0   rtee_pio                                   2002Oct8
PASS
0   rtee_prefetch_parse_nops                   2002Oct8
PASS
0   rtee_reg_linear_search_events_2            2002Oct8
PASS
0   rtee_reg_poll_comp_000_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_001_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_010_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_011_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_100_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_101_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_110_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_111_events_16           2002Oct8
PASS
0   rtee_reg_poll_comp_queued_nonqueued_events_16 2002Oct8
PASS
0   rtee_vip_comp_events_16                    2002Oct8
PASS
0   scratch_mem_read                           2002Oct8
PASS
0   scratch_mem_read2                          2002Oct8
PASS
642 set_cnst_alu_max_random                     2002Oct8
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
643 set_cnst_we_lcc_random                      2002Oct8
**FAIL**          mh_cp_rd.cmp
644 set_cnst_we_random                          2002Oct8

```

```

**FAIL**      mh_cp_rd.cmp
0  set_constant 20020ct8
PASS
0  set_constant2 20020ct8
PASS
647 set_constant_alu_max1 20020ct8
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
648 set_constant_alu_max2 20020ct8
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0  set_constant_bool_max1 20020ct8
PASS
0  set_constant_bool_max2 20020ct8
PASS
0  set_constant_indr1_short 20020ct8
PASS
0  set_constant_indr1_short2 20020ct8
PASS
0  set_constant_indr1_write_enable 20020ct8
PASS
0  set_constant_indr1_write_enable_lcc 20020ct8
PASS
0  set_constant_indr2_short 20020ct8
PASS
0  set_constant_indr2_short2 20020ct8
PASS
0  set_constant_indr2_write_enable 20020ct8
PASS
0  set_constant_indr2_write_enable_lcc 20020ct8
PASS
0  set_constant_loop_max1 20020ct8
PASS
0  set_constant_loop_max2 20020ct8
PASS
0  set_constant_ring_short 20020ct8
PASS
0  set_constant_ring_short2 20020ct8
PASS
0  set_constant_ring_write_enable 20020ct8
PASS
0  set_constant_ring_write_enable_lcc 20020ct8
PASS
0  set_constant_short 20020ct8
PASS
0  set_constant_short2 20020ct8
PASS
667 set_constant_tex_max1 20020ct8
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
668 set_constant_tex_max2 20020ct8

```

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0  set_constant_write_enable          2002Oct8
PASS
0  set_constant_write_enable_lcc      2002Oct8
PASS
0  set_constant_write_enable_lcc2     2002Oct8
PASS
0  set_scissors                       2002Oct8
PASS
0  set_st_indrl_match                 2002Oct8
PASS
0  set_st_indrl_mismatch              2002Oct8
PASS
0  set_st_indrl_mismatch2             2002Oct8
PASS
0  set_st_indrl_mismatch3             2002Oct8
PASS
0  set_st_indrl_mismatch4             2002Oct8
PASS
0  set_st_indrl_ps                    2002Oct8
PASS
0  set_st_indrl_ps_only                2002Oct8
PASS
0  set_st_indrl_sublk0                 2002Oct8
PASS
0  set_st_indrl_sublk1                 2002Oct8
PASS
0  set_st_indrl_sublk1_only            2002Oct8
PASS
0  set_st_indrl_sublk2                 2002Oct8
PASS
0  set_st_indrl_sublk2_only            2002Oct8
PASS
0  set_st_indrl_sublk3                 2002Oct8
PASS
0  set_st_indrl_sublk3_only            2002Oct8
PASS
0  set_st_indrl_sublk4                 2002Oct8
PASS
0  set_st_indrl_sublk4_only            2002Oct8
PASS
0  set_st_indrl_sublk5                 2002Oct8
PASS
0  set_st_indrl_sublk5_only            2002Oct8
PASS
0  set_st_indrl_sublk6                 2002Oct8
PASS
0  set_st_indrl_sublk6_only            2002Oct8

```


PASS		
0	set_st_indr1_sublk7	20020ct8
PASS		
0	set_st_indr1_sublk7_only	20020ct8
PASS		
0	set_st_indr1_vs	20020ct8
PASS		
0	set_st_indr1_vs_only	20020ct8
PASS		
0	set_st_indr2_match	20020ct8
PASS		
0	set_st_indr2_mismatch	20020ct8
PASS		
0	set_st_indr2_mismatch2	20020ct8
PASS		
0	set_st_indr2_mismatch3	20020ct8
PASS		
0	set_st_indr2_mismatch4	20020ct8
PASS		
0	set_st_indr2_ps	20020ct8
PASS		
0	set_st_indr2_ps_only	20020ct8
PASS		
0	set_st_indr2_sublk0	20020ct8
PASS		
0	set_st_indr2_sublk1	20020ct8
PASS		
0	set_st_indr2_sublk1_only	20020ct8
PASS		
0	set_st_indr2_sublk2	20020ct8
PASS		
0	set_st_indr2_sublk2_only	20020ct8
PASS		
0	set_st_indr2_sublk3	20020ct8
PASS		
0	set_st_indr2_sublk3_only	20020ct8
PASS		
0	set_st_indr2_sublk4	20020ct8
PASS		
0	set_st_indr2_sublk4_only	20020ct8
PASS		
0	set_st_indr2_sublk5	20020ct8
PASS		
0	set_st_indr2_sublk5_only	20020ct8
PASS		
0	set_st_indr2_sublk6	20020ct8
PASS		
0	set_st_indr2_sublk6_only	20020ct8

PASS		
0	set_st_indr2_sublk7	20020ct8
PASS		
0	set_st_indr2_sublk7_only	20020ct8
PASS		
0	set_st_indr2_vs	20020ct8
PASS		
0	set_st_indr2_vs_only	20020ct8
PASS		
0	set_st_pfmatch_disabled	20020ct8
PASS		
0	set_st_ring_match	20020ct8
PASS		
0	set_st_ring_mismatch	20020ct8
PASS		
0	set_st_ring_mismatch2	20020ct8
PASS		
0	set_st_ring_mismatch3	20020ct8
PASS		
0	set_st_ring_mismatch4	20020ct8
PASS		
0	set_st_ring_ps	20020ct8
PASS		
0	set_st_ring_ps_only	20020ct8
PASS		
0	set_st_ring_sublk0	20020ct8
PASS		
0	set_st_ring_sublk1	20020ct8
PASS		
0	set_st_ring_sublk1_only	20020ct8
PASS		
0	set_st_ring_sublk2	20020ct8
PASS		
0	set_st_ring_sublk2_only	20020ct8
PASS		
0	set_st_ring_sublk3	20020ct8
PASS		
0	set_st_ring_sublk3_only	20020ct8
PASS		
0	set_st_ring_sublk4	20020ct8
PASS		
0	set_st_ring_sublk4_only	20020ct8
PASS		
0	set_st_ring_sublk5	20020ct8
PASS		
0	set_st_ring_sublk5_only	20020ct8
PASS		
0	set_st_ring_sublk6	20020ct8

```

PASS
0   set_st_ring_sublk6_only          20020ct8
PASS
0   set_st_ring_sublk7              20020ct8
PASS
0   set_st_ring_sublk7_only         20020ct8
PASS
0   set_st_ring_vs                  20020ct8
PASS
0   set_st_ring_vs_only             20020ct8
PASS
0   set_st_stress_a                 20020ct8
PASS
0   set_st_stress_b                 20020ct8
PASS
748 set_st_stress_b_random           20020ct8
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0   set_st_stress_c                 20020ct8
PASS
0   set_st_stress_d                 20020ct8
PASS
751 set_st_stress_d_random           20020ct8
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0   set_st_stress_e                 20020ct8
PASS
0   set_st_stress_f                 20020ct8
PASS
754 set_st_stress_f_random           20020ct8
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0   set_state_rt_a                  20020ct8
PASS
0   set_state_rt_b                  20020ct8
PASS
0   set_state_rt_c                  20020ct8
PASS
0   set_state_rt_d                  20020ct8
PASS
759 smoke_nrt                       20020ct8
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   src_clip                        20020ct8
PASS
0   srf_cohr                        20020ct8
PASS
762 switch_pio_bm                   20020ct8
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   trans_bitblt                    20020ct8
PASS
0   type0_2long_pckts              20020ct8

```

PASS		
0	type0_3indr_buffs	20020ct8
PASS		
0	type0_4pckts	20020ct8
PASS		
0	type0_incr_updt_cnst	20020ct8
PASS		
0	type0_incr_updt_instrmem	20020ct8
PASS		
0	type0_incr_updt_st	20020ct8
PASS		
0	type0_incr_updt_stress	20020ct8
PASS		
0	type0_ind1_ind2_a	20020ct8
PASS		
0	type0_ind1_ind2_b	20020ct8
PASS		
0	type0_indr_buff	20020ct8
PASS		
0	type1_4pckts	20020ct8
PASS		
0	type1_indr_buff	20020ct8
PASS		
0	type1_pckt	20020ct8
PASS		
777	type_random	20020ct8
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
778	uram_wrt_rt_ucose	20020ct8
FAIL		
779	uram_wrt_ucose	20020ct8
FAIL		
0	viz_query_a	20020ct8
PASS		
0	viz_query_draw_indx	20020ct8
PASS		
0	viz_query_draw_indx_indr1	20020ct8
PASS		
0	viz_query_draw_indx_indr2	20020ct8
PASS		
0	viz_query_draw_indx_multipass	20020ct8
PASS		
0	viz_query_draw_indx_ring	20020ct8
PASS		
0	viz_query_indr1	20020ct8
PASS		
0	viz_query_indr1_simple	20020ct8
PASS		
0	viz_query_indr2	20020ct8

PASS
0 viz_query_indr2_simple 2002Oct8
PASS
0 viz_query_ring 2002Oct8
PASS
0 viz_query_ring_simple 2002Oct8
PASS
0 wait_for_idle 2002Oct8
PASS
0 wait_reg_mem_poll_mem0 2002Oct8
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct8
PASS
0 wait_reg_mem_poll_mem1 2002Oct8
PASS
0 wait_reg_mem_poll_mem2 2002Oct8
PASS
0 wait_reg_mem_poll_mem3 2002Oct8
PASS
0 wait_reg_mem_poll_reg0 2002Oct8
PASS
0 wait_reg_mem_poll_reg1 2002Oct8
PASS
0 wait_reg_mem_poll_reg2 2002Oct8
PASS
0 wait_reg_mem_poll_reg3 2002Oct8
PASS

=====
Tests_Run_count = 801 Pass_count = 701 Fail_count = 100 Incomplete_count = 0
=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep 11  8:01:27 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          635         635         565         88
              RBBM        98          98          75          76
+++++
              TOTAL        733         733         640         87
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug26
	PASS		
0	2d_indr_buffer_b		2002Aug26
	PASS		
0	2d_indr_buffer_c		2002Aug26
	PASS		
0	2d_indr_buffer_d		2002Aug26
	PASS		
0	2d_indr_buffer_e		2002Aug26
	PASS		
0	2d_indr_buffer_f		2002Aug26
	PASS		
0	2d_indr_buffer_g		2002Aug26
	PASS		
0	3d_draw_indx_2		2002Aug26
	PASS		
0	3d_draw_indx_2_b		2002Aug26
	PASS		
0	bitblt_multi		2002Aug26
	PASS		
0	brush_32x1mono		2002Aug26
	PASS		
12	brush_8x8color_16		2002Aug26
	FAIL cp_rbbm.cmp rbbmWr.cmp		
13	brush_8x8color_32		2002Aug26
	FAIL cp_rbbm.cmp rbbmWr.cmp		
14	brush_8x8color_64		2002Aug26
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	brush_8x8color_dst_0_1		2002Aug26
	PASS		
0	brush_8x8mono		2002Aug26
	PASS		
17	brush_color_mono_stress		2002Aug26
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	brush_flag		2002Aug26
	PASS		
0	brush_type0		2002Aug26
	PASS		
0	brush_type1		2002Aug26

PASS		
0	brush_type10	2002Aug26
PASS		
0	brush_type13	2002Aug26
PASS		
0	brush_type14	2002Aug26
PASS		
0	brush_type6	2002Aug26
PASS		
0	brush_type7	2002Aug26
PASS		
0	brush_type_other	2002Aug26
PASS		
0	brush_types_13_14	2002Aug26
PASS		
0	clear_const_wr_en_on_me_init	2002Aug26
PASS		
0	clear_const_wr_en_with_lcc	2002Aug26
PASS		
0	cond_write_mem_poll_mem	2002Aug26
PASS		
0	cond_write_mem_poll_mem2	2002Aug26
PASS		
0	cond_write_mem_poll_mem3	2002Aug26
PASS		
0	cond_write_mem_poll_mem_rt	2002Aug26
PASS		
0	cond_write_mem_poll_reg	2002Aug26
PASS		
0	cond_write_mem_poll_reg2	2002Aug26
PASS		
0	cond_write_mem_poll_reg3	2002Aug26
PASS		
0	cp_acpi_recovery_soft_reset	2002Aug26
PASS		
0	cp_bios_scratch	2002Aug26
PASS		
39	cp_ib2_base000_bufszXXX	2002Aug26
FAIL	rbbmWr.cmp	
40	cp_ib2_base001_bufszXXX	2002Aug26
FAIL	rbbmWr.cmp	
41	cp_ib2_base010_bufszXXX	2002Aug26
FAIL	rbbmWr.cmp	
42	cp_ib2_base011_bufszXXX	2002Aug26
FAIL	rbbmWr.cmp	
43	cp_ib2_base100_bufszXXX	2002Aug26
FAIL	rbbmWr.cmp	
44	cp_ib2_base101_bufszXXX	2002Aug26


```

**FAIL**      rbbmWr.cmp
45  cp_ib2_base110_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
46  cp_ib2_base111_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
47  cp_ib_base000_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
48  cp_ib_base001_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
49  cp_ib_base010_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
50  cp_ib_base011_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
51  cp_ib_base100_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
52  cp_ib_base101_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
53  cp_ib_base110_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
54  cp_ib_base111_bufszXXX      2002Aug26
**FAIL**      rbbmWr.cmp
0   cp_interrupt                  2002Aug26
PASS
0   cp_interrupt_rt_events_16    2002Aug26
PASS
57  cp_iq2_alignment_000        2002Aug26
**FAIL**      rbbmWr.cmp
58  cp_iq2_alignment_001        2002Aug26
**FAIL**      rbbmWr.cmp
59  cp_iq2_alignment_010        2002Aug26
**FAIL**      rbbmWr.cmp
60  cp_iq2_alignment_011        2002Aug26
**FAIL**      rbbmWr.cmp
61  cp_iq2_alignment_100        2002Aug26
**FAIL**      rbbmWr.cmp
62  cp_iq2_alignment_101        2002Aug26
**FAIL**      rbbmWr.cmp
63  cp_iq2_alignment_110        2002Aug26
**FAIL**      rbbmWr.cmp
64  cp_iq2_alignment_111        2002Aug26
**FAIL**      rbbmWr.cmp
65  cp_iq_alignment_000         2002Aug26
**FAIL**      rbbmWr.cmp
66  cp_iq_alignment_001         2002Aug26
**FAIL**      rbbmWr.cmp
67  cp_iq_alignment_010         2002Aug26
**FAIL**      rbbmWr.cmp
68  cp_iq_alignment_011         2002Aug26

```

FAIL	rbbmWr.cmp	
69	cp_iq_alignment_100	2002Aug26
FAIL	rbbmWr.cmp	
70	cp_iq_alignment_101	2002Aug26
FAIL	rbbmWr.cmp	
71	cp_iq_alignment_110	2002Aug26
FAIL	rbbmWr.cmp	
72	cp_iq_alignment_111	2002Aug26
FAIL	rbbmWr.cmp	
0	cp_max_size_packet	2002Aug26
PASS		
0	cp_nrt_rt_protection_mode	2002Aug26
PASS		
0	cp_output_stress	2002Aug26
PASS		
0	cp_protection_mode	2002Aug26
PASS		
0	cp_rb_alignment_000	2002Aug26
PASS		
0	cp_rb_alignment_001	2002Aug26
PASS		
0	cp_rb_alignment_010	2002Aug26
PASS		
0	cp_rb_alignment_011	2002Aug26
PASS		
0	cp_rb_alignment_100	2002Aug26
PASS		
0	cp_rb_alignment_101	2002Aug26
PASS		
0	cp_rb_alignment_110	2002Aug26
PASS		
0	cp_rb_alignment_111	2002Aug26
PASS		
0	cp_rb_wptrXXX_base000	2002Aug26
PASS		
0	cp_rb_wptrXXX_base001	2002Aug26
PASS		
0	cp_rb_wptrXXX_base010	2002Aug26
PASS		
0	cp_rb_wptrXXX_base011	2002Aug26
PASS		
0	cp_rb_wptrXXX_base100	2002Aug26
PASS		
0	cp_rb_wptrXXX_base101	2002Aug26
PASS		
0	cp_rb_wptrXXX_base110	2002Aug26
PASS		
0	cp_rb_wptrXXX_base111	2002Aug26

PASS		
0	cp_register_reads	2002Aug26
PASS		
0	cp_reg_rw_test	2002Aug26
PASS		
0	cp_rt_protection_mode	2002Aug26
PASS		
0	cp_scratch	2002Aug26
PASS		
0	cp_scratch_multi	2002Aug26
PASS		
0	dma_cp_sync	2002Aug26
PASS		
0	dma_dscrstr_chckn_bit	2002Aug26
PASS		
0	dma_dscrstr_flush1	2002Aug26
PASS		
0	dma_dscrstr_flush2	2002Aug26
PASS		
0	dma_dscrstr_flush3	2002Aug26
PASS		
0	dma_m2m_src00_dstXX	2002Aug26
PASS		
0	dma_m2m_src01_dstXX	2002Aug26
PASS		
0	dma_m2m_src10_dstXX	2002Aug26
PASS		
0	dma_m2m_src11_dstXX	2002Aug26
PASS		
0	dma_mem2mem	2002Aug26
PASS		
0	dma_mem2mem_abort	2002Aug26
PASS		
0	dma_mem2mem_alignments	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug26
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug26

PASS		
0	dma_mem2mem_byte_alignment7	2002Aug26
PASS		
0	dma_mem2mem_pio_abort	2002Aug26
PASS		
0	dma_mem2reg	2002Aug26
PASS		
0	dma_mem2reg_abort	2002Aug26
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug26
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug26
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug26
PASS		
0	dma_mem2reg_sync	2002Aug26
PASS		
0	dma_r2r_m2m_r2r	2002Aug26
PASS		
0	dma_r2r_m2r_r2r	2002Aug26
PASS		
0	dma_r2r_r2m_r2r	2002Aug26
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug26
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug26
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug26
PASS		
0	dma_reg2mem	2002Aug26
PASS		
0	dma_reg2mem_abort	2002Aug26
PASS		
0	dma_reg2reg	2002Aug26
PASS		
0	dma_reg2reg_2dw	2002Aug26
PASS		
0	dma_reg2reg_daic	2002Aug26
PASS		
0	dma_reg2reg_mult	2002Aug26
PASS		
0	dma_reg2reg_mult2	2002Aug26
PASS		
0	dma_reg2reg_mult3	2002Aug26
PASS		
0	dma_reg2reg_saic	2002Aug26
PASS		
0	dma_reg2reg_sdaic	2002Aug26

PASS		
0	dma_reg2reg_sdaic_stress	2002Aug26
PASS		
0	dma_reg2reg_stress	2002Aug26
PASS		
0	draw_indx_a	2002Aug26
PASS		
0	draw_indx_b	2002Aug26
PASS		
0	draw_indx_c	2002Aug26
PASS		
0	draw_indx_indr1_a	2002Aug26
PASS		
0	draw_indx_indr1_b	2002Aug26
PASS		
0	draw_indx_indr1_short_a	2002Aug26
PASS		
0	draw_indx_indr1_short_b	2002Aug26
PASS		
0	draw_indx_indr1_stress	2002Aug26
PASS		
0	draw_indx_indr2_a	2002Aug26
PASS		
0	draw_indx_indr2_b	2002Aug26
PASS		
0	draw_indx_indr2_short_a	2002Aug26
PASS		
0	draw_indx_indr2_short_b	2002Aug26
PASS		
0	draw_indx_indr2_stress	2002Aug26
PASS		
0	draw_indx_ring1	2002Aug26
PASS		
0	draw_indx_ring2	2002Aug26
PASS		
0	draw_indx_ring_short1	2002Aug26
PASS		
0	draw_indx_ring_short2	2002Aug26
PASS		
0	draw_indx_ring_stress	2002Aug26
PASS		
0	draw_indx_short_a	2002Aug26
PASS		
0	draw_indx_short_b	2002Aug26
PASS		
0	draw_indx_short_c	2002Aug26
PASS		
0	draw_indx_stress_a	2002Aug26

PASS		
0	draw_indx_stress_b	2002Aug26
PASS		
0	draw_indx_stress_c	2002Aug26
PASS		
0	event_timestamp_write	2002Aug26
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug26
PASS		
0	ft_indr1_july22	2002Aug26
PASS		
0	ft_indr2_july22	2002Aug26
PASS		
0	ft_july22	2002Aug26
PASS		
0	header_dump	2002Aug26
PASS		
0	header_dump_rt	2002Aug26
PASS		
0	header_dump_small	2002Aug26
PASS		
175	hostdata_blt	2002Aug26
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Aug26
PASS		
0	im_load_b	2002Aug26
PASS		
0	im_load_cid_mismatch	2002Aug26
PASS		
0	im_load_immediate	2002Aug26
PASS		
0	im_load_immediate_nrt_me	2002Aug26
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug26
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug26
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug26
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug26
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug26
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug26
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug26
PASS		
0	im_load_indr1_a	2002Aug26

PASS		
0	im_load_indr1_b	2002Aug26
PASS		
0	im_load_indr2_a	2002Aug26
PASS		
0	im_load_indr2_b	2002Aug26
PASS		
0	im_load_invalidate	2002Aug26
PASS		
0	im_load_match	2002Aug26
PASS		
0	im_load_match2	2002Aug26
PASS		
0	im_load_nrt_me	2002Aug26
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug26
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug26
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug26
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug26
PASS		
0	im_load_pfmacth_disable	2002Aug26
PASS		
0	im_load_ring_a	2002Aug26
PASS		
0	im_load_ring_b	2002Aug26
PASS		
0	im_load_rt	2002Aug26
PASS		
0	im_load_rt2	2002Aug26
PASS		
0	im_load_rt3	2002Aug26
PASS		
0	im_load_rt4	2002Aug26
PASS		
0	im_load_sz_mismatch	2002Aug26
PASS		
0	incremental_update_const	2002Aug26
PASS		
0	incremental_update_instr	2002Aug26
PASS		
0	incremental_update_state	2002Aug26
PASS		
0	indirect_buffer_a	2002Aug26
PASS		
0	indirect_buffer_b	2002Aug26

PASS		
0	indirect_buffer_c	2002Aug26
PASS		
0	indirect_buffer_d	2002Aug26
PASS		
0	indirect_buffer_e	2002Aug26
PASS		
0	indirect_buffer_f	2002Aug26
PASS		
0	indirect_buffer_g	2002Aug26
PASS		
0	indirect_buffer_h	2002Aug26
PASS		
0	indirect_buffer_i	2002Aug26
PASS		
0	indirect_buffer_j	2002Aug26
PASS		
0	indirect_buffer_k	2002Aug26
PASS		
0	indirect_buffer_l	2002Aug26
PASS		
0	indirect_buffer_mp_a	2002Aug26
PASS		
0	indirect_buffer_mp_b	2002Aug26
PASS		
0	indirect_buffer_mp_c	2002Aug26
PASS		
0	indirect_buffer_mp_d	2002Aug26
PASS		
0	indirect_buffer_mp_e	2002Aug26
PASS		
0	indirect_buffer_mp_f	2002Aug26
PASS		
0	invalidate_on_me_init	2002Aug26
PASS		
0	invalidate_state_constants	2002Aug26
PASS		
0	invalidate_state_shaders	2002Aug26
PASS		
0	invalidate_state_shaders2	2002Aug26
PASS		
0	invalidate_state_sublks	2002Aug26
PASS		
0	invalidate_state_sublks2	2002Aug26
PASS		
0	invalid_opcode	2002Aug26
PASS		
236	iql_iq2_mult	2002Aug26


```

**FAIL**      rbbmWr.cmp
237 iq1_iq2_push_wrap      2002Aug26
**FAIL**      rbbmWr.cmp
0 iq2_csq_full      2002Aug26
PASS
0 IQ2_Mult      2002Aug26
PASS
240 IQ2_Wrap_Legacy      2002Aug26
**FAIL**      rbbmWr.cmp
241 IQ_Wrap      2002Aug26
**FAIL**      rbbmWr.cmp
0 ld_cnst_cntx_coherency_cntrl      2002Aug26
PASS
0 ld_cnst_cntx_coherency_cntrl2      2002Aug26
PASS
0 ld_cnst_cntx_indr1_replicate      2002Aug26
PASS
0 ld_cnst_cntx_indr1_replicate4      2002Aug26
PASS
0 ld_cnst_cntx_indr1_replicate8      2002Aug26
PASS
0 ld_cnst_cntx_indr1_simple      2002Aug26
PASS
0 ld_cnst_cntx_indr2_replicate      2002Aug26
PASS
0 ld_cnst_cntx_indr2_replicate4      2002Aug26
PASS
0 ld_cnst_cntx_indr2_replicate8      2002Aug26
PASS
0 ld_cnst_cntx_indr2_simple      2002Aug26
PASS
0 ld_cnst_cntx_match      2002Aug26
PASS
0 ld_cnst_cntx_match_valid_preserved      2002Aug26
PASS
0 ld_cnst_cntx_mismatch_alutex      2002Aug26
PASS
0 ld_cnst_cntx_mismatch_base      2002Aug26
PASS
0 ld_cnst_cntx_mismatch_force      2002Aug26
PASS
0 ld_cnst_cntx_pfmatch_disable      2002Aug26
PASS
0 ld_cnst_cntx_replicate4_alu      2002Aug26
PASS
0 ld_cnst_cntx_replicate4_tex      2002Aug26
PASS
0 ld_cnst_cntx_replicate8      2002Aug26

```

PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug26
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug26
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug26
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug26
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug26
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug26
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug26
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug26
PASS		
0	load_constant_context_rt	2002Aug26
PASS		
0	load_palette	2002Aug26
PASS		
0	mem_write	2002Aug26
PASS		
0	mem_write_rt	2002Aug26
PASS		
0	mpeg_index	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug26
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug26
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug26
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug26
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug26

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug26
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug26
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug26
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug26
PASS		
0	nop_eating_disabled	2002Aug26
PASS		
0	paint_multi	2002Aug26
PASS		
0	pitch_clip	2002Aug26
PASS		
0	ply_nextscan	2002Aug26
PASS		
0	rbbm_soft_reset	2002Aug26
PASS		
0	rbbm_WU_2D_idle_01	2002Aug26
PASS		
0	rbbm_WU_2D_idle_02	2002Aug26
PASS		
0	rbbm_WU_2D_idle_03	2002Aug26
PASS		
0	rbbm_WU_2D_idle_04	2002Aug26
PASS		
0	rbbm_WU_2D_idle_05	2002Aug26
PASS		
0	rbbm_WU_2D_idle_06	2002Aug26
PASS		
0	rbbm_WU_2D_idle_07	2002Aug26
PASS		
0	rbbm_WU_2D_idle_08	2002Aug26
PASS		
0	rbbm_WU_2D_idle_09	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Aug26

PASS		
0	rbbm_WU_2D_idle_clean_07	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Aug26
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Aug26
PASS		
0	rbbm_WU_3D_idle_01	2002Aug26
PASS		
0	rbbm_WU_3D_idle_02	2002Aug26
PASS		
0	rbbm_WU_3D_idle_03	2002Aug26
PASS		
0	rbbm_WU_3D_idle_04	2002Aug26
PASS		
0	rbbm_WU_3D_idle_05	2002Aug26
PASS		
0	rbbm_WU_3D_idle_06	2002Aug26
PASS		
0	rbbm_WU_3D_idle_07	2002Aug26
PASS		
0	rbbm_WU_3D_idle_08	2002Aug26
PASS		
0	rbbm_WU_3D_idle_09	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Aug26

PASS		
0	rbbm_WU_3D_idle_clean_10	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Aug26
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Aug26
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug26
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Aug26
PASS		
0	rbbm_WU_cmdfifo_entries	2002Aug26
PASS		
0	rbbm_WU_CP_dma_idle	2002Aug26
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug26
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug26
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug26
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug26
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug26
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug26
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Aug26
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Aug26
PASS		
0	rbbm_WU_idct_semaphore	2002Aug26
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug26
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug26
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug26
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug26
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Aug26
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Aug26
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Aug26

PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug26
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug26
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Aug26
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Aug26
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Aug26
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_01	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_02	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_03	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_04	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Aug26
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Aug26
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Aug26
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Aug26
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Aug26
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Aug26

PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Aug26
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Aug26
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Aug26
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Aug26
PASS		
0	RB_BUFSZ_0	2002Aug26
PASS		
0	RB_BUFSZ_1	2002Aug26
PASS		
0	RB_Wrap	2002Aug26
PASS		
0	reg_to_mem	2002Aug26
PASS		
0	reg_to_mem_rt	2002Aug26
PASS		
0	rop_cfd_wmd	2002Aug26
PASS		
0	rstr_ordr	2002Aug26
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Aug26
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Aug26
PASS		
0	rtee_discretes_falling_00_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug26

PASS		
0	rtee_discretes_falling_11_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug26
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug26

PASS		
0	rtee_discretes_invert_03_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_11_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug26

PASS		
0	rtee_discretes_invert_27_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug26
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug26
PASS		
0	rtee_discretes_level_00_events_16	2002Aug26
PASS		
0	rtee_discretes_level_01_events_16	2002Aug26
PASS		
0	rtee_discretes_level_02_events_16	2002Aug26
PASS		
0	rtee_discretes_level_03_events_16	2002Aug26
PASS		
0	rtee_discretes_level_04_events_16	2002Aug26
PASS		
0	rtee_discretes_level_05_events_16	2002Aug26
PASS		
0	rtee_discretes_level_06_events_16	2002Aug26
PASS		
0	rtee_discretes_level_07_events_16	2002Aug26
PASS		
0	rtee_discretes_level_08_events_16	2002Aug26
PASS		
0	rtee_discretes_level_09_events_16	2002Aug26
PASS		
0	rtee_discretes_level_10_events_16	2002Aug26
PASS		
0	rtee_discretes_level_11_events_16	2002Aug26
PASS		
0	rtee_discretes_level_12_events_16	2002Aug26
PASS		
0	rtee_discretes_level_13_events_16	2002Aug26
PASS		
0	rtee_discretes_level_14_events_16	2002Aug26
PASS		
0	rtee_discretes_level_15_events_16	2002Aug26
PASS		
0	rtee_discretes_level_16_events_16	2002Aug26
PASS		
0	rtee_discretes_level_17_events_16	2002Aug26
PASS		
0	rtee_discretes_level_18_events_16	2002Aug26

PASS		
0	rtee_discretes_level_19_events_16	2002Aug26
PASS		
0	rtee_discretes_level_20_events_16	2002Aug26
PASS		
0	rtee_discretes_level_21_events_16	2002Aug26
PASS		
0	rtee_discretes_level_22_events_16	2002Aug26
PASS		
0	rtee_discretes_level_23_events_16	2002Aug26
PASS		
0	rtee_discretes_level_24_events_16	2002Aug26
PASS		
0	rtee_discretes_level_25_events_16	2002Aug26
PASS		
0	rtee_discretes_level_26_events_16	2002Aug26
PASS		
0	rtee_discretes_level_27_events_16	2002Aug26
PASS		
0	rtee_discretes_level_28_events_16	2002Aug26
PASS		
0	rtee_discretes_level_29_events_16	2002Aug26
PASS		
0	rtee_discretes_level_30_events_16	2002Aug26
PASS		
0	rtee_discretes_level_31_events_16	2002Aug26
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug26

PASS		
0	rtee_discretes_rising_10_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug26
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug26
PASS		
0	rtee_display1_comp_events_16	2002Aug26
PASS		
0	rtee_display2_comp_events_16	2002Aug26

PASS		
0	rtee_mem_linear_search_events_2	2002Aug26
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug26
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug26
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug26
PASS		
0	rtee_prefetch_parse_nops	2002Aug26
PASS		
0	rtee_reg_linear_search_events_2	2002Aug26
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug26
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug26
PASS		
0	rtee_vip_comp_events_16	2002Aug26

PASS		
0	rt_invalid_opcode	2002Aug26
PASS		
0	scratch_mem_read	2002Aug26
PASS		
0	scratch_mem_read2	2002Aug26
PASS		
0	set_constant	2002Aug26
PASS		
0	set_constant2	2002Aug26
PASS		
0	set_constant_alu_max1	2002Aug26
PASS		
0	set_constant_alu_max2	2002Aug26
PASS		
0	set_constant_bool_max1	2002Aug26
PASS		
0	set_constant_bool_max2	2002Aug26
PASS		
0	set_constant_indr1_short	2002Aug26
PASS		
0	set_constant_indr1_short2	2002Aug26
PASS		
0	set_constant_indr1_write_enable	2002Aug26
PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug26
PASS		
0	set_constant_indr2_short	2002Aug26
PASS		
0	set_constant_indr2_short2	2002Aug26
PASS		
0	set_constant_indr2_write_enable	2002Aug26
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug26
PASS		
0	set_constant_loop_max1	2002Aug26
PASS		
0	set_constant_loop_max2	2002Aug26
PASS		
0	set_constant_ring_short	2002Aug26
PASS		
0	set_constant_ring_short2	2002Aug26
PASS		
0	set_constant_ring_write_enable	2002Aug26
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug26
PASS		
0	set_constant_short	2002Aug26

PASS		
0	set_constant_short2	2002Aug26
PASS		
0	set_constant_tex_max1	2002Aug26
PASS		
0	set_constant_tex_max2	2002Aug26
PASS		
0	set_constant_write_enable	2002Aug26
PASS		
0	set_constant_write_enable_lcc	2002Aug26
PASS		
0	set_constant_write_enable_lcc2	2002Aug26
PASS		
0	set_scissors	2002Aug26
PASS		
0	set_state_rt_a	2002Aug26
PASS		
0	set_state_rt_b	2002Aug26
PASS		
0	set_state_rt_c	2002Aug26
PASS		
0	set_state_rt_d	2002Aug26
PASS		
0	set_st_indrl_match	2002Aug26
PASS		
0	set_st_indrl_mismatch	2002Aug26
PASS		
0	set_st_indrl_mismatch2	2002Aug26
PASS		
0	set_st_indrl_mismatch3	2002Aug26
PASS		
0	set_st_indrl_mismatch4	2002Aug26
PASS		
0	set_st_indrl_ps	2002Aug26
PASS		
0	set_st_indrl_ps_only	2002Aug26
PASS		
0	set_st_indrl_sublk0	2002Aug26
PASS		
0	set_st_indrl_sublk1	2002Aug26
PASS		
0	set_st_indrl_sublk1_only	2002Aug26
PASS		
0	set_st_indrl_sublk2	2002Aug26
PASS		
0	set_st_indrl_sublk2_only	2002Aug26
PASS		
0	set_st_indrl_sublk3	2002Aug26

PASS		
0	set_st_indr1_sublk3_only	2002Aug26
PASS		
0	set_st_indr1_sublk4	2002Aug26
PASS		
0	set_st_indr1_sublk4_only	2002Aug26
PASS		
0	set_st_indr1_sublk5	2002Aug26
PASS		
0	set_st_indr1_sublk5_only	2002Aug26
PASS		
0	set_st_indr1_sublk6	2002Aug26
PASS		
0	set_st_indr1_sublk6_only	2002Aug26
PASS		
0	set_st_indr1_sublk7	2002Aug26
PASS		
0	set_st_indr1_sublk7_only	2002Aug26
PASS		
0	set_st_indr1_vs	2002Aug26
PASS		
0	set_st_indr1_vs_only	2002Aug26
PASS		
0	set_st_indr2_match	2002Aug26
PASS		
0	set_st_indr2_mismatch	2002Aug26
PASS		
0	set_st_indr2_mismatch2	2002Aug26
PASS		
0	set_st_indr2_mismatch3	2002Aug26
PASS		
0	set_st_indr2_mismatch4	2002Aug26
PASS		
0	set_st_indr2_ps	2002Aug26
PASS		
0	set_st_indr2_ps_only	2002Aug26
PASS		
0	set_st_indr2_sublk0	2002Aug26
PASS		
0	set_st_indr2_sublk1	2002Aug26
PASS		
0	set_st_indr2_sublk1_only	2002Aug26
PASS		
0	set_st_indr2_sublk2	2002Aug26
PASS		
0	set_st_indr2_sublk2_only	2002Aug26
PASS		
0	set_st_indr2_sublk3	2002Aug26

PASS		
0	set_st_indr2_sublk3_only	2002Aug26
PASS		
0	set_st_indr2_sublk4	2002Aug26
PASS		
0	set_st_indr2_sublk4_only	2002Aug26
PASS		
0	set_st_indr2_sublk5	2002Aug26
PASS		
0	set_st_indr2_sublk5_only	2002Aug26
PASS		
0	set_st_indr2_sublk6	2002Aug26
PASS		
0	set_st_indr2_sublk6_only	2002Aug26
PASS		
0	set_st_indr2_sublk7	2002Aug26
PASS		
0	set_st_indr2_sublk7_only	2002Aug26
PASS		
0	set_st_indr2_vs	2002Aug26
PASS		
0	set_st_indr2_vs_only	2002Aug26
PASS		
0	set_st_pfmatch_disabled	2002Aug26
PASS		
0	set_st_ring_match	2002Aug26
PASS		
0	set_st_ring_mismatch	2002Aug26
PASS		
0	set_st_ring_mismatch2	2002Aug26
PASS		
0	set_st_ring_mismatch3	2002Aug26
PASS		
0	set_st_ring_mismatch4	2002Aug26
PASS		
0	set_st_ring_ps	2002Aug26
PASS		
0	set_st_ring_ps_only	2002Aug26
PASS		
0	set_st_ring_sublk0	2002Aug26
PASS		
0	set_st_ring_sublk1	2002Aug26
PASS		
0	set_st_ring_sublk1_only	2002Aug26
PASS		
0	set_st_ring_sublk2	2002Aug26
PASS		
0	set_st_ring_sublk2_only	2002Aug26

PASS		
0	set_st_ring_sublk3	2002Aug26
PASS		
0	set_st_ring_sublk3_only	2002Aug26
PASS		
0	set_st_ring_sublk4	2002Aug26
PASS		
0	set_st_ring_sublk4_only	2002Aug26
PASS		
0	set_st_ring_sublk5	2002Aug26
PASS		
0	set_st_ring_sublk5_only	2002Aug26
PASS		
0	set_st_ring_sublk6	2002Aug26
PASS		
0	set_st_ring_sublk6_only	2002Aug26
PASS		
0	set_st_ring_sublk7	2002Aug26
PASS		
0	set_st_ring_sublk7_only	2002Aug26
PASS		
0	set_st_ring_vs	2002Aug26
PASS		
0	set_st_ring_vs_only	2002Aug26
PASS		
0	set_st_stress_a	2002Aug26
PASS		
0	set_st_stress_b	2002Aug26
PASS		
0	set_st_stress_c	2002Aug26
PASS		
0	set_st_stress_d	2002Aug26
PASS		
0	set_st_stress_e	2002Aug26
PASS		
0	set_st_stress_f	2002Aug26
PASS		
663	Small_Text_EM	2002Aug26
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Aug26
PASS		
0	src_clip	2002Aug26
PASS		
0	srf_cohr	2002Aug26
PASS		
0	trans_bitblt	2002Aug26
PASS		
0	type0_2long_pckts	2002Aug26

PASS		
0	type0_3indr_bufifs	2002Aug26
PASS		
0	type0_4pckts	2002Aug26
PASS		
0	type0_incr_updt_cnst	2002Aug26
PASS		
0	type0_incr_updt_instrmem	2002Aug26
PASS		
0	type0_incr_updt_st	2002Aug26
PASS		
0	type0_incr_updt_stress	2002Aug26
PASS		
0	type0_ind1_ind2_a	2002Aug26
PASS		
0	type0_ind1_ind2_b	2002Aug26
PASS		
0	type0_indr_buff	2002Aug26
PASS		
0	Type0_IQ2_BM	2002Aug26
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug26
PASS		
0	Type0_IQ_BM	2002Aug26
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug26
PASS		
0	Type0_PQ_BM	2002Aug26
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug26
PASS		
0	typel_4pckts	2002Aug26
PASS		
0	typel_indr_buff	2002Aug26
PASS		
0	typel_pckt	2002Aug26
PASS		
0	Type3_Bitblt	2002Aug26
PASS		
688	Type3_Load_Palette	2002Aug26
FAIL	rbbmWr.cmp	
0	Type3_NOP	2002Aug26
PASS		
690	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
691	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Aug26

```

PASS
0    uram_wrt_ucose                               2002Aug26
PASS
0    viz_query_a                                   2002Aug26
PASS
695  viz_query_draw_indx                          2002Aug26
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
696  viz_query_draw_indx_indr1                     2002Aug26
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
697  viz_query_draw_indx_indr2                     2002Aug26
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
698  viz_query_draw_indx_multipass                 2002Aug26
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
699  viz_query_draw_indx_ring                     2002Aug26
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    viz_query_indr1                               2002Aug26
PASS
0    viz_query_indr1_simple                         2002Aug26
PASS
0    viz_query_indr2                               2002Aug26
PASS
0    viz_query_indr2_simple                        2002Aug26
PASS
0    viz_query_ring                               2002Aug26
PASS
0    viz_query_ring_simple                         2002Aug26
PASS
0    wait_for_idle                                2002Aug26
PASS
0    wait_reg_mem_poll_mem0                       2002Aug26
PASS
0    wait_reg_mem_poll_mem0_rt                    2002Aug26
PASS
0    wait_reg_mem_poll_mem1                       2002Aug26
PASS
0    wait_reg_mem_poll_mem2                       2002Aug26
PASS
0    wait_reg_mem_poll_mem3                       2002Aug26
PASS
0    wait_reg_mem_poll_reg0                       2002Aug26
PASS
0    wait_reg_mem_poll_reg1                       2002Aug26
PASS
0    wait_reg_mem_poll_reg2                       2002Aug26
PASS
0    wait_reg_mem_poll_reg3                       2002Aug26
PASS
=====

```

Tests_Run_count = 715 Pass_count = 665 Fail_count = 50 Incomplete_count = 0

=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep 30  1:21:56 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          667         667         611         91
              RBBM        113         113         113         100
+++++
              TOTAL        780         780         724         92
+++++

```

Regression Report For CP - Project = r400 Created on Sun Oct 13 23:42:15 2002

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buf_d_sm_meq		2002Oct13
	PASS		
0	2d_indr_buf_g_sm_meq		2002Oct13
	PASS		
0	2d_indr_buffer_a		2002Oct13
	PASS		
0	2d_indr_buffer_b		2002Oct13
	PASS		
0	2d_indr_buffer_c		2002Oct13
	PASS		
0	2d_indr_buffer_d		2002Oct13
	PASS		
0	2d_indr_buffer_e		2002Oct13
	PASS		
0	2d_indr_buffer_f		2002Oct13
	PASS		
0	2d_indr_buffer_f_sm_roq		2002Oct13
	PASS		
0	2d_indr_buffer_f_sm_roq2		2002Oct13
	PASS		
0	2d_indr_buffer_g		2002Oct13
	PASS		
12	2d_indr_buffer_random		2002Oct13
	FAIL cp_mh_req.cmp mh_cp_rd.cmp		
13	2d_indr_buffer_random2		2002Oct13
	FAIL cp_mh_req.cmp mh_cp_rd.cmp		
0	3d_draw_indx_2		2002Oct13
	PASS		
0	3d_draw_indx_2_b		2002Oct13
	PASS		
0	IQ2_Mult		2002Oct13
	PASS		
0	IQ2_Wrap_Legacy		2002Oct13
	PASS		
0	IQ_Wrap		2002Oct13
	PASS		
19	Isync_Control		2002Oct13
	FAIL cp_rbbm.cmp rbbmWr.cmp		
20	Isync_Control_2D_3D		2002Oct13

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
21  Isync_Control_3D_2D                2002Oct13
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   RB_BUFSZ_0                          2002Oct13
PASS
0   RB_BUFSZ_1                          2002Oct13
PASS
0   RB_Wrap                             2002Oct13
PASS
0   Small_Text_BM                       2002Oct13
PASS
0   Type0_IQ2_BM                        2002Oct13
PASS
0   Type0_IQ2_BM_IB_Packet              2002Oct13
PASS
0   Type0_IQ_BM                         2002Oct13
PASS
0   Type0_IQ_BM_IB_Packet                2002Oct13
PASS
0   Type0_PQ_BM                         2002Oct13
PASS
0   Type0_PQ_BM_Pre_Write_Timer          2002Oct13
PASS
0   Type3_Bitblt                        2002Oct13
PASS
0   Type3_Hostdata_blt                   2002Oct13
PASS
0   Type3_Hostdata_blt_PIO               2002Oct13
PASS
0   Type3_Load_Palette                   2002Oct13
PASS
0   Type3_NOP                            2002Oct13
PASS
0   Type3_Polyline                       2002Oct13
PASS
0   Type3_Polyline_Large                 2002Oct13
PASS
0   bitblt_multi                         2002Oct13
PASS
0   brush_32x1mono                       2002Oct13
PASS
0   brush_8x8color_16                    2002Oct13
PASS
0   brush_8x8color_32                    2002Oct13
PASS
0   brush_8x8color_64                    2002Oct13
PASS
0   brush_8x8color_dst_0_1              2002Oct13

```


PASS		
0	brush_8x8mono	2002Oct13
PASS		
0	brush_color_mono_stress	2002Oct13
PASS		
0	brush_flag	2002Oct13
PASS		
0	brush_type0	2002Oct13
PASS		
0	brush_type1	2002Oct13
PASS		
0	brush_type10	2002Oct13
PASS		
0	brush_type13	2002Oct13
PASS		
0	brush_type14	2002Oct13
PASS		
0	brush_type6	2002Oct13
PASS		
0	brush_type7	2002Oct13
PASS		
0	brush_type_other	2002Oct13
PASS		
0	brush_types_13_14	2002Oct13
PASS		
57	change_queue_sizes	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	clear_const_wr_en_on_me_init	2002Oct13
PASS		
0	clear_const_wr_en_with_lcc	2002Oct13
PASS		
0	cond_write_mem_poll_mem	2002Oct13
PASS		
0	cond_write_mem_poll_mem2	2002Oct13
PASS		
0	cond_write_mem_poll_mem3	2002Oct13
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct13
PASS		
0	cond_write_mem_poll_reg	2002Oct13
PASS		
0	cond_write_mem_poll_reg2	2002Oct13
PASS		
0	cond_write_mem_poll_reg3	2002Oct13
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct13
PASS		
0	cp_bios_scratch	2002Oct13

PASS		
0	cp_high_stress	2002Oct13
PASS		
0	cp_ib2_base000_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base010_bufszXXX_sm_roq	2002Oct13
PASS		
0	cp_ib2_base011_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct13
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct13
PASS		
0	cp_ib_base000_bufszXXX	2002Oct13
PASS		
0	cp_ib_base001_bufszXXX	2002Oct13
PASS		
0	cp_ib_base010_bufszXXX	2002Oct13
PASS		
0	cp_ib_base011_bufszXXX	2002Oct13
PASS		
0	cp_ib_base100_bufszXXX	2002Oct13
PASS		
0	cp_ib_base101_bufszXXX	2002Oct13
PASS		
0	cp_ib_base101_bufszXXX_sm_roq	2002Oct13
PASS		
0	cp_ib_base110_bufszXXX	2002Oct13
PASS		
0	cp_ib_base111_bufszXXX	2002Oct13
PASS		
0	cp_interrupt	2002Oct13
PASS		
0	cp_interrupt_rt_events_16	2002Oct13
PASS		
0	cp_iq2_alignment_000	2002Oct13
PASS		
0	cp_iq2_alignment_001	2002Oct13
PASS		
0	cp_iq2_alignment_010	2002Oct13

PASS		
0	cp_iq2_alignment_011	2002Oct13
PASS		
0	cp_iq2_alignment_100	2002Oct13
PASS		
0	cp_iq2_alignment_101	2002Oct13
PASS		
0	cp_iq2_alignment_110	2002Oct13
PASS		
0	cp_iq2_alignment_111	2002Oct13
PASS		
0	cp_iq_alignment_000	2002Oct13
PASS		
0	cp_iq_alignment_001	2002Oct13
PASS		
0	cp_iq_alignment_010	2002Oct13
PASS		
0	cp_iq_alignment_011	2002Oct13
PASS		
0	cp_iq_alignment_100	2002Oct13
PASS		
0	cp_iq_alignment_101	2002Oct13
PASS		
0	cp_iq_alignment_110	2002Oct13
PASS		
0	cp_iq_alignment_111	2002Oct13
PASS		
0	cp_max_size_packet	2002Oct13
PASS		
0	cp_max_size_packet_random	2002Oct13
PASS		
0	cp_nrt_rt_protection_mode	2002Oct13
PASS		
0	cp_output_stress	2002Oct13
PASS		
0	cp_output_stress_random	2002Oct13
PASS		
0	cp_protection_mode	2002Oct13
PASS		
0	cp_rb_alignment_000	2002Oct13
PASS		
0	cp_rb_alignment_001	2002Oct13
PASS		
0	cp_rb_alignment_010	2002Oct13
PASS		
0	cp_rb_alignment_011	2002Oct13
PASS		
0	cp_rb_alignment_100	2002Oct13

PASS		
0	cp_rb_alignment_101	2002Oct13
PASS		
0	cp_rb_alignment_110	2002Oct13
PASS		
0	cp_rb_alignment_111	2002Oct13
PASS		
0	cp_rb_wptrXXX_base000	2002Oct13
PASS		
0	cp_rb_wptrXXX_base001	2002Oct13
PASS		
0	cp_rb_wptrXXX_base010	2002Oct13
PASS		
0	cp_rb_wptrXXX_base011	2002Oct13
PASS		
0	cp_rb_wptrXXX_base100	2002Oct13
PASS		
0	cp_rb_wptrXXX_base101	2002Oct13
PASS		
0	cp_rb_wptrXXX_base101_sm_roq	2002Oct13
PASS		
0	cp_rb_wptrXXX_base110	2002Oct13
PASS		
0	cp_rb_wptrXXX_base111	2002Oct13
PASS		
0	cp_reg_rw_test	2002Oct13
PASS		
0	cp_register_reads	2002Oct13
PASS		
0	cp_rt_protection_mode	2002Oct13
PASS		
0	cp_scratch	2002Oct13
PASS		
0	cp_scratch_multi	2002Oct13
PASS		
0	csf_reg_rd_default	2002Oct13
PASS		
135	csf_reg_rd_only	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	csf_reg_wr_rd	2002Oct13
PASS		
0	dma_cp_sync	2002Oct13
PASS		
0	dma_dscrstr_chckn_bit	2002Oct13
PASS		
0	dma_dscrstr_flush1	2002Oct13
PASS		
0	dma_dscrstr_flush2	2002Oct13

PASS		
0	dma_dscrstr_flush3	2002Oct13
PASS		
0	dma_m2m_src00_dstXX	2002Oct13
PASS		
0	dma_m2m_src01_dstXX	2002Oct13
PASS		
0	dma_m2m_src10_dstXX	2002Oct13
PASS		
0	dma_m2m_src11_dstXX	2002Oct13
PASS		
0	dma_mem2mem	2002Oct13
PASS		
0	dma_mem2mem_abort	2002Oct13
PASS		
0	dma_mem2mem_alignments	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment1	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment2	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment3	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment4	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct13
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct13
PASS		
0	dma_mem2mem_large	2002Oct13
PASS		
0	dma_mem2mem_pio_abort	2002Oct13
PASS		
0	dma_mem2mem_random	2002Oct13
PASS		
0	dma_mem2reg	2002Oct13
PASS		
0	dma_mem2reg_abort	2002Oct13
PASS		
0	dma_mem2reg_dtaq_stress	2002Oct13
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Oct13
PASS		
0	dma_mem2reg_pio_stressbm	2002Oct13

PASS		
0	dma_mem2reg_pio_stresspio	2002Oct13
PASS		
0	dma_mem2reg_sync	2002Oct13
PASS		
0	dma_r2r_m2m_r2r	2002Oct13
PASS		
0	dma_r2r_m2r_r2r	2002Oct13
PASS		
0	dma_r2r_r2m_r2r	2002Oct13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct13
PASS		
0	dma_reg2mem	2002Oct13
PASS		
0	dma_reg2mem_abort	2002Oct13
PASS		
0	dma_reg2reg	2002Oct13
PASS		
0	dma_reg2reg_2dw	2002Oct13
PASS		
0	dma_reg2reg_daic	2002Oct13
PASS		
0	dma_reg2reg_mult	2002Oct13
PASS		
0	dma_reg2reg_mult2	2002Oct13
PASS		
0	dma_reg2reg_mult3	2002Oct13
PASS		
0	dma_reg2reg_saic	2002Oct13
PASS		
0	dma_reg2reg_sdaic	2002Oct13
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct13
PASS		
0	dma_reg2reg_stress	2002Oct13
PASS		
0	draw_indx_a	2002Oct13
PASS		
0	draw_indx_b	2002Oct13
PASS		
0	draw_indx_c	2002Oct13
PASS		
0	draw_indx_indr1_a	2002Oct13

PASS		
0	draw_indx_indr1_b	2002Oct13
PASS		
0	draw_indx_indr1_short_a	2002Oct13
PASS		
0	draw_indx_indr1_short_b	2002Oct13
PASS		
0	draw_indx_indr1_stress	2002Oct13
PASS		
0	draw_indx_indr2_a	2002Oct13
PASS		
0	draw_indx_indr2_b	2002Oct13
PASS		
0	draw_indx_indr2_short_a	2002Oct13
PASS		
0	draw_indx_indr2_short_b	2002Oct13
PASS		
0	draw_indx_indr2_stress	2002Oct13
PASS		
198	draw_indx_random_a	2002Oct13
FAIL	mh_cp_rd.cmp	
199	draw_indx_random_b	2002Oct13
FAIL	mh_cp_rd.cmp	
200	draw_indx_random_c	2002Oct13
FAIL	mh_cp_rd.cmp	
0	draw_indx_ring1	2002Oct13
PASS		
0	draw_indx_ring2	2002Oct13
PASS		
0	draw_indx_ring_short1	2002Oct13
PASS		
0	draw_indx_ring_short2	2002Oct13
PASS		
0	draw_indx_ring_stress	2002Oct13
PASS		
0	draw_indx_short_a	2002Oct13
PASS		
0	draw_indx_short_b	2002Oct13
PASS		
0	draw_indx_short_c	2002Oct13
PASS		
0	draw_indx_stress_a	2002Oct13
PASS		
0	draw_indx_stress_b	2002Oct13
PASS		
0	draw_indx_stress_b_sm_meq	2002Oct13
PASS		
0	draw_indx_stress_b_sm_roq	2002Oct13

PASS		
0	draw_indx_stress_b_sm_roq2	2002Oct13
PASS		
0	draw_indx_stress_c	2002Oct13
PASS		
0	draw_indx_stress_c_sm_meq	2002Oct13
PASS		
0	event_timestamp_write	2002Oct13
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct13
PASS		
0	fix2flt_reg	2002Oct13
PASS		
0	ft_indr1_july22	2002Oct13
PASS		
0	ft_indr2_july22	2002Oct13
PASS		
0	ft_july22	2002Oct13
PASS		
0	ft_july22_random	2002Oct13
PASS		
0	header_dump	2002Oct13
PASS		
0	header_dump_rt	2002Oct13
PASS		
0	header_dump_small	2002Oct13
PASS		
0	hostdata_blt	2002Oct13
PASS		
0	im_ld_cid_mismatch_sm_meq	2002Oct13
PASS		
0	im_ld_invalidate_sm_meq	2002Oct13
PASS		
0	im_ld_match2_sm_meq	2002Oct13
PASS		
0	im_ld_match_sm_meq	2002Oct13
PASS		
0	im_ld_sz_mismatch_sm_meq	2002Oct13
PASS		
0	im_load_a	2002Oct13
PASS		
0	im_load_b	2002Oct13
PASS		
0	im_load_cid_mismatch	2002Oct13
PASS		
0	im_load_immediate	2002Oct13
PASS		
0	im_load_immediate_nrt_me	2002Oct13

PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct13
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct13
PASS		
239	im_load_immediate_nrt_me_ps_wrap	2002Oct13
FAIL	mh_cp_rd.cmp	
240	im_load_immediate_nrt_me_vs_nowrap	2002Oct13
FAIL	mh_cp_rd.cmp	
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct13
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct13
PASS		
243	im_load_immediate_nrt_me_vs_wrap	2002Oct13
FAIL	mh_cp_rd.cmp	
0	im_load_indr1_a	2002Oct13
PASS		
0	im_load_indr1_b	2002Oct13
PASS		
0	im_load_indr2_a	2002Oct13
PASS		
0	im_load_indr2_b	2002Oct13
PASS		
0	im_load_invalidate	2002Oct13
PASS		
0	im_load_invalidate_sm_roq	2002Oct13
PASS		
0	im_load_match	2002Oct13
PASS		
0	im_load_match2	2002Oct13
PASS		
0	im_load_nrt_me	2002Oct13
PASS		
253	im_load_nrt_me_ps_nowrap	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
254	im_load_nrt_me_ps_wrap	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
255	im_load_nrt_me_vs_nowrap	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
256	im_load_nrt_me_vs_wrap	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_pfmatch_disable	2002Oct13
PASS		
258	im_load_random_a	2002Oct13
FAIL	mh_cp_rd.cmp	
259	im_load_random_b	2002Oct13
FAIL	mh_cp_rd.cmp	
260	im_load_random_c	2002Oct13

```

**FAIL**          mh_cp_rd.cmp
0  im_load_ring_a          2002Oct13
PASS
0  im_load_ring_b          2002Oct13
PASS
0  im_load_rt              2002Oct13
PASS
0  im_load_rt2             2002Oct13
PASS
0  im_load_rt3             2002Oct13
PASS
0  im_load_rt3_random      2002Oct13
PASS
0  im_load_rt4             2002Oct13
PASS
0  im_load_rt4_random      2002Oct13
PASS
0  im_load_sz_mismatch     2002Oct13
PASS
270 incr_update_cnst_random 2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  incr_update_const_sm_meq 2002Oct13
PASS
0  incr_update_instr_sm_meq 2002Oct13
PASS
0  incr_update_state_sm_meq 2002Oct13
PASS
0  incremental_update_const 2002Oct13
PASS
0  incremental_update_instr 2002Oct13
PASS
0  incremental_update_state 2002Oct13
PASS
0  incremental_update_state_sm_roq 2002Oct13
PASS
0  indirect_buffer_a       2002Oct13
PASS
0  indirect_buffer_b       2002Oct13
PASS
0  indirect_buffer_c       2002Oct13
PASS
0  indirect_buffer_d       2002Oct13
PASS
282 indirect_buffer_e      2002Oct13
INCOMPLETE          rbbmWr.cmp
0  indirect_buffer_f       2002Oct13
PASS
0  indirect_buffer_f_sm_roq 2002Oct13

```

PASS		
0	indirect_buffer_g	2002Oct13
PASS		
0	indirect_buffer_h	2002Oct13
PASS		
0	indirect_buffer_i	2002Oct13
PASS		
0	indirect_buffer_j	2002Oct13
PASS		
0	indirect_buffer_k	2002Oct13
PASS		
0	indirect_buffer_l	2002Oct13
PASS		
0	indirect_buffer_mp_a	2002Oct13
PASS		
0	indirect_buffer_mp_b	2002Oct13
PASS		
0	indirect_buffer_mp_c	2002Oct13
PASS		
0	indirect_buffer_mp_d	2002Oct13
PASS		
0	indirect_buffer_mp_e	2002Oct13
PASS		
0	indirect_buffer_mp_f	2002Oct13
PASS		
0	indirect_buffer_mp_f_sm_roq	2002Oct13
PASS		
298	indirect_buffer_mp_random1	2002Oct13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
299	indirect_buffer_mp_random2	2002Oct13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
300	indirect_buffer_random1	2002Oct13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
301	indirect_buffer_random2	2002Oct13
FAIL	mh_cp_rd.cmp	
0	indr_buf_e_sm_meq	2002Oct13
PASS		
0	indr_buf_k_sm_meq	2002Oct13
PASS		
0	indr_buf_mp_c_sm_meq	2002Oct13
PASS		
0	indr_buf_mp_f_sm_meq	2002Oct13
PASS		
0	invalid_opcode	2002Oct13
PASS		
0	invalidate_on_me_init	2002Oct13
PASS		
308	invalidate_on_me_init_random	2002Oct13

```

**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  invalidate_on_me_init_sm_meq                2002Oct13
PASS
0  invalidate_state_constants                  2002Oct13
PASS
0  invalidate_state_shaders                    2002Oct13
PASS
0  invalidate_state_shaders2                   2002Oct13
PASS
313 invalidate_state_shaders_random            2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  invalidate_state_shaders_sm_meq            2002Oct13
PASS
0  invalidate_state_sublks                     2002Oct13
PASS
0  invalidate_state_sublks2                    2002Oct13
PASS
0  invalidate_state_sublks2_sm_meq            2002Oct13
PASS
318 invalidate_state_sublks_random            2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  invalidate_state_sublks_sm_roq             2002Oct13
PASS
0  iq1_iq2_mult                                2002Oct13
PASS
0  iq1_iq2_push_wrap                           2002Oct13
PASS
0  iq2_csq_full                                2002Oct13
PASS
323 lcc_coherency_cntrl_sm_meq                2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
324 lcc_coherency_cntr2_sm_meq                2002Oct13
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
325 lcc_coherency_cntr_random                 2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
326 lcc_match_random                          2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  lcc_match_sm_meq                            2002Oct13
PASS
0  lcc_match_valid_preserved_sm_meq           2002Oct13
PASS
329 lcc_mismatch_alutex_random                2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  lcc_mismatch_alutex_sm_meq                 2002Oct13
PASS
331 lcc_mismatch_base_random                  2002Oct13
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  lcc_mismatch_base_sm_meq                   2002Oct13

```

PASS		
0	lcc_rt1	2002Oct13
PASS		
0	lcc_rt2	2002Oct13
PASS		
0	lcc_rt3	2002Oct13
PASS		
0	lcc_rt3_random	2002Oct13
PASS		
0	lcc_rt4	2002Oct13
PASS		
0	lcc_rt4_random	2002Oct13
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct13
PASS		
0	ld_cnst_cntx_coherency_cntrl_sm_roq	2002Oct13
PASS		
0	ld_cnst_cntx_coherency_cntrl2	2002Oct13
PASS		
0	ld_cnst_cntx_coherency_cntrl2_sm_roq	2002Oct13
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct13
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct13
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct13
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct13
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct13
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct13
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct13
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct13
PASS		
0	ld_cnst_cntx_match	2002Oct13
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct13
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct13
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct13
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct13
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct13

PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct13
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct13
PASS		
0	ld_cnst_cntx_replicate8	2002Oct13
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct13
PASS		
0	ld_cnst_cntx_replicate_tex	2002Oct13
PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct13
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct13
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct13
PASS		
0	ld_cnst_cntx_ring_simple	2002Oct13
PASS		
0	ld_cnst_cntx_simple_alu	2002Oct13
PASS		
0	ld_cnst_cntx_simple_tex	2002Oct13
PASS		
0	load_constant_context_rt	2002Oct13
PASS		
0	load_palette	2002Oct13
PASS		
0	lp_2d3d	2002Oct13
PASS		
371	me_reg_wr_rd	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	2002Oct13
PASS		
0	mem_write_rt	2002Oct13
PASS		
0	mpeg_index	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_0	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_1	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_2	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_3	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_4	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct13

PASS		
0	mpeg_index_multi_context_max_context_6	2002Oct13
PASS		
0	mpeg_index_multi_context_max_context_7	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct13
PASS		
0	nop_eating_disabled	2002Oct13
PASS		
0	nrt_rt_interaction1	2002Oct13
PASS		
0	nrt_rt_interaction1_random	2002Oct13
PASS		
0	nrt_rt_interaction2	2002Oct13
PASS		
0	nrt_rt_interaction3	2002Oct13
PASS		
0	paint_multi	2002Oct13
PASS		
0	paint_packet	2002Oct13
PASS		
0	paint_packet_gui_control	2002Oct13
PASS		
398	pfpr_reg_reads1	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
399	pfpr_reg_reads2	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	pitch_clip	2002Oct13
PASS		
0	ply_nextscan	2002Oct13
PASS		
0	rbbm_NQWU_BIF_01	2002Oct13
PASS		
0	rbbm_NQWU_BIF_02	2002Oct13
PASS		
0	rbbm_NQWU_CF_01	2002Oct13

PASS		
0	rbbm_NQWU_CPnq_01	2002Oct13
PASS		
0	rbbm_WU_2D_idle_01	2002Oct13
PASS		
0	rbbm_WU_2D_idle_02	2002Oct13
PASS		
0	rbbm_WU_2D_idle_03	2002Oct13
PASS		
0	rbbm_WU_2D_idle_04	2002Oct13
PASS		
0	rbbm_WU_2D_idle_05	2002Oct13
PASS		
0	rbbm_WU_2D_idle_06	2002Oct13
PASS		
0	rbbm_WU_2D_idle_07	2002Oct13
PASS		
0	rbbm_WU_2D_idle_08	2002Oct13
PASS		
0	rbbm_WU_2D_idle_09	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct13
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Oct13
PASS		
0	rbbm_WU_3D_idle_01	2002Oct13
PASS		
0	rbbm_WU_3D_idle_02	2002Oct13

PASS		
0	rbbm_WU_3D_idle_03	2002Oct13
PASS		
0	rbbm_WU_3D_idle_04	2002Oct13
PASS		
0	rbbm_WU_3D_idle_05	2002Oct13
PASS		
0	rbbm_WU_3D_idle_06	2002Oct13
PASS		
0	rbbm_WU_3D_idle_07	2002Oct13
PASS		
0	rbbm_WU_3D_idle_08	2002Oct13
PASS		
0	rbbm_WU_3D_idle_09	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct13
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Oct13
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct13
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Oct13
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct13
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct13
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct13

PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct13
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct13
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct13
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct13
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct13
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct13
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct13
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct13
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct13
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct13
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct13
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct13
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct13
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct13
PASS		
0	rbbm_WU_idct_semaphore	2002Oct13
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct13
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct13
PASS		
0	rbbm_WUrt_BOTH_GRPH_update_pending_off	2002Oct13
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct13

PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct13
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct13
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct13
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct13
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_01	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_02	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_03	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_04	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Oct13
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Oct13
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Oct13
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Oct13
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Oct13
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Oct13
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Oct13
PASS		
0	rbbm_clk_active_deassert_min_max	2002Oct13
PASS		
0	rbbm_clk_active_each_pipe	2002Oct13
PASS		
0	rbbm_clk_active_for_read_error	2002Oct13
PASS		
0	rbbm_clk_active_for_read_request	2002Oct13

PASS		
0	rbbm_clk_active_on_reset	2002Oct13
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Oct13
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Oct13
PASS		
504	rbbm_cp_queued_holds_nonqueued	2002Oct13
FAIL	cp_rbbm.cmp	
0	rbbm_cp_queued_rdwr	2002Oct13
PASS		
506	rbbm_hi_data_and_be_swap	2002Oct13
FAIL	rbbmWr.cmp	
0	rbbm_hi_data_endian_swap	2002Oct13
PASS		
0	rbbm_hi_data_no_swap_for_cppush	2002Oct13
PASS		
0	rbbm_hi_data_swap_for_cppush	2002Oct13
PASS		
0	rbbm_int_ack	2002Oct13
PASS		
0	rbbm_int_each_client	2002Oct13
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Oct13
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Oct13
PASS		
0	rbbm_int_rderr	2002Oct13
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Oct13
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Oct13
PASS		
0	rbbm_isync_wait_idlegui	2002Oct13
PASS		
0	rbbm_soft_reset	2002Oct13
PASS		
0	rbbm_vgt_default_req	2002Oct13
PASS		
0	rbbm_vgt_max_req	2002Oct13
PASS		
0	rbbm_vgt_min_req	2002Oct13
PASS		
0	reg_to_mem	2002Oct13
PASS		
0	reg_to_mem_rt	2002Oct13
PASS		
0	ring_il_pio_i2_bm	2002Oct13

PASS		
525	ring_pio_ibs_bm_stress	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rop_cfd_wmd	2002Oct13
PASS		
527	roq_reg_reads	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rstr_ordr	2002Oct13
PASS		
0	rt_invalid_opcode	2002Oct13
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Oct13
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct13
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct13

PASS		
0	rtee_discretes_falling_17_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct13
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct13

PASS		
0	rtee_discretes_invert_09_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_11_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct13
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct13
PASS		
0	rtee_discretes_level_00_events_16	2002Oct13

PASS		
0	rtee_discretes_level_01_events_16	2002Oct13
PASS		
0	rtee_discretes_level_02_events_16	2002Oct13
PASS		
0	rtee_discretes_level_03_events_16	2002Oct13
PASS		
0	rtee_discretes_level_04_events_16	2002Oct13
PASS		
0	rtee_discretes_level_05_events_16	2002Oct13
PASS		
0	rtee_discretes_level_06_events_16	2002Oct13
PASS		
0	rtee_discretes_level_07_events_16	2002Oct13
PASS		
0	rtee_discretes_level_08_events_16	2002Oct13
PASS		
0	rtee_discretes_level_09_events_16	2002Oct13
PASS		
0	rtee_discretes_level_10_events_16	2002Oct13
PASS		
0	rtee_discretes_level_11_events_16	2002Oct13
PASS		
0	rtee_discretes_level_12_events_16	2002Oct13
PASS		
0	rtee_discretes_level_13_events_16	2002Oct13
PASS		
0	rtee_discretes_level_14_events_16	2002Oct13
PASS		
0	rtee_discretes_level_15_events_16	2002Oct13
PASS		
0	rtee_discretes_level_16_events_16	2002Oct13
PASS		
0	rtee_discretes_level_17_events_16	2002Oct13
PASS		
0	rtee_discretes_level_18_events_16	2002Oct13
PASS		
0	rtee_discretes_level_19_events_16	2002Oct13
PASS		
0	rtee_discretes_level_20_events_16	2002Oct13
PASS		
0	rtee_discretes_level_21_events_16	2002Oct13
PASS		
0	rtee_discretes_level_22_events_16	2002Oct13
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0	rtee_discretes_level_23_events_16	2002Oct13
PASS		
0	rtee_discretes_level_24_events_16	2002Oct13

PASS		
0	rtee_discretes_level_25_events_16	2002Oct13
PASS		
0	rtee_discretes_level_26_events_16	2002Oct13
PASS		
0	rtee_discretes_level_27_events_16	2002Oct13
PASS		
0	rtee_discretes_level_28_events_16	2002Oct13
PASS		
0	rtee_discretes_level_29_events_16	2002Oct13
PASS		
0	rtee_discretes_level_30_events_16	2002Oct13
PASS		
0	rtee_discretes_level_31_events_16	2002Oct13
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct13

PASS		
0	rtee_discretes_rising_16_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_23_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct13
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct13
PASS		
0	rtee_display1_comp_events_16	2002Oct13
PASS		
0	rtee_display2_comp_events_16	2002Oct13
PASS		
0	rtee_mem_linear_search_events_2	2002Oct13
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct13

PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct13
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct13
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct13
PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct13
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct13
PASS		
0	rtee_pio	2002Oct13
PASS		
0	rtee_prefetch_parse_nops	2002Oct13
PASS		
0	rtee_reg_linear_search_events_2	2002Oct13
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Oct13
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Oct13
PASS		
0	rtee_vip_comp_events_16	2002Oct13
PASS		
0	scratch_mem_read	2002Oct13
PASS		
0	scratch_mem_read2	2002Oct13
PASS		
692	set_cnst2_sm_meq	2002Oct13

```

**FAIL**      mh_cp_rd.cmp
693  set_cnst_alu_max1_sm_meq                2002Oct13
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
694  set_cnst_alu_max_random                2002Oct13
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0    set_cnst_bool_max2_sm_meq            2002Oct13
PASS
696  set_cnst_loop_max2_sm_meq            2002Oct13
**FAIL**      mh_cp_rd.cmp
697  set_cnst_sm_meq                      2002Oct13
**FAIL**      mh_cp_rd.cmp
698  set_cnst_tex_max2_sm_meq            2002Oct13
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
699  set_cnst_we_lcc_random              2002Oct13
**FAIL**      mh_cp_rd.cmp
700  set_cnst_we_random                  2002Oct13
**FAIL**      mh_cp_rd.cmp
701  set_cnst_write_enable_lcc2_sm_meq    2002Oct13
**FAIL**      mh_cp_rd.cmp
702  set_cnst_write_enable_sm_meq        2002Oct13
**FAIL**      mh_cp_rd.cmp
0    set_constant                        2002Oct13
PASS
0    set_constant2                      2002Oct13
PASS
705  set_constant_alu_max1              2002Oct13
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
706  set_constant_alu_max2              2002Oct13
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0    set_constant_bool_max1            2002Oct13
PASS
0    set_constant_bool_max2            2002Oct13
PASS
0    set_constant_indr1_short           2002Oct13
PASS
0    set_constant_indr1_short2          2002Oct13
PASS
0    set_constant_indr1_write_enable     2002Oct13
PASS
0    set_constant_indr1_write_enable_lcc 2002Oct13
PASS
0    set_constant_indr2_short           2002Oct13
PASS
0    set_constant_indr2_short2          2002Oct13
PASS
0    set_constant_indr2_write_enable     2002Oct13
PASS
0    set_constant_indr2_write_enable_lcc 2002Oct13

```

PASS		
0	set_constant_loop_max1	2002Oct13
PASS		
0	set_constant_loop_max2	2002Oct13
PASS		
0	set_constant_ring_short	2002Oct13
PASS		
0	set_constant_ring_short2	2002Oct13
PASS		
0	set_constant_ring_write_enable	2002Oct13
PASS		
0	set_constant_ring_write_enable_lcc	2002Oct13
PASS		
0	set_constant_short	2002Oct13
PASS		
0	set_constant_short2	2002Oct13
PASS		
725	set_constant_tex_max1	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
726	set_constant_tex_max2	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	set_constant_tex_max2_sm_roq	2002Oct13
PASS		
0	set_constant_write_enable	2002Oct13
PASS		
0	set_constant_write_enable_lcc	2002Oct13
PASS		
0	set_constant_write_enable_lcc2	2002Oct13
PASS		
0	set_constant_write_enable_lcc2_sm_roq	2002Oct13
PASS		
0	set_scissors	2002Oct13
PASS		
0	set_st_indrl_match	2002Oct13
PASS		
0	set_st_indrl_mismatch	2002Oct13
PASS		
0	set_st_indrl_mismatch2	2002Oct13
PASS		
0	set_st_indrl_mismatch3	2002Oct13
PASS		
0	set_st_indrl_mismatch4	2002Oct13
PASS		
0	set_st_indrl_ps	2002Oct13
PASS		
0	set_st_indrl_ps_only	2002Oct13
PASS		
0	set_st_indrl_sublk0	2002Oct13

PASS		
0	set_st_indr1_sublk1	2002Oct13
PASS		
0	set_st_indr1_sublk1_only	2002Oct13
PASS		
0	set_st_indr1_sublk2	2002Oct13
PASS		
0	set_st_indr1_sublk2_only	2002Oct13
PASS		
0	set_st_indr1_sublk3	2002Oct13
PASS		
0	set_st_indr1_sublk3_only	2002Oct13
PASS		
0	set_st_indr1_sublk4	2002Oct13
PASS		
0	set_st_indr1_sublk4_only	2002Oct13
PASS		
0	set_st_indr1_sublk5	2002Oct13
PASS		
0	set_st_indr1_sublk5_only	2002Oct13
PASS		
0	set_st_indr1_sublk6	2002Oct13
PASS		
0	set_st_indr1_sublk6_only	2002Oct13
PASS		
0	set_st_indr1_sublk7	2002Oct13
PASS		
0	set_st_indr1_sublk7_only	2002Oct13
PASS		
0	set_st_indr1_vs	2002Oct13
PASS		
0	set_st_indr1_vs_only	2002Oct13
PASS		
0	set_st_indr2_match	2002Oct13
PASS		
0	set_st_indr2_mismatch	2002Oct13
PASS		
0	set_st_indr2_mismatch2	2002Oct13
PASS		
0	set_st_indr2_mismatch3	2002Oct13
PASS		
0	set_st_indr2_mismatch4	2002Oct13
PASS		
0	set_st_indr2_ps	2002Oct13
PASS		
0	set_st_indr2_ps_only	2002Oct13
PASS		
0	set_st_indr2_sublk0	2002Oct13

PASS		
0	set_st_indr2_sublk1	2002Oct13
PASS		
0	set_st_indr2_sublk1_only	2002Oct13
PASS		
0	set_st_indr2_sublk2	2002Oct13
PASS		
0	set_st_indr2_sublk2_only	2002Oct13
PASS		
0	set_st_indr2_sublk3	2002Oct13
PASS		
0	set_st_indr2_sublk3_only	2002Oct13
PASS		
0	set_st_indr2_sublk4	2002Oct13
PASS		
0	set_st_indr2_sublk4_only	2002Oct13
PASS		
0	set_st_indr2_sublk5	2002Oct13
PASS		
0	set_st_indr2_sublk5_only	2002Oct13
PASS		
0	set_st_indr2_sublk6	2002Oct13
PASS		
0	set_st_indr2_sublk6_only	2002Oct13
PASS		
0	set_st_indr2_sublk7	2002Oct13
PASS		
0	set_st_indr2_sublk7_only	2002Oct13
PASS		
0	set_st_indr2_vs	2002Oct13
PASS		
0	set_st_indr2_vs_only	2002Oct13
PASS		
0	set_st_pfmatch_disabled	2002Oct13
PASS		
0	set_st_ring_match	2002Oct13
PASS		
0	set_st_ring_mismatch	2002Oct13
PASS		
0	set_st_ring_mismatch2	2002Oct13
PASS		
0	set_st_ring_mismatch3	2002Oct13
PASS		
0	set_st_ring_mismatch4	2002Oct13
PASS		
0	set_st_ring_ps	2002Oct13
PASS		
0	set_st_ring_ps_only	2002Oct13

PASS		
0	set_st_ring_sublk0	2002Oct13
PASS		
0	set_st_ring_sublk1	2002Oct13
PASS		
0	set_st_ring_sublk1_only	2002Oct13
PASS		
0	set_st_ring_sublk2	2002Oct13
PASS		
0	set_st_ring_sublk2_only	2002Oct13
PASS		
0	set_st_ring_sublk3	2002Oct13
PASS		
0	set_st_ring_sublk3_only	2002Oct13
PASS		
0	set_st_ring_sublk4	2002Oct13
PASS		
0	set_st_ring_sublk4_only	2002Oct13
PASS		
0	set_st_ring_sublk5	2002Oct13
PASS		
0	set_st_ring_sublk5_only	2002Oct13
PASS		
0	set_st_ring_sublk6	2002Oct13
PASS		
0	set_st_ring_sublk6_only	2002Oct13
PASS		
0	set_st_ring_sublk7	2002Oct13
PASS		
0	set_st_ring_sublk7_only	2002Oct13
PASS		
0	set_st_ring_vs	2002Oct13
PASS		
0	set_st_ring_vs_only	2002Oct13
PASS		
0	set_st_stress_a	2002Oct13
PASS		
0	set_st_stress_b	2002Oct13
PASS		
808	set_st_stress_b_random	2002Oct13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_b_sm_meq	2002Oct13
PASS		
0	set_st_stress_c	2002Oct13
PASS		
0	set_st_stress_c_sm_roq	2002Oct13
PASS		
0	set_st_stress_d	2002Oct13

PASS		
813	set_st_stress_d_random	2002Oct13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_d_sm_meq	2002Oct13
PASS		
0	set_st_stress_e	2002Oct13
PASS		
0	set_st_stress_e_sm_meq	2002Oct13
PASS		
0	set_st_stress_f	2002Oct13
PASS		
818	set_st_stress_f_random	2002Oct13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_state_rt_a	2002Oct13
PASS		
0	set_state_rt_b	2002Oct13
PASS		
0	set_state_rt_c	2002Oct13
PASS		
0	set_state_rt_d	2002Oct13
PASS		
0	set_state_rt_d_random	2002Oct13
PASS		
0	set_state_rt_e	2002Oct13
PASS		
0	set_state_rt_e_random	2002Oct13
PASS		
0	smoke_nrt	2002Oct13
PASS		
0	src_clip	2002Oct13
PASS		
0	srf_cohr	2002Oct13
PASS		
829	switch_pio_bm	2002Oct13
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	trans_bitblt	2002Oct13
PASS		
0	type0_2long_pckts	2002Oct13
PASS		
0	type0_3indr_buffs	2002Oct13
PASS		
0	type0_4pckts	2002Oct13
PASS		
0	type0_incr_updt_cnst	2002Oct13
PASS		
0	type0_incr_updt_instrmem	2002Oct13
PASS		
0	type0_incr_updt_st	2002Oct13

```

PASS
0   type0_incr_updt_stress                2002Oct13
PASS
0   type0_ind1_ind2_a                    2002Oct13
PASS
0   type0_ind1_ind2_b                    2002Oct13
PASS
0   type0_indr_buff                      2002Oct13
PASS
0   type0_real_time                      2002Oct13
PASS
0   type0_real_time_random              2002Oct13
PASS
0   type1_4pckts                        2002Oct13
PASS
0   type1_indr_buff                     2002Oct13
PASS
0   type1_pckt                          2002Oct13
PASS
846 type_random                          2002Oct13
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   uram_wrt_rt_ucode                    2002Oct13
PASS
0   uram_wrt_ucode                      2002Oct13
PASS
0   viz_query_a                          2002Oct13
PASS
0   viz_query_draw_indx                 2002Oct13
PASS
0   viz_query_draw_indx_indr1           2002Oct13
PASS
0   viz_query_draw_indx_indr2           2002Oct13
PASS
0   viz_query_draw_indx_multipass       2002Oct13
PASS
0   viz_query_draw_indx_ring            2002Oct13
PASS
0   viz_query_indr1                     2002Oct13
PASS
0   viz_query_indr1_simple              2002Oct13
PASS
0   viz_query_indr2                     2002Oct13
PASS
0   viz_query_indr2_simple              2002Oct13
PASS
0   viz_query_ring                      2002Oct13
PASS
0   viz_query_ring_simple               2002Oct13

```

PASS
0 wait_for_idle 2002Oct13
PASS
0 wait_reg_mem_poll_mem0 2002Oct13
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct13
PASS
0 wait_reg_mem_poll_mem1 2002Oct13
PASS
0 wait_reg_mem_poll_mem2 2002Oct13
PASS
0 wait_reg_mem_poll_mem3 2002Oct13
PASS
0 wait_reg_mem_poll_reg0 2002Oct13
PASS
0 wait_reg_mem_poll_reg1 2002Oct13
PASS
0 wait_reg_mem_poll_reg2 2002Oct13
PASS
0 wait_reg_mem_poll_reg3 2002Oct13
PASS

=====
Tests_Run_count = 870 Pass_count = 809 Fail_count = 60 Incomplete_count = 1
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buf_d_sm_meq		2002Oct16
	PASS		
0	2d_indr_buf_g_sm_meq		2002Oct16
	PASS		
0	2d_indr_buffer_a		2002Oct16
	PASS		
0	2d_indr_buffer_b		2002Oct16
	PASS		
0	2d_indr_buffer_c		2002Oct16
	PASS		
0	2d_indr_buffer_d		2002Oct16
	PASS		
0	2d_indr_buffer_e		2002Oct16
	PASS		
0	2d_indr_buffer_f		2002Oct16
	PASS		
0	2d_indr_buffer_f_sm_roq		2002Oct16
	PASS		
0	2d_indr_buffer_f_sm_roq2		2002Oct16
	PASS		
0	2d_indr_buffer_g		2002Oct16
	PASS		
0	2d_indr_buffer_random		2002Oct16
	PASS		
0	2d_indr_buffer_random2		2002Oct16
	PASS		
0	3d_draw_indx_2		2002Oct16
	PASS		
0	3d_draw_indx_2_b		2002Oct16
	PASS		
0	IQ2_Mult		2002Oct16
	PASS		
0	IQ2_Wrap_Legacy		2002Oct16
	PASS		
0	IQ_Wrap		2002Oct16
	PASS		
19	Isync_Control		2002Oct16
	FAIL cp_rbbm.cmp rbbmWr.cmp		
20	Isync_Control_2D_3D		2002Oct16

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
21  Isync_Control_3D_2D                                2002Oct16
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   RB_BUFSZ_0                                          2002Oct16
PASS
0   RB_BUFSZ_1                                          2002Oct16
PASS
0   RB_Wrap                                             2002Oct16
PASS
0   Small_Text_BM                                       2002Oct16
PASS
0   Type0_IQ2_BM                                         2002Oct16
PASS
0   Type0_IQ2_BM_IB_Packet                             2002Oct16
PASS
0   Type0_IQ_BM                                         2002Oct16
PASS
0   Type0_IQ_BM_IB_Packet                             2002Oct16
PASS
0   Type0_PQ_BM                                         2002Oct16
PASS
0   Type0_PQ_BM_Pre_Write_Timer                       2002Oct16
PASS
0   Type3_Bitblt                                        2002Oct16
PASS
33  Type3_Hostdata_blt                                  2002Oct16
**FAIL**          cp_mh_req.cmp
34  Type3_Hostdata_blt_PIO                             2002Oct16
**FAIL**          cp_mh_req.cmp
0   Type3_Load_Palette                                 2002Oct16
PASS
0   Type3_NOP                                           2002Oct16
PASS
0   Type3_Polyline                                      2002Oct16
PASS
0   Type3_Polyline_Large                               2002Oct16
PASS
0   bitblt_multi                                       2002Oct16
PASS
0   brush_32x1mono                                     2002Oct16
PASS
0   brush_8x8color_16                                  2002Oct16
PASS
0   brush_8x8color_32                                  2002Oct16
PASS
0   brush_8x8color_64                                  2002Oct16
PASS
0   brush_8x8color_dst_0_1                             2002Oct16

```

PASS		
0	brush_8x8mono	2002Oct16
PASS		
0	brush_color_mono_stress	2002Oct16
PASS		
0	brush_flag	2002Oct16
PASS		
0	brush_type0	2002Oct16
PASS		
0	brush_type1	2002Oct16
PASS		
50	brush_type10	2002Oct16
FAIL	cp_mh_req.cmp	
0	brush_type13	2002Oct16
PASS		
0	brush_type14	2002Oct16
PASS		
0	brush_type6	2002Oct16
PASS		
0	brush_type7	2002Oct16
PASS		
0	brush_type_other	2002Oct16
PASS		
0	brush_types_13_14	2002Oct16
PASS		
0	change_queue_sizes	2002Oct16
PASS		
0	clear_const_wr_en_on_me_init	2002Oct16
PASS		
0	clear_const_wr_en_with_lcc	2002Oct16
PASS		
0	cond_write_mem_poll_mem	2002Oct16
PASS		
0	cond_write_mem_poll_mem2	2002Oct16
PASS		
0	cond_write_mem_poll_mem3	2002Oct16
PASS		
0	cond_write_mem_poll_mem_rt	2002Oct16
PASS		
0	cond_write_mem_poll_reg	2002Oct16
PASS		
0	cond_write_mem_poll_reg2	2002Oct16
PASS		
0	cond_write_mem_poll_reg3	2002Oct16
PASS		
0	cp_acpi_recovery_soft_reset	2002Oct16
PASS		
0	cp_bios_scratch	2002Oct16

PASS		
69	cp_high_stress	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_ib2_base000_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base001_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base010_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base010_bufszXXX_sm_roq	2002Oct16
PASS		
0	cp_ib2_base011_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base100_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base101_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base110_bufszXXX	2002Oct16
PASS		
0	cp_ib2_base111_bufszXXX	2002Oct16
PASS		
0	cp_ib_base000_bufszXXX	2002Oct16
PASS		
0	cp_ib_base001_bufszXXX	2002Oct16
PASS		
0	cp_ib_base010_bufszXXX	2002Oct16
PASS		
0	cp_ib_base011_bufszXXX	2002Oct16
PASS		
0	cp_ib_base100_bufszXXX	2002Oct16
PASS		
0	cp_ib_base101_bufszXXX	2002Oct16
PASS		
0	cp_ib_base101_bufszXXX_sm_roq	2002Oct16
PASS		
0	cp_ib_base110_bufszXXX	2002Oct16
PASS		
0	cp_ib_base111_bufszXXX	2002Oct16
PASS		
0	cp_interrupt	2002Oct16
PASS		
0	cp_interrupt_rt_events_16	2002Oct16
PASS		
0	cp_iq2_alignment_000	2002Oct16
PASS		
0	cp_iq2_alignment_001	2002Oct16
PASS		
0	cp_iq2_alignment_010	2002Oct16

PASS		
0	cp_iq2_alignment_011	2002Oct16
PASS		
0	cp_iq2_alignment_100	2002Oct16
PASS		
0	cp_iq2_alignment_101	2002Oct16
PASS		
0	cp_iq2_alignment_110	2002Oct16
PASS		
0	cp_iq2_alignment_111	2002Oct16
PASS		
0	cp_iq_alignment_000	2002Oct16
PASS		
0	cp_iq_alignment_001	2002Oct16
PASS		
0	cp_iq_alignment_010	2002Oct16
PASS		
0	cp_iq_alignment_011	2002Oct16
PASS		
0	cp_iq_alignment_100	2002Oct16
PASS		
0	cp_iq_alignment_101	2002Oct16
PASS		
0	cp_iq_alignment_110	2002Oct16
PASS		
0	cp_iq_alignment_111	2002Oct16
PASS		
0	cp_max_size_packet	2002Oct16
PASS		
107	cp_max_size_packet_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
108	cp_nrt_rt_protection_mode	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
109	cp_output_stress	2002Oct16
FAIL	cp_mh_req.cmp	
110	cp_output_stress_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_protection_mode	2002Oct16
PASS		
0	cp_rb_alignment_000	2002Oct16
PASS		
0	cp_rb_alignment_001	2002Oct16
PASS		
0	cp_rb_alignment_010	2002Oct16
PASS		
0	cp_rb_alignment_011	2002Oct16
PASS		
0	cp_rb_alignment_100	2002Oct16

PASS		
0	cp_rb_alignment_101	2002Oct16
PASS		
0	cp_rb_alignment_110	2002Oct16
PASS		
0	cp_rb_alignment_111	2002Oct16
PASS		
0	cp_rb_wptrXXX_base000	2002Oct16
PASS		
0	cp_rb_wptrXXX_base001	2002Oct16
PASS		
0	cp_rb_wptrXXX_base010	2002Oct16
PASS		
0	cp_rb_wptrXXX_base011	2002Oct16
PASS		
0	cp_rb_wptrXXX_base100	2002Oct16
PASS		
0	cp_rb_wptrXXX_base101	2002Oct16
PASS		
0	cp_rb_wptrXXX_base101_sm_roq	2002Oct16
PASS		
0	cp_rb_wptrXXX_base110	2002Oct16
PASS		
0	cp_rb_wptrXXX_base111	2002Oct16
PASS		
129	cp_reg_rw_test	2002Oct16
FAIL		
0	cp_register_reads	2002Oct16
PASS		
0	cp_rt_protection_mode	2002Oct16
PASS		
0	cp_scratch	2002Oct16
PASS		
133	cp_scratch_multi	2002Oct16
FAIL	cp_mh_req.cmp	
0	csf_reg_rd_default	2002Oct16
PASS		
0	csf_reg_rd_only	2002Oct16
PASS		
0	csf_reg_wr_rd	2002Oct16
PASS		
0	dma_cp_sync	2002Oct16
PASS		
0	dma_dscrstr_chckn_bit	2002Oct16
PASS		
0	dma_dscrstr_flush1	2002Oct16
PASS		
0	dma_dscrstr_flush2	2002Oct16

PASS		
0	dma_dscrstr_flush3	2002Oct16
PASS		
0	dma_m2m_src00_dstXX	2002Oct16
PASS		
0	dma_m2m_src01_dstXX	2002Oct16
PASS		
0	dma_m2m_src10_dstXX	2002Oct16
PASS		
0	dma_m2m_src11_dstXX	2002Oct16
PASS		
0	dma_mem2mem	2002Oct16
PASS		
0	dma_mem2mem_abort	2002Oct16
PASS		
0	dma_mem2mem_alignments	2002Oct16
PASS		
0	dma_mem2mem_byte_alignment0	2002Oct16
PASS		
150	dma_mem2mem_byte_alignment1	2002Oct16
FAIL	cp_mh_req.cmp	
151	dma_mem2mem_byte_alignment2	2002Oct16
FAIL	cp_mh_req.cmp	
152	dma_mem2mem_byte_alignment3	2002Oct16
FAIL	cp_mh_req.cmp	
0	dma_mem2mem_byte_alignment4	2002Oct16
PASS		
0	dma_mem2mem_byte_alignment5	2002Oct16
PASS		
0	dma_mem2mem_byte_alignment6	2002Oct16
PASS		
0	dma_mem2mem_byte_alignment7	2002Oct16
PASS		
0	dma_mem2mem_large	2002Oct16
PASS		
0	dma_mem2mem_pio_abort	2002Oct16
PASS		
159	dma_mem2mem_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	dma_mem2reg	2002Oct16
PASS		
161	dma_mem2reg_abort	2002Oct16
FAIL	rbbmWr.cmp	
0	dma_mem2reg_dtaq_stress	2002Oct16
PASS		
163	dma_mem2reg_dtaq_stress_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	dma_mem2reg_pio_stressbm	2002Oct16

PASS		
0	dma_mem2reg_pio_stresspio	2002Oct16
PASS		
0	dma_mem2reg_sync	2002Oct16
PASS		
0	dma_r2r_m2m_r2r	2002Oct16
PASS		
0	dma_r2r_m2r_r2r	2002Oct16
PASS		
0	dma_r2r_r2m_r2r	2002Oct16
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct16
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct16
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct16
PASS		
0	dma_reg2mem	2002Oct16
PASS		
0	dma_reg2mem_abort	2002Oct16
PASS		
0	dma_reg2reg	2002Oct16
PASS		
0	dma_reg2reg_2dw	2002Oct16
PASS		
0	dma_reg2reg_daic	2002Oct16
PASS		
0	dma_reg2reg_mult	2002Oct16
PASS		
0	dma_reg2reg_mult2	2002Oct16
PASS		
0	dma_reg2reg_mult3	2002Oct16
PASS		
0	dma_reg2reg_saic	2002Oct16
PASS		
0	dma_reg2reg_sdaic	2002Oct16
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct16
PASS		
0	dma_reg2reg_stress	2002Oct16
PASS		
0	draw_indx_a	2002Oct16
PASS		
0	draw_indx_b	2002Oct16
PASS		
0	draw_indx_c	2002Oct16
PASS		
0	draw_indx_indr1_a	2002Oct16

PASS		
0	draw_indx_indr1_b	2002Oct16
PASS		
0	draw_indx_indr1_short_a	2002Oct16
PASS		
0	draw_indx_indr1_short_b	2002Oct16
PASS		
0	draw_indx_indr1_stress	2002Oct16
PASS		
0	draw_indx_indr2_a	2002Oct16
PASS		
0	draw_indx_indr2_b	2002Oct16
PASS		
0	draw_indx_indr2_short_a	2002Oct16
PASS		
0	draw_indx_indr2_short_b	2002Oct16
PASS		
0	draw_indx_indr2_stress	2002Oct16
PASS		
0	draw_indx_random_a	2002Oct16
PASS		
0	draw_indx_random_b	2002Oct16
PASS		
0	draw_indx_random_c	2002Oct16
PASS		
0	draw_indx_ring1	2002Oct16
PASS		
0	draw_indx_ring2	2002Oct16
PASS		
0	draw_indx_ring_short1	2002Oct16
PASS		
0	draw_indx_ring_short2	2002Oct16
PASS		
0	draw_indx_ring_stress	2002Oct16
PASS		
0	draw_indx_short_a	2002Oct16
PASS		
0	draw_indx_short_b	2002Oct16
PASS		
0	draw_indx_short_c	2002Oct16
PASS		
0	draw_indx_stress_a	2002Oct16
PASS		
0	draw_indx_stress_b	2002Oct16
PASS		
0	draw_indx_stress_b_sm_meq	2002Oct16
PASS		
0	draw_indx_stress_b_sm_roq	2002Oct16

PASS		
0	draw_indx_stress_b_sm_roq2	2002Oct16
PASS		
0	draw_indx_stress_c	2002Oct16
PASS		
0	draw_indx_stress_c_sm_meq	2002Oct16
PASS		
216	event_timestamp_write	2002Oct16
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct16
PASS		
0	fix2flt_reg	2002Oct16
PASS		
0	ft_indr1_july22	2002Oct16
PASS		
0	ft_indr2_july22	2002Oct16
PASS		
0	ft_july22	2002Oct16
PASS		
222	ft_july22_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	header_dump	2002Oct16
PASS		
0	header_dump_rt	2002Oct16
PASS		
0	header_dump_small	2002Oct16
PASS		
226	hostdata_blt	2002Oct16
FAIL	cp_mh_req.cmp	
0	im_ld_cid_mismatch_sm_meq	2002Oct16
PASS		
0	im_ld_invalidate_sm_meq	2002Oct16
PASS		
0	im_ld_match2_sm_meq	2002Oct16
PASS		
0	im_ld_match_sm_meq	2002Oct16
PASS		
0	im_ld_sz_mismatch_sm_meq	2002Oct16
PASS		
0	im_load_a	2002Oct16
PASS		
0	im_load_b	2002Oct16
PASS		
0	im_load_cid_mismatch	2002Oct16
PASS		
0	im_load_immediate	2002Oct16
PASS		
0	im_load_immediate_nrt_me	2002Oct16

PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct16
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct16
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct16
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct16
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct16
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct16
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct16
PASS		
0	im_load_indr1_a	2002Oct16
PASS		
0	im_load_indr1_b	2002Oct16
PASS		
0	im_load_indr2_a	2002Oct16
PASS		
0	im_load_indr2_b	2002Oct16
PASS		
0	im_load_invalidate	2002Oct16
PASS		
0	im_load_invalidate_sm_roq	2002Oct16
PASS		
0	im_load_match	2002Oct16
PASS		
0	im_load_match2	2002Oct16
PASS		
0	im_load_nrt_me	2002Oct16
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct16
PASS		
0	im_load_nrt_me_ps_wrap	2002Oct16
PASS		
0	im_load_nrt_me_vs_nowrap	2002Oct16
PASS		
0	im_load_nrt_me_vs_wrap	2002Oct16
PASS		
0	im_load_pfmacth_disable	2002Oct16
PASS		
0	im_load_random_a	2002Oct16
PASS		
0	im_load_random_b	2002Oct16
PASS		
0	im_load_random_c	2002Oct16

PASS		
0	im_load_ring_a	2002Oct16
PASS		
0	im_load_ring_b	2002Oct16
PASS		
0	im_load_rt	2002Oct16
PASS		
0	im_load_rt2	2002Oct16
PASS		
0	im_load_rt3	2002Oct16
PASS		
266	im_load_rt3_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	im_load_rt4	2002Oct16
PASS		
268	im_load_rt4_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	im_load_sz_mismatch	2002Oct16
PASS		
0	incr_update_cnst_random	2002Oct16
PASS		
0	incr_update_const_sm_meq	2002Oct16
PASS		
0	incr_update_instr_sm_meq	2002Oct16
PASS		
0	incr_update_state_sm_meq	2002Oct16
PASS		
0	incremental_update_const	2002Oct16
PASS		
0	incremental_update_instr	2002Oct16
PASS		
0	incremental_update_state	2002Oct16
PASS		
0	incremental_update_state_sm_roq	2002Oct16
PASS		
0	indirect_buffer_a	2002Oct16
PASS		
0	indirect_buffer_b	2002Oct16
PASS		
0	indirect_buffer_c	2002Oct16
PASS		
0	indirect_buffer_d	2002Oct16
PASS		
0	indirect_buffer_e	2002Oct16
PASS		
0	indirect_buffer_f	2002Oct16
PASS		
0	indirect_buffer_f_sm_roq	2002Oct16

PASS		
0	indirect_buffer_g	2002Oct16
PASS		
0	indirect_buffer_h	2002Oct16
PASS		
0	indirect_buffer_i	2002Oct16
PASS		
0	indirect_buffer_j	2002Oct16
PASS		
0	indirect_buffer_k	2002Oct16
PASS		
0	indirect_buffer_l	2002Oct16
PASS		
0	indirect_buffer_mp_a	2002Oct16
PASS		
0	indirect_buffer_mp_b	2002Oct16
PASS		
0	indirect_buffer_mp_c	2002Oct16
PASS		
0	indirect_buffer_mp_d	2002Oct16
PASS		
0	indirect_buffer_mp_e	2002Oct16
PASS		
0	indirect_buffer_mp_f	2002Oct16
PASS		
0	indirect_buffer_mp_f_sm_roq	2002Oct16
PASS		
0	indirect_buffer_mp_random1	2002Oct16
PASS		
0	indirect_buffer_mp_random2	2002Oct16
PASS		
0	indirect_buffer_random1	2002Oct16
PASS		
0	indirect_buffer_random2	2002Oct16
PASS		
0	indr_buf_e_sm_meq	2002Oct16
PASS		
0	indr_buf_k_sm_meq	2002Oct16
PASS		
0	indr_buf_mp_c_sm_meq	2002Oct16
PASS		
0	indr_buf_mp_f_sm_meq	2002Oct16
PASS		
0	invalid_opcode	2002Oct16
PASS		
0	invalidate_on_me_init	2002Oct16
PASS		
0	invalidate_on_me_init_random	2002Oct16

PASS		
0	invalidate_on_me_init_sm_meq	2002Oct16
PASS		
0	invalidate_state_constants	2002Oct16
PASS		
0	invalidate_state_shaders	2002Oct16
PASS		
0	invalidate_state_shaders2	2002Oct16
PASS		
0	invalidate_state_shaders_random	2002Oct16
PASS		
0	invalidate_state_shaders_sm_meq	2002Oct16
PASS		
0	invalidate_state_sublks	2002Oct16
PASS		
0	invalidate_state_sublks2	2002Oct16
PASS		
0	invalidate_state_sublks2_sm_meq	2002Oct16
PASS		
0	invalidate_state_sublks_random	2002Oct16
PASS		
0	invalidate_state_sublks_sm_roq	2002Oct16
PASS		
0	iq1_iq2_mult	2002Oct16
PASS		
0	iq1_iq2_push_wrap	2002Oct16
PASS		
322	iq2_csq_full	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
323	lcc_coherency_cntrl_sm_meq	2002Oct16
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
324	lcc_coherency_cntr2_sm_meq	2002Oct16
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
325	lcc_coherency_cntr_random	2002Oct16
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	lcc_match_random	2002Oct16
PASS		
0	lcc_match_sm_meq	2002Oct16
PASS		
0	lcc_match_valid_preserved_sm_meq	2002Oct16
PASS		
0	lcc_mismatch_alutex_random	2002Oct16
PASS		
0	lcc_mismatch_alutex_sm_meq	2002Oct16
PASS		
0	lcc_mismatch_base_random	2002Oct16
PASS		
0	lcc_mismatch_base_sm_meq	2002Oct16

PASS		
0	lcc_rt1	2002Oct16
PASS		
0	lcc_rt2	2002Oct16
PASS		
0	lcc_rt3	2002Oct16
PASS		
336	lcc_rt3_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	lcc_rt4	2002Oct16
PASS		
338	lcc_rt4_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_coherency_cntrl	2002Oct16
PASS		
0	ld_cnst_cntx_coherency_cntrl_sm_roq	2002Oct16
PASS		
341	ld_cnst_cntx_coherency_cntrl2	2002Oct16
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_coherency_cntrl2_sm_roq	2002Oct16
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct16
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct16
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct16
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct16
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct16
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct16
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct16
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct16
PASS		
0	ld_cnst_cntx_match	2002Oct16
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct16
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct16
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct16
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct16
PASS		
0	ld_cnst_cntx_pfmismatch_disable	2002Oct16

PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct16
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct16
PASS		
0	ld_cnst_cntx_replicate8	2002Oct16
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct16
PASS		
0	ld_cnst_cntx_replicate_tex	2002Oct16
PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct16
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct16
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct16
PASS		
0	ld_cnst_cntx_ring_simple	2002Oct16
PASS		
0	ld_cnst_cntx_simple_alu	2002Oct16
PASS		
0	ld_cnst_cntx_simple_tex	2002Oct16
PASS		
0	load_constant_context_rt	2002Oct16
PASS		
0	load_palette	2002Oct16
PASS		
0	lp_2d3d	2002Oct16
PASS		
0	max_context_change	2002Oct16
PASS		
0	me_reg_wr_rd	2002Oct16
PASS		
0	mem_write	2002Oct16
PASS		
0	mem_write_rt	2002Oct16
PASS		
0	mpeg_index	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_0	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_1	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_2	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_3	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_4	2002Oct16

PASS		
0	mpeg_index_multi_context_max_context_5	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_6	2002Oct16
PASS		
0	mpeg_index_multi_context_max_context_7	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Oct16
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct16
PASS		
0	nop_eating_disabled	2002Oct16
PASS		
392	nrt_rt_interaction1	2002Oct16
FAIL		
393	nrt_rt_interaction1_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	nrt_rt_interaction2	2002Oct16
PASS		
0	nrt_rt_interaction3	2002Oct16
PASS		
0	nrt_rt_interaction4	2002Oct16
PASS		
0	paint_multi	2002Oct16
PASS		
0	paint_packet	2002Oct16
PASS		
0	paint_packet_gui_control	2002Oct16
PASS		
0	pfp_reg_reads1	2002Oct16
PASS		
0	pfp_reg_reads2	2002Oct16
PASS		
0	pitch_clip	2002Oct16
PASS		
0	ply_nextscan	2002Oct16
PASS		
404	rbbm_FAO_by_all	2002Oct16

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**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
405 rbbm_FAO_by_bif                                2002Oct16
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
406 rbbm_FAO_by_cpnq                                2002Oct16
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
407 rbbm_FAO_by_cpq                                  2002Oct16
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
408 rbbm_FAO_by_cpvt                                2002Oct16
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
409 rbbm_NQWU_BIF_01                                2002Oct16
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
410 rbbm_NQWU_BIF_02                                2002Oct16
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
411 rbbm_NQWU_CF_01                                 2002Oct16
**FAIL**
412 rbbm_NQWU_CPnq_01                               2002Oct16
**FAIL**
413 rbbm_WU_2D_idle_01                             2002Oct16
**FAIL**
414 rbbm_WU_2D_idle_02                             2002Oct16
**FAIL**
415 rbbm_WU_2D_idle_03                             2002Oct16
**FAIL**
416 rbbm_WU_2D_idle_04                             2002Oct16
**FAIL**
0 rbbm_WU_2D_idle_05                               2002Oct16
PASS
418 rbbm_WU_2D_idle_06                             2002Oct16
**FAIL**
419 rbbm_WU_2D_idle_07                             2002Oct16
**FAIL**
420 rbbm_WU_2D_idle_08                             2002Oct16
**FAIL**
421 rbbm_WU_2D_idle_09                             2002Oct16
**FAIL**
422 rbbm_WU_2D_idle_clean_01                       2002Oct16
**FAIL**
423 rbbm_WU_2D_idle_clean_02                       2002Oct16
**FAIL**
424 rbbm_WU_2D_idle_clean_03                       2002Oct16
**FAIL**
425 rbbm_WU_2D_idle_clean_04                       2002Oct16
**FAIL**
0 rbbm_WU_2D_idle_clean_05                         2002Oct16
PASS
427 rbbm_WU_2D_idle_clean_06                       2002Oct16
**FAIL**
428 rbbm_WU_2D_idle_clean_07                       2002Oct16

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FAIL		
429	rbbm_WU_2D_idle_clean_08	2002Oct16
FAIL		
430	rbbm_WU_2D_idle_clean_09	2002Oct16
FAIL		
431	rbbm_WU_2D_idle_clean_10	2002Oct16
FAIL		
432	rbbm_WU_2D_idle_clean_11	2002Oct16
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Oct16
PASS		
434	rbbm_WU_3D_idle_01	2002Oct16
FAIL		
435	rbbm_WU_3D_idle_02	2002Oct16
FAIL		
436	rbbm_WU_3D_idle_03	2002Oct16
FAIL		
437	rbbm_WU_3D_idle_04	2002Oct16
FAIL		
0	rbbm_WU_3D_idle_05	2002Oct16
PASS		
439	rbbm_WU_3D_idle_06	2002Oct16
FAIL		
440	rbbm_WU_3D_idle_07	2002Oct16
FAIL		
441	rbbm_WU_3D_idle_08	2002Oct16
FAIL		
442	rbbm_WU_3D_idle_09	2002Oct16
FAIL		
443	rbbm_WU_3D_idle_clean_01	2002Oct16
FAIL		
444	rbbm_WU_3D_idle_clean_02	2002Oct16
FAIL		
445	rbbm_WU_3D_idle_clean_03	2002Oct16
FAIL		
446	rbbm_WU_3D_idle_clean_04	2002Oct16
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Oct16
PASS		
448	rbbm_WU_3D_idle_clean_06	2002Oct16
FAIL		
449	rbbm_WU_3D_idle_clean_07	2002Oct16
FAIL		
450	rbbm_WU_3D_idle_clean_08	2002Oct16
FAIL		
451	rbbm_WU_3D_idle_clean_09	2002Oct16
FAIL		
452	rbbm_WU_3D_idle_clean_10	2002Oct16

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**FAIL**
453  rbbm_WU_3D_idle_clean_11                2002Oct16
**FAIL**
0    rbbm_WU_3D_idle_clean_12                2002Oct16
PASS
455  rbbm_WU_BIF_RBBM_agp_flush_off          2002Oct16
**FAIL**
456  rbbm_WU_BOTH_GRPX_update_pending_off    2002Oct16
**FAIL**
0    rbbm_WU_CP_dma_idle                      2002Oct16
PASS
458  rbbm_WU_CRTC_D1MODE_vline_fe            2002Oct16
**FAIL**
459  rbbm_WU_CRTC_D1MODE_vline_on            2002Oct16
**FAIL**
460  rbbm_WU_CRTC_D1MODE_vline_re            2002Oct16
**FAIL**
461  rbbm_WU_CRTC_D2MODE_vline_fe            2002Oct16
**FAIL**
462  rbbm_WU_CRTC_D2MODE_vline_on            2002Oct16
**FAIL**
463  rbbm_WU_CRTC_D2MODE_vline_re            2002Oct16
**FAIL**
464  rbbm_WU_CRTC_PFLIP_D1GRPX_update_pending_off 2002Oct16
**FAIL**
465  rbbm_WU_CRTC_PFLIP_D2GRPX_update_pending_off 2002Oct16
**FAIL**
466  rbbm_WU_MH_hdp_clean                     2002Oct16
**FAIL**
467  rbbm_WU_OV_FLIP_D1OVL_update_pending_off 2002Oct16
**FAIL**
468  rbbm_WU_OV_FLIP_D2OVL_update_pending_off 2002Oct16
**FAIL**
469  rbbm_WU_VIPH0_dma_idle                   2002Oct16
**FAIL**
470  rbbm_WU_VIPH1_dma_idle                   2002Oct16
**FAIL**
471  rbbm_WU_VIPH2_dma_idle                   2002Oct16
**FAIL**
472  rbbm_WU_VIPH3_dma_idle                   2002Oct16
**FAIL**
0    rbbm_WU_cmdfifo_entries                  2002Oct16
PASS
474  rbbm_WU_idct_semaphore                   2002Oct16
**FAIL**
475  rbbm_WU_pad_ext_sig_rising_edge          2002Oct16
**FAIL**
476  rbbm_WUrt_BIF_RBBM_agp_flush_off        2002Oct16

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**FAIL**      cp_rbbm.cmp
477  rbbm_WUrt_BOTH_GRP update_pending_off      2002Oct16
**FAIL**      cp_rbbm.cmp
478  rbbm_WUrt_CP_dma_idle      2002Oct16
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
479  rbbm_WUrt_CRTC_D1MODE_vline_fe      2002Oct16
**FAIL**      cp_rbbm.cmp
480  rbbm_WUrt_CRTC_D1MODE_vline_on      2002Oct16
**FAIL**      cp_rbbm.cmp
481  rbbm_WUrt_CRTC_D1MODE_vline_re      2002Oct16
**FAIL**      cp_rbbm.cmp
482  rbbm_WUrt_CRTC_D2MODE_vline_fe      2002Oct16
**FAIL**      cp_rbbm.cmp
483  rbbm_WUrt_CRTC_D2MODE_vline_on      2002Oct16
**FAIL**      cp_rbbm.cmp
484  rbbm_WUrt_CRTC_D2MODE_vline_re      2002Oct16
**FAIL**      cp_rbbm.cmp
485  rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off      2002Oct16
**FAIL**      cp_rbbm.cmp
486  rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off      2002Oct16
**FAIL**      cp_rbbm.cmp
487  rbbm_WUrt_MH_hdp_clean      2002Oct16
**FAIL**      cp_rbbm.cmp
488  rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off      2002Oct16
**FAIL**      cp_rbbm.cmp
489  rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off      2002Oct16
**FAIL**      cp_rbbm.cmp
490  rbbm_WUrt_RT_idle_01      2002Oct16
**FAIL**      cp_rbbm.cmp
491  rbbm_WUrt_RT_idle_02      2002Oct16
**FAIL**      cp_rbbm.cmp
492  rbbm_WUrt_RT_idle_03      2002Oct16
**FAIL**      cp_rbbm.cmp
493  rbbm_WUrt_RT_idle_04      2002Oct16
**FAIL**      cp_rbbm.cmp
494  rbbm_WUrt_RT_idle_clean_01      2002Oct16
**FAIL**      cp_rbbm.cmp
495  rbbm_WUrt_RT_idle_clean_02      2002Oct16
**FAIL**      cp_rbbm.cmp
496  rbbm_WUrt_RT_idle_clean_03      2002Oct16
**FAIL**      cp_rbbm.cmp
497  rbbm_WUrt_RT_idle_clean_04      2002Oct16
**FAIL**      cp_rbbm.cmp
498  rbbm_WUrt_RT_idle_clean_05      2002Oct16
**FAIL**      cp_rbbm.cmp
499  rbbm_WUrt_VIPH0_dma_idle      2002Oct16
**FAIL**      cp_rbbm.cmp
500  rbbm_WUrt_VIPH1_dma_idle      2002Oct16

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**FAIL**          cp_rbbm.cmp
501  rbbm_WUrt_VIPH2_dma_idle                2002Oct16
**FAIL**          cp_rbbm.cmp
502  rbbm_WUrt_VIPH3_dma_idle                2002Oct16
**FAIL**          cp_rbbm.cmp
503  rbbm_WUrt_pad_ext_sig_rising_edge      2002Oct16
**FAIL**          cp_rbbm.cmp
0    rbbm_clk_active_deassert_min_max       2002Oct16
PASS
0    rbbm_clk_active_each_pipe              2002Oct16
PASS
0    rbbm_clk_active_for_read_error         2002Oct16
PASS
0    rbbm_clk_active_for_read_request       2002Oct16
PASS
0    rbbm_clk_active_on_reset               2002Oct16
PASS
0    rbbm_cp_non_queued_rdwr_01             2002Oct16
PASS
510  rbbm_cp_non_queued_rdwr_02            2002Oct16
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
511  rbbm_cp_queued_holds_nonqueued         2002Oct16
**FAIL**          cp_rbbm.cmp
512  rbbm_cp_queued_rdwr                   2002Oct16
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
0    rbbm_hi_data_and_be_swap               2002Oct16
PASS
0    rbbm_hi_data_endian_swap              2002Oct16
PASS
0    rbbm_hi_data_no_swap_for_cppush        2002Oct16
PASS
0    rbbm_hi_data_swap_for_cppush           2002Oct16
PASS
0    rbbm_int_ack                           2002Oct16
PASS
518  rbbm_int_each_client                   2002Oct16
**FAIL**
0    rbbm_int_nrt_gfxpipe_idle              2002Oct16
PASS
0    rbbm_int_nrt_gfxpipe_idle_rderr        2002Oct16
PASS
0    rbbm_int_rderr                         2002Oct16
PASS
0    rbbm_int_st_restore_nrt_gfxpipe_idle_rderr 2002Oct16
PASS
523  rbbm_isync_cpscratch_idlegui           2002Oct16
**FAIL**
524  rbbm_isync_wait_idlegui               2002Oct16

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**FAIL**
0    rbbm_sch_bif_01                2002Oct16
PASS
526  rbbm_sch_bif_02                2002Oct16
**FAIL**
527  rbbm_sch_cf_01                 2002Oct16
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
528  rbbm_sch_cf_02                 2002Oct16
**FAIL**
529  rbbm_sch_cp_01                 2002Oct16
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0    rbbm_sch_cppfp_01              2002Oct16
PASS
0    rbbm_sch_cpvt_01                2002Oct16
PASS
0    rbbm_soft_reset                 2002Oct16
PASS
0    rbbm_vgt_default_req            2002Oct16
PASS
534  rbbm_vgt_max_req                2002Oct16
**FAIL**
535  rbbm_vgt_min_req                2002Oct16
**FAIL**
0    rbiu_reg_wr_rd                  2002Oct16
PASS
0    reg_to_mem                       2002Oct16
PASS
0    reg_to_mem_rt                    2002Oct16
PASS
0    ring_il_pio_i2_bm                2002Oct16
PASS
540  ring_pio_ibs_bm_stress           2002Oct16
**FAIL**          cp_mh_req.cmp
0    rop_cfd_wmd                      2002Oct16
PASS
0    roq_reg_reads                    2002Oct16
PASS
0    rstr_ordr                        2002Oct16
PASS
0    rt_invalid_opcode                 2002Oct16
PASS
545  rtee_arm_wait_idle_trigger_events_16 2002Oct16
**FAIL**
0    rtee_discretes_arm_rearm_events_32 2002Oct16
PASS
0    rtee_discretes_falling_00_events_16 2002Oct16
PASS
0    rtee_discretes_falling_01_events_16 2002Oct16

```

PASS		
0	rtee_discretes_falling_02_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_04_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct16

PASS		
0	rtee_discretes_falling_26_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_28_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct16
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_09_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_11_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct16

PASS		
0	rtee_discretes_invert_18_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_20_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct16
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct16
PASS		
0	rtee_discretes_level_00_events_16	2002Oct16
PASS		
0	rtee_discretes_level_01_events_16	2002Oct16
PASS		
0	rtee_discretes_level_02_events_16	2002Oct16
PASS		
0	rtee_discretes_level_03_events_16	2002Oct16
PASS		
0	rtee_discretes_level_04_events_16	2002Oct16
PASS		
0	rtee_discretes_level_05_events_16	2002Oct16
PASS		
0	rtee_discretes_level_06_events_16	2002Oct16
PASS		
0	rtee_discretes_level_07_events_16	2002Oct16
PASS		
0	rtee_discretes_level_08_events_16	2002Oct16
PASS		
0	rtee_discretes_level_09_events_16	2002Oct16

PASS		
0	rtee_discretes_level_10_events_16	2002Oct16
PASS		
0	rtee_discretes_level_11_events_16	2002Oct16
PASS		
0	rtee_discretes_level_12_events_16	2002Oct16
PASS		
0	rtee_discretes_level_13_events_16	2002Oct16
PASS		
0	rtee_discretes_level_14_events_16	2002Oct16
PASS		
0	rtee_discretes_level_15_events_16	2002Oct16
PASS		
0	rtee_discretes_level_16_events_16	2002Oct16
PASS		
0	rtee_discretes_level_17_events_16	2002Oct16
PASS		
0	rtee_discretes_level_18_events_16	2002Oct16
PASS		
0	rtee_discretes_level_19_events_16	2002Oct16
PASS		
0	rtee_discretes_level_20_events_16	2002Oct16
PASS		
0	rtee_discretes_level_21_events_16	2002Oct16
PASS		
0	rtee_discretes_level_22_events_16	2002Oct16
PASS		
0	rtee_discretes_level_23_events_16	2002Oct16
PASS		
0	rtee_discretes_level_24_events_16	2002Oct16
PASS		
0	rtee_discretes_level_25_events_16	2002Oct16
PASS		
0	rtee_discretes_level_26_events_16	2002Oct16
PASS		
0	rtee_discretes_level_27_events_16	2002Oct16
PASS		
0	rtee_discretes_level_28_events_16	2002Oct16
PASS		
0	rtee_discretes_level_29_events_16	2002Oct16
PASS		
0	rtee_discretes_level_30_events_16	2002Oct16
PASS		
0	rtee_discretes_level_31_events_16	2002Oct16
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct16

PASS		
0	rtee_discretes_rising_01_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_03_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_16_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_23_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct16

PASS		
0	rtee_discretes_rising_25_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_27_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct16
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct16
PASS		
676	rtee_display1_comp_events_16	2002Oct16
FAIL	rbbmWr.cmp	
677	rtee_display2_comp_events_16	2002Oct16
FAIL	rbbmWr.cmp	
0	rtee_mem_linear_search_events_2	2002Oct16
PASS		
679	rtee_mem_poll_comp_000_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
680	rtee_mem_poll_comp_001_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
681	rtee_mem_poll_comp_010_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
682	rtee_mem_poll_comp_011_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
683	rtee_mem_poll_comp_100_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
684	rtee_mem_poll_comp_101_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
685	rtee_mem_poll_comp_110_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
686	rtee_mem_poll_comp_111_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
687	rtee_mem_poll_comp_masks_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
688	rtee_mem_poll_comp_masks_events_16_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp rbbmWr.cmp	
689	rtee_mem_poll_comp_swaps_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
690	rtee_mem_poll_intervals_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
691	rtee_mem_poll_short_intervals_events_16	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	rtee_pio	2002Oct16

PASS		
0	rtee_prefetch_parse_nops	2002Oct16
PASS		
0	rtee_reg_linear_search_events_2	2002Oct16
PASS		
695	rtee_reg_poll_comp_000_events_16	2002Oct16
FAIL		
696	rtee_reg_poll_comp_001_events_16	2002Oct16
FAIL		
697	rtee_reg_poll_comp_010_events_16	2002Oct16
FAIL		
698	rtee_reg_poll_comp_011_events_16	2002Oct16
FAIL		
699	rtee_reg_poll_comp_100_events_16	2002Oct16
FAIL		
700	rtee_reg_poll_comp_101_events_16	2002Oct16
FAIL		
701	rtee_reg_poll_comp_110_events_16	2002Oct16
FAIL		
702	rtee_reg_poll_comp_111_events_16	2002Oct16
FAIL		
703	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Oct16
FAIL	cp_rbbm.cmp	
704	rtee_vip_comp_events_16	2002Oct16
FAIL	rbbmWr.cmp	
0	scratch_mem_read	2002Oct16
PASS		
0	scratch_mem_read2	2002Oct16
PASS		
707	set_cnst2_sm_meq	2002Oct16
FAIL	mh_cp_rd.cmp	
708	set_cnst_alu_max1_sm_meq	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
709	set_cnst_alu_max_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_cnst_bool_max2_sm_meq	2002Oct16
PASS		
711	set_cnst_loop_max2_sm_meq	2002Oct16
FAIL	mh_cp_rd.cmp	
712	set_cnst_sm_meq	2002Oct16
FAIL	mh_cp_rd.cmp	
713	set_cnst_tex_max2_sm_meq	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
714	set_cnst_we_lcc_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
715	set_cnst_we_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
716	set_cnst_write_enable_lcc2_sm_meq	2002Oct16

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**FAIL**          mh_cp_rd.cmp
717 set_cnst_write_enable_sm_meq          2002Oct16
**FAIL**          mh_cp_rd.cmp
0   set_constant                          2002Oct16
PASS
0   set_constant2                          2002Oct16
PASS
0   set_constant_alu_max1                  2002Oct16
PASS
0   set_constant_alu_max2                  2002Oct16
PASS
0   set_constant_bool_max1                 2002Oct16
PASS
0   set_constant_bool_max2                 2002Oct16
PASS
0   set_constant_indr1_short                2002Oct16
PASS
0   set_constant_indr1_short2               2002Oct16
PASS
0   set_constant_indr1_write_enable         2002Oct16
PASS
0   set_constant_indr1_write_enable_lcc    2002Oct16
PASS
0   set_constant_indr2_short                2002Oct16
PASS
0   set_constant_indr2_short2               2002Oct16
PASS
0   set_constant_indr2_write_enable         2002Oct16
PASS
0   set_constant_indr2_write_enable_lcc    2002Oct16
PASS
0   set_constant_loop_max1                  2002Oct16
PASS
0   set_constant_loop_max2                  2002Oct16
PASS
0   set_constant_ring_short                 2002Oct16
PASS
0   set_constant_ring_short2                2002Oct16
PASS
0   set_constant_ring_write_enable          2002Oct16
PASS
0   set_constant_ring_write_enable_lcc     2002Oct16
PASS
0   set_constant_short                       2002Oct16
PASS
0   set_constant_short2                     2002Oct16
PASS
0   set_constant_tex_max1                   2002Oct16

```

PASS		
0	set_constant_tex_max2	2002Oct16
PASS		
0	set_constant_tex_max2_sm_roq	2002Oct16
PASS		
0	set_constant_write_enable	2002Oct16
PASS		
0	set_constant_write_enable_lcc	2002Oct16
PASS		
0	set_constant_write_enable_lcc2	2002Oct16
PASS		
0	set_constant_write_enable_lcc2_sm_roq	2002Oct16
PASS		
0	set_scissors	2002Oct16
PASS		
0	set_st_indrl_match	2002Oct16
PASS		
0	set_st_indrl_mismatch	2002Oct16
PASS		
0	set_st_indrl_mismatch2	2002Oct16
PASS		
0	set_st_indrl_mismatch3	2002Oct16
PASS		
0	set_st_indrl_mismatch4	2002Oct16
PASS		
0	set_st_indrl_ps	2002Oct16
PASS		
0	set_st_indrl_ps_only	2002Oct16
PASS		
0	set_st_indrl_sublk0	2002Oct16
PASS		
0	set_st_indrl_sublk1	2002Oct16
PASS		
0	set_st_indrl_sublk1_only	2002Oct16
PASS		
0	set_st_indrl_sublk2	2002Oct16
PASS		
0	set_st_indrl_sublk2_only	2002Oct16
PASS		
0	set_st_indrl_sublk3	2002Oct16
PASS		
0	set_st_indrl_sublk3_only	2002Oct16
PASS		
0	set_st_indrl_sublk4	2002Oct16
PASS		
0	set_st_indrl_sublk4_only	2002Oct16
PASS		
0	set_st_indrl_sublk5	2002Oct16

PASS		
0	set_st_indr1_sublk5_only	2002Oct16
PASS		
0	set_st_indr1_sublk6	2002Oct16
PASS		
0	set_st_indr1_sublk6_only	2002Oct16
PASS		
0	set_st_indr1_sublk7	2002Oct16
PASS		
0	set_st_indr1_sublk7_only	2002Oct16
PASS		
0	set_st_indr1_vs	2002Oct16
PASS		
0	set_st_indr1_vs_only	2002Oct16
PASS		
0	set_st_indr2_match	2002Oct16
PASS		
0	set_st_indr2_mismatch	2002Oct16
PASS		
0	set_st_indr2_mismatch2	2002Oct16
PASS		
0	set_st_indr2_mismatch3	2002Oct16
PASS		
0	set_st_indr2_mismatch4	2002Oct16
PASS		
0	set_st_indr2_ps	2002Oct16
PASS		
0	set_st_indr2_ps_only	2002Oct16
PASS		
0	set_st_indr2_sublk0	2002Oct16
PASS		
0	set_st_indr2_sublk1	2002Oct16
PASS		
0	set_st_indr2_sublk1_only	2002Oct16
PASS		
0	set_st_indr2_sublk2	2002Oct16
PASS		
0	set_st_indr2_sublk2_only	2002Oct16
PASS		
0	set_st_indr2_sublk3	2002Oct16
PASS		
0	set_st_indr2_sublk3_only	2002Oct16
PASS		
0	set_st_indr2_sublk4	2002Oct16
PASS		
0	set_st_indr2_sublk4_only	2002Oct16
PASS		
0	set_st_indr2_sublk5	2002Oct16

PASS		
0	set_st_indr2_sublk5_only	2002Oct16
PASS		
0	set_st_indr2_sublk6	2002Oct16
PASS		
0	set_st_indr2_sublk6_only	2002Oct16
PASS		
0	set_st_indr2_sublk7	2002Oct16
PASS		
0	set_st_indr2_sublk7_only	2002Oct16
PASS		
0	set_st_indr2_vs	2002Oct16
PASS		
0	set_st_indr2_vs_only	2002Oct16
PASS		
0	set_st_pfmatch_disabled	2002Oct16
PASS		
0	set_st_ring_match	2002Oct16
PASS		
0	set_st_ring_mismatch	2002Oct16
PASS		
0	set_st_ring_mismatch2	2002Oct16
PASS		
0	set_st_ring_mismatch3	2002Oct16
PASS		
0	set_st_ring_mismatch4	2002Oct16
PASS		
0	set_st_ring_ps	2002Oct16
PASS		
0	set_st_ring_ps_only	2002Oct16
PASS		
0	set_st_ring_sublk0	2002Oct16
PASS		
0	set_st_ring_sublk1	2002Oct16
PASS		
0	set_st_ring_sublk1_only	2002Oct16
PASS		
0	set_st_ring_sublk2	2002Oct16
PASS		
0	set_st_ring_sublk2_only	2002Oct16
PASS		
0	set_st_ring_sublk3	2002Oct16
PASS		
0	set_st_ring_sublk3_only	2002Oct16
PASS		
0	set_st_ring_sublk4	2002Oct16
PASS		
0	set_st_ring_sublk4_only	2002Oct16

PASS		
0	set_st_ring_sublk5	2002Oct16
PASS		
0	set_st_ring_sublk5_only	2002Oct16
PASS		
0	set_st_ring_sublk6	2002Oct16
PASS		
0	set_st_ring_sublk6_only	2002Oct16
PASS		
0	set_st_ring_sublk7	2002Oct16
PASS		
0	set_st_ring_sublk7_only	2002Oct16
PASS		
0	set_st_ring_vs	2002Oct16
PASS		
0	set_st_ring_vs_only	2002Oct16
PASS		
0	set_st_stress_a	2002Oct16
PASS		
0	set_st_stress_b	2002Oct16
PASS		
0	set_st_stress_b_random	2002Oct16
PASS		
0	set_st_stress_b_sm_meq	2002Oct16
PASS		
0	set_st_stress_c	2002Oct16
PASS		
0	set_st_stress_c_sm_roq	2002Oct16
PASS		
0	set_st_stress_d	2002Oct16
PASS		
0	set_st_stress_d_random	2002Oct16
PASS		
0	set_st_stress_d_sm_meq	2002Oct16
PASS		
0	set_st_stress_e	2002Oct16
PASS		
0	set_st_stress_e_sm_meq	2002Oct16
PASS		
0	set_st_stress_f	2002Oct16
PASS		
0	set_st_stress_f_random	2002Oct16
PASS		
0	set_state_rt_a	2002Oct16
PASS		
0	set_state_rt_b	2002Oct16
PASS		
0	set_state_rt_c	2002Oct16

PASS		
0	set_state_rt_d	2002Oct16
PASS		
838	set_state_rt_d_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_state_rt_e	2002Oct16
PASS		
840	set_state_rt_e_random	2002Oct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	smoke_nrt	2002Oct16
PASS		
0	src_clip	2002Oct16
PASS		
0	srf_cohr	2002Oct16
PASS		
0	switch_pio_bm	2002Oct16
PASS		
0	trans_bitblt	2002Oct16
PASS		
0	type0_2long_pckts	2002Oct16
PASS		
0	type0_3indr_buffs	2002Oct16
PASS		
0	type0_4pckts	2002Oct16
PASS		
0	type0_incr_updt_cnst	2002Oct16
PASS		
0	type0_incr_updt_instrmem	2002Oct16
PASS		
0	type0_incr_updt_st	2002Oct16
PASS		
0	type0_incr_updt_stress	2002Oct16
PASS		
0	type0_ind1_ind2_a	2002Oct16
PASS		
0	type0_ind1_ind2_b	2002Oct16
PASS		
0	type0_indr_buff	2002Oct16
PASS		
0	type0_real_time	2002Oct16
PASS		
857	type0_real_time_random	2002Oct16
FAIL	mh_cp_rd.cmp	
0	type1_4pckts	2002Oct16
PASS		
0	type1_indr_buff	2002Oct16
PASS		
0	type1_pckt	2002Oct16

PASS		
861	type_random	20020ct16
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	uram_wrt_rt_ucode	20020ct16
PASS		
0	uram_wrt_ucode	20020ct16
PASS		
0	viz_query_a	20020ct16
PASS		
0	viz_query_draw_indx	20020ct16
PASS		
0	viz_query_draw_indx_indr1	20020ct16
PASS		
0	viz_query_draw_indx_indr2	20020ct16
PASS		
0	viz_query_draw_indx_multipass	20020ct16
PASS		
0	viz_query_draw_indx_ring	20020ct16
PASS		
0	viz_query_indr1	20020ct16
PASS		
0	viz_query_indr1_simple	20020ct16
PASS		
0	viz_query_indr2	20020ct16
PASS		
0	viz_query_indr2_simple	20020ct16
PASS		
0	viz_query_ring	20020ct16
PASS		
0	viz_query_ring_simple	20020ct16
PASS		
0	wait_for_idle	20020ct16
PASS		
877	wait_reg_mem_poll_mem0	20020ct16
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	wait_reg_mem_poll_mem0_rt	20020ct16
PASS		
879	wait_reg_mem_poll_mem1	20020ct16
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
880	wait_reg_mem_poll_mem2	20020ct16
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
881	wait_reg_mem_poll_mem3	20020ct16
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
882	wait_reg_mem_poll_reg0	20020ct16
FAIL	cp_rbbm.cmp	
883	wait_reg_mem_poll_reg1	20020ct16
FAIL	cp_rbbm.cmp	
884	wait_reg_mem_poll_reg2	20020ct16


```
**FAIL**          cp_rbbm.cmp
885 wait_reg_mem_poll_reg3          2002Oct16
**FAIL**          cp_rbbm.cmp
```

```
=====
Tests_Run_count = 885 Pass_count = 699 Fail_count = 186 Incomplete_count = 0
=====
```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep5
PASS			
0	2d_indr_buffer_b		2002Sep5
PASS			
0	2d_indr_buffer_c		2002Sep5
PASS			
0	2d_indr_buffer_d		2002Sep5
PASS			
0	2d_indr_buffer_e		2002Sep5
PASS			
0	2d_indr_buffer_f		2002Sep5
PASS			
0	2d_indr_buffer_g		2002Sep5
PASS			
0	3d_draw_indx_2		2002Sep5
PASS			
0	3d_draw_indx_2_b		2002Sep5
PASS			
0	bitblt_multi		2002Sep5
PASS			
0	brush_32x1mono		2002Sep5
PASS			
0	brush_8x8color_16		2002Sep5
PASS			
0	brush_8x8color_32		2002Sep5
PASS			
0	brush_8x8color_64		2002Sep5
PASS			
0	brush_8x8color_dst_0_1		2002Sep5
PASS			
0	brush_8x8mono		2002Sep5
PASS			
0	brush_color_mono_stress		2002Sep5
PASS			
0	brush_flag		2002Sep5
PASS			
0	brush_type0		2002Sep5
PASS			
0	brush_type1		2002Sep5

PASS		
0	brush_type10	2002Sep5
PASS		
0	brush_type13	2002Sep5
PASS		
0	brush_type14	2002Sep5
PASS		
0	brush_type6	2002Sep5
PASS		
0	brush_type7	2002Sep5
PASS		
0	brush_type_other	2002Sep5
PASS		
0	brush_types_13_14	2002Sep5
PASS		
0	clear_const_wr_en_on_me_init	2002Sep5
PASS		
0	clear_const_wr_en_with_lcc	2002Sep5
PASS		
0	cond_write_mem_poll_mem	2002Sep5
PASS		
0	cond_write_mem_poll_mem2	2002Sep5
PASS		
0	cond_write_mem_poll_mem3	2002Sep5
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep5
PASS		
0	cond_write_mem_poll_reg	2002Sep5
PASS		
0	cond_write_mem_poll_reg2	2002Sep5
PASS		
0	cond_write_mem_poll_reg3	2002Sep5
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep5
PASS		
0	cp_bios_scratch	2002Sep5
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep5
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep5
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep5
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep5
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep5
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep5

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep5
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep5
PASS		
0	cp_ib_base000_bufszXXX	2002Sep5
PASS		
0	cp_ib_base001_bufszXXX	2002Sep5
PASS		
0	cp_ib_base010_bufszXXX	2002Sep5
PASS		
0	cp_ib_base011_bufszXXX	2002Sep5
PASS		
0	cp_ib_base100_bufszXXX	2002Sep5
PASS		
0	cp_ib_base101_bufszXXX	2002Sep5
PASS		
0	cp_ib_base110_bufszXXX	2002Sep5
PASS		
0	cp_ib_base111_bufszXXX	2002Sep5
PASS		
0	cp_interrupt	2002Sep5
PASS		
0	cp_interrupt_rt_events_16	2002Sep5
PASS		
0	cp_iq2_alignment_000	2002Sep5
PASS		
0	cp_iq2_alignment_001	2002Sep5
PASS		
0	cp_iq2_alignment_010	2002Sep5
PASS		
0	cp_iq2_alignment_011	2002Sep5
PASS		
0	cp_iq2_alignment_100	2002Sep5
PASS		
0	cp_iq2_alignment_101	2002Sep5
PASS		
0	cp_iq2_alignment_110	2002Sep5
PASS		
0	cp_iq2_alignment_111	2002Sep5
PASS		
0	cp_iq_alignment_000	2002Sep5
PASS		
0	cp_iq_alignment_001	2002Sep5
PASS		
0	cp_iq_alignment_010	2002Sep5
PASS		
0	cp_iq_alignment_011	2002Sep5

PASS		
0	cp_iq_alignment_100	2002Sep5
PASS		
0	cp_iq_alignment_101	2002Sep5
PASS		
0	cp_iq_alignment_110	2002Sep5
PASS		
0	cp_iq_alignment_111	2002Sep5
PASS		
0	cp_max_size_packet	2002Sep5
PASS		
0	cp_max_size_packet_random	2002Sep5
PASS		
0	cp_nrt_rt_protection_mode	2002Sep5
PASS		
0	cp_output_stress	2002Sep5
PASS		
0	cp_output_stress_random	2002Sep5
PASS		
0	cp_protection_mode	2002Sep5
PASS		
0	cp_rb_alignment_000	2002Sep5
PASS		
0	cp_rb_alignment_001	2002Sep5
PASS		
0	cp_rb_alignment_010	2002Sep5
PASS		
0	cp_rb_alignment_011	2002Sep5
PASS		
0	cp_rb_alignment_100	2002Sep5
PASS		
0	cp_rb_alignment_101	2002Sep5
PASS		
0	cp_rb_alignment_110	2002Sep5
PASS		
0	cp_rb_alignment_111	2002Sep5
PASS		
0	cp_rb_wptrXXX_base000	2002Sep5
PASS		
0	cp_rb_wptrXXX_base001	2002Sep5
PASS		
0	cp_rb_wptrXXX_base010	2002Sep5
PASS		
0	cp_rb_wptrXXX_base011	2002Sep5
PASS		
0	cp_rb_wptrXXX_base100	2002Sep5
PASS		
0	cp_rb_wptrXXX_base101	2002Sep5

PASS		
0	cp_rb_wptrXXX_base110	2002Sep5
PASS		
0	cp_rb_wptrXXX_base111	2002Sep5
PASS		
0	cp_register_reads	2002Sep5
PASS		
0	cp_reg_rw_test	2002Sep5
PASS		
0	cp_rt_protection_mode	2002Sep5
PASS		
0	cp_scratch	2002Sep5
PASS		
0	cp_scratch_multi	2002Sep5
PASS		
0	dma_cp_sync	2002Sep5
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep5
PASS		
0	dma_dscrstr_flush1	2002Sep5
PASS		
0	dma_dscrstr_flush2	2002Sep5
PASS		
0	dma_dscrstr_flush3	2002Sep5
PASS		
0	dma_m2m_src00_dstXX	2002Sep5
PASS		
0	dma_m2m_src01_dstXX	2002Sep5
PASS		
0	dma_m2m_src10_dstXX	2002Sep5
PASS		
0	dma_m2m_src11_dstXX	2002Sep5
PASS		
0	dma_mem2mem	2002Sep5
PASS		
0	dma_mem2mem_abort	2002Sep5
PASS		
0	dma_mem2mem_alignments	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep5

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep5
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep5
PASS		
0	dma_mem2mem_pio_abort	2002Sep5
PASS		
0	dma_mem2mem_random	2002Sep5
PASS		
0	dma_mem2reg	2002Sep5
PASS		
0	dma_mem2reg_abort	2002Sep5
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep5
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep5
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep5
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep5
PASS		
0	dma_mem2reg_sync	2002Sep5
PASS		
0	dma_r2r_m2m_r2r	2002Sep5
PASS		
0	dma_r2r_m2r_r2r	2002Sep5
PASS		
0	dma_r2r_r2m_r2r	2002Sep5
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep5
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep5
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep5
PASS		
0	dma_reg2mem	2002Sep5
PASS		
0	dma_reg2mem_abort	2002Sep5
PASS		
0	dma_reg2reg	2002Sep5
PASS		
0	dma_reg2reg_2dw	2002Sep5
PASS		
0	dma_reg2reg_daic	2002Sep5
PASS		
0	dma_reg2reg_mult	2002Sep5

PASS		
0	dma_reg2reg_mult2	2002Sep5
PASS		
0	dma_reg2reg_mult3	2002Sep5
PASS		
0	dma_reg2reg_saic	2002Sep5
PASS		
0	dma_reg2reg_sdaic	2002Sep5
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep5
PASS		
0	dma_reg2reg_stress	2002Sep5
PASS		
0	draw_indx_a	2002Sep5
PASS		
0	draw_indx_b	2002Sep5
PASS		
0	draw_indx_c	2002Sep5
PASS		
0	draw_indx_indr1_a	2002Sep5
PASS		
0	draw_indx_indr1_b	2002Sep5
PASS		
0	draw_indx_indr1_short_a	2002Sep5
PASS		
0	draw_indx_indr1_short_b	2002Sep5
PASS		
0	draw_indx_indr1_stress	2002Sep5
PASS		
0	draw_indx_indr2_a	2002Sep5
PASS		
0	draw_indx_indr2_b	2002Sep5
PASS		
0	draw_indx_indr2_short_a	2002Sep5
PASS		
0	draw_indx_indr2_short_b	2002Sep5
PASS		
0	draw_indx_indr2_stress	2002Sep5
PASS		
0	draw_indx_ring1	2002Sep5
PASS		
0	draw_indx_ring2	2002Sep5
PASS		
0	draw_indx_ring_short1	2002Sep5
PASS		
0	draw_indx_ring_short2	2002Sep5
PASS		
0	draw_indx_ring_stress	2002Sep5

PASS		
0	draw_indx_short_a	2002Sep5
PASS		
0	draw_indx_short_b	2002Sep5
PASS		
0	draw_indx_short_c	2002Sep5
PASS		
0	draw_indx_stress_a	2002Sep5
PASS		
0	draw_indx_stress_b	2002Sep5
PASS		
0	draw_indx_stress_c	2002Sep5
PASS		
0	event_timestamp_write	2002Sep5
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep5
PASS		
0	ft_indr1_july22	2002Sep5
PASS		
0	ft_indr2_july22	2002Sep5
PASS		
0	ft_july22	2002Sep5
PASS		
0	ft_july22_random	2002Sep5
PASS		
0	header_dump	2002Sep5
PASS		
0	header_dump_rt	2002Sep5
PASS		
0	header_dump_small	2002Sep5
PASS		
0	hostdata_blt	2002Sep5
PASS		
0	im_load_a	2002Sep5
PASS		
0	im_load_b	2002Sep5
PASS		
0	im_load_cid_mismatch	2002Sep5
PASS		
0	im_load_immediate	2002Sep5
PASS		
0	im_load_immediate_nrt_me	2002Sep5
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep5
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep5
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep5

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep5
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep5
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep5
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep5
PASS		
0	im_load_indr1_a	2002Sep5
PASS		
0	im_load_indr1_b	2002Sep5
PASS		
0	im_load_indr2_a	2002Sep5
PASS		
0	im_load_indr2_b	2002Sep5
PASS		
0	im_load_invalidate	2002Sep5
PASS		
0	im_load_match	2002Sep5
PASS		
0	im_load_match2	2002Sep5
PASS		
0	im_load_nrt_me	2002Sep5
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep5
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep5
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep5
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep5
PASS		
0	im_load_pfmatch_disable	2002Sep5
PASS		
0	im_load_ring_a	2002Sep5
PASS		
0	im_load_ring_b	2002Sep5
PASS		
0	im_load_rt	2002Sep5
PASS		
0	im_load_rt2	2002Sep5
PASS		
0	im_load_rt3	2002Sep5
PASS		
0	im_load_rt4	2002Sep5
PASS		
0	im_load_sz_mismatch	2002Sep5

PASS		
0	incremental_update_const	2002Sep5
PASS		
0	incremental_update_instr	2002Sep5
PASS		
0	incremental_update_state	2002Sep5
PASS		
0	indirect_buffer_a	2002Sep5
PASS		
0	indirect_buffer_b	2002Sep5
PASS		
0	indirect_buffer_c	2002Sep5
PASS		
0	indirect_buffer_d	2002Sep5
PASS		
0	indirect_buffer_e	2002Sep5
PASS		
0	indirect_buffer_f	2002Sep5
PASS		
0	indirect_buffer_g	2002Sep5
PASS		
0	indirect_buffer_h	2002Sep5
PASS		
0	indirect_buffer_i	2002Sep5
PASS		
0	indirect_buffer_j	2002Sep5
PASS		
0	indirect_buffer_k	2002Sep5
PASS		
0	indirect_buffer_l	2002Sep5
PASS		
0	indirect_buffer_mp_a	2002Sep5
PASS		
0	indirect_buffer_mp_b	2002Sep5
PASS		
0	indirect_buffer_mp_c	2002Sep5
PASS		
0	indirect_buffer_mp_d	2002Sep5
PASS		
0	indirect_buffer_mp_e	2002Sep5
PASS		
0	indirect_buffer_mp_f	2002Sep5
PASS		
0	invalidate_on_me_init	2002Sep5
PASS		
0	invalidate_state_constants	2002Sep5
PASS		
0	invalidate_state_shaders	2002Sep5

PASS		
0	invalidate_state_shaders2	2002Sep5
PASS		
0	invalidate_state_sublks	2002Sep5
PASS		
0	invalidate_state_sublks2	2002Sep5
PASS		
0	invalid_opcode	2002Sep5
PASS		
0	iq1_iq2_mult	2002Sep5
PASS		
0	iq1_iq2_push_wrap	2002Sep5
PASS		
0	iq2_csq_full	2002Sep5
PASS		
0	IQ2_Mult	2002Sep5
PASS		
0	IQ2_Wrap_Legacy	2002Sep5
PASS		
0	IQ_Wrap	2002Sep5
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep5
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep5
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep5
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep5
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep5
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep5
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep5
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep5
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep5
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep5
PASS		
0	ld_cnst_cntx_match	2002Sep5
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep5
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep5
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep5

PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep5
PASS		
0	ld_cnst_cntx_pfmatch_disable	2002Sep5
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep5
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep5
PASS		
0	ld_cnst_cntx_replicate8	2002Sep5
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep5
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep5
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep5
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep5
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep5
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep5
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep5
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep5
PASS		
0	load_constant_context_rt	2002Sep5
PASS		
0	load_palette	2002Sep5
PASS		
0	lp_2d3d	2002Sep5
PASS		
0	mem_write	2002Sep5
PASS		
0	mem_write_rt	2002Sep5
PASS		
0	mpeg_index	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep5

PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep5
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep5
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep5
PASS		
0	nop_eating_disabled	2002Sep5
PASS		
0	paint_multi	2002Sep5
PASS		
0	pitch_clip	2002Sep5
PASS		
0	ply_nextscan	2002Sep5
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep5
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep5
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep5
PASS		
0	rbbm_cp_queued_rdwr	2002Sep5
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep5
PASS		
0	rbbm_isync_wait_idlegui	2002Sep5
PASS		
0	rbbm_soft_reset	2002Sep5
PASS		
0	rbbm_WU_2D_idle_01	2002Sep5
PASS		
0	rbbm_WU_2D_idle_02	2002Sep5
PASS		
0	rbbm_WU_2D_idle_03	2002Sep5

PASS		
0	rbbm_WU_2D_idle_04	2002Sep5
PASS		
0	rbbm_WU_2D_idle_05	2002Sep5
PASS		
0	rbbm_WU_2D_idle_06	2002Sep5
PASS		
0	rbbm_WU_2D_idle_07	2002Sep5
PASS		
0	rbbm_WU_2D_idle_08	2002Sep5
PASS		
0	rbbm_WU_2D_idle_09	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep5
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep5
PASS		
0	rbbm_WU_3D_idle_01	2002Sep5
PASS		
0	rbbm_WU_3D_idle_02	2002Sep5
PASS		
0	rbbm_WU_3D_idle_03	2002Sep5
PASS		
0	rbbm_WU_3D_idle_04	2002Sep5
PASS		
0	rbbm_WU_3D_idle_05	2002Sep5
PASS		
0	rbbm_WU_3D_idle_06	2002Sep5

PASS		
0	rbbm_WU_3D_idle_07	2002Sep5
PASS		
0	rbbm_WU_3D_idle_08	2002Sep5
PASS		
0	rbbm_WU_3D_idle_09	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep5
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep5
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep5
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep5
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep5
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep5
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep5
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep5
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep5
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep5
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep5

PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep5
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep5
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep5
PASS		
0	rbbm_WU_idct_semaphore	2002Sep5
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep5
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep5
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep5
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep5
PASS		
365	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep5
FAIL	cp_rbbm.cmp	
366	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep5
FAIL	cp_rbbm.cmp	
367	rbbm_WUrt_CP_dma_idle	2002Sep5
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
368	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep5
FAIL	cp_rbbm.cmp	
369	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep5
FAIL	cp_rbbm.cmp	
370	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep5
FAIL	cp_rbbm.cmp	
371	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep5
FAIL	cp_rbbm.cmp	
372	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep5
FAIL	cp_rbbm.cmp	
373	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep5
FAIL	cp_rbbm.cmp	
374	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep5
FAIL	cp_rbbm.cmp	
375	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep5
FAIL	cp_rbbm.cmp	
376	rbbm_WUrt_MH_hdp_clean	2002Sep5
FAIL	cp_rbbm.cmp	
377	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep5
FAIL	cp_rbbm.cmp	
378	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep5
FAIL	cp_rbbm.cmp	
379	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep5
FAIL	cp_rbbm.cmp	
380	rbbm_WUrt_RT_idle_01	2002Sep5

```

**FAIL**      cp_rbbm.cmp
381  rbbm_WUrt_RT_idle_02                2002Sep5
**FAIL**      cp_rbbm.cmp
382  rbbm_WUrt_RT_idle_03                2002Sep5
**FAIL**      cp_rbbm.cmp
383  rbbm_WUrt_RT_idle_04                2002Sep5
**FAIL**      cp_rbbm.cmp
384  rbbm_WUrt_RT_idle_clean_01          2002Sep5
**FAIL**      cp_rbbm.cmp
385  rbbm_WUrt_RT_idle_clean_02          2002Sep5
**FAIL**      cp_rbbm.cmp
386  rbbm_WUrt_RT_idle_clean_03          2002Sep5
**FAIL**      cp_rbbm.cmp
387  rbbm_WUrt_RT_idle_clean_04          2002Sep5
**FAIL**      cp_rbbm.cmp
388  rbbm_WUrt_RT_idle_clean_05          2002Sep5
**FAIL**      cp_rbbm.cmp
389  rbbm_WUrt_VIPH0_dma_idle            2002Sep5
**FAIL**      cp_rbbm.cmp
390  rbbm_WUrt_VIPH1_dma_idle            2002Sep5
**FAIL**      cp_rbbm.cmp
391  rbbm_WUrt_VIPH2_dma_idle            2002Sep5
**FAIL**      cp_rbbm.cmp
392  rbbm_WUrt_VIPH3_dma_idle            2002Sep5
**FAIL**      cp_rbbm.cmp
0    rbbm_WU_VIPH0_dma_idle              2002Sep5
PASS
0    rbbm_WU_VIPH1_dma_idle              2002Sep5
PASS
0    rbbm_WU_VIPH2_dma_idle              2002Sep5
PASS
0    rbbm_WU_VIPH3_dma_idle              2002Sep5
PASS
0    RB_BUFSZ_0                          2002Sep5
PASS
0    RB_BUFSZ_1                          2002Sep5
PASS
0    RB_Wrap                              2002Sep5
PASS
0    reg_to_mem                           2002Sep5
PASS
0    reg_to_mem_rt                        2002Sep5
PASS
0    rop_cfd_wmd                          2002Sep5
PASS
0    rstr_ordr                            2002Sep5
PASS
404  rtee_arm_wait_idle_trigger_events_16 2002Sep5

```

FAIL

0	rtee_discretes_arm_rearm_events_32	2002Sep5
	PASS	
0	rtee_discretes_falling_00_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_01_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_02_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_03_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_04_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_05_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_06_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_07_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_08_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_09_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_10_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_11_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_12_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_13_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_14_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_15_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_16_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_17_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_18_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_19_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_20_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_21_events_16	2002Sep5
	PASS	
0	rtee_discretes_falling_22_events_16	2002Sep5

PASS		
0	rtee_discretes_falling_23_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep5
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep5

PASS		
0	rtee_discretes_invert_15_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep5
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep5
PASS		
0	rtee_discretes_level_00_events_16	2002Sep5
PASS		
0	rtee_discretes_level_01_events_16	2002Sep5
PASS		
0	rtee_discretes_level_02_events_16	2002Sep5
PASS		
0	rtee_discretes_level_03_events_16	2002Sep5
PASS		
0	rtee_discretes_level_04_events_16	2002Sep5
PASS		
0	rtee_discretes_level_05_events_16	2002Sep5
PASS		
0	rtee_discretes_level_06_events_16	2002Sep5

PASS		
0	rtee_discretes_level_07_events_16	2002Sep5
PASS		
0	rtee_discretes_level_08_events_16	2002Sep5
PASS		
0	rtee_discretes_level_09_events_16	2002Sep5
PASS		
0	rtee_discretes_level_10_events_16	2002Sep5
PASS		
0	rtee_discretes_level_11_events_16	2002Sep5
PASS		
0	rtee_discretes_level_12_events_16	2002Sep5
PASS		
0	rtee_discretes_level_13_events_16	2002Sep5
PASS		
0	rtee_discretes_level_14_events_16	2002Sep5
PASS		
0	rtee_discretes_level_15_events_16	2002Sep5
PASS		
0	rtee_discretes_level_16_events_16	2002Sep5
PASS		
0	rtee_discretes_level_17_events_16	2002Sep5
PASS		
0	rtee_discretes_level_18_events_16	2002Sep5
PASS		
0	rtee_discretes_level_19_events_16	2002Sep5
PASS		
0	rtee_discretes_level_20_events_16	2002Sep5
PASS		
0	rtee_discretes_level_21_events_16	2002Sep5
PASS		
0	rtee_discretes_level_22_events_16	2002Sep5
PASS		
0	rtee_discretes_level_23_events_16	2002Sep5
PASS		
0	rtee_discretes_level_24_events_16	2002Sep5
PASS		
0	rtee_discretes_level_25_events_16	2002Sep5
PASS		
0	rtee_discretes_level_26_events_16	2002Sep5
PASS		
0	rtee_discretes_level_27_events_16	2002Sep5
PASS		
0	rtee_discretes_level_28_events_16	2002Sep5
PASS		
0	rtee_discretes_level_29_events_16	2002Sep5
PASS		
0	rtee_discretes_level_30_events_16	2002Sep5

PASS		
0	rtee_discretes_level_31_events_16	2002Sep5
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep5

PASS		
0	rtee_discretes_rising_22_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep5
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep5
PASS		
0	rtee_display1_comp_events_16	2002Sep5
PASS		
0	rtee_display2_comp_events_16	2002Sep5
PASS		
0	rtee_mem_linear_search_events_2	2002Sep5
PASS		
538	rtee_mem_poll_comp_000_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
539	rtee_mem_poll_comp_001_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
540	rtee_mem_poll_comp_010_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
541	rtee_mem_poll_comp_011_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
542	rtee_mem_poll_comp_100_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
543	rtee_mem_poll_comp_101_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
544	rtee_mem_poll_comp_110_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
545	rtee_mem_poll_comp_111_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
546	rtee_mem_poll_comp_masks_events_16	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
547	rtee_mem_poll_comp_masks_events_16_random	2002Sep5
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
548	rtee_mem_poll_comp_swaps_events_16	2002Sep5


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**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
549  rtee_mem_poll_intervals_events_16          2002Sep5
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0    rtee_prefetch_parse_nops                  2002Sep5
PASS
0    rtee_reg_linear_search_events_2           2002Sep5
PASS
552  rtee_reg_poll_comp_000_events_16          2002Sep5
**FAIL**
553  rtee_reg_poll_comp_001_events_16          2002Sep5
**FAIL**
554  rtee_reg_poll_comp_010_events_16          2002Sep5
**FAIL**
555  rtee_reg_poll_comp_011_events_16          2002Sep5
**FAIL**
556  rtee_reg_poll_comp_100_events_16          2002Sep5
**FAIL**
557  rtee_reg_poll_comp_101_events_16          2002Sep5
**FAIL**
558  rtee_reg_poll_comp_110_events_16          2002Sep5
**FAIL**
559  rtee_reg_poll_comp_111_events_16          2002Sep5
**FAIL**
560  rtee_reg_poll_comp_queued_nonqueued_events_16 2002Sep5
**FAIL**          cp_rbbm.cmp
0    rtee_vip_comp_events_16                   2002Sep5
PASS
0    rt_invalid_opcode                         2002Sep5
PASS
0    scratch_mem_read                          2002Sep5
PASS
0    scratch_mem_read2                         2002Sep5
PASS
0    set_constant                             2002Sep5
PASS
0    set_constant2                            2002Sep5
PASS
0    set_constant_alu_max1                    2002Sep5
PASS
0    set_constant_alu_max2                    2002Sep5
PASS
0    set_constant_bool_max1                   2002Sep5
PASS
0    set_constant_bool_max2                   2002Sep5
PASS
0    set_constant_indr1_short                  2002Sep5
PASS
0    set_constant_indr1_short2                 2002Sep5

```

PASS		
0	set_constant_indr1_write_enable	2002Sep5
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep5
PASS		
0	set_constant_indr2_short	2002Sep5
PASS		
0	set_constant_indr2_short2	2002Sep5
PASS		
0	set_constant_indr2_write_enable	2002Sep5
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep5
PASS		
0	set_constant_loop_max1	2002Sep5
PASS		
0	set_constant_loop_max2	2002Sep5
PASS		
0	set_constant_ring_short	2002Sep5
PASS		
0	set_constant_ring_short2	2002Sep5
PASS		
0	set_constant_ring_write_enable	2002Sep5
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep5
PASS		
0	set_constant_short	2002Sep5
PASS		
0	set_constant_short2	2002Sep5
PASS		
0	set_constant_tex_max1	2002Sep5
PASS		
0	set_constant_tex_max2	2002Sep5
PASS		
0	set_constant_write_enable	2002Sep5
PASS		
0	set_constant_write_enable_lcc	2002Sep5
PASS		
0	set_constant_write_enable_lcc2	2002Sep5
PASS		
0	set_scissors	2002Sep5
PASS		
0	set_state_rt_a	2002Sep5
PASS		
0	set_state_rt_b	2002Sep5
PASS		
0	set_state_rt_c	2002Sep5
PASS		
0	set_state_rt_d	2002Sep5

PASS		
0	set_st_indrl_match	2002Sep5
PASS		
0	set_st_indrl_mismatch	2002Sep5
PASS		
0	set_st_indrl_mismatch2	2002Sep5
PASS		
0	set_st_indrl_mismatch3	2002Sep5
PASS		
0	set_st_indrl_mismatch4	2002Sep5
PASS		
0	set_st_indrl_ps	2002Sep5
PASS		
0	set_st_indrl_ps_only	2002Sep5
PASS		
0	set_st_indrl_sublk0	2002Sep5
PASS		
0	set_st_indrl_sublk1	2002Sep5
PASS		
0	set_st_indrl_sublk1_only	2002Sep5
PASS		
0	set_st_indrl_sublk2	2002Sep5
PASS		
0	set_st_indrl_sublk2_only	2002Sep5
PASS		
0	set_st_indrl_sublk3	2002Sep5
PASS		
0	set_st_indrl_sublk3_only	2002Sep5
PASS		
0	set_st_indrl_sublk4	2002Sep5
PASS		
0	set_st_indrl_sublk4_only	2002Sep5
PASS		
0	set_st_indrl_sublk5	2002Sep5
PASS		
0	set_st_indrl_sublk5_only	2002Sep5
PASS		
0	set_st_indrl_sublk6	2002Sep5
PASS		
0	set_st_indrl_sublk6_only	2002Sep5
PASS		
0	set_st_indrl_sublk7	2002Sep5
PASS		
0	set_st_indrl_sublk7_only	2002Sep5
PASS		
0	set_st_indrl_vs	2002Sep5
PASS		
0	set_st_indrl_vs_only	2002Sep5

PASS		
0	set_st_indr2_match	2002Sep5
PASS		
0	set_st_indr2_mismatch	2002Sep5
PASS		
0	set_st_indr2_mismatch2	2002Sep5
PASS		
0	set_st_indr2_mismatch3	2002Sep5
PASS		
0	set_st_indr2_mismatch4	2002Sep5
PASS		
0	set_st_indr2_ps	2002Sep5
PASS		
0	set_st_indr2_ps_only	2002Sep5
PASS		
0	set_st_indr2_sublk0	2002Sep5
PASS		
0	set_st_indr2_sublk1	2002Sep5
PASS		
0	set_st_indr2_sublk1_only	2002Sep5
PASS		
0	set_st_indr2_sublk2	2002Sep5
PASS		
0	set_st_indr2_sublk2_only	2002Sep5
PASS		
0	set_st_indr2_sublk3	2002Sep5
PASS		
0	set_st_indr2_sublk3_only	2002Sep5
PASS		
0	set_st_indr2_sublk4	2002Sep5
PASS		
0	set_st_indr2_sublk4_only	2002Sep5
PASS		
0	set_st_indr2_sublk5	2002Sep5
PASS		
0	set_st_indr2_sublk5_only	2002Sep5
PASS		
0	set_st_indr2_sublk6	2002Sep5
PASS		
0	set_st_indr2_sublk6_only	2002Sep5
PASS		
0	set_st_indr2_sublk7	2002Sep5
PASS		
0	set_st_indr2_sublk7_only	2002Sep5
PASS		
0	set_st_indr2_vs	2002Sep5
PASS		
0	set_st_indr2_vs_only	2002Sep5

PASS		
0	set_st_pfmatch_disabled	2002Sep5
PASS		
0	set_st_ring_match	2002Sep5
PASS		
0	set_st_ring_mismatch	2002Sep5
PASS		
0	set_st_ring_mismatch2	2002Sep5
PASS		
0	set_st_ring_mismatch3	2002Sep5
PASS		
0	set_st_ring_mismatch4	2002Sep5
PASS		
0	set_st_ring_ps	2002Sep5
PASS		
0	set_st_ring_ps_only	2002Sep5
PASS		
0	set_st_ring_subblk0	2002Sep5
PASS		
0	set_st_ring_subblk1	2002Sep5
PASS		
0	set_st_ring_subblk1_only	2002Sep5
PASS		
0	set_st_ring_subblk2	2002Sep5
PASS		
0	set_st_ring_subblk2_only	2002Sep5
PASS		
0	set_st_ring_subblk3	2002Sep5
PASS		
0	set_st_ring_subblk3_only	2002Sep5
PASS		
0	set_st_ring_subblk4	2002Sep5
PASS		
0	set_st_ring_subblk4_only	2002Sep5
PASS		
0	set_st_ring_subblk5	2002Sep5
PASS		
0	set_st_ring_subblk5_only	2002Sep5
PASS		
0	set_st_ring_subblk6	2002Sep5
PASS		
0	set_st_ring_subblk6_only	2002Sep5
PASS		
0	set_st_ring_subblk7	2002Sep5
PASS		
0	set_st_ring_subblk7_only	2002Sep5
PASS		
0	set_st_ring_vs	2002Sep5

PASS		
0	set_st_ring_vs_only	2002Sep5
PASS		
0	set_st_stress_a	2002Sep5
PASS		
0	set_st_stress_b	2002Sep5
PASS		
0	set_st_stress_c	2002Sep5
PASS		
0	set_st_stress_d	2002Sep5
PASS		
0	set_st_stress_e	2002Sep5
PASS		
0	set_st_stress_f	2002Sep5
PASS		
0	Small_Text_BM	2002Sep5
PASS		
0	smoke_nrt	2002Sep5
PASS		
0	src_clip	2002Sep5
PASS		
0	srf_cohr	2002Sep5
PASS		
0	trans_bitblt	2002Sep5
PASS		
0	type0_2long_pckts	2002Sep5
PASS		
0	type0_3indr_buffs	2002Sep5
PASS		
0	type0_4pckts	2002Sep5
PASS		
0	type0_incr_updt_cnst	2002Sep5
PASS		
0	type0_incr_updt_instrmem	2002Sep5
PASS		
0	type0_incr_updt_st	2002Sep5
PASS		
0	type0_incr_updt_stress	2002Sep5
PASS		
0	type0_ind1_ind2_a	2002Sep5
PASS		
0	type0_ind1_ind2_b	2002Sep5
PASS		
0	type0_indr_buff	2002Sep5
PASS		
0	Type0_IQ2_BM	2002Sep5
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep5

PASS		
0	Type0_IQ_BM	2002Sep5
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep5
PASS		
0	Type0_PQ_BM	2002Sep5
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep5
PASS		
0	typel_4pckts	2002Sep5
PASS		
0	typel_indr_buff	2002Sep5
PASS		
0	typel_pckt	2002Sep5
PASS		
0	Type3_Bitblt	2002Sep5
PASS		
0	Type3_Load_Palette	2002Sep5
PASS		
0	Type3_NOP	2002Sep5
PASS		
0	type_random	2002Sep5
PASS		
704	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
705	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep5
PASS		
0	uram_wrt_ucode	2002Sep5
PASS		
708	viz_query_a	2002Sep5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
709	viz_query_draw_indx	2002Sep5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
710	viz_query_draw_indx_indr1	2002Sep5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
711	viz_query_draw_indx_indr2	2002Sep5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
712	viz_query_draw_indx_multipass	2002Sep5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
713	viz_query_draw_indx_ring	2002Sep5
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_indr1	2002Sep5
PASS		
0	viz_query_indr1_simple	2002Sep5
PASS		
0	viz_query_indr2	2002Sep5

```

PASS
0 viz_query_indr2_simple 2002Sep5
PASS
0 viz_query_ring 2002Sep5
PASS
0 viz_query_ring_simple 2002Sep5
PASS
0 wait_for_idle 2002Sep5
PASS
721 wait_reg_mem_poll_mem0 2002Sep5
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_mem0_rt 2002Sep5
PASS
723 wait_reg_mem_poll_mem1 2002Sep5
**FAIL** cp_mh_req.cmp mh_cp_rd.cmp
724 wait_reg_mem_poll_mem2 2002Sep5
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_mem3 2002Sep5
PASS
0 wait_reg_mem_poll_reg0 2002Sep5
PASS
0 wait_reg_mem_poll_reg1 2002Sep5
PASS
0 wait_reg_mem_poll_reg2 2002Sep5
PASS
0 wait_reg_mem_poll_reg3 2002Sep5
PASS
=====
Tests_Run_count = 729 Pass_count = 668 Fail_count = 61 Incomplete_count = 0
=====

```



```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Aug 29  2:31:20 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          627         627         610         97
              RBBM        92          92          92         100
+++++
              TOTAL        719         719         702         97
+++++

```

=====
Regression Report For CP - Project = r400 Created on Wed Aug 7 13:11:45 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug7
PASS			
0	2d_indr_buffer_b		2002Aug7
PASS			
0	2d_indr_buffer_c		2002Aug7
PASS			
0	2d_indr_buffer_d		2002Aug7
PASS			
0	2d_indr_buffer_e		2002Aug7
PASS			
0	2d_indr_buffer_f		2002Aug7
PASS			
0	2d_indr_buffer_g		2002Aug7
PASS			
0	3d_draw_indx_2		2002Aug7
PASS			
0	3d_draw_indx_2_b		2002Aug7
PASS			
0	cond_write_mem_poll_mem		2002Aug7
PASS			
0	cond_write_mem_poll_mem2		2002Aug7
PASS			
0	cond_write_mem_poll_mem3		2002Aug7
PASS			
0	cond_write_mem_poll_mem_rt		2002Aug7
PASS			
0	cond_write_mem_poll_reg		2002Aug7
PASS			
0	cond_write_mem_poll_reg2		2002Aug7
PASS			
0	cond_write_mem_poll_reg3		2002Aug7
PASS			
0	cp_acpi_recovery_soft_reset		2002Aug7
PASS			
0	cp_bios_scratch		2002Aug7
PASS			
0	cp_ib2_base000_bufszXXX		2002Aug7
PASS			
0	cp_ib2_base001_bufszXXX		2002Aug7

PASS		
0	cp_ib2_base010_bufszXXX	2002Aug7
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug7
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug7
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug7
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug7
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug7
PASS		
0	cp_ib_base000_bufszXXX	2002Aug7
PASS		
0	cp_ib_base001_bufszXXX	2002Aug7
PASS		
0	cp_ib_base010_bufszXXX	2002Aug7
PASS		
0	cp_ib_base011_bufszXXX	2002Aug7
PASS		
0	cp_ib_base100_bufszXXX	2002Aug7
PASS		
0	cp_ib_base101_bufszXXX	2002Aug7
PASS		
33	cp_ib_base110_bufszXXX	2002Aug7
FAIL	cp_mh_req.cmp	
34	cp_ib_base111_bufszXXX	2002Aug7
FAIL	cp_mh_req.cmp	
0	cp_interrupt	2002Aug7
PASS		
0	cp_interrupt_rt_events_16	2002Aug7
PASS		
0	cp_iq2_alignment_000	2002Aug7
PASS		
0	cp_iq2_alignment_001	2002Aug7
PASS		
0	cp_iq2_alignment_010	2002Aug7
PASS		
0	cp_iq2_alignment_011	2002Aug7
PASS		
0	cp_iq2_alignment_100	2002Aug7
PASS		
0	cp_iq2_alignment_101	2002Aug7
PASS		
0	cp_iq2_alignment_110	2002Aug7
PASS		
0	cp_iq2_alignment_111	2002Aug7

PASS		
0	cp_iq_alignment_000	2002Aug7
PASS		
0	cp_iq_alignment_001	2002Aug7
PASS		
0	cp_iq_alignment_010	2002Aug7
PASS		
0	cp_iq_alignment_011	2002Aug7
PASS		
0	cp_iq_alignment_100	2002Aug7
PASS		
0	cp_iq_alignment_101	2002Aug7
PASS		
0	cp_iq_alignment_110	2002Aug7
PASS		
0	cp_iq_alignment_111	2002Aug7
PASS		
0	cp_max_size_packet	2002Aug7
PASS		
0	cp_nrt_rt_protection_mode	2002Aug7
PASS		
0	cp_output_stress	2002Aug7
PASS		
0	cp_protection_mode	2002Aug7
PASS		
0	cp_rb_alignment_000	2002Aug7
PASS		
0	cp_rb_alignment_001	2002Aug7
PASS		
0	cp_rb_alignment_010	2002Aug7
PASS		
0	cp_rb_alignment_011	2002Aug7
PASS		
0	cp_rb_alignment_100	2002Aug7
PASS		
0	cp_rb_alignment_101	2002Aug7
PASS		
0	cp_rb_alignment_110	2002Aug7
PASS		
0	cp_rb_alignment_111	2002Aug7
PASS		
0	cp_register_reads	2002Aug7
PASS		
0	cp_reg_rw_test	2002Aug7
PASS		
0	cp_rt_protection_mode	2002Aug7
PASS		
0	cp_scratch	2002Aug7

PASS		
0	cp_scratch_multi	2002Aug7
PASS		
0	dma_cp_sync	2002Aug7
PASS		
0	dma_dscrstr_chckn_bit	2002Aug7
PASS		
0	dma_dscrstr_flush1	2002Aug7
PASS		
0	dma_dscrstr_flush2	2002Aug7
PASS		
0	dma_dscrstr_flush3	2002Aug7
PASS		
0	dma_m2m_src00_dstXX	2002Aug7
PASS		
0	dma_m2m_src01_dstXX	2002Aug7
PASS		
0	dma_m2m_src10_dstXX	2002Aug7
PASS		
0	dma_m2m_src11_dstXX	2002Aug7
PASS		
0	dma_mem2mem	2002Aug7
PASS		
0	dma_mem2mem_abort	2002Aug7
PASS		
0	dma_mem2mem_alignments	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug7
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug7
PASS		
0	dma_mem2mem_pio_abort	2002Aug7
PASS		
0	dma_mem2reg	2002Aug7
PASS		
0	dma_mem2reg_abort	2002Aug7

PASS		
0	dma_mem2reg_dtaq_stress	2002Aug7
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug7
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug7
PASS		
0	dma_mem2reg_sync	2002Aug7
PASS		
0	dma_r2r_m2m_r2r	2002Aug7
PASS		
0	dma_r2r_m2r_r2r	2002Aug7
PASS		
0	dma_r2r_r2m_r2r	2002Aug7
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug7
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug7
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug7
PASS		
0	dma_reg2mem	2002Aug7
PASS		
0	dma_reg2mem_abort	2002Aug7
PASS		
0	dma_reg2reg	2002Aug7
PASS		
0	dma_reg2reg_2dw	2002Aug7
PASS		
0	dma_reg2reg_daic	2002Aug7
PASS		
0	dma_reg2reg_mult	2002Aug7
PASS		
0	dma_reg2reg_mult2	2002Aug7
PASS		
0	dma_reg2reg_mult3	2002Aug7
PASS		
0	dma_reg2reg_saic	2002Aug7
PASS		
0	dma_reg2reg_sdaic	2002Aug7
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug7
PASS		
0	dma_reg2reg_stress	2002Aug7
PASS		
0	draw_indx_a	2002Aug7
PASS		
0	draw_indx_b	2002Aug7

PASS		
0	draw_indx_c	2002Aug7
PASS		
0	draw_indx_indr1_a	2002Aug7
PASS		
0	draw_indx_indr1_b	2002Aug7
PASS		
0	draw_indx_indr1_short_a	2002Aug7
PASS		
0	draw_indx_indr1_short_b	2002Aug7
PASS		
0	draw_indx_indr1_stress	2002Aug7
PASS		
0	draw_indx_indr2_a	2002Aug7
PASS		
0	draw_indx_indr2_b	2002Aug7
PASS		
0	draw_indx_indr2_short_a	2002Aug7
PASS		
0	draw_indx_indr2_short_b	2002Aug7
PASS		
0	draw_indx_indr2_stress	2002Aug7
PASS		
0	draw_indx_ring1	2002Aug7
PASS		
0	draw_indx_ring2	2002Aug7
PASS		
0	draw_indx_ring_short1	2002Aug7
PASS		
0	draw_indx_ring_short2	2002Aug7
PASS		
0	draw_indx_ring_stress	2002Aug7
PASS		
0	draw_indx_short_a	2002Aug7
PASS		
0	draw_indx_short_b	2002Aug7
PASS		
0	draw_indx_short_c	2002Aug7
PASS		
0	draw_indx_stress_a	2002Aug7
PASS		
0	draw_indx_stress_b	2002Aug7
PASS		
0	draw_indx_stress_c	2002Aug7
PASS		
0	event_timestamp_write	2002Aug7
PASS		
0	ft_indr1_july22	2002Aug7

PASS		
0	ft_indr2_july22	2002Aug7
PASS		
0	ft_july22	2002Aug7
PASS		
0	header_dump	2002Aug7
PASS		
0	header_dump_rt	2002Aug7
PASS		
0	header_dump_small	2002Aug7
PASS		
0	im_load	2002Aug7
PASS		
0	im_load_immediate	2002Aug7
PASS		
0	im_load_immediate_nrt_me	2002Aug7
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug7
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug7
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug7
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug7
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug7
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug7
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug7
PASS		
0	im_load_nrt_me	2002Aug7
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug7
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug7
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug7
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug7
PASS		
0	incremental_update_const	2002Aug7
PASS		
0	incremental_update_instr	2002Aug7
PASS		
0	incremental_update_state	2002Aug7
PASS		
0	indirect_buffer_a	2002Aug7

PASS		
165	indirect_buffer_b	2002Aug7
FAIL	cp_mh_req.cmp	
166	indirect_buffer_c	2002Aug7
FAIL	cp_mh_req.cmp	
167	indirect_buffer_d	2002Aug7
FAIL	cp_mh_req.cmp	
0	indirect_buffer_e	2002Aug7
PASS		
0	indirect_buffer_f	2002Aug7
PASS		
0	indirect_buffer_g	2002Aug7
PASS		
0	indirect_buffer_h	2002Aug7
PASS		
0	indirect_buffer_i	2002Aug7
PASS		
0	indirect_buffer_j	2002Aug7
PASS		
0	indirect_buffer_k	2002Aug7
PASS		
0	indirect_buffer_l	2002Aug7
PASS		
176	indirect_buffer_mp_a	2002Aug7
FAIL	cp_rbbm.cmp	
177	indirect_buffer_mp_b	2002Aug7
FAIL	rbbmWr.cmp	
178	indirect_buffer_mp_c	2002Aug7
FAIL	cp_rbbm.cmp	
179	indirect_buffer_mp_d	2002Aug7
FAIL	cp_rbbm.cmp	
180	indirect_buffer_mp_e	2002Aug7
FAIL	cp_rbbm.cmp	
181	indirect_buffer_mp_f	2002Aug7
FAIL	cp_rbbm.cmp	
0	invalidate_on_me_init	2002Aug7
PASS		
0	invalidate_state_constants	2002Aug7
PASS		
0	invalidate_state_shaders	2002Aug7
PASS		
0	invalidate_state_shaders2	2002Aug7
PASS		
0	invalidate_state_sublks	2002Aug7
PASS		
0	invalidate_state_sublks2	2002Aug7
PASS		
0	invalid_opcode	2002Aug7

PASS		
0	iq1_iq2_mult	2002Aug7
PASS		
190	iq1_iq2_push_wrap	2002Aug7
INCOMPLETE		
0	iq2_csq_full	2002Aug7
PASS		
0	IQ2_Mult	2002Aug7
PASS		
0	IQ2_Wrap_Legacy	2002Aug7
PASS		
0	IQ_Wrap	2002Aug7
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug7
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug7
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug7
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug7
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug7
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug7
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug7
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug7
PASS		
0	ld_cnst_cntx_match	2002Aug7
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug7
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug7
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug7
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug7
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug7
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug7
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug7
PASS		
0	ld_cnst_cntx_replicate8	2002Aug7
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug7

PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug7
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug7
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug7
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug7
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug7
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug7
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug7
PASS		
0	load_constant_context_rt	2002Aug7
PASS		
0	mem_write	2002Aug7
PASS		
0	mem_write_rt	2002Aug7
PASS		
223	mpeg_index	2002Aug7
FAIL	rbbmWr.cmp	
0	mpeg_index_multi_context_max_context_0	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug7
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug7
PASS		
0	nop_eating_disabled	2002Aug7
PASS		
0	RB_BUFSZ_0	2002Aug7
PASS		
0	RB_BUFSZ_1	2002Aug7
PASS		
0	RB_Wrap	2002Aug7
PASS		
0	reg_to_mem	2002Aug7

PASS		
0	reg_to_mem_rt	2002Aug7
PASS		
0	rtee_discretes_falling_00_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug7

PASS		
0	rtee_discretes_falling_23_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug7
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_11_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug7

PASS		
0	rtee_discretes_invert_15_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug7
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug7
PASS		
0	rtee_discretes_level_00_events_16	2002Aug7
PASS		
0	rtee_discretes_level_01_events_16	2002Aug7
PASS		
0	rtee_discretes_level_02_events_16	2002Aug7
PASS		
0	rtee_discretes_level_03_events_16	2002Aug7
PASS		
0	rtee_discretes_level_04_events_16	2002Aug7
PASS		
0	rtee_discretes_level_05_events_16	2002Aug7
PASS		
0	rtee_discretes_level_06_events_16	2002Aug7

PASS		
0	rtee_discretes_level_07_events_16	2002Aug7
PASS		
0	rtee_discretes_level_08_events_16	2002Aug7
PASS		
0	rtee_discretes_level_09_events_16	2002Aug7
PASS		
0	rtee_discretes_level_10_events_16	2002Aug7
PASS		
0	rtee_discretes_level_11_events_16	2002Aug7
PASS		
0	rtee_discretes_level_12_events_16	2002Aug7
PASS		
0	rtee_discretes_level_13_events_16	2002Aug7
PASS		
0	rtee_discretes_level_14_events_16	2002Aug7
PASS		
0	rtee_discretes_level_15_events_16	2002Aug7
PASS		
0	rtee_discretes_level_16_events_16	2002Aug7
PASS		
0	rtee_discretes_level_17_events_16	2002Aug7
PASS		
0	rtee_discretes_level_18_events_16	2002Aug7
PASS		
0	rtee_discretes_level_19_events_16	2002Aug7
PASS		
0	rtee_discretes_level_20_events_16	2002Aug7
PASS		
0	rtee_discretes_level_21_events_16	2002Aug7
PASS		
0	rtee_discretes_level_22_events_16	2002Aug7
PASS		
0	rtee_discretes_level_23_events_16	2002Aug7
PASS		
0	rtee_discretes_level_24_events_16	2002Aug7
PASS		
0	rtee_discretes_level_25_events_16	2002Aug7
PASS		
0	rtee_discretes_level_26_events_16	2002Aug7
PASS		
0	rtee_discretes_level_27_events_16	2002Aug7
PASS		
0	rtee_discretes_level_28_events_16	2002Aug7
PASS		
0	rtee_discretes_level_29_events_16	2002Aug7
PASS		
0	rtee_discretes_level_30_events_16	2002Aug7

PASS		
0	rtee_discretes_level_31_events_16	2002Aug7
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug7

PASS		
0	rtee_discretes_rising_22_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug7
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug7
PASS		
0	rtee_display1_comp_events_16	2002Aug7
PASS		
0	rtee_display2_comp_events_16	2002Aug7
PASS		
0	rtee_mem_linear_search_events_2	2002Aug7
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug7
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug7
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug7

PASS		
0	rtee_prefetch_parse_nops	2002Aug7
PASS		
0	rtee_reg_linear_search_events_2	2002Aug7
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug7
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug7
PASS		
0	rtee_vip_comp_events_16	2002Aug7
PASS		
0	rt_invalid_opcode	2002Aug7
PASS		
0	scratch_mem_read	2002Aug7
PASS		
0	scratch_mem_read2	2002Aug7
PASS		
0	set_constant	2002Aug7
PASS		
0	set_constant2	2002Aug7
PASS		
0	set_constant_indr1_short	2002Aug7
PASS		
0	set_constant_indr1_short2	2002Aug7
PASS		
0	set_constant_indr2_short	2002Aug7
PASS		
0	set_constant_indr2_short2	2002Aug7
PASS		
0	set_constant_ring_short	2002Aug7
PASS		
0	set_constant_ring_short2	2002Aug7
PASS		
0	set_constant_short	2002Aug7

```

PASS
405 set_state_rt_a 2002Aug7
**FAIL** cp_rbbm.cmp rbbmWr.cmp
406 set_state_rt_b 2002Aug7
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
407 set_state_rt_c 2002Aug7
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
408 set_state_rt_d 2002Aug7
**FAIL** cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 set_st_indrl_match 2002Aug7
PASS
0 set_st_indrl_mismatch 2002Aug7
PASS
0 set_st_indrl_mismatch2 2002Aug7
PASS
0 set_st_indrl_mismatch3 2002Aug7
PASS
0 set_st_indrl_mismatch4 2002Aug7
PASS
0 set_st_indrl_ps 2002Aug7
PASS
0 set_st_indrl_ps_only 2002Aug7
PASS
416 set_st_indrl_sublk0 2002Aug7
**FAIL** rbbmWr.cmp
0 set_st_indrl_sublk1 2002Aug7
PASS
0 set_st_indrl_sublk1_only 2002Aug7
PASS
0 set_st_indrl_sublk2 2002Aug7
PASS
0 set_st_indrl_sublk2_only 2002Aug7
PASS
0 set_st_indrl_sublk3 2002Aug7
PASS
0 set_st_indrl_sublk3_only 2002Aug7
PASS
0 set_st_indrl_sublk4 2002Aug7
PASS
0 set_st_indrl_sublk4_only 2002Aug7
PASS
0 set_st_indrl_sublk5 2002Aug7
PASS
0 set_st_indrl_sublk5_only 2002Aug7
PASS
0 set_st_indrl_sublk6 2002Aug7
PASS
0 set_st_indrl_sublk6_only 2002Aug7

```

PASS		
0	set_st_indr1_sublk7	2002Aug7
PASS		
0	set_st_indr1_sublk7_only	2002Aug7
PASS		
0	set_st_indr1_vs	2002Aug7
PASS		
0	set_st_indr1_vs_only	2002Aug7
PASS		
0	set_st_indr2_match	2002Aug7
PASS		
0	set_st_indr2_mismatch	2002Aug7
PASS		
0	set_st_indr2_mismatch2	2002Aug7
PASS		
436	set_st_indr2_mismatch3	2002Aug7
FAIL	rbbmWr.cmp	
437	set_st_indr2_mismatch4	2002Aug7
FAIL	rbbmWr.cmp	
0	set_st_indr2_ps	2002Aug7
PASS		
0	set_st_indr2_ps_only	2002Aug7
PASS		
440	set_st_indr2_sublk0	2002Aug7
FAIL	rbbmWr.cmp	
0	set_st_indr2_sublk1	2002Aug7
PASS		
0	set_st_indr2_sublk1_only	2002Aug7
PASS		
0	set_st_indr2_sublk2	2002Aug7
PASS		
0	set_st_indr2_sublk2_only	2002Aug7
PASS		
0	set_st_indr2_sublk3	2002Aug7
PASS		
0	set_st_indr2_sublk3_only	2002Aug7
PASS		
0	set_st_indr2_sublk4	2002Aug7
PASS		
0	set_st_indr2_sublk4_only	2002Aug7
PASS		
0	set_st_indr2_sublk5	2002Aug7
PASS		
0	set_st_indr2_sublk5_only	2002Aug7
PASS		
0	set_st_indr2_sublk6	2002Aug7
PASS		
0	set_st_indr2_sublk6_only	2002Aug7

PASS		
0	set_st_indr2_sublk7	2002Aug7
PASS		
0	set_st_indr2_sublk7_only	2002Aug7
PASS		
0	set_st_indr2_vs	2002Aug7
PASS		
0	set_st_indr2_vs_only	2002Aug7
PASS		
0	set_st_pfmatch_disabled	2002Aug7
PASS		
0	set_st_ring_match	2002Aug7
PASS		
0	set_st_ring_mismatch	2002Aug7
PASS		
0	set_st_ring_mismatch2	2002Aug7
PASS		
0	set_st_ring_mismatch3	2002Aug7
PASS		
0	set_st_ring_mismatch4	2002Aug7
PASS		
0	set_st_ring_ps	2002Aug7
PASS		
0	set_st_ring_ps_only	2002Aug7
PASS		
465	set_st_ring_sublk0	2002Aug7
FAIL	rbbmWr.cmp	
0	set_st_ring_sublk1	2002Aug7
PASS		
0	set_st_ring_sublk1_only	2002Aug7
PASS		
0	set_st_ring_sublk2	2002Aug7
PASS		
0	set_st_ring_sublk2_only	2002Aug7
PASS		
0	set_st_ring_sublk3	2002Aug7
PASS		
0	set_st_ring_sublk3_only	2002Aug7
PASS		
0	set_st_ring_sublk4	2002Aug7
PASS		
0	set_st_ring_sublk4_only	2002Aug7
PASS		
0	set_st_ring_sublk5	2002Aug7
PASS		
0	set_st_ring_sublk5_only	2002Aug7
PASS		
0	set_st_ring_sublk6	2002Aug7

PASS		
0	set_st_ring_sublk6_only	2002Aug7
PASS		
0	set_st_ring_sublk7	2002Aug7
PASS		
0	set_st_ring_sublk7_only	2002Aug7
PASS		
0	set_st_ring_vs	2002Aug7
PASS		
0	set_st_ring_vs_only	2002Aug7
PASS		
0	set_st_stress_a	2002Aug7
PASS		
0	set_st_stress_b	2002Aug7
PASS		
0	set_st_stress_c	2002Aug7
PASS		
0	set_st_stress_d	2002Aug7
PASS		
486	set_st_stress_e	2002Aug7
FAIL	rbbmWr.cmp	
0	set_st_stress_f	2002Aug7
PASS		
0	smoke_nrt	2002Aug7
PASS		
489	type0_2long_pckts	2002Aug7
FAIL	cp_mh_req.cmp	
490	type0_3indr_buffs	2002Aug7
FAIL	cp_mh_req.cmp	
0	type0_4pckts	2002Aug7
PASS		
0	type0_incr_updt_cnst	2002Aug7
PASS		
0	type0_incr_updt_instrmem	2002Aug7
PASS		
0	type0_incr_updt_st	2002Aug7
PASS		
0	type0_incr_updt_stress	2002Aug7
PASS		
496	type0_ind1_ind2_a	2002Aug7
FAIL	cp_mh_req.cmp	
497	type0_ind1_ind2_b	2002Aug7
FAIL	cp_mh_req.cmp	
0	type0_indr_buff	2002Aug7
PASS		
0	Type0_IQ2_BM	2002Aug7
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug7

```

PASS
0   Type0_IQ_BM                               2002Aug7
PASS
0   Type0_IQ_BM_IB_Packet                     2002Aug7
PASS
0   Type0_PQ_BM                               2002Aug7
PASS
0   Type0_PQ_BM_Pre_Write_Timer               2002Aug7
PASS
0   typel_4pckts                              2002Aug7
PASS
0   typel_indr_buff                           2002Aug7
PASS
0   typel_pckt                                2002Aug7
PASS
0   Type3_NOP                                 2002Aug7
PASS
509 uram_rd_wrt                               2002Aug7
INCOMPLETE
510 uram_rd_wrt_rt                            2002Aug7
INCOMPLETE
511 uram_wrt_rt_ucode                          2002Aug7
**FAIL**      rbbmWr.cmp
512 uram_wrt_ucode                            2002Aug7
**FAIL**      rbbmWr.cmp
0   wait_for_idle                             2002Aug7
PASS
514 wait_reg_mem_poll_mem0                    2002Aug7
**FAIL**      cp_rbbm.cmp mh_cp_rd.cmp
515 wait_reg_mem_poll_mem0_rt                 2002Aug7
**FAIL**      mh_cp_rd.cmp
516 wait_reg_mem_poll_mem1                    2002Aug7
**FAIL**      cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
517 wait_reg_mem_poll_mem2                    2002Aug7
**FAIL**      cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
518 wait_reg_mem_poll_mem3                    2002Aug7
**FAIL**      mh_cp_rd.cmp
0   wait_reg_mem_poll_reg0                    2002Aug7
PASS
0   wait_reg_mem_poll_reg1                    2002Aug7
PASS
0   wait_reg_mem_poll_reg2                    2002Aug7
PASS
0   wait_reg_mem_poll_reg3                    2002Aug7
PASS

```

```

=====
Tests_Run_count = 522  Pass_count = 486  Fail_count = 33  Incomplete_count = 3
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buf_d_sm_meq		2002Oct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
2	2d_indr_buf_g_sm_meq		2002Oct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	2d_indr_buffer_a		2002Oct9
	PASS		
0	2d_indr_buffer_b		2002Oct9
	PASS		
0	2d_indr_buffer_c		2002Oct9
	PASS		
0	2d_indr_buffer_d		2002Oct9
	PASS		
0	2d_indr_buffer_e		2002Oct9
	PASS		
0	2d_indr_buffer_f		2002Oct9
	PASS		
0	2d_indr_buffer_f_sm_roq		2002Oct9
	PASS		
0	2d_indr_buffer_f_sm_roq2		2002Oct9
	PASS		
0	2d_indr_buffer_g		2002Oct9
	PASS		
12	2d_indr_buffer_random		2002Oct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
13	2d_indr_buffer_random2		2002Oct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	3d_draw_indx_2		2002Oct9
	PASS		
0	3d_draw_indx_2_b		2002Oct9
	PASS		
0	IQ2_Mult		2002Oct9
	PASS		
0	IQ2_Wrap_Legacy		2002Oct9
	PASS		
0	IQ_Wrap		2002Oct9
	PASS		
19	Isync_Control		2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp		
20	Isync_Control_2D_3D		2002Oct9


```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
21  Isync_Control_3D_2D                                2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   RB_BUFSZ_0                                          2002Oct9
PASS
0   RB_BUFSZ_1                                          2002Oct9
PASS
0   RB_Wrap                                             2002Oct9
PASS
25  Small_Text_BM                                       2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type0_IQ2_BM                                        2002Oct9
PASS
0   Type0_IQ2_BM_IB_Packet                             2002Oct9
PASS
0   Type0_IQ_BM                                         2002Oct9
PASS
0   Type0_IQ_BM_IB_Packet                             2002Oct9
PASS
0   Type0_PQ_BM                                         2002Oct9
PASS
0   Type0_PQ_BM_Pre_Write_Timer                       2002Oct9
PASS
0   Type3_Bitblt                                       2002Oct9
PASS
33  Type3_Hostdata_blt                                  2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
34  Type3_Hostdata_blt_PIO                             2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type3_Load_Palette                                 2002Oct9
PASS
0   Type3_NOP                                           2002Oct9
PASS
37  Type3_Polyline                                      2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
38  Type3_Polyline_Large                               2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
39  bitblt_multi                                       2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
40  brush_32x1mono                                      2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
41  brush_8x8color_16                                  2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
42  brush_8x8color_32                                  2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
43  brush_8x8color_64                                  2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
44  brush_8x8color_dst_0_1                             2002Oct9

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
45  brush_8x8mono                                20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
46  brush_color_mono_stress                      20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
47  brush_flag                                    20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
48  brush_type0                                   20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
49  brush_type1                                   20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
50  brush_type10                                  20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
51  brush_type13                                  20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
52  brush_type14                                  20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
53  brush_type6                                   20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
54  brush_type7                                   20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
55  brush_type_other                              20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
56  brush_types_13_14                            20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
57  change_queue_sizes                           20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   clear_const_wr_en_on_me_init                 20020ct9
PASS
0   clear_const_wr_en_with_lcc                   20020ct9
PASS
0   cond_write_mem_poll_mem                      20020ct9
PASS
0   cond_write_mem_poll_mem2                     20020ct9
PASS
0   cond_write_mem_poll_mem3                     20020ct9
PASS
0   cond_write_mem_poll_mem_rt                   20020ct9
PASS
0   cond_write_mem_poll_reg                      20020ct9
PASS
0   cond_write_mem_poll_reg2                     20020ct9
PASS
0   cond_write_mem_poll_reg3                     20020ct9
PASS
0   cp_acpi_recovery_soft_reset                  20020ct9
PASS
0   cp_bios_scratch                              20020ct9

```

PASS		
69	cp_high_stress	20020ct9
FAIL	mh_cp_rd.cmp	
0	cp_ib2_base000_bufszXXX	20020ct9
PASS		
0	cp_ib2_base001_bufszXXX	20020ct9
PASS		
0	cp_ib2_base010_bufszXXX	20020ct9
PASS		
0	cp_ib2_base010_bufszXXX_sm_roq	20020ct9
PASS		
0	cp_ib2_base011_bufszXXX	20020ct9
PASS		
0	cp_ib2_base100_bufszXXX	20020ct9
PASS		
0	cp_ib2_base101_bufszXXX	20020ct9
PASS		
0	cp_ib2_base110_bufszXXX	20020ct9
PASS		
0	cp_ib2_base111_bufszXXX	20020ct9
PASS		
0	cp_ib_base000_bufszXXX	20020ct9
PASS		
0	cp_ib_base001_bufszXXX	20020ct9
PASS		
0	cp_ib_base010_bufszXXX	20020ct9
PASS		
0	cp_ib_base011_bufszXXX	20020ct9
PASS		
0	cp_ib_base100_bufszXXX	20020ct9
PASS		
0	cp_ib_base101_bufszXXX	20020ct9
PASS		
0	cp_ib_base101_bufszXXX_sm_roq	20020ct9
PASS		
0	cp_ib_base110_bufszXXX	20020ct9
PASS		
0	cp_ib_base111_bufszXXX	20020ct9
PASS		
0	cp_interrupt	20020ct9
PASS		
0	cp_interrupt_rt_events_16	20020ct9
PASS		
0	cp_iq2_alignment_000	20020ct9
PASS		
0	cp_iq2_alignment_001	20020ct9
PASS		
0	cp_iq2_alignment_010	20020ct9

PASS		
0	cp_iq2_alignment_011	20020ct9
PASS		
0	cp_iq2_alignment_100	20020ct9
PASS		
0	cp_iq2_alignment_101	20020ct9
PASS		
0	cp_iq2_alignment_110	20020ct9
PASS		
0	cp_iq2_alignment_111	20020ct9
PASS		
0	cp_iq_alignment_000	20020ct9
PASS		
0	cp_iq_alignment_001	20020ct9
PASS		
0	cp_iq_alignment_010	20020ct9
PASS		
0	cp_iq_alignment_011	20020ct9
PASS		
0	cp_iq_alignment_100	20020ct9
PASS		
0	cp_iq_alignment_101	20020ct9
PASS		
0	cp_iq_alignment_110	20020ct9
PASS		
0	cp_iq_alignment_111	20020ct9
PASS		
0	cp_max_size_packet	20020ct9
PASS		
107	cp_max_size_packet_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	20020ct9
PASS		
0	cp_output_stress	20020ct9
PASS		
110	cp_output_stress_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_protection_mode	20020ct9
PASS		
0	cp_rb_alignment_000	20020ct9
PASS		
0	cp_rb_alignment_001	20020ct9
PASS		
0	cp_rb_alignment_010	20020ct9
PASS		
0	cp_rb_alignment_011	20020ct9
PASS		
0	cp_rb_alignment_100	20020ct9

PASS		
0	cp_rb_alignment_101	20020ct9
PASS		
0	cp_rb_alignment_110	20020ct9
PASS		
0	cp_rb_alignment_111	20020ct9
PASS		
0	cp_rb_wptrXXX_base000	20020ct9
PASS		
0	cp_rb_wptrXXX_base001	20020ct9
PASS		
0	cp_rb_wptrXXX_base010	20020ct9
PASS		
0	cp_rb_wptrXXX_base011	20020ct9
PASS		
0	cp_rb_wptrXXX_base100	20020ct9
PASS		
0	cp_rb_wptrXXX_base101	20020ct9
PASS		
0	cp_rb_wptrXXX_base101_sm_roq	20020ct9
PASS		
0	cp_rb_wptrXXX_base110	20020ct9
PASS		
0	cp_rb_wptrXXX_base111	20020ct9
PASS		
0	cp_reg_rw_test	20020ct9
PASS		
0	cp_register_reads	20020ct9
PASS		
0	cp_rt_protection_mode	20020ct9
PASS		
0	cp_scratch	20020ct9
PASS		
0	cp_scratch_multi	20020ct9
PASS		
0	csf_reg_rd_default	20020ct9
PASS		
135	csf_reg_rd_only	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	csf_reg_wr_rd	20020ct9
PASS		
0	dma_cp_sync	20020ct9
PASS		
0	dma_dscrstr_chckn_bit	20020ct9
PASS		
0	dma_dscrstr_flush1	20020ct9
PASS		
0	dma_dscrstr_flush2	20020ct9

PASS		
0	dma_dscrstr_flush3	20020ct9
PASS		
0	dma_m2m_src00_dstXX	20020ct9
PASS		
0	dma_m2m_src01_dstXX	20020ct9
PASS		
0	dma_m2m_src10_dstXX	20020ct9
PASS		
0	dma_m2m_src11_dstXX	20020ct9
PASS		
0	dma_mem2mem	20020ct9
PASS		
0	dma_mem2mem_abort	20020ct9
PASS		
0	dma_mem2mem_alignments	20020ct9
PASS		
0	dma_mem2mem_byte_alignment0	20020ct9
PASS		
0	dma_mem2mem_byte_alignment1	20020ct9
PASS		
0	dma_mem2mem_byte_alignment2	20020ct9
PASS		
0	dma_mem2mem_byte_alignment3	20020ct9
PASS		
0	dma_mem2mem_byte_alignment4	20020ct9
PASS		
0	dma_mem2mem_byte_alignment5	20020ct9
PASS		
0	dma_mem2mem_byte_alignment6	20020ct9
PASS		
0	dma_mem2mem_byte_alignment7	20020ct9
PASS		
0	dma_mem2mem_large	20020ct9
PASS		
158	dma_mem2mem_pio_abort	20020ct9
FAIL		
159	dma_mem2mem_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	dma_mem2reg	20020ct9
PASS		
161	dma_mem2reg_abort	20020ct9
FAIL	rbbmWr.cmp	
0	dma_mem2reg_dtaq_stress	20020ct9
PASS		
163	dma_mem2reg_dtaq_stress_random	20020ct9
FAIL	mh_cp_rd.cmp	
0	dma_mem2reg_pio_stressbm	20020ct9

PASS		
0	dma_mem2reg_pio_stresspio	2002Oct9
PASS		
0	dma_mem2reg_sync	2002Oct9
PASS		
0	dma_r2r_m2m_r2r	2002Oct9
PASS		
0	dma_r2r_m2r_r2r	2002Oct9
PASS		
0	dma_r2r_r2m_r2r	2002Oct9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Oct9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Oct9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Oct9
PASS		
0	dma_reg2mem	2002Oct9
PASS		
0	dma_reg2mem_abort	2002Oct9
PASS		
0	dma_reg2reg	2002Oct9
PASS		
0	dma_reg2reg_2dw	2002Oct9
PASS		
0	dma_reg2reg_daic	2002Oct9
PASS		
0	dma_reg2reg_mult	2002Oct9
PASS		
0	dma_reg2reg_mult2	2002Oct9
PASS		
0	dma_reg2reg_mult3	2002Oct9
PASS		
0	dma_reg2reg_saic	2002Oct9
PASS		
0	dma_reg2reg_sdaic	2002Oct9
PASS		
0	dma_reg2reg_sdaic_stress	2002Oct9
PASS		
0	dma_reg2reg_stress	2002Oct9
PASS		
0	draw_indx_a	2002Oct9
PASS		
0	draw_indx_b	2002Oct9
PASS		
0	draw_indx_c	2002Oct9
PASS		
0	draw_indx_indr1_a	2002Oct9

PASS		
0	draw_indx_indr1_b	20020ct9
PASS		
0	draw_indx_indr1_short_a	20020ct9
PASS		
0	draw_indx_indr1_short_b	20020ct9
PASS		
0	draw_indx_indr1_stress	20020ct9
PASS		
0	draw_indx_indr2_a	20020ct9
PASS		
0	draw_indx_indr2_b	20020ct9
PASS		
0	draw_indx_indr2_short_a	20020ct9
PASS		
0	draw_indx_indr2_short_b	20020ct9
PASS		
0	draw_indx_indr2_stress	20020ct9
PASS		
198	draw_indx_random_a	20020ct9
FAIL	mh_cp_rd.cmp	
199	draw_indx_random_b	20020ct9
FAIL	mh_cp_rd.cmp	
200	draw_indx_random_c	20020ct9
FAIL	mh_cp_rd.cmp	
0	draw_indx_ring1	20020ct9
PASS		
0	draw_indx_ring2	20020ct9
PASS		
0	draw_indx_ring_short1	20020ct9
PASS		
0	draw_indx_ring_short2	20020ct9
PASS		
0	draw_indx_ring_stress	20020ct9
PASS		
0	draw_indx_short_a	20020ct9
PASS		
0	draw_indx_short_b	20020ct9
PASS		
0	draw_indx_short_c	20020ct9
PASS		
0	draw_indx_stress_a	20020ct9
PASS		
0	draw_indx_stress_b	20020ct9
PASS		
211	draw_indx_stress_b_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
0	draw_indx_stress_b_sm_roq	20020ct9

PASS		
0	draw_indx_stress_b_sm_roq2	2002Oct9
PASS		
0	draw_indx_stress_c	2002Oct9
PASS		
215	draw_indx_stress_c_sm_meq	2002Oct9
FAIL	mh_cp_rd.cmp	
0	event_timestamp_write	2002Oct9
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Oct9
PASS		
0	fix2flt_reg	2002Oct9
PASS		
0	ft_indr1_july22	2002Oct9
PASS		
0	ft_indr2_july22	2002Oct9
PASS		
0	ft_july22	2002Oct9
PASS		
0	ft_july22_random	2002Oct9
PASS		
0	header_dump	2002Oct9
PASS		
0	header_dump_rt	2002Oct9
PASS		
0	header_dump_small	2002Oct9
PASS		
226	hostdata_blt	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
227	im_ld_cid_mismatch_sm_meq	2002Oct9
FAIL	mh_cp_rd.cmp	
228	im_ld_invalidate_sm_meq	2002Oct9
FAIL	mh_cp_rd.cmp	
229	im_ld_match2_sm_meq	2002Oct9
FAIL	mh_cp_rd.cmp	
230	im_ld_match_sm_meq	2002Oct9
FAIL	mh_cp_rd.cmp	
231	im_ld_sz_mismatch_sm_meq	2002Oct9
FAIL	mh_cp_rd.cmp	
0	im_load_a	2002Oct9
PASS		
0	im_load_b	2002Oct9
PASS		
0	im_load_cid_mismatch	2002Oct9
PASS		
235	im_load_immediate	2002Oct9
FAIL	rbbmWr.cmp	
236	im_load_immediate_nrt_me	2002Oct9

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
237 im_load_immediate_nrt_me_ps_nowrap          2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
238 im_load_immediate_nrt_me_ps_nowrap_large_count 2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
239 im_load_immediate_nrt_me_ps_wrap           2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
240 im_load_immediate_nrt_me_vs_nowrap         2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
241 im_load_immediate_nrt_me_vs_nowrap_large_count 2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
242 im_load_immediate_nrt_me_vs_ps_nowrap      2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
243 im_load_immediate_nrt_me_vs_wrap           2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0 im_load_indr1_a                              2002Oct9
PASS
0 im_load_indr1_b                              2002Oct9
PASS
0 im_load_indr2_a                              2002Oct9
PASS
0 im_load_indr2_b                              2002Oct9
PASS
0 im_load_invalidate                           2002Oct9
PASS
0 im_load_invalidate_sm_roq                    2002Oct9
PASS
0 im_load_match                                2002Oct9
PASS
0 im_load_match2                               2002Oct9
PASS
252 im_load_nrt_me                             2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
253 im_load_nrt_me_ps_nowrap                    2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
254 im_load_nrt_me_ps_wrap                      2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
255 im_load_nrt_me_vs_nowrap                    2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
256 im_load_nrt_me_vs_wrap                      2002Oct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0 im_load_pfmacth_disable                       2002Oct9
PASS
258 im_load_random_a                           2002Oct9
**FAIL**          mh_cp_rd.cmp
259 im_load_random_b                           2002Oct9
**FAIL**          mh_cp_rd.cmp
260 im_load_random_c                           2002Oct9

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**FAIL**          mh_cp_rd.cmp
0  im_load_ring_a          20020ct9
PASS
0  im_load_ring_b          20020ct9
PASS
0  im_load_rt              20020ct9
PASS
0  im_load_rt2             20020ct9
PASS
0  im_load_rt3             20020ct9
PASS
0  im_load_rt4             20020ct9
PASS
0  im_load_sz_mismatch     20020ct9
PASS
268 incr_update_cnst_random 20020ct9
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
269 incr_update_const_sm_meq 20020ct9
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
270 incr_update_instr_sm_meq 20020ct9
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
271 incr_update_state_sm_meq 20020ct9
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_const 20020ct9
PASS
0  incremental_update_instr 20020ct9
PASS
0  incremental_update_state 20020ct9
PASS
0  incremental_update_state_sm_roq 20020ct9
PASS
0  indirect_buffer_a        20020ct9
PASS
0  indirect_buffer_b        20020ct9
PASS
0  indirect_buffer_c        20020ct9
PASS
0  indirect_buffer_d        20020ct9
PASS
0  indirect_buffer_e        20020ct9
PASS
0  indirect_buffer_f        20020ct9
PASS
0  indirect_buffer_f_sm_roq 20020ct9
PASS
0  indirect_buffer_g        20020ct9
PASS
0  indirect_buffer_h        20020ct9

```

PASS		
0	indirect_buffer_i	20020ct9
PASS		
0	indirect_buffer_j	20020ct9
PASS		
0	indirect_buffer_k	20020ct9
PASS		
0	indirect_buffer_l	20020ct9
PASS		
0	indirect_buffer_mp_a	20020ct9
PASS		
0	indirect_buffer_mp_b	20020ct9
PASS		
0	indirect_buffer_mp_c	20020ct9
PASS		
0	indirect_buffer_mp_d	20020ct9
PASS		
0	indirect_buffer_mp_e	20020ct9
PASS		
0	indirect_buffer_mp_f	20020ct9
PASS		
0	indirect_buffer_mp_f_sm_roq	20020ct9
PASS		
296	indirect_buffer_mp_random1	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
297	indirect_buffer_mp_random2	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
298	indirect_buffer_random1	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
299	indirect_buffer_random2	20020ct9
FAIL	mh_cp_rd.cmp	
300	indr_buf_e_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
301	indr_buf_k_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
302	indr_buf_mp_c_sm_meq	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
303	indr_buf_mp_f_sm_meq	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalid_opcode	20020ct9
PASS		
0	invalidate_on_me_init	20020ct9
PASS		
306	invalidate_on_me_init_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
307	invalidate_on_me_init_sm_meq	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	invalidate_state_constants	20020ct9

```

PASS
0   invalidate_state_shaders                2002Oct9
PASS
0   invalidate_state_shaders2              2002Oct9
PASS
311 invalidate_state_shaders_random        2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
312 invalidate_state_shaders_sm_meq        2002Oct9
**FAIL**      mh_cp_rd.cmp
0   invalidate_state_sublks                2002Oct9
PASS
0   invalidate_state_sublks2              2002Oct9
PASS
315 invalidate_state_sublks2_sm_meq        2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
316 invalidate_state_sublks_random        2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   invalidate_state_sublks_sm_roq        2002Oct9
PASS
0   iq1_iq2_mult                          2002Oct9
PASS
0   iq1_iq2_push_wrap                    2002Oct9
PASS
0   iq2_csq_full                         2002Oct9
PASS
321 lcc_coherency_cntrl_sm_meq            2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
322 lcc_coherency_cntrl2_sm_meq          2002Oct9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
323 lcc_coherency_cntrl_random           2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
324 lcc_match_random                     2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
325 lcc_match_sm_meq                     2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
326 lcc_match_valid_preserved_sm_meq     2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
327 lcc_mismatch_alutex_random           2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
328 lcc_mismatch_alutex_sm_meq          2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
329 lcc_mismatch_base_random             2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
330 lcc_mismatch_base_sm_meq            2002Oct9
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0   ld_cnst_cntx_coherency_cntrl        2002Oct9
PASS
0   ld_cnst_cntx_coherency_cntrl_sm_roq  2002Oct9

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PASS		
333	ld_cnst_cntx_coherency_cntr2	2002Oct9
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
334	ld_cnst_cntx_coherency_cntr2_sm_roq	2002Oct9
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Oct9
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct9
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct9
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct9
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct9
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct9
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct9
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct9
PASS		
0	ld_cnst_cntx_match	2002Oct9
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct9
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct9
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct9
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct9
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct9
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct9
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct9
PASS		
0	ld_cnst_cntx_replicate8	2002Oct9
PASS		
0	ld_cnst_cntx_replicate_alu	2002Oct9
PASS		
0	ld_cnst_cntx_replicate_tex	2002Oct9
PASS		
0	ld_cnst_cntx_ring_replicate	2002Oct9
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Oct9
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Oct9

PASS		
0	ld_cnst_cntx_ring_simple	20020ct9
PASS		
0	ld_cnst_cntx_simple_alu	20020ct9
PASS		
0	ld_cnst_cntx_simple_tex	20020ct9
PASS		
0	load_constant_context_rt	20020ct9
PASS		
0	load_palette	20020ct9
PASS		
362	lp_2d3d	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
363	me_reg_wr_rd	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	mem_write	20020ct9
PASS		
0	mem_write_rt	20020ct9
PASS		
0	mpeg_index	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_5	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_6	20020ct9
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	20020ct9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	20020ct9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	20020ct9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	20020ct9
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	20020ct9

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Oct9
PASS		
0	nop_eating_disabled	2002Oct9
PASS		
0	nrt_rt_interaction1	2002Oct9
PASS		
384	paint_multi	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
385	paint_packet	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
386	paint_packet_gui_control	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
387	pfpr_reg_reads1	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
388	pfpr_reg_reads2	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
389	pitch_clip	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
390	ply_nextscan	2002Oct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	rbbm_NQWU_BIF_01	2002Oct9
PASS		
0	rbbm_NQWU_BIF_02	2002Oct9
PASS		
0	rbbm_NQWU_CF_01	2002Oct9
PASS		
0	rbbm_NQWU_CPnq_01	2002Oct9
PASS		
0	rbbm_WU_2D_idle_01	2002Oct9
PASS		
0	rbbm_WU_2D_idle_02	2002Oct9
PASS		
0	rbbm_WU_2D_idle_03	2002Oct9
PASS		
0	rbbm_WU_2D_idle_04	2002Oct9
PASS		
399	rbbm_WU_2D_idle_05	2002Oct9
FAIL	rbbmWr.cmp	
0	rbbm_WU_2D_idle_06	2002Oct9
PASS		
0	rbbm_WU_2D_idle_07	2002Oct9
PASS		
0	rbbm_WU_2D_idle_08	2002Oct9
PASS		
0	rbbm_WU_2D_idle_09	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Oct9

PASS		
0	rbbm_WU_2D_idle_clean_02	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Oct9
PASS		
408	rbbm_WU_2D_idle_clean_05	2002Oct9
FAIL	rbbmWr.cmp	
0	rbbm_WU_2D_idle_clean_06	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Oct9
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Oct9
PASS		
415	rbbm_WU_2D_idle_clean_12	2002Oct9
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_01	2002Oct9
PASS		
0	rbbm_WU_3D_idle_02	2002Oct9
PASS		
0	rbbm_WU_3D_idle_03	2002Oct9
PASS		
0	rbbm_WU_3D_idle_04	2002Oct9
PASS		
420	rbbm_WU_3D_idle_05	2002Oct9
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_06	2002Oct9
PASS		
0	rbbm_WU_3D_idle_07	2002Oct9
PASS		
0	rbbm_WU_3D_idle_08	2002Oct9
PASS		
0	rbbm_WU_3D_idle_09	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Oct9

PASS		
429	rbbm_WU_3D_idle_clean_05	2002Oct9
FAIL	rbbmWr.cmp	
0	rbbm_WU_3D_idle_clean_06	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Oct9
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Oct9
PASS		
436	rbbm_WU_3D_idle_clean_12	2002Oct9
FAIL	rbbmWr.cmp	
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Oct9
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Oct9
PASS		
0	rbbm_WU_CP_dma_idle	2002Oct9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Oct9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Oct9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Oct9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Oct9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Oct9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Oct9
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct9
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct9
PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct9
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct9
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct9
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct9
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct9

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PASS
0   rbbm_WU_VIPH2_dma_idle           2002Oct9
PASS
0   rbbm_WU_VIPH3_dma_idle           2002Oct9
PASS
455 rbbm_WU_cmdfifo_entries           2002Oct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   rbbm_WU_idct_semaphore           2002Oct9
PASS
0   rbbm_WU_pad_ext_sig_rising_edge  2002Oct9
PASS
458 rbbm_WUrt_BIF_RBBM_agp_flush_off 2002Oct9
**FAIL**      cp_rbbm.cmp
459 rbbm_WUrt_BOTH_GRP update_pending_off 2002Oct9
**FAIL**      cp_rbbm.cmp
460 rbbm_WUrt_CP_dma_idle            2002Oct9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
461 rbbm_WUrt_CRTC_D1MODE_vline_fe   2002Oct9
**FAIL**      cp_rbbm.cmp
462 rbbm_WUrt_CRTC_D1MODE_vline_on   2002Oct9
**FAIL**      cp_rbbm.cmp
463 rbbm_WUrt_CRTC_D1MODE_vline_re   2002Oct9
**FAIL**      cp_rbbm.cmp
464 rbbm_WUrt_CRTC_D2MODE_vline_fe   2002Oct9
**FAIL**      cp_rbbm.cmp
465 rbbm_WUrt_CRTC_D2MODE_vline_on   2002Oct9
**FAIL**      cp_rbbm.cmp
466 rbbm_WUrt_CRTC_D2MODE_vline_re   2002Oct9
**FAIL**      cp_rbbm.cmp
467 rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Oct9
**FAIL**      cp_rbbm.cmp
468 rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Oct9
**FAIL**      cp_rbbm.cmp
469 rbbm_WUrt_MH_hdp_clean           2002Oct9
**FAIL**      cp_rbbm.cmp
470 rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off 2002Oct9
**FAIL**      cp_rbbm.cmp
471 rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off 2002Oct9
**FAIL**      cp_rbbm.cmp
0   rbbm_WUrt_RT_idle_01             2002Oct9
PASS
0   rbbm_WUrt_RT_idle_02             2002Oct9
PASS
0   rbbm_WUrt_RT_idle_03             2002Oct9
PASS
0   rbbm_WUrt_RT_idle_04             2002Oct9
PASS
476 rbbm_WUrt_RT_idle_clean_01       2002Oct9

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**FAIL**      cp_rbbm.cmp
477 rbbm_WUrt_RT_idle_clean_02      2002Oct9
**FAIL**      cp_rbbm.cmp
478 rbbm_WUrt_RT_idle_clean_03      2002Oct9
**FAIL**      cp_rbbm.cmp
479 rbbm_WUrt_RT_idle_clean_04      2002Oct9
**FAIL**      cp_rbbm.cmp
0   rbbm_WUrt_RT_idle_clean_05      2002Oct9
PASS
481 rbbm_WUrt_VIPH0_dma_idle        2002Oct9
**FAIL**      cp_rbbm.cmp
482 rbbm_WUrt_VIPH1_dma_idle        2002Oct9
**FAIL**      cp_rbbm.cmp
483 rbbm_WUrt_VIPH2_dma_idle        2002Oct9
**FAIL**      cp_rbbm.cmp
484 rbbm_WUrt_VIPH3_dma_idle        2002Oct9
**FAIL**      cp_rbbm.cmp
485 rbbm_WUrt_pad_ext_sig_rising_edge 2002Oct9
**FAIL**      cp_rbbm.cmp
0   rbbm_clk_active_deassert_min_max 2002Oct9
PASS
0   rbbm_clk_active_each_pipe      2002Oct9
PASS
0   rbbm_clk_active_for_read_error  2002Oct9
PASS
0   rbbm_clk_active_for_read_request 2002Oct9
PASS
0   rbbm_clk_active_on_reset        2002Oct9
PASS
0   rbbm_cp_non_queued_rdwr_01      2002Oct9
PASS
492 rbbm_cp_non_queued_rdwr_02      2002Oct9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
493 rbbm_cp_queued_holds_nonqueued  2002Oct9
**FAIL**      cp_rbbm.cmp
494 rbbm_cp_queued_rdwr            2002Oct9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
0   rbbm_hi_data_and_be_swap        2002Oct9
PASS
0   rbbm_hi_data_endian_swap        2002Oct9
PASS
0   rbbm_hi_data_no_swap_for_cppush  2002Oct9
PASS
0   rbbm_hi_data_swap_for_cppush    2002Oct9
PASS
0   rbbm_int_ack                    2002Oct9
PASS
500 rbbm_int_each_client            2002Oct9

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**FAIL**
0   rbbm_int_nrt_gfxpipe_idle           20020ct9
PASS
0   rbbm_int_nrt_gfxpipe_idle_rderr     20020ct9
PASS
0   rbbm_int_rderr                       20020ct9
PASS
0   rbbm_int_st_restore_nrt_gfxpipe_idle_rderr 20020ct9
PASS
0   rbbm_isync_cpscratch_idlegui       20020ct9
PASS
0   rbbm_isync_wait_idlegui            20020ct9
PASS
0   rbbm_soft_reset                     20020ct9
PASS
0   rbbm_vgt_default_req                20020ct9
PASS
509 rbbm_vgt_max_req                    20020ct9
**FAIL**
510 rbbm_vgt_min_req                    20020ct9
**FAIL**
0   reg_to_mem                           20020ct9
PASS
0   reg_to_mem_rt                        20020ct9
PASS
0   ring_il_pio_i2_bm                    20020ct9
PASS
514 ring_pio_ibs_bm_stress              20020ct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
515 rop_cfd_wmd                          20020ct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
516 roq_reg_reads                        20020ct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
517 rstr_ordr                            20020ct9
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   rt_invalid_opcode                    20020ct9
PASS
0   rtee_arm_wait_idle_trigger_events_16 20020ct9
PASS
0   rtee_discretes_arm_rearm_events_32   20020ct9
PASS
0   rtee_discretes_falling_00_events_16  20020ct9
PASS
0   rtee_discretes_falling_01_events_16  20020ct9
PASS
0   rtee_discretes_falling_02_events_16  20020ct9
PASS
0   rtee_discretes_falling_03_events_16  20020ct9

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PASS		
0	rtee_discretes_falling_04_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_05_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_06_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_07_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_08_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_09_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_10_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_11_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_12_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_13_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_14_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_15_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_16_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_17_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_18_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_19_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_20_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_21_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_22_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_23_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_24_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_25_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_26_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_27_events_16	2002Oct9

PASS		
0	rtee_discretes_falling_28_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_29_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_30_events_16	2002Oct9
PASS		
0	rtee_discretes_falling_31_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_03_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_04_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_05_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_06_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_07_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_08_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_09_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_10_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_11_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_12_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_13_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_14_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_15_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_16_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_17_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_18_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_19_events_16	2002Oct9

PASS		
0	rtee_discretes_invert_20_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_21_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_22_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_23_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_24_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_25_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_26_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_27_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_28_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_29_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_30_events_16	2002Oct9
PASS		
0	rtee_discretes_invert_31_events_16	2002Oct9
PASS		
0	rtee_discretes_level_00_events_16	2002Oct9
PASS		
0	rtee_discretes_level_01_events_16	2002Oct9
PASS		
0	rtee_discretes_level_02_events_16	2002Oct9
PASS		
0	rtee_discretes_level_03_events_16	2002Oct9
PASS		
0	rtee_discretes_level_04_events_16	2002Oct9
PASS		
0	rtee_discretes_level_05_events_16	2002Oct9
PASS		
0	rtee_discretes_level_06_events_16	2002Oct9
PASS		
0	rtee_discretes_level_07_events_16	2002Oct9
PASS		
0	rtee_discretes_level_08_events_16	2002Oct9
PASS		
0	rtee_discretes_level_09_events_16	2002Oct9
PASS		
0	rtee_discretes_level_10_events_16	2002Oct9
PASS		
0	rtee_discretes_level_11_events_16	2002Oct9

PASS		
0	rtee_discretes_level_12_events_16	2002Oct9
PASS		
0	rtee_discretes_level_13_events_16	2002Oct9
PASS		
0	rtee_discretes_level_14_events_16	2002Oct9
PASS		
0	rtee_discretes_level_15_events_16	2002Oct9
PASS		
0	rtee_discretes_level_16_events_16	2002Oct9
PASS		
0	rtee_discretes_level_17_events_16	2002Oct9
PASS		
0	rtee_discretes_level_18_events_16	2002Oct9
PASS		
0	rtee_discretes_level_19_events_16	2002Oct9
PASS		
0	rtee_discretes_level_20_events_16	2002Oct9
PASS		
0	rtee_discretes_level_21_events_16	2002Oct9
PASS		
0	rtee_discretes_level_22_events_16	2002Oct9
PASS		
0	rtee_discretes_level_23_events_16	2002Oct9
PASS		
0	rtee_discretes_level_24_events_16	2002Oct9
PASS		
0	rtee_discretes_level_25_events_16	2002Oct9
PASS		
0	rtee_discretes_level_26_events_16	2002Oct9
PASS		
0	rtee_discretes_level_27_events_16	2002Oct9
PASS		
0	rtee_discretes_level_28_events_16	2002Oct9
PASS		
0	rtee_discretes_level_29_events_16	2002Oct9
PASS		
0	rtee_discretes_level_30_events_16	2002Oct9
PASS		
0	rtee_discretes_level_31_events_16	2002Oct9
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_00_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_02_events_16	2002Oct9

PASS		
0	rtee_discretes_rising_03_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_04_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_05_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_06_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_07_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_08_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_09_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_10_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_11_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_12_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_13_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_14_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_15_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_16_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_17_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_18_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_19_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_20_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_21_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_22_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_23_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_24_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_25_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_26_events_16	2002Oct9

PASS		
0	rtee_discretes_rising_27_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_28_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_29_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_30_events_16	2002Oct9
PASS		
0	rtee_discretes_rising_31_events_16	2002Oct9
PASS		
0	rtee_display1_comp_events_16	2002Oct9
PASS		
0	rtee_display2_comp_events_16	2002Oct9
PASS		
0	rtee_mem_linear_search_events_2	2002Oct9
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct9
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct9
PASS		
662	rtee_mem_poll_comp_masks_events_16_random	2002Oct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	rtee_mem_poll_comp_swaps_events_16	2002Oct9
PASS		
0	rtee_mem_poll_intervals_events_16	2002Oct9
PASS		
0	rtee_mem_poll_short_intervals_events_16	2002Oct9
PASS		
0	rtee_pio	2002Oct9
PASS		
0	rtee_prefetch_parse_nops	2002Oct9
PASS		
0	rtee_reg_linear_search_events_2	2002Oct9

PASS		
0	rtee_reg_poll_comp_000_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_001_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_010_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_011_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_100_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_101_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_110_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_111_events_16	20020ct9
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	20020ct9
PASS		
0	rtee_vip_comp_events_16	20020ct9
PASS		
0	scratch_mem_read	20020ct9
PASS		
0	scratch_mem_read2	20020ct9
PASS		
681	set_cnst2_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
682	set_cnst_alu_max1_sm_meq	20020ct9
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
683	set_cnst_alu_max_random	20020ct9
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	set_cnst_bool_max2_sm_meq	20020ct9
PASS		
685	set_cnst_loop_max2_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
686	set_cnst_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
687	set_cnst_tex_max2_sm_meq	20020ct9
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
688	set_cnst_we_lcc_random	20020ct9
FAIL	mh_cp_rd.cmp	
689	set_cnst_we_random	20020ct9
FAIL	mh_cp_rd.cmp	
690	set_cnst_write_enable_lcc2_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
691	set_cnst_write_enable_sm_meq	20020ct9
FAIL	mh_cp_rd.cmp	
0	set_constant	20020ct9

PASS		
0	set_constant2	20020ct9
PASS		
694	set_constant_alu_max1	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
695	set_constant_alu_max2	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	set_constant_bool_max1	20020ct9
PASS		
0	set_constant_bool_max2	20020ct9
PASS		
0	set_constant_indr1_short	20020ct9
PASS		
0	set_constant_indr1_short2	20020ct9
PASS		
0	set_constant_indr1_write_enable	20020ct9
PASS		
0	set_constant_indr1_write_enable_lcc	20020ct9
PASS		
0	set_constant_indr2_short	20020ct9
PASS		
0	set_constant_indr2_short2	20020ct9
PASS		
0	set_constant_indr2_write_enable	20020ct9
PASS		
0	set_constant_indr2_write_enable_lcc	20020ct9
PASS		
0	set_constant_loop_max1	20020ct9
PASS		
0	set_constant_loop_max2	20020ct9
PASS		
0	set_constant_ring_short	20020ct9
PASS		
0	set_constant_ring_short2	20020ct9
PASS		
0	set_constant_ring_write_enable	20020ct9
PASS		
0	set_constant_ring_write_enable_lcc	20020ct9
PASS		
0	set_constant_short	20020ct9
PASS		
0	set_constant_short2	20020ct9
PASS		
714	set_constant_tex_max1	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
715	set_constant_tex_max2	20020ct9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
716	set_constant_tex_max2_sm_roq	20020ct9

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0  set_constant_write_enable          2002Oct9
PASS
0  set_constant_write_enable_lcc      2002Oct9
PASS
0  set_constant_write_enable_lcc2     2002Oct9
PASS
0  set_constant_write_enable_lcc2_sm_roq 2002Oct9
PASS
0  set_scissors                       2002Oct9
PASS
0  set_st_indrl_match                 2002Oct9
PASS
0  set_st_indrl_mismatch              2002Oct9
PASS
0  set_st_indrl_mismatch2             2002Oct9
PASS
0  set_st_indrl_mismatch3             2002Oct9
PASS
0  set_st_indrl_mismatch4             2002Oct9
PASS
0  set_st_indrl_ps                    2002Oct9
PASS
0  set_st_indrl_ps_only                2002Oct9
PASS
0  set_st_indrl_subblk0                2002Oct9
PASS
0  set_st_indrl_subblk1                2002Oct9
PASS
0  set_st_indrl_subblk1_only           2002Oct9
PASS
0  set_st_indrl_subblk2                2002Oct9
PASS
0  set_st_indrl_subblk2_only           2002Oct9
PASS
0  set_st_indrl_subblk3                2002Oct9
PASS
0  set_st_indrl_subblk3_only           2002Oct9
PASS
0  set_st_indrl_subblk4                2002Oct9
PASS
0  set_st_indrl_subblk4_only           2002Oct9
PASS
0  set_st_indrl_subblk5                2002Oct9
PASS
0  set_st_indrl_subblk5_only           2002Oct9
PASS
0  set_st_indrl_subblk6                2002Oct9

```

PASS		
0	set_st_indr1_sublk6_only	20020ct9
PASS		
0	set_st_indr1_sublk7	20020ct9
PASS		
0	set_st_indr1_sublk7_only	20020ct9
PASS		
0	set_st_indr1_vs	20020ct9
PASS		
0	set_st_indr1_vs_only	20020ct9
PASS		
0	set_st_indr2_match	20020ct9
PASS		
0	set_st_indr2_mismatch	20020ct9
PASS		
0	set_st_indr2_mismatch2	20020ct9
PASS		
0	set_st_indr2_mismatch3	20020ct9
PASS		
0	set_st_indr2_mismatch4	20020ct9
PASS		
0	set_st_indr2_ps	20020ct9
PASS		
0	set_st_indr2_ps_only	20020ct9
PASS		
0	set_st_indr2_sublk0	20020ct9
PASS		
0	set_st_indr2_sublk1	20020ct9
PASS		
0	set_st_indr2_sublk1_only	20020ct9
PASS		
0	set_st_indr2_sublk2	20020ct9
PASS		
0	set_st_indr2_sublk2_only	20020ct9
PASS		
0	set_st_indr2_sublk3	20020ct9
PASS		
0	set_st_indr2_sublk3_only	20020ct9
PASS		
0	set_st_indr2_sublk4	20020ct9
PASS		
0	set_st_indr2_sublk4_only	20020ct9
PASS		
0	set_st_indr2_sublk5	20020ct9
PASS		
0	set_st_indr2_sublk5_only	20020ct9
PASS		
0	set_st_indr2_sublk6	20020ct9

PASS		
0	set_st_indr2_sublk6_only	20020ct9
PASS		
0	set_st_indr2_sublk7	20020ct9
PASS		
0	set_st_indr2_sublk7_only	20020ct9
PASS		
0	set_st_indr2_vs	20020ct9
PASS		
0	set_st_indr2_vs_only	20020ct9
PASS		
0	set_st_pfmatch_disabled	20020ct9
PASS		
0	set_st_ring_match	20020ct9
PASS		
0	set_st_ring_mismatch	20020ct9
PASS		
0	set_st_ring_mismatch2	20020ct9
PASS		
0	set_st_ring_mismatch3	20020ct9
PASS		
0	set_st_ring_mismatch4	20020ct9
PASS		
0	set_st_ring_ps	20020ct9
PASS		
0	set_st_ring_ps_only	20020ct9
PASS		
0	set_st_ring_sublk0	20020ct9
PASS		
0	set_st_ring_sublk1	20020ct9
PASS		
0	set_st_ring_sublk1_only	20020ct9
PASS		
0	set_st_ring_sublk2	20020ct9
PASS		
0	set_st_ring_sublk2_only	20020ct9
PASS		
0	set_st_ring_sublk3	20020ct9
PASS		
0	set_st_ring_sublk3_only	20020ct9
PASS		
0	set_st_ring_sublk4	20020ct9
PASS		
0	set_st_ring_sublk4_only	20020ct9
PASS		
0	set_st_ring_sublk5	20020ct9
PASS		
0	set_st_ring_sublk5_only	20020ct9

PASS		
0	set_st_ring_sublk6	20020ct9
PASS		
0	set_st_ring_sublk6_only	20020ct9
PASS		
0	set_st_ring_sublk7	20020ct9
PASS		
0	set_st_ring_sublk7_only	20020ct9
PASS		
0	set_st_ring_vs	20020ct9
PASS		
0	set_st_ring_vs_only	20020ct9
PASS		
0	set_st_stress_a	20020ct9
PASS		
0	set_st_stress_b	20020ct9
PASS		
797	set_st_stress_b_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
798	set_st_stress_b_sm_meq	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	20020ct9
PASS		
0	set_st_stress_c_sm_roq	20020ct9
PASS		
0	set_st_stress_d	20020ct9
PASS		
802	set_st_stress_d_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
803	set_st_stress_d_sm_meq	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_e	20020ct9
PASS		
805	set_st_stress_e_sm_meq	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_f	20020ct9
PASS		
807	set_st_stress_f_random	20020ct9
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_state_rt_a	20020ct9
PASS		
0	set_state_rt_b	20020ct9
PASS		
0	set_state_rt_c	20020ct9
PASS		
0	set_state_rt_d	20020ct9
PASS		
812	smoke_nrt	20020ct9

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
813 src_clip                                     20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
814 srf_cohr                                       20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
815 switch_pio_bm                                   20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
816 trans_bitblt                                    20020ct9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0 type0_2long_pckts                                20020ct9
PASS
0 type0_3indr_buffs                                20020ct9
PASS
0 type0_4pckts                                     20020ct9
PASS
0 type0_incr_updt_cnst                             20020ct9
PASS
0 type0_incr_updt_instrmem                          20020ct9
PASS
0 type0_incr_updt_st                                20020ct9
PASS
0 type0_incr_updt_stress                           20020ct9
PASS
0 type0_ind1_ind2_a                                20020ct9
PASS
0 type0_ind1_ind2_b                                20020ct9
PASS
0 type0_indr_buff                                   20020ct9
PASS
0 type1_4pckts                                     20020ct9
PASS
0 type1_indr_buff                                   20020ct9
PASS
0 type1_pckt                                        20020ct9
PASS
830 type_random                                     20020ct9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 uram_wrt_rt_ucode                                20020ct9
PASS
0 uram_wrt_ucode                                    20020ct9
PASS
0 viz_query_a                                       20020ct9
PASS
0 viz_query_draw_indx                               20020ct9
PASS
0 viz_query_draw_indx_indr1                         20020ct9
PASS
0 viz_query_draw_indx_indr2                         20020ct9

```

PASS
0 viz_query_draw_indx_multipass 2002Oct9
PASS
0 viz_query_draw_indx_ring 2002Oct9
PASS
0 viz_query_indr1 2002Oct9
PASS
0 viz_query_indr1_simple 2002Oct9
PASS
0 viz_query_indr2 2002Oct9
PASS
0 viz_query_indr2_simple 2002Oct9
PASS
0 viz_query_ring 2002Oct9
PASS
0 viz_query_ring_simple 2002Oct9
PASS
0 wait_for_idle 2002Oct9
PASS
0 wait_reg_mem_poll_mem0 2002Oct9
PASS
0 wait_reg_mem_poll_mem0_rt 2002Oct9
PASS
0 wait_reg_mem_poll_mem1 2002Oct9
PASS
0 wait_reg_mem_poll_mem2 2002Oct9
PASS
0 wait_reg_mem_poll_mem3 2002Oct9
PASS
0 wait_reg_mem_poll_reg0 2002Oct9
PASS
0 wait_reg_mem_poll_reg1 2002Oct9
PASS
0 wait_reg_mem_poll_reg2 2002Oct9
PASS
0 wait_reg_mem_poll_reg3 2002Oct9
PASS

=====
Tests_Run_count = 854 Pass_count = 680 Fail_count = 174 Incomplete_count = 0
=====

=====
Regression Report For CP - Project = r400 Created on Mon Aug 12 16:34:42 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug12
PASS			
0	2d_indr_buffer_b		2002Aug12
PASS			
0	2d_indr_buffer_c		2002Aug12
PASS			
0	2d_indr_buffer_d		2002Aug12
PASS			
0	2d_indr_buffer_e		2002Aug12
PASS			
0	2d_indr_buffer_f		2002Aug12
PASS			
0	2d_indr_buffer_g		2002Aug12
PASS			
0	3d_draw_indx_2		2002Aug12
PASS			
0	3d_draw_indx_2_b		2002Aug12
PASS			
10	bitblt_multi		2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp		
0	brush_flag		2002Aug12
PASS			
0	cond_write_mem_poll_mem		2002Aug12
PASS			
0	cond_write_mem_poll_mem2		2002Aug12
PASS			
0	cond_write_mem_poll_mem3		2002Aug12
PASS			
0	cond_write_mem_poll_mem_rt		2002Aug12
PASS			
0	cond_write_mem_poll_reg		2002Aug12
PASS			
0	cond_write_mem_poll_reg2		2002Aug12
PASS			
0	cond_write_mem_poll_reg3		2002Aug12
PASS			
0	cp_acpi_recovery_soft_reset		2002Aug12
PASS			
0	cp_bios_scratch		2002Aug12

PASS		
0	cp_ib2_base000_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug12
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug12
PASS		
0	cp_ib_base000_bufszXXX	2002Aug12
PASS		
0	cp_ib_base001_bufszXXX	2002Aug12
PASS		
0	cp_ib_base010_bufszXXX	2002Aug12
PASS		
0	cp_ib_base011_bufszXXX	2002Aug12
PASS		
0	cp_ib_base100_bufszXXX	2002Aug12
PASS		
0	cp_ib_base101_bufszXXX	2002Aug12
PASS		
35	cp_ib_base110_bufszXXX	2002Aug12
FAIL	cp_mh_req.cmp	
36	cp_ib_base111_bufszXXX	2002Aug12
FAIL	cp_mh_req.cmp	
0	cp_interrupt	2002Aug12
PASS		
0	cp_interrupt_rt_events_16	2002Aug12
PASS		
0	cp_iq2_alignment_000	2002Aug12
PASS		
0	cp_iq2_alignment_001	2002Aug12
PASS		
0	cp_iq2_alignment_010	2002Aug12
PASS		
0	cp_iq2_alignment_011	2002Aug12
PASS		
0	cp_iq2_alignment_100	2002Aug12
PASS		
0	cp_iq2_alignment_101	2002Aug12

PASS		
0	cp_iq2_alignment_110	2002Aug12
PASS		
0	cp_iq2_alignment_111	2002Aug12
PASS		
0	cp_iq_alignment_000	2002Aug12
PASS		
0	cp_iq_alignment_001	2002Aug12
PASS		
0	cp_iq_alignment_010	2002Aug12
PASS		
0	cp_iq_alignment_011	2002Aug12
PASS		
0	cp_iq_alignment_100	2002Aug12
PASS		
0	cp_iq_alignment_101	2002Aug12
PASS		
0	cp_iq_alignment_110	2002Aug12
PASS		
0	cp_iq_alignment_111	2002Aug12
PASS		
0	cp_max_size_packet	2002Aug12
PASS		
0	cp_nrt_rt_protection_mode	2002Aug12
PASS		
0	cp_output_stress	2002Aug12
PASS		
0	cp_protection_mode	2002Aug12
PASS		
0	cp_rb_alignment_000	2002Aug12
PASS		
0	cp_rb_alignment_001	2002Aug12
PASS		
0	cp_rb_alignment_010	2002Aug12
PASS		
0	cp_rb_alignment_011	2002Aug12
PASS		
0	cp_rb_alignment_100	2002Aug12
PASS		
0	cp_rb_alignment_101	2002Aug12
PASS		
0	cp_rb_alignment_110	2002Aug12
PASS		
0	cp_rb_alignment_111	2002Aug12
PASS		
0	cp_register_reads	2002Aug12
PASS		
0	cp_reg_rw_test	2002Aug12

PASS		
0	cp_rt_protection_mode	2002Aug12
PASS		
0	cp_scratch	2002Aug12
PASS		
0	cp_scratch_multi	2002Aug12
PASS		
0	dma_cp_sync	2002Aug12
PASS		
0	dma_dscrstr_chckn_bit	2002Aug12
PASS		
0	dma_dscrstr_flush1	2002Aug12
PASS		
0	dma_dscrstr_flush2	2002Aug12
PASS		
0	dma_dscrstr_flush3	2002Aug12
PASS		
0	dma_m2m_src00_dstXX	2002Aug12
PASS		
0	dma_m2m_src01_dstXX	2002Aug12
PASS		
0	dma_m2m_src10_dstXX	2002Aug12
PASS		
0	dma_m2m_src11_dstXX	2002Aug12
PASS		
0	dma_mem2mem	2002Aug12
PASS		
0	dma_mem2mem_abort	2002Aug12
PASS		
0	dma_mem2mem_alignments	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug12
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug12
PASS		
0	dma_mem2mem_pio_abort	2002Aug12

PASS		
0	dma_mem2reg	2002Aug12
PASS		
0	dma_mem2reg_abort	2002Aug12
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug12
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug12
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug12
PASS		
0	dma_mem2reg_sync	2002Aug12
PASS		
0	dma_r2r_m2m_r2r	2002Aug12
PASS		
0	dma_r2r_m2r_r2r	2002Aug12
PASS		
0	dma_r2r_r2m_r2r	2002Aug12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug12
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug12
PASS		
0	dma_reg2mem	2002Aug12
PASS		
0	dma_reg2mem_abort	2002Aug12
PASS		
0	dma_reg2reg	2002Aug12
PASS		
0	dma_reg2reg_2dw	2002Aug12
PASS		
0	dma_reg2reg_daic	2002Aug12
PASS		
0	dma_reg2reg_mult	2002Aug12
PASS		
0	dma_reg2reg_mult2	2002Aug12
PASS		
0	dma_reg2reg_mult3	2002Aug12
PASS		
0	dma_reg2reg_saic	2002Aug12
PASS		
0	dma_reg2reg_sdaic	2002Aug12
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug12
PASS		
0	dma_reg2reg_stress	2002Aug12

PASS		
0	draw_indx_a	2002Aug12
PASS		
0	draw_indx_b	2002Aug12
PASS		
0	draw_indx_c	2002Aug12
PASS		
0	draw_indx_indr1_a	2002Aug12
PASS		
0	draw_indx_indr1_b	2002Aug12
PASS		
0	draw_indx_indr1_short_a	2002Aug12
PASS		
0	draw_indx_indr1_short_b	2002Aug12
PASS		
0	draw_indx_indr1_stress	2002Aug12
PASS		
0	draw_indx_indr2_a	2002Aug12
PASS		
0	draw_indx_indr2_b	2002Aug12
PASS		
0	draw_indx_indr2_short_a	2002Aug12
PASS		
0	draw_indx_indr2_short_b	2002Aug12
PASS		
0	draw_indx_indr2_stress	2002Aug12
PASS		
0	draw_indx_ring1	2002Aug12
PASS		
0	draw_indx_ring2	2002Aug12
PASS		
0	draw_indx_ring_short1	2002Aug12
PASS		
0	draw_indx_ring_short2	2002Aug12
PASS		
0	draw_indx_ring_stress	2002Aug12
PASS		
0	draw_indx_short_a	2002Aug12
PASS		
0	draw_indx_short_b	2002Aug12
PASS		
0	draw_indx_short_c	2002Aug12
PASS		
0	draw_indx_stress_a	2002Aug12
PASS		
0	draw_indx_stress_b	2002Aug12
PASS		
0	draw_indx_stress_c	2002Aug12

PASS		
141	event_timestamp_write	2002Aug12
FAIL		
142	ft_indr1_july22	2002Aug12
FAIL		
143	ft_indr2_july22	2002Aug12
FAIL		
144	ft_july22	2002Aug12
FAIL	cp_mh_req.cmp	
0	header_dump	2002Aug12
PASS		
0	header_dump_rt	2002Aug12
PASS		
0	header_dump_small	2002Aug12
PASS		
0	im_load	2002Aug12
PASS		
0	im_load_immediate	2002Aug12
PASS		
0	im_load_immediate_nrt_me	2002Aug12
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug12
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug12
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug12
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug12
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug12
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug12
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug12
PASS		
0	im_load_nrt_me	2002Aug12
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug12
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug12
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug12
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug12
PASS		
0	incremental_update_const	2002Aug12
PASS		
164	incremental_update_instr	2002Aug12

```

**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_state                2002Aug12
PASS
0  indirect_buffer_a                      2002Aug12
PASS
0  indirect_buffer_b                      2002Aug12
PASS
0  indirect_buffer_c                      2002Aug12
PASS
0  indirect_buffer_d                      2002Aug12
PASS
170 indirect_buffer_e                    2002Aug12
**FAIL**      cp_rbbm.cmp
171 indirect_buffer_f                    2002Aug12
**FAIL**      cp_rbbm.cmp
172 indirect_buffer_g                    2002Aug12
**FAIL**      cp_rbbm.cmp
173 indirect_buffer_h                    2002Aug12
**FAIL**      cp_rbbm.cmp
174 indirect_buffer_i                    2002Aug12
**FAIL**      cp_rbbm.cmp
175 indirect_buffer_j                    2002Aug12
**FAIL**      cp_rbbm.cmp
176 indirect_buffer_k                    2002Aug12
**FAIL**      cp_rbbm.cmp
0  indirect_buffer_l                      2002Aug12
PASS
178 indirect_buffer_mp_a                  2002Aug12
**FAIL**      cp_rbbm.cmp
179 indirect_buffer_mp_b                  2002Aug12
**FAIL**      cp_rbbm.cmp
180 indirect_buffer_mp_c                  2002Aug12
**FAIL**      cp_rbbm.cmp
181 indirect_buffer_mp_d                  2002Aug12
**FAIL**      cp_rbbm.cmp
182 indirect_buffer_mp_e                  2002Aug12
**FAIL**      cp_rbbm.cmp
183 indirect_buffer_mp_f                  2002Aug12
**FAIL**      cp_rbbm.cmp
184 invalidate_on_me_init                 2002Aug12
**FAIL**      cp_mh_req.cmp mh_cp_rd.cmp
0  invalidate_state_constants             2002Aug12
PASS
0  invalidate_state_shaders               2002Aug12
PASS
0  invalidate_state_shaders2              2002Aug12
PASS
0  invalidate_state_sublks                 2002Aug12

```

PASS		
0	invalidate_state_sublks2	2002Aug12
PASS		
0	invalid_opcode	2002Aug12
PASS		
0	iq1_iq2_mult	2002Aug12
PASS		
192	iq1_iq2_push_wrap	2002Aug12
INCOMPLETE		
193	iq2_csq_full	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	IQ2_Mult	2002Aug12
PASS		
0	IQ2_Wrap_Legacy	2002Aug12
PASS		
0	IQ_Wrap	2002Aug12
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug12
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug12
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug12
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug12
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug12
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug12
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug12
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug12
PASS		
0	ld_cnst_cntx_match	2002Aug12
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug12
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug12
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug12
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug12
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug12
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug12
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug12

PASS		
0	ld_cnst_cntx_replicate8	2002Aug12
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug12
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug12
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug12
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug12
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug12
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug12
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug12
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug12
PASS		
0	load_constant_context_rt	2002Aug12
PASS		
0	mem_write	2002Aug12
PASS		
0	mem_write_rt	2002Aug12
PASS		
0	mpeg_index	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug12
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug12
PASS		
0	nop_eating_disabled	2002Aug12
PASS		
0	paint_multi	2002Aug12
PASS		
0	pitch_clip	2002Aug12

PASS		
0	rbbm_soft_reset	2002Aug12
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug12
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Aug12
PASS		
0	rbbm_WU_CP_dma_idle	2002Aug12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug12
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug12
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug12
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug12
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug12
PASS		
0	rbbm_WU_idct_semaphore	2002Aug12
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug12
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug12
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug12
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug12
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Aug12
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Aug12
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Aug12
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Aug12
PASS		
0	RB_BUFSZ_0	2002Aug12
PASS		
0	RB_BUFSZ_1	2002Aug12
PASS		
0	RB_Wrap	2002Aug12

PASS		
0	reg_to_mem	2002Aug12
PASS		
0	reg_to_mem_rt	2002Aug12
PASS		
0	rop_cfd_wmd	2002Aug12
PASS		
264	rstr_ordr	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	rtee_discretes_falling_00_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_19_events_16	2002Aug12

PASS		
0	rtee_discretes_falling_20_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug12
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_11_events_16	2002Aug12

PASS		
0	rtee_discretes_invert_12_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug12
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug12
PASS		
0	rtee_discretes_level_00_events_16	2002Aug12
PASS		
0	rtee_discretes_level_01_events_16	2002Aug12
PASS		
0	rtee_discretes_level_02_events_16	2002Aug12
PASS		
0	rtee_discretes_level_03_events_16	2002Aug12

PASS		
0	rtee_discretes_level_04_events_16	2002Aug12
PASS		
0	rtee_discretes_level_05_events_16	2002Aug12
PASS		
0	rtee_discretes_level_06_events_16	2002Aug12
PASS		
0	rtee_discretes_level_07_events_16	2002Aug12
PASS		
0	rtee_discretes_level_08_events_16	2002Aug12
PASS		
0	rtee_discretes_level_09_events_16	2002Aug12
PASS		
0	rtee_discretes_level_10_events_16	2002Aug12
PASS		
0	rtee_discretes_level_11_events_16	2002Aug12
PASS		
0	rtee_discretes_level_12_events_16	2002Aug12
PASS		
0	rtee_discretes_level_13_events_16	2002Aug12
PASS		
0	rtee_discretes_level_14_events_16	2002Aug12
PASS		
0	rtee_discretes_level_15_events_16	2002Aug12
PASS		
0	rtee_discretes_level_16_events_16	2002Aug12
PASS		
0	rtee_discretes_level_17_events_16	2002Aug12
PASS		
0	rtee_discretes_level_18_events_16	2002Aug12
PASS		
0	rtee_discretes_level_19_events_16	2002Aug12
PASS		
0	rtee_discretes_level_20_events_16	2002Aug12
PASS		
0	rtee_discretes_level_21_events_16	2002Aug12
PASS		
0	rtee_discretes_level_22_events_16	2002Aug12
PASS		
0	rtee_discretes_level_23_events_16	2002Aug12
PASS		
0	rtee_discretes_level_24_events_16	2002Aug12
PASS		
0	rtee_discretes_level_25_events_16	2002Aug12
PASS		
0	rtee_discretes_level_26_events_16	2002Aug12
PASS		
0	rtee_discretes_level_27_events_16	2002Aug12

PASS		
0	rtee_discretes_level_28_events_16	2002Aug12
PASS		
0	rtee_discretes_level_29_events_16	2002Aug12
PASS		
0	rtee_discretes_level_30_events_16	2002Aug12
PASS		
0	rtee_discretes_level_31_events_16	2002Aug12
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_18_events_16	2002Aug12

PASS		
0	rtee_discretes_rising_19_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug12
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug12
PASS		
0	rtee_display1_comp_events_16	2002Aug12
PASS		
0	rtee_display2_comp_events_16	2002Aug12
PASS		
0	rtee_mem_linear_search_events_2	2002Aug12
PASS		
397	rtee_mem_poll_comp_000_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
398	rtee_mem_poll_comp_001_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
399	rtee_mem_poll_comp_010_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
400	rtee_mem_poll_comp_011_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
401	rtee_mem_poll_comp_100_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
402	rtee_mem_poll_comp_101_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
403	rtee_mem_poll_comp_110_events_16	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
404	rtee_mem_poll_comp_111_events_16	2002Aug12

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**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
405  rtee_mem_poll_comp_masks_events_16                2002Aug12
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
406  rtee_mem_poll_comp_swaps_events_16                2002Aug12
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0    rtee_mem_poll_intervals_events_16                 2002Aug12
PASS
0    rtee_prefetch_parse_nops                          2002Aug12
PASS
0    rtee_reg_linear_search_events_2                   2002Aug12
PASS
0    rtee_reg_poll_comp_000_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_001_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_010_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_011_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_100_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_101_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_110_events_16                  2002Aug12
PASS
0    rtee_reg_poll_comp_111_events_16                  2002Aug12
PASS
418  rtee_reg_poll_comp_queued_nonqueued_events_16    2002Aug12
**FAIL**          cp_rbbm.cmp
0    rtee_vip_comp_events_16                           2002Aug12
PASS
0    rt_invalid_opcode                                 2002Aug12
PASS
0    scratch_mem_read                                  2002Aug12
PASS
0    scratch_mem_read2                                 2002Aug12
PASS
0    set_constant                                      2002Aug12
PASS
0    set_constant2                                     2002Aug12
PASS
0    set_constant_indr1_short                          2002Aug12
PASS
0    set_constant_indr1_short2                         2002Aug12
PASS
0    set_constant_indr2_short                          2002Aug12
PASS
0    set_constant_indr2_short2                         2002Aug12

```

PASS		
0	set_constant_ring_short	2002Aug12
PASS		
0	set_constant_ring_short2	2002Aug12
PASS		
0	set_constant_short	2002Aug12
PASS		
0	set_scissors	2002Aug12
PASS		
0	set_state_rt_a	2002Aug12
PASS		
0	set_state_rt_b	2002Aug12
PASS		
0	set_state_rt_c	2002Aug12
PASS		
0	set_state_rt_d	2002Aug12
PASS		
0	set_st_indrl_match	2002Aug12
PASS		
0	set_st_indrl_mismatch	2002Aug12
PASS		
0	set_st_indrl_mismatch2	2002Aug12
PASS		
0	set_st_indrl_mismatch3	2002Aug12
PASS		
0	set_st_indrl_mismatch4	2002Aug12
PASS		
0	set_st_indrl_ps	2002Aug12
PASS		
0	set_st_indrl_ps_only	2002Aug12
PASS		
0	set_st_indrl_sublk0	2002Aug12
PASS		
0	set_st_indrl_sublk1	2002Aug12
PASS		
0	set_st_indrl_sublk1_only	2002Aug12
PASS		
0	set_st_indrl_sublk2	2002Aug12
PASS		
0	set_st_indrl_sublk2_only	2002Aug12
PASS		
0	set_st_indrl_sublk3	2002Aug12
PASS		
0	set_st_indrl_sublk3_only	2002Aug12
PASS		
0	set_st_indrl_sublk4	2002Aug12
PASS		
0	set_st_indrl_sublk4_only	2002Aug12

PASS		
0	set_st_indr1_sublk5	2002Aug12
PASS		
0	set_st_indr1_sublk5_only	2002Aug12
PASS		
0	set_st_indr1_sublk6	2002Aug12
PASS		
0	set_st_indr1_sublk6_only	2002Aug12
PASS		
0	set_st_indr1_sublk7	2002Aug12
PASS		
0	set_st_indr1_sublk7_only	2002Aug12
PASS		
0	set_st_indr1_vs	2002Aug12
PASS		
0	set_st_indr1_vs_only	2002Aug12
PASS		
0	set_st_indr2_match	2002Aug12
PASS		
0	set_st_indr2_mismatch	2002Aug12
PASS		
0	set_st_indr2_mismatch2	2002Aug12
PASS		
0	set_st_indr2_mismatch3	2002Aug12
PASS		
0	set_st_indr2_mismatch4	2002Aug12
PASS		
0	set_st_indr2_ps	2002Aug12
PASS		
0	set_st_indr2_ps_only	2002Aug12
PASS		
0	set_st_indr2_sublk0	2002Aug12
PASS		
0	set_st_indr2_sublk1	2002Aug12
PASS		
0	set_st_indr2_sublk1_only	2002Aug12
PASS		
0	set_st_indr2_sublk2	2002Aug12
PASS		
0	set_st_indr2_sublk2_only	2002Aug12
PASS		
0	set_st_indr2_sublk3	2002Aug12
PASS		
0	set_st_indr2_sublk3_only	2002Aug12
PASS		
0	set_st_indr2_sublk4	2002Aug12
PASS		
0	set_st_indr2_sublk4_only	2002Aug12

PASS		
0	set_st_indr2_sublk5	2002Aug12
PASS		
0	set_st_indr2_sublk5_only	2002Aug12
PASS		
0	set_st_indr2_sublk6	2002Aug12
PASS		
0	set_st_indr2_sublk6_only	2002Aug12
PASS		
0	set_st_indr2_sublk7	2002Aug12
PASS		
0	set_st_indr2_sublk7_only	2002Aug12
PASS		
0	set_st_indr2_vs	2002Aug12
PASS		
0	set_st_indr2_vs_only	2002Aug12
PASS		
0	set_st_pfmatch_disabled	2002Aug12
PASS		
0	set_st_ring_match	2002Aug12
PASS		
0	set_st_ring_mismatch	2002Aug12
PASS		
0	set_st_ring_mismatch2	2002Aug12
PASS		
0	set_st_ring_mismatch3	2002Aug12
PASS		
0	set_st_ring_mismatch4	2002Aug12
PASS		
0	set_st_ring_ps	2002Aug12
PASS		
0	set_st_ring_ps_only	2002Aug12
PASS		
0	set_st_ring_sublk0	2002Aug12
PASS		
0	set_st_ring_sublk1	2002Aug12
PASS		
0	set_st_ring_sublk1_only	2002Aug12
PASS		
0	set_st_ring_sublk2	2002Aug12
PASS		
0	set_st_ring_sublk2_only	2002Aug12
PASS		
0	set_st_ring_sublk3	2002Aug12
PASS		
0	set_st_ring_sublk3_only	2002Aug12
PASS		
0	set_st_ring_sublk4	2002Aug12

PASS		
0	set_st_ring_sublk4_only	2002Aug12
PASS		
0	set_st_ring_sublk5	2002Aug12
PASS		
0	set_st_ring_sublk5_only	2002Aug12
PASS		
0	set_st_ring_sublk6	2002Aug12
PASS		
0	set_st_ring_sublk6_only	2002Aug12
PASS		
0	set_st_ring_sublk7	2002Aug12
PASS		
0	set_st_ring_sublk7_only	2002Aug12
PASS		
0	set_st_ring_vs	2002Aug12
PASS		
0	set_st_ring_vs_only	2002Aug12
PASS		
0	set_st_stress_a	2002Aug12
PASS		
511	set_st_stress_b	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug12
PASS		
0	set_st_stress_d	2002Aug12
PASS		
0	set_st_stress_e	2002Aug12
PASS		
0	set_st_stress_f	2002Aug12
PASS		
0	smoke_nrt	2002Aug12
PASS		
517	src_clip	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	srf_cohr	2002Aug12
PASS		
519	trans_bitblt	2002Aug12
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
520	type0_2long_pckts	2002Aug12
FAIL	cp_mh_req.cmp	
0	type0_3indr_buffs	2002Aug12
PASS		
0	type0_4pckts	2002Aug12
PASS		
0	type0_incr_updt_cnst	2002Aug12
PASS		
0	type0_incr_updt_instrmem	2002Aug12

PASS		
0	type0_incr_updt_st	2002Aug12
PASS		
0	type0_incr_updt_stress	2002Aug12
PASS		
0	type0_ind1_ind2_a	2002Aug12
PASS		
0	type0_ind1_ind2_b	2002Aug12
PASS		
0	type0_indr_buff	2002Aug12
PASS		
0	Type0_IQ2_BM	2002Aug12
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug12
PASS		
0	Type0_IQ_BM	2002Aug12
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug12
PASS		
0	Type0_PQ_BM	2002Aug12
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Aug12
PASS		
0	type1_4pckts	2002Aug12
PASS		
0	type1_indr_buff	2002Aug12
PASS		
0	type1_pckt	2002Aug12
PASS		
0	Type3_Bitblt	2002Aug12
PASS		
540	Type3_Load_Palette	2002Aug12
INCOMPLETE		
0	Type3_NOP	2002Aug12
PASS		
542	uram_rd_wrt	2002Aug12
FAIL		
543	uram_rd_wrt_rt	2002Aug12
FAIL		
544	uram_wrt_rt_ucode	2002Aug12
FAIL	rbbmWr.cmp	
0	uram_wrt_ucode	2002Aug12
PASS		
0	wait_for_idle	2002Aug12
PASS		
547	wait_reg_mem_poll_mem0	2002Aug12
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
548	wait_reg_mem_poll_mem0_rt	2002Aug12

```
**FAIL**          mh_cp_rd.cmp
549 wait_reg_mem_poll_mem1                2002Aug12
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
550 wait_reg_mem_poll_mem2                2002Aug12
**FAIL**          cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
551 wait_reg_mem_poll_mem3                2002Aug12
**FAIL**          mh_cp_rd.cmp
552 wait_reg_mem_poll_reg0                2002Aug12
**FAIL**          cp_rbbm.cmp
0 wait_reg_mem_poll_reg1                  2002Aug12
PASS
0 wait_reg_mem_poll_reg2                  2002Aug12
PASS
0 wait_reg_mem_poll_reg3                  2002Aug12
PASS
```

```
=====
Tests_Run_count = 555 Pass_count = 505 Fail_count = 48 Incomplete_count = 2
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Sep  4  6:10:48 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          632         632         627         99
              RBBM        96          96          96         100
+++++
              TOTAL        728         728         723         99
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buffer_a		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
2	2d_indr_buffer_b		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
3	2d_indr_buffer_c		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
4	2d_indr_buffer_d		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
5	2d_indr_buffer_e		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
6	2d_indr_buffer_f		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
7	2d_indr_buffer_g		2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp		
8	2d_indr_buffer_random		2002Sep20
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep20
PASS			
0	3d_draw_indx_2_b		2002Sep20
PASS			
0	bitblt_multi		2002Sep20
PASS			
0	brush_32x1mono		2002Sep20
PASS			
0	brush_8x8color_16		2002Sep20
PASS			
0	brush_8x8color_32		2002Sep20
PASS			
0	brush_8x8color_64		2002Sep20
PASS			
0	brush_8x8color_dst_0_1		2002Sep20
PASS			
0	brush_8x8mono		2002Sep20
PASS			
0	brush_color_mono_stress		2002Sep20
PASS			
0	brush_flag		2002Sep20
PASS			
20	brush_type0		2002Sep20

```

**FAIL**      cp_mh_req.cmp
21  brush_type1                                2002Sep20
**FAIL**      cp_mh_req.cmp
22  brush_type10                               2002Sep20
**FAIL**      cp_mh_req.cmp
0   brush_type13                               2002Sep20
PASS
0   brush_type14                               2002Sep20
PASS
25  brush_type6                               2002Sep20
**FAIL**      cp_mh_req.cmp
0   brush_type7                               2002Sep20
PASS
0   brush_type_other                          2002Sep20
PASS
0   brush_types_13_14                        2002Sep20
PASS
0   clear_const_wr_en_on_me_init              2002Sep20
PASS
0   clear_const_wr_en_with_lcc                2002Sep20
PASS
0   cond_write_mem_poll_mem                  2002Sep20
PASS
0   cond_write_mem_poll_mem2                 2002Sep20
PASS
0   cond_write_mem_poll_mem3                 2002Sep20
PASS
0   cond_write_mem_poll_mem_rt               2002Sep20
PASS
0   cond_write_mem_poll_reg                  2002Sep20
PASS
0   cond_write_mem_poll_reg2                 2002Sep20
PASS
0   cond_write_mem_poll_reg3                 2002Sep20
PASS
38  cp_acpi_recovery_soft_reset              2002Sep20
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   cp_bios_scratch                          2002Sep20
PASS
0   cp_ib2_base000_bufszXXX                   2002Sep20
PASS
0   cp_ib2_base001_bufszXXX                   2002Sep20
PASS
0   cp_ib2_base010_bufszXXX                   2002Sep20
PASS
0   cp_ib2_base011_bufszXXX                   2002Sep20
PASS
0   cp_ib2_base100_bufszXXX                   2002Sep20

```

PASS		
0	cp_ib2_base101_bufszXXX	2002Sep20
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep20
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep20
PASS		
0	cp_ib_base000_bufszXXX	2002Sep20
PASS		
0	cp_ib_base001_bufszXXX	2002Sep20
PASS		
0	cp_ib_base010_bufszXXX	2002Sep20
PASS		
0	cp_ib_base011_bufszXXX	2002Sep20
PASS		
0	cp_ib_base100_bufszXXX	2002Sep20
PASS		
0	cp_ib_base101_bufszXXX	2002Sep20
PASS		
0	cp_ib_base110_bufszXXX	2002Sep20
PASS		
0	cp_ib_base111_bufszXXX	2002Sep20
PASS		
0	cp_interrupt	2002Sep20
PASS		
0	cp_interrupt_rt_events_16	2002Sep20
PASS		
0	cp_iq2_alignment_000	2002Sep20
PASS		
0	cp_iq2_alignment_001	2002Sep20
PASS		
0	cp_iq2_alignment_010	2002Sep20
PASS		
0	cp_iq2_alignment_011	2002Sep20
PASS		
0	cp_iq2_alignment_100	2002Sep20
PASS		
0	cp_iq2_alignment_101	2002Sep20
PASS		
0	cp_iq2_alignment_110	2002Sep20
PASS		
0	cp_iq2_alignment_111	2002Sep20
PASS		
0	cp_iq_alignment_000	2002Sep20
PASS		
0	cp_iq_alignment_001	2002Sep20
PASS		
0	cp_iq_alignment_010	2002Sep20

PASS		
0	cp_iq_alignment_011	2002Sep20
PASS		
0	cp_iq_alignment_100	2002Sep20
PASS		
0	cp_iq_alignment_101	2002Sep20
PASS		
0	cp_iq_alignment_110	2002Sep20
PASS		
0	cp_iq_alignment_111	2002Sep20
PASS		
0	cp_max_size_packet	2002Sep20
PASS		
75	cp_max_size_packet_random	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep20
PASS		
0	cp_output_stress	2002Sep20
PASS		
0	cp_output_stress_random	2002Sep20
PASS		
0	cp_protection_mode	2002Sep20
PASS		
0	cp_rb_alignment_000	2002Sep20
PASS		
0	cp_rb_alignment_001	2002Sep20
PASS		
0	cp_rb_alignment_010	2002Sep20
PASS		
0	cp_rb_alignment_011	2002Sep20
PASS		
0	cp_rb_alignment_100	2002Sep20
PASS		
0	cp_rb_alignment_101	2002Sep20
PASS		
0	cp_rb_alignment_110	2002Sep20
PASS		
0	cp_rb_alignment_111	2002Sep20
PASS		
0	cp_rb_wptrXXX_base000	2002Sep20
PASS		
0	cp_rb_wptrXXX_base001	2002Sep20
PASS		
0	cp_rb_wptrXXX_base010	2002Sep20
PASS		
0	cp_rb_wptrXXX_base011	2002Sep20
PASS		
0	cp_rb_wptrXXX_base100	2002Sep20

PASS		
0	cp_rb_wptrXXX_base101	2002Sep20
PASS		
0	cp_rb_wptrXXX_base110	2002Sep20
PASS		
0	cp_rb_wptrXXX_base111	2002Sep20
PASS		
0	cp_register_reads	2002Sep20
PASS		
97	cp_reg_rw_test	2002Sep20
FAIL		
0	cp_rt_protection_mode	2002Sep20
PASS		
0	cp_scratch	2002Sep20
PASS		
0	cp_scratch_multi	2002Sep20
PASS		
0	dma_cp_sync	2002Sep20
PASS		
0	dma_dscrstr_chckn_bit	2002Sep20
PASS		
0	dma_dscrstr_flush1	2002Sep20
PASS		
0	dma_dscrstr_flush2	2002Sep20
PASS		
0	dma_dscrstr_flush3	2002Sep20
PASS		
0	dma_m2m_src00_dstXX	2002Sep20
PASS		
0	dma_m2m_src01_dstXX	2002Sep20
PASS		
0	dma_m2m_src10_dstXX	2002Sep20
PASS		
0	dma_m2m_src11_dstXX	2002Sep20
PASS		
0	dma_mem2mem	2002Sep20
PASS		
0	dma_mem2mem_abort	2002Sep20
PASS		
0	dma_mem2mem_alignments	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep20

PASS		
0	dma_mem2mem_byte_alignment4	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep20
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep20
PASS		
0	dma_mem2mem_pio_abort	2002Sep20
PASS		
0	dma_mem2mem_random	2002Sep20
PASS		
0	dma_mem2reg	2002Sep20
PASS		
0	dma_mem2reg_abort	2002Sep20
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep20
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep20
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep20
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep20
PASS		
0	dma_mem2reg_sync	2002Sep20
PASS		
0	dma_r2r_m2m_r2r	2002Sep20
PASS		
0	dma_r2r_m2r_r2r	2002Sep20
PASS		
0	dma_r2r_r2m_r2r	2002Sep20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep20
PASS		
0	dma_reg2mem	2002Sep20
PASS		
0	dma_reg2mem_abort	2002Sep20
PASS		
0	dma_reg2reg	2002Sep20
PASS		
0	dma_reg2reg_2dw	2002Sep20
PASS		
0	dma_reg2reg_daic	2002Sep20

PASS		
0	dma_reg2reg_mult	2002Sep20
PASS		
0	dma_reg2reg_mult2	2002Sep20
PASS		
0	dma_reg2reg_mult3	2002Sep20
PASS		
0	dma_reg2reg_saic	2002Sep20
PASS		
0	dma_reg2reg_sdaic	2002Sep20
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep20
PASS		
0	dma_reg2reg_stress	2002Sep20
PASS		
0	draw_indx_a	2002Sep20
PASS		
0	draw_indx_b	2002Sep20
PASS		
0	draw_indx_c	2002Sep20
PASS		
0	draw_indx_indr1_a	2002Sep20
PASS		
0	draw_indx_indr1_b	2002Sep20
PASS		
0	draw_indx_indr1_short_a	2002Sep20
PASS		
0	draw_indx_indr1_short_b	2002Sep20
PASS		
0	draw_indx_indr1_stress	2002Sep20
PASS		
0	draw_indx_indr2_a	2002Sep20
PASS		
0	draw_indx_indr2_b	2002Sep20
PASS		
0	draw_indx_indr2_short_a	2002Sep20
PASS		
0	draw_indx_indr2_short_b	2002Sep20
PASS		
0	draw_indx_indr2_stress	2002Sep20
PASS		
0	draw_indx_ring1	2002Sep20
PASS		
0	draw_indx_ring2	2002Sep20
PASS		
0	draw_indx_ring_short1	2002Sep20
PASS		
0	draw_indx_ring_short2	2002Sep20

PASS		
0	draw_indx_ring_stress	2002Sep20
PASS		
0	draw_indx_short_a	2002Sep20
PASS		
0	draw_indx_short_b	2002Sep20
PASS		
0	draw_indx_short_c	2002Sep20
PASS		
0	draw_indx_stress_a	2002Sep20
PASS		
0	draw_indx_stress_b	2002Sep20
PASS		
0	draw_indx_stress_c	2002Sep20
PASS		
172	event_timestamp_write	2002Sep20
FAIL		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep20
PASS		
0	ft_indr1_july22	2002Sep20
PASS		
0	ft_indr2_july22	2002Sep20
PASS		
0	ft_july22	2002Sep20
PASS		
177	ft_july22_random	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	header_dump	2002Sep20
PASS		
0	header_dump_rt	2002Sep20
PASS		
0	header_dump_small	2002Sep20
PASS		
0	hostdata_blt	2002Sep20
PASS		
0	im_load_a	2002Sep20
PASS		
0	im_load_b	2002Sep20
PASS		
0	im_load_cid_mismatch	2002Sep20
PASS		
0	im_load_immediate	2002Sep20
PASS		
0	im_load_immediate_nrt_me	2002Sep20
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep20
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep20

PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep20
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep20
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep20
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep20
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep20
PASS		
0	im_load_indr1_a	2002Sep20
PASS		
0	im_load_indr1_b	2002Sep20
PASS		
0	im_load_indr2_a	2002Sep20
PASS		
0	im_load_indr2_b	2002Sep20
PASS		
0	im_load_invalidate	2002Sep20
PASS		
0	im_load_match	2002Sep20
PASS		
0	im_load_match2	2002Sep20
PASS		
0	im_load_nrt_me	2002Sep20
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep20
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep20
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep20
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep20
PASS		
0	im_load_pfmacth_disable	2002Sep20
PASS		
0	im_load_ring_a	2002Sep20
PASS		
0	im_load_ring_b	2002Sep20
PASS		
0	im_load_rt	2002Sep20
PASS		
0	im_load_rt2	2002Sep20
PASS		
0	im_load_rt3	2002Sep20
PASS		
0	im_load_rt4	2002Sep20

PASS		
0	im_load_sz_mismatch	2002Sep20
PASS		
0	incremental_update_const	2002Sep20
PASS		
215	incremental_update_instr	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	incremental_update_state	2002Sep20
PASS		
0	indirect_buffer_a	2002Sep20
PASS		
0	indirect_buffer_b	2002Sep20
PASS		
0	indirect_buffer_c	2002Sep20
PASS		
0	indirect_buffer_d	2002Sep20
PASS		
0	indirect_buffer_e	2002Sep20
PASS		
0	indirect_buffer_f	2002Sep20
PASS		
0	indirect_buffer_g	2002Sep20
PASS		
0	indirect_buffer_h	2002Sep20
PASS		
0	indirect_buffer_i	2002Sep20
PASS		
0	indirect_buffer_j	2002Sep20
PASS		
0	indirect_buffer_k	2002Sep20
PASS		
0	indirect_buffer_l	2002Sep20
PASS		
0	indirect_buffer_mp_a	2002Sep20
PASS		
0	indirect_buffer_mp_b	2002Sep20
PASS		
0	indirect_buffer_mp_c	2002Sep20
PASS		
0	indirect_buffer_mp_d	2002Sep20
PASS		
0	indirect_buffer_mp_e	2002Sep20
PASS		
0	indirect_buffer_mp_f	2002Sep20
PASS		
0	invalidate_on_me_init	2002Sep20
PASS		
0	invalidate_state_constants	2002Sep20

PASS		
0	invalidate_state_shaders	2002Sep20
PASS		
0	invalidate_state_shaders2	2002Sep20
PASS		
0	invalidate_state_sublks	2002Sep20
PASS		
0	invalidate_state_sublks2	2002Sep20
PASS		
241	invalid_opcode	2002Sep20
INCOMPLETE		
0	iq1_iq2_mult	2002Sep20
PASS		
0	iq1_iq2_push_wrap	2002Sep20
PASS		
244	iq2_csq_full	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	IQ2_Mult	2002Sep20
PASS		
0	IQ2_Wrap_Legacy	2002Sep20
PASS		
0	IQ_Wrap	2002Sep20
PASS		
0	Isync_Control	2002Sep20
PASS		
0	Isync_Control_2D_3D	2002Sep20
PASS		
0	Isync_Control_3D_2D	2002Sep20
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep20
PASS		
252	ld_cnst_cntx_coherency_cntr2	2002Sep20
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_indr1_replicate	2002Sep20
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep20
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep20
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep20
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep20
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep20
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep20
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep20

PASS		
0	ld_cnst_cntx_match	2002Sep20
PASS		
262	ld_cnst_cntx_match_valid_preserved	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	ld_cnst_cntx_mismatch_alutex	2002Sep20
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep20
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep20
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep20
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep20
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep20
PASS		
0	ld_cnst_cntx_replicate8	2002Sep20
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep20
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep20
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep20
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep20
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep20
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep20
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep20
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep20
PASS		
0	load_constant_context_rt	2002Sep20
PASS		
0	load_palette	2002Sep20
PASS		
0	lp_2d3d	2002Sep20
PASS		
0	mem_write	2002Sep20
PASS		
0	mem_write_rt	2002Sep20
PASS		
0	mpeg_index	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep20

PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep20
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep20
PASS		
0	nop_eating_disabled	2002Sep20
PASS		
0	paint_multi	2002Sep20
PASS		
0	paint_packet	2002Sep20
PASS		
0	paint_packet_gui_control	2002Sep20
PASS		
0	pitch_clip	2002Sep20
PASS		
0	ply_nextscan	2002Sep20
PASS		
0	rbbm_clk_active_deassert_min_max	2002Sep20
PASS		
0	rbbm_clk_active_each_pipe	2002Sep20
PASS		
0	rbbm_clk_active_for_read_error	2002Sep20
PASS		
0	rbbm_clk_active_for_read_request	2002Sep20

PASS
0 rbbm_clk_active_on_reset 2002Sep20
PASS
0 rbbm_cp_non_queued_rdwr_01 2002Sep20
PASS
0 rbbm_cp_non_queued_rdwr_02 2002Sep20
PASS
312 rbbm_cp_queued_holds_nonqueued 2002Sep20
FAIL cp_rbbm.cmp
313 rbbm_cp_queued_rdwr 2002Sep20
FAIL cp_rbbm.cmp
0 rbbm_int_nrt_gfxpipe_idle 2002Sep20
PASS
0 rbbm_int_rderr 2002Sep20
PASS
0 rbbm_isync_cpscratch_idlegui 2002Sep20
PASS
0 rbbm_isync_wait_idlegui 2002Sep20
PASS
0 rbbm_soft_reset 2002Sep20
PASS
0 rbbm_WU_2D_idle_01 2002Sep20
PASS
0 rbbm_WU_2D_idle_02 2002Sep20
PASS
0 rbbm_WU_2D_idle_03 2002Sep20
PASS
0 rbbm_WU_2D_idle_04 2002Sep20
PASS
0 rbbm_WU_2D_idle_05 2002Sep20
PASS
0 rbbm_WU_2D_idle_06 2002Sep20
PASS
0 rbbm_WU_2D_idle_07 2002Sep20
PASS
0 rbbm_WU_2D_idle_08 2002Sep20
PASS
0 rbbm_WU_2D_idle_09 2002Sep20
PASS
0 rbbm_WU_2D_idle_clean_01 2002Sep20
PASS
0 rbbm_WU_2D_idle_clean_02 2002Sep20
PASS
0 rbbm_WU_2D_idle_clean_03 2002Sep20
PASS
0 rbbm_WU_2D_idle_clean_04 2002Sep20
PASS
0 rbbm_WU_2D_idle_clean_05 2002Sep20

PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep20
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep20
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep20
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep20
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep20
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep20
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep20
PASS		
0	rbbm_WU_3D_idle_01	2002Sep20
PASS		
0	rbbm_WU_3D_idle_02	2002Sep20
PASS		
0	rbbm_WU_3D_idle_03	2002Sep20
PASS		
0	rbbm_WU_3D_idle_04	2002Sep20
PASS		
0	rbbm_WU_3D_idle_05	2002Sep20
PASS		
0	rbbm_WU_3D_idle_06	2002Sep20
PASS		
0	rbbm_WU_3D_idle_07	2002Sep20
PASS		
0	rbbm_WU_3D_idle_08	2002Sep20
PASS		
0	rbbm_WU_3D_idle_09	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep20

PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep20
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep20
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep20
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep20
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep20
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep20
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep20
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep20
PASS		
0	rbbm_WU_idct_semaphore	2002Sep20
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep20
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep20
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep20
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep20
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep20
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep20
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep20

PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep20
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep20
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep20
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep20
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep20
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep20
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep20
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep20
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep20
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep20

PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep20
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep20
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep20
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep20
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep20
PASS		
0	RB_BUFSZ_0	2002Sep20
PASS		
0	RB_BUFSZ_1	2002Sep20
PASS		
0	RB_Wrap	2002Sep20
PASS		
0	reg_to_mem	2002Sep20
PASS		
0	reg_to_mem_rt	2002Sep20
PASS		
0	rop_cfd_wmd	2002Sep20
PASS		
0	rstr_ordr	2002Sep20
PASS		
417	rtee_arm_wait_idle_trigger_events_16	2002Sep20
FAIL		
0	rtee_discretes_arm_rearm_events_32	2002Sep20
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep20

PASS		
0	rtee_discretes_falling_10_events_16	2002Sep20
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0	rtee_discretes_falling_11_events_16	2002Sep20
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0	rtee_discretes_falling_12_events_16	2002Sep20
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0	rtee_discretes_falling_13_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep20
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0	rtee_discretes_falling_18_events_16	2002Sep20
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0	rtee_discretes_falling_19_events_16	2002Sep20
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0	rtee_discretes_falling_20_events_16	2002Sep20
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0	rtee_discretes_falling_21_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep20
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep20

PASS		
0	rtee_discretes_invert_02_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep20
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0	rtee_discretes_invert_07_events_16	2002Sep20
PASS		
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PASS		
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0	rtee_discretes_invert_14_events_16	2002Sep20
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0	rtee_discretes_invert_16_events_16	2002Sep20
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0	rtee_discretes_invert_17_events_16	2002Sep20
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0	rtee_discretes_invert_18_events_16	2002Sep20
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0	rtee_discretes_invert_19_events_16	2002Sep20
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0	rtee_discretes_invert_21_events_16	2002Sep20
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0	rtee_discretes_invert_22_events_16	2002Sep20
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0	rtee_discretes_invert_23_events_16	2002Sep20
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0	rtee_discretes_invert_25_events_16	2002Sep20

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0	rtee_discretes_invert_26_events_16	2002Sep20
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0	rtee_discretes_invert_27_events_16	2002Sep20
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0	rtee_discretes_invert_29_events_16	2002Sep20
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0	rtee_discretes_invert_30_events_16	2002Sep20
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep20
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0	rtee_discretes_level_00_events_16	2002Sep20
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0	rtee_discretes_level_01_events_16	2002Sep20
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0	rtee_discretes_level_02_events_16	2002Sep20
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0	rtee_discretes_level_03_events_16	2002Sep20
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0	rtee_discretes_level_04_events_16	2002Sep20
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0	rtee_discretes_level_05_events_16	2002Sep20
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0	rtee_discretes_level_06_events_16	2002Sep20
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0	rtee_discretes_level_07_events_16	2002Sep20
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0	rtee_discretes_level_08_events_16	2002Sep20
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0	rtee_discretes_level_09_events_16	2002Sep20
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0	rtee_discretes_level_10_events_16	2002Sep20
PASS		
0	rtee_discretes_level_11_events_16	2002Sep20
PASS		
0	rtee_discretes_level_12_events_16	2002Sep20
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0	rtee_discretes_level_13_events_16	2002Sep20
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0	rtee_discretes_level_14_events_16	2002Sep20
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0	rtee_discretes_level_15_events_16	2002Sep20
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0	rtee_discretes_level_17_events_16	2002Sep20

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0	rtee_discretes_level_21_events_16	2002Sep20
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0	rtee_discretes_level_22_events_16	2002Sep20
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0	rtee_discretes_level_23_events_16	2002Sep20
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0	rtee_discretes_level_24_events_16	2002Sep20
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0	rtee_discretes_level_25_events_16	2002Sep20
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0	rtee_discretes_level_26_events_16	2002Sep20
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0	rtee_discretes_level_27_events_16	2002Sep20
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0	rtee_discretes_level_29_events_16	2002Sep20
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0	rtee_discretes_level_30_events_16	2002Sep20
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0	rtee_discretes_level_31_events_16	2002Sep20
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0	rtee_discretes_manual_disarm_events_16	2002Sep20
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0	rtee_discretes_rising_00_events_16	2002Sep20
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0	rtee_discretes_rising_01_events_16	2002Sep20
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0	rtee_discretes_rising_02_events_16	2002Sep20
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0	rtee_discretes_rising_03_events_16	2002Sep20
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0	rtee_discretes_rising_04_events_16	2002Sep20
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0	rtee_discretes_rising_05_events_16	2002Sep20
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0	rtee_discretes_rising_06_events_16	2002Sep20
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0	rtee_discretes_rising_07_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep20

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0	rtee_discretes_rising_09_events_16	2002Sep20
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0	rtee_discretes_rising_10_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep20
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep20
PASS		
0	rtee_display1_comp_events_16	2002Sep20

PASS		
0	rtee_display2_comp_events_16	2002Sep20
PASS		
0	rtee_mem_linear_search_events_2	2002Sep20
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep20
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep20
PASS		
560	rtee_mem_poll_comp_masks_events_16_random	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	rtee_mem_poll_comp_swaps_events_16	2002Sep20
PASS		
562	rtee_mem_poll_intervals_events_16	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	rtee_prefetch_parse_nops	2002Sep20
PASS		
0	rtee_reg_linear_search_events_2	2002Sep20
PASS		
565	rtee_reg_poll_comp_000_events_16	2002Sep20
FAIL		
566	rtee_reg_poll_comp_001_events_16	2002Sep20
FAIL		
567	rtee_reg_poll_comp_010_events_16	2002Sep20
FAIL		
568	rtee_reg_poll_comp_011_events_16	2002Sep20
FAIL		
569	rtee_reg_poll_comp_100_events_16	2002Sep20
FAIL		
570	rtee_reg_poll_comp_101_events_16	2002Sep20
FAIL		
571	rtee_reg_poll_comp_110_events_16	2002Sep20
FAIL		
572	rtee_reg_poll_comp_111_events_16	2002Sep20

```

**FAIL**
573  rtee_reg_poll_comp_queued_nonqueued_events_16      2002Sep20
**FAIL**
0    rtee_vip_comp_events_16                          2002Sep20
PASS
0    rt_invalid_opcode                                2002Sep20
PASS
0    scratch_mem_read                                  2002Sep20
PASS
0    scratch_mem_read2                                2002Sep20
PASS
0    set_constant                                      2002Sep20
PASS
0    set_constant2                                    2002Sep20
PASS
580  set_constant_alu_max1                             2002Sep20
**FAIL**      cp_mh_req.cmp
0    set_constant_alu_max2                             2002Sep20
PASS
0    set_constant_bool_max1                           2002Sep20
PASS
0    set_constant_bool_max2                           2002Sep20
PASS
0    set_constant_indr1_short                         2002Sep20
PASS
0    set_constant_indr1_short2                       2002Sep20
PASS
0    set_constant_indr1_write_enable                 2002Sep20
PASS
0    set_constant_indr1_write_enable_lcc             2002Sep20
PASS
0    set_constant_indr2_short                         2002Sep20
PASS
0    set_constant_indr2_short2                       2002Sep20
PASS
0    set_constant_indr2_write_enable                 2002Sep20
PASS
0    set_constant_indr2_write_enable_lcc             2002Sep20
PASS
0    set_constant_loop_max1                          2002Sep20
PASS
0    set_constant_loop_max2                          2002Sep20
PASS
0    set_constant_ring_short                         2002Sep20
PASS
0    set_constant_ring_short2                       2002Sep20
PASS
0    set_constant_ring_write_enable                 2002Sep20

```

PASS		
0	set_constant_ring_write_enable_lcc	2002Sep20
PASS		
0	set_constant_short	2002Sep20
PASS		
0	set_constant_short2	2002Sep20
PASS		
0	set_constant_tex_max1	2002Sep20
PASS		
0	set_constant_tex_max2	2002Sep20
PASS		
0	set_constant_write_enable	2002Sep20
PASS		
0	set_constant_write_enable_lcc	2002Sep20
PASS		
0	set_constant_write_enable_lcc2	2002Sep20
PASS		
0	set_scissors	2002Sep20
PASS		
606	set_state_rt_a	2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp	
607	set_state_rt_b	2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp	
608	set_state_rt_c	2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp	
609	set_state_rt_d	2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	set_st_indrl_match	2002Sep20
PASS		
0	set_st_indrl_mismatch	2002Sep20
PASS		
0	set_st_indrl_mismatch2	2002Sep20
PASS		
0	set_st_indrl_mismatch3	2002Sep20
PASS		
0	set_st_indrl_mismatch4	2002Sep20
PASS		
0	set_st_indrl_ps	2002Sep20
PASS		
0	set_st_indrl_ps_only	2002Sep20
PASS		
0	set_st_indrl_sublk0	2002Sep20
PASS		
0	set_st_indrl_sublk1	2002Sep20
PASS		
0	set_st_indrl_sublk1_only	2002Sep20
PASS		
0	set_st_indrl_sublk2	2002Sep20

PASS		
0	set_st_indr1_sublk2_only	2002Sep20
PASS		
0	set_st_indr1_sublk3	2002Sep20
PASS		
0	set_st_indr1_sublk3_only	2002Sep20
PASS		
0	set_st_indr1_sublk4	2002Sep20
PASS		
0	set_st_indr1_sublk4_only	2002Sep20
PASS		
0	set_st_indr1_sublk5	2002Sep20
PASS		
0	set_st_indr1_sublk5_only	2002Sep20
PASS		
0	set_st_indr1_sublk6	2002Sep20
PASS		
0	set_st_indr1_sublk6_only	2002Sep20
PASS		
0	set_st_indr1_sublk7	2002Sep20
PASS		
0	set_st_indr1_sublk7_only	2002Sep20
PASS		
0	set_st_indr1_vs	2002Sep20
PASS		
0	set_st_indr1_vs_only	2002Sep20
PASS		
0	set_st_indr2_match	2002Sep20
PASS		
0	set_st_indr2_mismatch	2002Sep20
PASS		
0	set_st_indr2_mismatch2	2002Sep20
PASS		
0	set_st_indr2_mismatch3	2002Sep20
PASS		
0	set_st_indr2_mismatch4	2002Sep20
PASS		
0	set_st_indr2_ps	2002Sep20
PASS		
0	set_st_indr2_ps_only	2002Sep20
PASS		
0	set_st_indr2_sublk0	2002Sep20
PASS		
0	set_st_indr2_sublk1	2002Sep20
PASS		
0	set_st_indr2_sublk1_only	2002Sep20
PASS		
0	set_st_indr2_sublk2	2002Sep20

PASS		
0	set_st_indr2_sublk2_only	2002Sep20
PASS		
0	set_st_indr2_sublk3	2002Sep20
PASS		
0	set_st_indr2_sublk3_only	2002Sep20
PASS		
0	set_st_indr2_sublk4	2002Sep20
PASS		
0	set_st_indr2_sublk4_only	2002Sep20
PASS		
0	set_st_indr2_sublk5	2002Sep20
PASS		
0	set_st_indr2_sublk5_only	2002Sep20
PASS		
0	set_st_indr2_sublk6	2002Sep20
PASS		
0	set_st_indr2_sublk6_only	2002Sep20
PASS		
0	set_st_indr2_sublk7	2002Sep20
PASS		
0	set_st_indr2_sublk7_only	2002Sep20
PASS		
0	set_st_indr2_vs	2002Sep20
PASS		
0	set_st_indr2_vs_only	2002Sep20
PASS		
0	set_st_pfmatch_disabled	2002Sep20
PASS		
0	set_st_ring_match	2002Sep20
PASS		
0	set_st_ring_mismatch	2002Sep20
PASS		
0	set_st_ring_mismatch2	2002Sep20
PASS		
0	set_st_ring_mismatch3	2002Sep20
PASS		
0	set_st_ring_mismatch4	2002Sep20
PASS		
0	set_st_ring_ps	2002Sep20
PASS		
0	set_st_ring_ps_only	2002Sep20
PASS		
0	set_st_ring_sublk0	2002Sep20
PASS		
0	set_st_ring_sublk1	2002Sep20
PASS		
0	set_st_ring_sublk1_only	2002Sep20

PASS		
0	set_st_ring_sublk2	2002Sep20
PASS		
0	set_st_ring_sublk2_only	2002Sep20
PASS		
0	set_st_ring_sublk3	2002Sep20
PASS		
0	set_st_ring_sublk3_only	2002Sep20
PASS		
0	set_st_ring_sublk4	2002Sep20
PASS		
0	set_st_ring_sublk4_only	2002Sep20
PASS		
0	set_st_ring_sublk5	2002Sep20
PASS		
0	set_st_ring_sublk5_only	2002Sep20
PASS		
0	set_st_ring_sublk6	2002Sep20
PASS		
0	set_st_ring_sublk6_only	2002Sep20
PASS		
0	set_st_ring_sublk7	2002Sep20
PASS		
0	set_st_ring_sublk7_only	2002Sep20
PASS		
0	set_st_ring_vs	2002Sep20
PASS		
0	set_st_ring_vs_only	2002Sep20
PASS		
0	set_st_stress_a	2002Sep20
PASS		
684	set_st_stress_b	2002Sep20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Sep20
PASS		
0	set_st_stress_d	2002Sep20
PASS		
0	set_st_stress_e	2002Sep20
PASS		
0	set_st_stress_f	2002Sep20
PASS		
0	Small_Text_BM	2002Sep20
PASS		
0	smoke_nrt	2002Sep20
PASS		
0	src_clip	2002Sep20
PASS		
0	srf_cohr	2002Sep20

PASS		
0	trans_bitblt	2002Sep20
PASS		
694	type0_2long_pckts	2002Sep20
FAIL	cp_mh_req.cmp	
0	type0_3indr_buffs	2002Sep20
PASS		
0	type0_4pckts	2002Sep20
PASS		
0	type0_incr_updt_cnst	2002Sep20
PASS		
0	type0_incr_updt_instrmem	2002Sep20
PASS		
0	type0_incr_updt_st	2002Sep20
PASS		
0	type0_incr_updt_stress	2002Sep20
PASS		
0	type0_ind1_ind2_a	2002Sep20
PASS		
0	type0_ind1_ind2_b	2002Sep20
PASS		
0	type0_indr_buff	2002Sep20
PASS		
0	Type0_IQ2_BM	2002Sep20
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep20
PASS		
0	Type0_IQ_BM	2002Sep20
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep20
PASS		
0	Type0_PQ_BM	2002Sep20
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep20
PASS		
0	type1_4pckts	2002Sep20
PASS		
0	type1_indr_buff	2002Sep20
PASS		
0	type1_pckt	2002Sep20
PASS		
0	Type3_Bitblt	2002Sep20
PASS		
0	Type3_Load_Palette	2002Sep20
PASS		
0	Type3_NOP	2002Sep20
PASS		
0	Type3_Polyline	2002Sep20

PASS		
0	Type3_Polyline_Large	2002Sep20
PASS		
0	type_random	2002Sep20
PASS		
719	uram_wrt_rt_ucode	2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp	
720	uram_wrt_ucode	2002Sep20
FAIL	rbbmWr.cmp	
721	viz_query_a	2002Sep20
FAIL	rbbmWr.cmp	
722	viz_query_draw_indx	2002Sep20
FAIL	rbbmWr.cmp	
723	viz_query_draw_indx_indr1	2002Sep20
FAIL	rbbmWr.cmp	
724	viz_query_draw_indx_indr2	2002Sep20
FAIL	rbbmWr.cmp	
725	viz_query_draw_indx_multipass	2002Sep20
FAIL	cp_rbbm.cmp rbbmWr.cmp	
726	viz_query_draw_indx_ring	2002Sep20
FAIL	rbbmWr.cmp	
727	viz_query_indr1	2002Sep20
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep20
PASS		
729	viz_query_indr2	2002Sep20
FAIL	rbbmWr.cmp	
0	viz_query_indr2_simple	2002Sep20
PASS		
731	viz_query_ring	2002Sep20
FAIL	rbbmWr.cmp	
0	viz_query_ring_simple	2002Sep20
PASS		
0	wait_for_idle	2002Sep20
PASS		
0	wait_reg_mem_poll_mem0	2002Sep20
PASS		
0	wait_reg_mem_poll_mem0_rt	2002Sep20
PASS		
0	wait_reg_mem_poll_mem1	2002Sep20
PASS		
0	wait_reg_mem_poll_mem2	2002Sep20
PASS		
0	wait_reg_mem_poll_mem3	2002Sep20
PASS		
0	wait_reg_mem_poll_reg0	2002Sep20
PASS		
0	wait_reg_mem_poll_reg1	2002Sep20

PASS

0 wait_reg_mem_poll_reg2

2002Sep20

PASS

0 wait_reg_mem_poll_reg3

2002Sep20

PASS

=====

Tests_Run_count = 742 Pass_count = 688 Fail_count = 53 Incomplete_count = 1

=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Mon Aug 12 16:34:42 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC                0          0          0          0
              CL                0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU                0          0          0          0
              CP             538        538        484         89
              RBBM           21         21         21          0
+++++
              TOTAL           538        538        484         89
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Aug 14  3:59:33 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC               0          0          0          0
              CL               0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU               0          0          0          0
              CP             564        564        520         92
              RBBM           21         21         21        100
+++++
              TOTAL           585        585        541         92
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Oct6
	PASS		
0	2d_indr_buffer_b		2002Oct6
	PASS		
0	2d_indr_buffer_c		2002Oct6
	PASS		
0	2d_indr_buffer_d		2002Oct6
	PASS		
0	2d_indr_buffer_e		2002Oct6
	PASS		
0	2d_indr_buffer_f		2002Oct6
	PASS		
0	2d_indr_buffer_g		2002Oct6
	PASS		
0	2d_indr_buffer_random		2002Oct6
	PASS		
9	2d_indr_buffer_random2		2002Sep29
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Oct6
	PASS		
0	3d_draw_indx_2_b		2002Oct6
	PASS		
0	IQ2_Mult		2002Oct6
	PASS		
0	IQ2_Wrap_Legacy		2002Oct6
	PASS		
0	IQ_Wrap		2002Oct6
	PASS		
0	Isync_Control		2002Oct6
	PASS		
0	Isync_Control_2D_3D		2002Oct6
	PASS		
0	Isync_Control_3D_2D		2002Oct6
	PASS		
0	RB_BUFSZ_0		2002Oct6
	PASS		
0	RB_BUFSZ_1		2002Oct6
	PASS		
0	RB_Wrap		2002Oct6

PASS		
0	Small_Text_BM	20020ct6
PASS		
0	Type0_IQ2_BM	20020ct6
PASS		
0	Type0_IQ2_BM_IB_Packet	20020ct6
PASS		
0	Type0_IQ_BM	20020ct6
PASS		
0	Type0_IQ_BM_IB_Packet	20020ct6
PASS		
0	Type0_PQ_BM	20020ct6
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	20020ct6
PASS		
0	Type3_Bitblt	20020ct6
PASS		
0	Type3_Hostdata_blt	20020ct6
PASS		
0	Type3_Hostdata_blt_PIO	20020ct6
PASS		
0	Type3_Load_Palette	20020ct6
PASS		
0	Type3_NOP	20020ct6
PASS		
0	Type3_Polyline	20020ct6
PASS		
0	Type3_Polyline_Large	20020ct6
PASS		
0	bitblt_multi	20020ct6
PASS		
0	brush_32x1mono	20020ct6
PASS		
0	brush_8x8color_16	20020ct6
PASS		
0	brush_8x8color_32	20020ct6
PASS		
0	brush_8x8color_64	20020ct6
PASS		
0	brush_8x8color_dst_0_1	20020ct6
PASS		
0	brush_8x8mono	20020ct6
PASS		
0	brush_color_mono_stress	20020ct6
PASS		
0	brush_flag	20020ct6
PASS		
0	brush_type0	20020ct6

PASS		
0	brush_type1	20020ct6
PASS		
0	brush_type10	20020ct6
PASS		
0	brush_type13	20020ct6
PASS		
0	brush_type14	20020ct6
PASS		
0	brush_type6	20020ct6
PASS		
0	brush_type7	20020ct6
PASS		
0	brush_type_other	20020ct6
PASS		
0	brush_types_13_14	20020ct6
PASS		
0	change_queue_sizes	20020ct6
PASS		
0	clear_const_wr_en_on_me_init	20020ct6
PASS		
0	clear_const_wr_en_with_lcc	20020ct6
PASS		
0	cond_write_mem_poll_mem	20020ct6
PASS		
0	cond_write_mem_poll_mem2	20020ct6
PASS		
0	cond_write_mem_poll_mem3	20020ct6
PASS		
0	cond_write_mem_poll_mem_rt	20020ct6
PASS		
0	cond_write_mem_poll_reg	20020ct6
PASS		
0	cond_write_mem_poll_reg2	20020ct6
PASS		
0	cond_write_mem_poll_reg3	20020ct6
PASS		
0	cp_acpi_recovery_soft_reset	20020ct6
PASS		
0	cp_bios_scratch	20020ct6
PASS		
0	cp_high_stress	20020ct6
PASS		
0	cp_ib2_base000_bufszXXX	20020ct6
PASS		
0	cp_ib2_base001_bufszXXX	20020ct6
PASS		
0	cp_ib2_base010_bufszXXX	20020ct6

PASS		
0	cp_ib2_base011_bufszXXX	20020ct6
PASS		
0	cp_ib2_base100_bufszXXX	20020ct6
PASS		
0	cp_ib2_base101_bufszXXX	20020ct6
PASS		
0	cp_ib2_base110_bufszXXX	20020ct6
PASS		
0	cp_ib2_base111_bufszXXX	20020ct6
PASS		
0	cp_ib_base000_bufszXXX	20020ct6
PASS		
0	cp_ib_base001_bufszXXX	20020ct6
PASS		
0	cp_ib_base010_bufszXXX	20020ct6
PASS		
0	cp_ib_base011_bufszXXX	20020ct6
PASS		
0	cp_ib_base100_bufszXXX	20020ct6
PASS		
0	cp_ib_base101_bufszXXX	20020ct6
PASS		
0	cp_ib_base110_bufszXXX	20020ct6
PASS		
0	cp_ib_base111_bufszXXX	20020ct6
PASS		
0	cp_interrupt	20020ct6
PASS		
0	cp_interrupt_rt_events_16	20020ct6
PASS		
0	cp_iq2_alignment_000	20020ct6
PASS		
0	cp_iq2_alignment_001	20020ct6
PASS		
0	cp_iq2_alignment_010	20020ct6
PASS		
0	cp_iq2_alignment_011	20020ct6
PASS		
0	cp_iq2_alignment_100	20020ct6
PASS		
0	cp_iq2_alignment_101	20020ct6
PASS		
0	cp_iq2_alignment_110	20020ct6
PASS		
0	cp_iq2_alignment_111	20020ct6
PASS		
0	cp_iq_alignment_000	20020ct6

PASS		
0	cp_iq_alignment_001	20020ct6
PASS		
0	cp_iq_alignment_010	20020ct6
PASS		
0	cp_iq_alignment_011	20020ct6
PASS		
0	cp_iq_alignment_100	20020ct6
PASS		
0	cp_iq_alignment_101	20020ct6
PASS		
0	cp_iq_alignment_110	20020ct6
PASS		
0	cp_iq_alignment_111	20020ct6
PASS		
0	cp_max_size_packet	20020ct6
PASS		
0	cp_max_size_packet_random	20020ct6
PASS		
0	cp_nrt_rt_protection_mode	20020ct6
PASS		
0	cp_output_stress	20020ct6
PASS		
0	cp_output_stress_random	20020ct6
PASS		
0	cp_protection_mode	20020ct6
PASS		
0	cp_rb_alignment_000	20020ct6
PASS		
0	cp_rb_alignment_001	20020ct6
PASS		
0	cp_rb_alignment_010	20020ct6
PASS		
0	cp_rb_alignment_011	20020ct6
PASS		
0	cp_rb_alignment_100	20020ct6
PASS		
0	cp_rb_alignment_101	20020ct6
PASS		
0	cp_rb_alignment_110	20020ct6
PASS		
0	cp_rb_alignment_111	20020ct6
PASS		
0	cp_rb_wptrXXX_base000	20020ct6
PASS		
0	cp_rb_wptrXXX_base001	20020ct6
PASS		
0	cp_rb_wptrXXX_base010	20020ct6

PASS		
0	cp_rb_wptrXXX_base011	20020ct6
PASS		
0	cp_rb_wptrXXX_base100	20020ct6
PASS		
0	cp_rb_wptrXXX_base101	20020ct6
PASS		
0	cp_rb_wptrXXX_base110	20020ct6
PASS		
0	cp_rb_wptrXXX_base111	20020ct6
PASS		
0	cp_reg_rw_test	20020ct6
PASS		
0	cp_register_reads	20020ct6
PASS		
0	cp_rt_protection_mode	20020ct6
PASS		
0	cp_scratch	20020ct6
PASS		
0	cp_scratch_multi	20020ct6
PASS		
0	csf_reg_rd_default	20020ct6
PASS		
128	csf_reg_rd_only	20020ct6
FAIL		
0	csf_reg_wr_rd	20020ct6
PASS		
0	dma_cp_sync	20020ct6
PASS		
0	dma_dscrstr_chckn_bit	20020ct6
PASS		
0	dma_dscrstr_flush1	20020ct6
PASS		
0	dma_dscrstr_flush2	20020ct6
PASS		
0	dma_dscrstr_flush3	20020ct6
PASS		
0	dma_m2m_src00_dstXX	20020ct6
PASS		
0	dma_m2m_src01_dstXX	20020ct6
PASS		
0	dma_m2m_src10_dstXX	20020ct6
PASS		
0	dma_m2m_src11_dstXX	20020ct6
PASS		
0	dma_mem2mem	20020ct6
PASS		
0	dma_mem2mem_abort	20020ct6

PASS		
0	dma_mem2mem_alignments	20020ct6
PASS		
0	dma_mem2mem_byte_alignment0	20020ct6
PASS		
0	dma_mem2mem_byte_alignment1	20020ct6
PASS		
0	dma_mem2mem_byte_alignment2	20020ct6
PASS		
0	dma_mem2mem_byte_alignment3	20020ct6
PASS		
0	dma_mem2mem_byte_alignment4	20020ct6
PASS		
0	dma_mem2mem_byte_alignment5	20020ct6
PASS		
0	dma_mem2mem_byte_alignment6	20020ct6
PASS		
0	dma_mem2mem_byte_alignment7	20020ct6
PASS		
0	dma_mem2mem_large	20020ct6
PASS		
0	dma_mem2mem_pio_abort	20020ct6
PASS		
0	dma_mem2mem_random	20020ct6
PASS		
0	dma_mem2reg	20020ct6
PASS		
0	dma_mem2reg_abort	20020ct6
PASS		
0	dma_mem2reg_dtaq_stress	20020ct6
PASS		
0	dma_mem2reg_dtaq_stress_random	20020ct6
PASS		
0	dma_mem2reg_pio_stressbm	20020ct6
PASS		
0	dma_mem2reg_pio_stresspio	20020ct6
PASS		
0	dma_mem2reg_sync	20020ct6
PASS		
0	dma_r2r_m2m_r2r	20020ct6
PASS		
0	dma_r2r_m2r_r2r	20020ct6
PASS		
0	dma_r2r_r2m_r2r	20020ct6
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	20020ct6
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	20020ct6

PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	20020ct6
PASS		
0	dma_reg2mem	20020ct6
PASS		
0	dma_reg2mem_abort	20020ct6
PASS		
0	dma_reg2reg	20020ct6
PASS		
0	dma_reg2reg_2dw	20020ct6
PASS		
0	dma_reg2reg_daic	20020ct6
PASS		
0	dma_reg2reg_mult	20020ct6
PASS		
0	dma_reg2reg_mult2	20020ct6
PASS		
0	dma_reg2reg_mult3	20020ct6
PASS		
0	dma_reg2reg_saic	20020ct6
PASS		
0	dma_reg2reg_sdaic	20020ct6
PASS		
0	dma_reg2reg_sdaic_stress	20020ct6
PASS		
0	dma_reg2reg_stress	20020ct6
PASS		
0	draw_indx_a	20020ct6
PASS		
0	draw_indx_b	20020ct6
PASS		
0	draw_indx_c	20020ct6
PASS		
0	draw_indx_indr1_a	20020ct6
PASS		
0	draw_indx_indr1_b	20020ct6
PASS		
0	draw_indx_indr1_short_a	20020ct6
PASS		
0	draw_indx_indr1_short_b	20020ct6
PASS		
0	draw_indx_indr1_stress	20020ct6
PASS		
0	draw_indx_indr2_a	20020ct6
PASS		
0	draw_indx_indr2_b	20020ct6
PASS		
0	draw_indx_indr2_short_a	20020ct6

PASS		
0	draw_indx_indr2_short_b	20020ct6
PASS		
0	draw_indx_indr2_stress	20020ct6
PASS		
0	draw_indx_random_a	20020ct6
PASS		
0	draw_indx_random_b	20020ct6
PASS		
0	draw_indx_random_c	20020ct6
PASS		
0	draw_indx_ring1	20020ct6
PASS		
0	draw_indx_ring2	20020ct6
PASS		
0	draw_indx_ring_short1	20020ct6
PASS		
0	draw_indx_ring_short2	20020ct6
PASS		
0	draw_indx_ring_stress	20020ct6
PASS		
0	draw_indx_short_a	20020ct6
PASS		
0	draw_indx_short_b	20020ct6
PASS		
0	draw_indx_short_c	20020ct6
PASS		
0	draw_indx_stress_a	20020ct6
PASS		
0	draw_indx_stress_b	20020ct6
PASS		
0	draw_indx_stress_c	20020ct6
PASS		
0	event_timestamp_write	20020ct6
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	20020ct6
PASS		
0	fix2flt_reg	20020ct6
PASS		
0	ft_indr1_july22	20020ct6
PASS		
0	ft_indr2_july22	20020ct6
PASS		
0	ft_july22	20020ct6
PASS		
0	ft_july22_random	20020ct6
PASS		
0	header_dump	20020ct6

PASS		
0	header_dump_rt	2002Oct6
PASS		
0	header_dump_small	2002Oct6
PASS		
0	hostdata_blt	2002Oct6
PASS		
0	im_load_a	2002Oct6
PASS		
0	im_load_b	2002Oct6
PASS		
0	im_load_cid_mismatch	2002Oct6
PASS		
0	im_load_immediate	2002Oct6
PASS		
0	im_load_immediate_nrt_me	2002Oct6
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Oct6
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Oct6
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Oct6
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Oct6
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Oct6
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Oct6
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Oct6
PASS		
0	im_load_indr1_a	2002Oct6
PASS		
0	im_load_indr1_b	2002Oct6
PASS		
0	im_load_indr2_a	2002Oct6
PASS		
0	im_load_indr2_b	2002Oct6
PASS		
0	im_load_invalidate	2002Oct6
PASS		
0	im_load_match	2002Oct6
PASS		
0	im_load_match2	2002Oct6
PASS		
0	im_load_nrt_me	2002Oct6
PASS		
0	im_load_nrt_me_ps_nowrap	2002Oct6

PASS		
0	im_load_nrt_me_ps_wrap	2002Oct6
PASS		
0	im_load_nrt_me_vs_nowrap	2002Oct6
PASS		
0	im_load_nrt_me_vs_wrap	2002Oct6
PASS		
0	im_load_pfmatch_disable	2002Oct6
PASS		
241	im_load_random_a	2002Oct6
FAIL	mh_cp_rd.cmp	
242	im_load_random_b	2002Oct6
FAIL	mh_cp_rd.cmp	
243	im_load_random_c	2002Sep29
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Oct6
PASS		
0	im_load_ring_b	2002Oct6
PASS		
0	im_load_rt	2002Oct6
PASS		
0	im_load_rt2	2002Oct6
PASS		
0	im_load_rt3	2002Oct6
PASS		
0	im_load_rt4	2002Oct6
PASS		
0	im_load_sz_mismatch	2002Oct6
PASS		
0	incremental_update_const	2002Oct6
PASS		
0	incremental_update_instr	2002Oct6
PASS		
0	incremental_update_state	2002Oct6
PASS		
0	indirect_buffer_a	2002Oct6
PASS		
0	indirect_buffer_b	2002Oct6
PASS		
0	indirect_buffer_c	2002Oct6
PASS		
0	indirect_buffer_d	2002Oct6
PASS		
0	indirect_buffer_e	2002Oct6
PASS		
0	indirect_buffer_f	2002Oct6
PASS		
0	indirect_buffer_g	2002Oct6

PASS		
0	indirect_buffer_h	20020ct6
PASS		
0	indirect_buffer_i	20020ct6
PASS		
0	indirect_buffer_j	20020ct6
PASS		
0	indirect_buffer_k	20020ct6
PASS		
0	indirect_buffer_l	20020ct6
PASS		
0	indirect_buffer_mp_a	20020ct6
PASS		
0	indirect_buffer_mp_b	20020ct6
PASS		
0	indirect_buffer_mp_c	20020ct6
PASS		
0	indirect_buffer_mp_d	20020ct6
PASS		
0	indirect_buffer_mp_e	20020ct6
PASS		
0	indirect_buffer_mp_f	20020ct6
PASS		
272	indirect_buffer_mp_random1	20020ct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
273	indirect_buffer_mp_random2	20020ct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
274	indirect_buffer_random1	20020ct6
FAIL	mh_cp_rd.cmp	
275	indirect_buffer_random2	20020ct6
FAIL	mh_cp_rd.cmp	
0	invalid_opcode	20020ct6
PASS		
0	invalidate_on_me_init	20020ct6
PASS		
0	invalidate_state_constants	20020ct6
PASS		
0	invalidate_state_shaders	20020ct6
PASS		
0	invalidate_state_shaders2	20020ct6
PASS		
0	invalidate_state_sublks	20020ct6
PASS		
0	invalidate_state_sublks2	20020ct6
PASS		
0	iq1_iq2_mult	20020ct6
PASS		
0	iq1_iq2_push_wrap	20020ct6

PASS		
0	iq2_csq_full	2002Oct6
PASS		
286	lcc_coherency_cntr_random	2002Sep29
FAIL	cp_mh_req.cmp cp_rbbm.cmp	
0	lcc_match_random	2002Oct6
PASS		
0	lcc_mismatch_alutex_random	2002Oct6
PASS		
0	lcc_mismatch_base_random	2002Oct6
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Oct6
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Oct6
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Oct6
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Oct6
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Oct6
PASS		
0	ld_cnst_cntx_indr1_simple	2002Oct6
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Oct6
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Oct6
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Oct6
PASS		
0	ld_cnst_cntx_indr2_simple	2002Oct6
PASS		
0	ld_cnst_cntx_match	2002Oct6
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Oct6
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Oct6
PASS		
0	ld_cnst_cntx_mismatch_base	2002Oct6
PASS		
0	ld_cnst_cntx_mismatch_force	2002Oct6
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Oct6
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Oct6
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Oct6
PASS		
0	ld_cnst_cntx_replicate8	2002Oct6

PASS		
0	ld_cnst_cntx_replicate_alu	20020ct6
PASS		
0	ld_cnst_cntx_replicate_tex	20020ct6
PASS		
0	ld_cnst_cntx_ring_replicate	20020ct6
PASS		
0	ld_cnst_cntx_ring_replicate4	20020ct6
PASS		
0	ld_cnst_cntx_ring_replicate8	20020ct6
PASS		
0	ld_cnst_cntx_ring_simple	20020ct6
PASS		
0	ld_cnst_cntx_simple_alu	20020ct6
PASS		
0	ld_cnst_cntx_simple_tex	20020ct6
PASS		
0	load_constant_context_rt	20020ct6
PASS		
0	load_palette	20020ct6
PASS		
0	lp_2d3d	20020ct6
PASS		
0	me_reg_wr_rd	20020ct6
PASS		
0	mem_write	20020ct6
PASS		
0	mem_write_rt	20020ct6
PASS		
0	mpeg_index	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_0	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_1	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_2	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_3	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_4	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_5	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_6	20020ct6
PASS		
0	mpeg_index_multi_context_max_context_7	20020ct6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	20020ct6

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	20020ct6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	20020ct6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	20020ct6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	20020ct6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	20020ct6
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	20020ct6
PASS		
0	nop_eating_disabled	20020ct6
PASS		
0	nrt_rt_interaction1	20020ct6
PASS		
0	paint_multi	20020ct6
PASS		
0	paint_packet	20020ct6
PASS		
0	paint_packet_gui_control	20020ct6
PASS		
0	pfpr_reg_reads1	20020ct6
PASS		
0	pfpr_reg_reads2	20020ct6
PASS		
0	pitch_clip	20020ct6
PASS		
0	ply_nextscan	20020ct6
PASS		
0	rbbm_NQWU_BIF_01	20020ct6
PASS		
0	rbbm_NQWU_BIF_02	20020ct6
PASS		
0	rbbm_NQWU_CF_01	20020ct6
PASS		
0	rbbm_NQWU_CPnq_01	20020ct6
PASS		
0	rbbm_WU_2D_idle_01	20020ct6
PASS		
0	rbbm_WU_2D_idle_02	20020ct6
PASS		
0	rbbm_WU_2D_idle_03	20020ct6
PASS		
0	rbbm_WU_2D_idle_04	20020ct6
PASS		
0	rbbm_WU_2D_idle_05	20020ct6

PASS		
0	rbbm_WU_2D_idle_06	20020ct6
PASS		
0	rbbm_WU_2D_idle_07	20020ct6
PASS		
0	rbbm_WU_2D_idle_08	20020ct6
PASS		
0	rbbm_WU_2D_idle_09	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_01	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_02	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_03	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_04	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_05	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_06	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_07	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_08	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_09	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_10	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_11	20020ct6
PASS		
0	rbbm_WU_2D_idle_clean_12	20020ct6
PASS		
0	rbbm_WU_3D_idle_01	20020ct6
PASS		
0	rbbm_WU_3D_idle_02	20020ct6
PASS		
0	rbbm_WU_3D_idle_03	20020ct6
PASS		
0	rbbm_WU_3D_idle_04	20020ct6
PASS		
0	rbbm_WU_3D_idle_05	20020ct6
PASS		
0	rbbm_WU_3D_idle_06	20020ct6
PASS		
0	rbbm_WU_3D_idle_07	20020ct6
PASS		
0	rbbm_WU_3D_idle_08	20020ct6

PASS		
0	rbbm_WU_3D_idle_09	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_01	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_02	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_03	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_04	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_05	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_06	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_07	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_08	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_09	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_10	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_11	20020ct6
PASS		
0	rbbm_WU_3D_idle_clean_12	20020ct6
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	20020ct6
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	20020ct6
PASS		
0	rbbm_WU_CP_dma_idle	20020ct6
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	20020ct6
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	20020ct6
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	20020ct6
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	20020ct6
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	20020ct6
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	20020ct6
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP update_pending_off	20020ct6
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP update_pending_off	20020ct6

PASS		
0	rbbm_WU_MH_hdp_clean	2002Oct6
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Oct6
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Oct6
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Oct6
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Oct6
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Oct6
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Oct6
PASS		
0	rbbm_WU_cmdfifo_entries	2002Oct6
PASS		
0	rbbm_WU_idct_semaphore	2002Oct6
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Oct6
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Oct6
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Oct6
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Oct6
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Oct6
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Oct6
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Oct6
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Oct6

PASS		
0	rbbm_WUrt_RT_idle_01	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_02	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_03	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_04	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_clean_01	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_clean_02	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_clean_03	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_clean_04	20020ct6
PASS		
0	rbbm_WUrt_RT_idle_clean_05	20020ct6
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	20020ct6
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	20020ct6
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	20020ct6
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	20020ct6
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	20020ct6
PASS		
0	rbbm_clk_active_deassert_min_max	20020ct6
PASS		
0	rbbm_clk_active_each_pipe	20020ct6
PASS		
0	rbbm_clk_active_for_read_error	20020ct6
PASS		
0	rbbm_clk_active_for_read_request	20020ct6
PASS		
0	rbbm_clk_active_on_reset	20020ct6
PASS		
0	rbbm_cp_non_queued_rdwr_01	20020ct6
PASS		
0	rbbm_cp_non_queued_rdwr_02	20020ct6
PASS		
0	rbbm_cp_queued_holds_nonqueued	20020ct6
PASS		
0	rbbm_cp_queued_rdwr	20020ct6
PASS		
0	rbbm_hi_data_and_be_swap	20020ct6

PASS		
0	rbbm_hi_data_endian_swap	20020ct6
PASS		
0	rbbm_hi_data_no_swap_for_cppush	20020ct6
PASS		
0	rbbm_hi_data_swap_for_cppush	20020ct6
PASS		
0	rbbm_int_ack	20020ct6
PASS		
0	rbbm_int_each_client	20020ct6
PASS		
0	rbbm_int_nrt_gfxpipe_idle	20020ct6
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	20020ct6
PASS		
0	rbbm_int_rderr	20020ct6
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	20020ct6
PASS		
0	rbbm_isync_cpscratch_idlegui	20020ct6
PASS		
0	rbbm_isync_wait_idlegui	20020ct6
PASS		
0	rbbm_soft_reset	20020ct6
PASS		
0	rbbm_vgt_default_req	20020ct6
PASS		
0	rbbm_vgt_max_req	20020ct6
PASS		
0	rbbm_vgt_min_req	20020ct6
PASS		
0	reg_to_mem	20020ct6
PASS		
0	reg_to_mem_rt	20020ct6
PASS		
0	ring_i1_pio_i2_bm	20020ct6
PASS		
0	ring_pio_ibs_bm_stress	20020ct6
PASS		
0	rop_cfd_wmd	20020ct6
PASS		
0	roq_reg_reads	20020ct6
PASS		
0	rstr_ordr	20020ct6
PASS		
0	rt_invalid_opcode	20020ct6
PASS		
0	rtee_arm_wait_idle_trigger_events_16	20020ct6

PASS		
0	rtee_discretes_arm_rearm_events_32	2002Oct6
PASS		
0	rtee_discretes_falling_00_events_16	2002Oct6
PASS		
0	rtee_discretes_falling_01_events_16	2002Oct6
PASS		
0	rtee_discretes_falling_02_events_16	2002Oct6
PASS		
0	rtee_discretes_falling_03_events_16	2002Oct6
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0	rtee_discretes_falling_04_events_16	2002Oct6
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0	rtee_discretes_falling_05_events_16	2002Oct6
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0	rtee_discretes_falling_06_events_16	2002Oct6
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0	rtee_discretes_falling_07_events_16	2002Oct6
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0	rtee_discretes_falling_08_events_16	2002Oct6
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0	rtee_discretes_falling_09_events_16	2002Oct6
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0	rtee_discretes_falling_12_events_16	2002Oct6
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0	rtee_discretes_falling_13_events_16	2002Oct6
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0	rtee_discretes_falling_14_events_16	2002Oct6
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0	rtee_discretes_falling_15_events_16	2002Oct6
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0	rtee_discretes_falling_16_events_16	2002Oct6
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0	rtee_discretes_falling_17_events_16	2002Oct6
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0	rtee_discretes_falling_18_events_16	2002Oct6
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0	rtee_discretes_falling_22_events_16	2002Oct6

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PASS		
0	rtee_discretes_falling_31_events_16	2002Oct6
PASS		
0	rtee_discretes_invert_00_events_16	2002Oct6
PASS		
0	rtee_discretes_invert_01_events_16	2002Oct6
PASS		
0	rtee_discretes_invert_02_events_16	2002Oct6
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0	rtee_discretes_invert_03_events_16	2002Oct6
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0	rtee_discretes_invert_04_events_16	2002Oct6
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0	rtee_discretes_invert_05_events_16	2002Oct6
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0	rtee_discretes_invert_06_events_16	2002Oct6
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0	rtee_discretes_invert_07_events_16	2002Oct6
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0	rtee_discretes_invert_21_events_16	2002Oct6
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0	rtee_discretes_invert_22_events_16	2002Oct6
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0	rtee_discretes_invert_31_events_16	2002Oct6
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0	rtee_discretes_level_06_events_16	2002Oct6

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0	rtee_discretes_level_29_events_16	2002Oct6
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0	rtee_discretes_level_30_events_16	2002Oct6

PASS		
0	rtee_discretes_level_31_events_16	2002Oct6
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0	rtee_discretes_manual_disarm_events_16	2002Oct6
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0	rtee_discretes_rising_00_events_16	2002Oct6
PASS		
0	rtee_discretes_rising_01_events_16	2002Oct6
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0	rtee_discretes_rising_02_events_16	2002Oct6
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PASS		
0	rtee_discretes_rising_31_events_16	2002Oct6
PASS		
0	rtee_display1_comp_events_16	2002Oct6
PASS		
0	rtee_display2_comp_events_16	2002Oct6
PASS		
0	rtee_mem_linear_search_events_2	2002Oct6
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Oct6
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Oct6
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Oct6

PASS		
0	rtee_mem_poll_intervals_events_16	20020ct6
PASS		
0	rtee_mem_poll_short_intervals_events_16	20020ct6
PASS		
0	rtee_pio	20020ct6
PASS		
0	rtee_prefetch_parse_nops	20020ct6
PASS		
0	rtee_reg_linear_search_events_2	20020ct6
PASS		
0	rtee_reg_poll_comp_000_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_001_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_010_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_011_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_100_events_16	20020ct6
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0	rtee_reg_poll_comp_101_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_110_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_111_events_16	20020ct6
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	20020ct6
PASS		
0	rtee_vip_comp_events_16	20020ct6
PASS		
0	scratch_mem_read	20020ct6
PASS		
0	scratch_mem_read2	20020ct6
PASS		
0	set_cnst_alu_max_random	20020ct6
PASS		
0	set_cnst_we_lcc_random	20020ct6
PASS		
0	set_cnst_we_random	20020ct6
PASS		
0	set_constant	20020ct6
PASS		
0	set_constant2	20020ct6
PASS		
0	set_constant_alu_max1	20020ct6
PASS		
0	set_constant_alu_max2	20020ct6

PASS		
0	set_constant_bool_max1	20020ct6
PASS		
0	set_constant_bool_max2	20020ct6
PASS		
0	set_constant_indr1_short	20020ct6
PASS		
0	set_constant_indr1_short2	20020ct6
PASS		
0	set_constant_indr1_write_enable	20020ct6
PASS		
0	set_constant_indr1_write_enable_lcc	20020ct6
PASS		
0	set_constant_indr2_short	20020ct6
PASS		
0	set_constant_indr2_short2	20020ct6
PASS		
0	set_constant_indr2_write_enable	20020ct6
PASS		
0	set_constant_indr2_write_enable_lcc	20020ct6
PASS		
0	set_constant_loop_max1	20020ct6
PASS		
0	set_constant_loop_max2	20020ct6
PASS		
0	set_constant_ring_short	20020ct6
PASS		
0	set_constant_ring_short2	20020ct6
PASS		
0	set_constant_ring_write_enable	20020ct6
PASS		
0	set_constant_ring_write_enable_lcc	20020ct6
PASS		
0	set_constant_short	20020ct6
PASS		
0	set_constant_short2	20020ct6
PASS		
0	set_constant_tex_max1	20020ct6
PASS		
0	set_constant_tex_max2	20020ct6
PASS		
0	set_constant_write_enable	20020ct6
PASS		
0	set_constant_write_enable_lcc	20020ct6
PASS		
0	set_constant_write_enable_lcc2	20020ct6
PASS		
0	set_scissors	20020ct6

PASS		
0	set_st_indrl_match	20020ct6
PASS		
0	set_st_indrl_mismatch	20020ct6
PASS		
0	set_st_indrl_mismatch2	20020ct6
PASS		
0	set_st_indrl_mismatch3	20020ct6
PASS		
0	set_st_indrl_mismatch4	20020ct6
PASS		
0	set_st_indrl_ps	20020ct6
PASS		
0	set_st_indrl_ps_only	20020ct6
PASS		
0	set_st_indrl_sublk0	20020ct6
PASS		
0	set_st_indrl_sublk1	20020ct6
PASS		
0	set_st_indrl_sublk1_only	20020ct6
PASS		
0	set_st_indrl_sublk2	20020ct6
PASS		
0	set_st_indrl_sublk2_only	20020ct6
PASS		
0	set_st_indrl_sublk3	20020ct6
PASS		
0	set_st_indrl_sublk3_only	20020ct6
PASS		
0	set_st_indrl_sublk4	20020ct6
PASS		
0	set_st_indrl_sublk4_only	20020ct6
PASS		
0	set_st_indrl_sublk5	20020ct6
PASS		
0	set_st_indrl_sublk5_only	20020ct6
PASS		
0	set_st_indrl_sublk6	20020ct6
PASS		
0	set_st_indrl_sublk6_only	20020ct6
PASS		
0	set_st_indrl_sublk7	20020ct6
PASS		
0	set_st_indrl_sublk7_only	20020ct6
PASS		
0	set_st_indrl_vs	20020ct6
PASS		
0	set_st_indrl_vs_only	20020ct6

PASS		
0	set_st_indr2_match	20020ct6
PASS		
0	set_st_indr2_mismatch	20020ct6
PASS		
0	set_st_indr2_mismatch2	20020ct6
PASS		
0	set_st_indr2_mismatch3	20020ct6
PASS		
0	set_st_indr2_mismatch4	20020ct6
PASS		
0	set_st_indr2_ps	20020ct6
PASS		
0	set_st_indr2_ps_only	20020ct6
PASS		
0	set_st_indr2_sublk0	20020ct6
PASS		
0	set_st_indr2_sublk1	20020ct6
PASS		
0	set_st_indr2_sublk1_only	20020ct6
PASS		
0	set_st_indr2_sublk2	20020ct6
PASS		
0	set_st_indr2_sublk2_only	20020ct6
PASS		
0	set_st_indr2_sublk3	20020ct6
PASS		
0	set_st_indr2_sublk3_only	20020ct6
PASS		
0	set_st_indr2_sublk4	20020ct6
PASS		
0	set_st_indr2_sublk4_only	20020ct6
PASS		
0	set_st_indr2_sublk5	20020ct6
PASS		
0	set_st_indr2_sublk5_only	20020ct6
PASS		
0	set_st_indr2_sublk6	20020ct6
PASS		
0	set_st_indr2_sublk6_only	20020ct6
PASS		
0	set_st_indr2_sublk7	20020ct6
PASS		
0	set_st_indr2_sublk7_only	20020ct6
PASS		
0	set_st_indr2_vs	20020ct6
PASS		
0	set_st_indr2_vs_only	20020ct6

PASS		
0	set_st_pfmatch_disabled	2002Oct6
PASS		
0	set_st_ring_match	2002Oct6
PASS		
0	set_st_ring_mismatch	2002Oct6
PASS		
0	set_st_ring_mismatch2	2002Oct6
PASS		
0	set_st_ring_mismatch3	2002Oct6
PASS		
0	set_st_ring_mismatch4	2002Oct6
PASS		
0	set_st_ring_ps	2002Oct6
PASS		
0	set_st_ring_ps_only	2002Oct6
PASS		
0	set_st_ring_subblk0	2002Oct6
PASS		
0	set_st_ring_subblk1	2002Oct6
PASS		
0	set_st_ring_subblk1_only	2002Oct6
PASS		
0	set_st_ring_subblk2	2002Oct6
PASS		
0	set_st_ring_subblk2_only	2002Oct6
PASS		
0	set_st_ring_subblk3	2002Oct6
PASS		
0	set_st_ring_subblk3_only	2002Oct6
PASS		
0	set_st_ring_subblk4	2002Oct6
PASS		
0	set_st_ring_subblk4_only	2002Oct6
PASS		
0	set_st_ring_subblk5	2002Oct6
PASS		
0	set_st_ring_subblk5_only	2002Oct6
PASS		
0	set_st_ring_subblk6	2002Oct6
PASS		
0	set_st_ring_subblk6_only	2002Oct6
PASS		
0	set_st_ring_subblk7	2002Oct6
PASS		
0	set_st_ring_subblk7_only	2002Oct6
PASS		
0	set_st_ring_vs	2002Oct6

PASS		
0	set_st_ring_vs_only	2002Oct6
PASS		
0	set_st_stress_a	2002Oct6
PASS		
0	set_st_stress_b	2002Oct6
PASS		
744	set_st_stress_c	2002Oct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
745	set_st_stress_d	2002Oct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
746	set_st_stress_e	2002Oct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_f	2002Oct6
PASS		
0	set_state_rt_a	2002Oct6
PASS		
749	set_state_rt_b	2002Oct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
750	set_state_rt_c	2002Oct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
751	set_state_rt_d	2002Oct6
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	smoke_nrt	2002Oct6
PASS		
0	src_clip	2002Oct6
PASS		
0	srf_cohr	2002Oct6
PASS		
0	switch_pio_bm	2002Oct6
PASS		
756	trans_bitblt	2002Oct6
FAIL	rbbmWr.cmp	
0	type0_2long_pckts	2002Oct6
PASS		
0	type0_3indr_buffs	2002Oct6
PASS		
0	type0_4pckts	2002Oct6
PASS		
0	type0_incr_updt_cnst	2002Oct6
PASS		
0	type0_incr_updt_instrmem	2002Oct6
PASS		
0	type0_incr_updt_st	2002Oct6
PASS		
0	type0_incr_updt_stress	2002Oct6
PASS		
0	type0_ind1_ind2_a	2002Oct6

PASS		
0	type0_ind1_ind2_b	20020ct6
PASS		
0	type0_indr_buff	20020ct6
PASS		
0	typel_4pckts	20020ct6
PASS		
0	typel_indr_buff	20020ct6
PASS		
0	typel_pckt	20020ct6
PASS		
0	type_random	20020ct6
PASS		
771	uram_wrt_rt_ucose	20020ct6
FAIL	cp_rbbm.cmp rbbmWr.cmp	
772	uram_wrt_ucose	20020ct6
FAIL	rbbmWr.cmp	
0	viz_query_a	20020ct6
PASS		
0	viz_query_draw_indx	20020ct6
PASS		
0	viz_query_draw_indx_indr1	20020ct6
PASS		
0	viz_query_draw_indx_indr2	20020ct6
PASS		
0	viz_query_draw_indx_multipass	20020ct6
PASS		
0	viz_query_draw_indx_ring	20020ct6
PASS		
0	viz_query_indr1	20020ct6
PASS		
0	viz_query_indr1_simple	20020ct6
PASS		
0	viz_query_indr2	20020ct6
PASS		
0	viz_query_indr2_simple	20020ct6
PASS		
0	viz_query_ring	20020ct6
PASS		
0	viz_query_ring_simple	20020ct6
PASS		
0	wait_for_idle	20020ct6
PASS		
0	wait_reg_mem_poll_mem0	20020ct6
PASS		
0	wait_reg_mem_poll_mem0_rt	20020ct6
PASS		
0	wait_reg_mem_poll_mem1	20020ct6

```
PASS
0 wait_reg_mem_poll_mem2 2002Oct6
PASS
0 wait_reg_mem_poll_mem3 2002Oct6
PASS
0 wait_reg_mem_poll_reg0 2002Oct6
PASS
0 wait_reg_mem_poll_reg1 2002Oct6
PASS
0 wait_reg_mem_poll_reg2 2002Oct6
PASS
0 wait_reg_mem_poll_reg3 2002Oct6
PASS
```

```
=====
Tests_Run_count = 794 Pass_count = 775 Fail_count = 19 Incomplete_count = 0
=====
```



```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Sep 19  7:06:26 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          639        639        583         91
              RBBM        98         98         60         61
+++++
              TOTAL        737        737        643         87
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Oct 17  4:09:57 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY    SU              0          0          0          0
              CP             755         755         671         88
              RBBM           132         132          28          21
+++++
              TOTAL           887         887         699         78
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Wed Aug  7 13:11:45 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
++++          VGT          0          0          0          0
++++          SC           0          0          0          0
++++          CL           0          0          0          0
++++          VTE          0          0          0          0
++++    SPECIALTY        0          0          0          0
++++          SU           0          0          0          0
++++          CP          522        522        486        93
+++++
++++          TEST          522        522        486        93
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Fri Sep  6  8:58:17 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU              0          0          0          0
              CP             634         634         627         98
              RBBM           98          98          70          71
+++++
              TOTAL           732         732         697         95
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug20
PASS			
0	2d_indr_buffer_b		2002Aug20
PASS			
0	2d_indr_buffer_c		2002Aug20
PASS			
0	2d_indr_buffer_d		2002Aug20
PASS			
0	2d_indr_buffer_e		2002Aug20
PASS			
0	2d_indr_buffer_f		2002Aug20
PASS			
0	2d_indr_buffer_g		2002Aug20
PASS			
0	3d_draw_indx_2		2002Aug20
PASS			
0	3d_draw_indx_2_b		2002Aug20
PASS			
0	bitblt_multi		2002Aug20
PASS			
0	brush_flag		2002Aug20
PASS			
0	clear_const_wr_en_on_me_init		2002Aug20
PASS			
0	clear_const_wr_en_with_lcc		2002Aug20
PASS			
0	cond_write_mem_poll_mem		2002Aug20
PASS			
0	cond_write_mem_poll_mem2		2002Aug20
PASS			
0	cond_write_mem_poll_mem3		2002Aug20
PASS			
0	cond_write_mem_poll_mem_rt		2002Aug20
PASS			
0	cond_write_mem_poll_reg		2002Aug20
PASS			
0	cond_write_mem_poll_reg2		2002Aug20
PASS			
0	cond_write_mem_poll_reg3		2002Aug20

PASS		
0	cp_acpi_recovery_soft_reset	2002Aug20
PASS		
0	cp_bios_scratch	2002Aug20
PASS		
0	cp_ib2_base000_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug20
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug20
PASS		
0	cp_ib_base000_bufszXXX	2002Aug20
PASS		
0	cp_ib_base001_bufszXXX	2002Aug20
PASS		
0	cp_ib_base010_bufszXXX	2002Aug20
PASS		
0	cp_ib_base011_bufszXXX	2002Aug20
PASS		
0	cp_ib_base100_bufszXXX	2002Aug20
PASS		
0	cp_ib_base101_bufszXXX	2002Aug20
PASS		
0	cp_ib_base110_bufszXXX	2002Aug20
PASS		
0	cp_ib_base111_bufszXXX	2002Aug20
PASS		
39	cp_interrupt	2002Aug20
FAIL	cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	cp_interrupt_rt_events_16	2002Aug20
PASS		
0	cp_iq2_alignment_000	2002Aug20
PASS		
0	cp_iq2_alignment_001	2002Aug20
PASS		
0	cp_iq2_alignment_010	2002Aug20
PASS		
0	cp_iq2_alignment_011	2002Aug20

PASS		
0	cp_iq2_alignment_100	2002Aug20
PASS		
0	cp_iq2_alignment_101	2002Aug20
PASS		
0	cp_iq2_alignment_110	2002Aug20
PASS		
0	cp_iq2_alignment_111	2002Aug20
PASS		
0	cp_iq_alignment_000	2002Aug20
PASS		
0	cp_iq_alignment_001	2002Aug20
PASS		
0	cp_iq_alignment_010	2002Aug20
PASS		
0	cp_iq_alignment_011	2002Aug20
PASS		
0	cp_iq_alignment_100	2002Aug20
PASS		
0	cp_iq_alignment_101	2002Aug20
PASS		
0	cp_iq_alignment_110	2002Aug20
PASS		
0	cp_iq_alignment_111	2002Aug20
PASS		
0	cp_max_size_packet	2002Aug20
PASS		
0	cp_nrt_rt_protection_mode	2002Aug20
PASS		
0	cp_output_stress	2002Aug20
PASS		
0	cp_protection_mode	2002Aug20
PASS		
0	cp_rb_alignment_000	2002Aug20
PASS		
0	cp_rb_alignment_001	2002Aug20
PASS		
0	cp_rb_alignment_010	2002Aug20
PASS		
0	cp_rb_alignment_011	2002Aug20
PASS		
0	cp_rb_alignment_100	2002Aug20
PASS		
0	cp_rb_alignment_101	2002Aug20
PASS		
0	cp_rb_alignment_110	2002Aug20
PASS		
0	cp_rb_alignment_111	2002Aug20

PASS		
0	cp_register_reads	2002Aug20
PASS		
0	cp_reg_rw_test	2002Aug20
PASS		
0	cp_rt_protection_mode	2002Aug20
PASS		
0	cp_scratch	2002Aug20
PASS		
0	cp_scratch_multi	2002Aug20
PASS		
0	dma_cp_sync	2002Aug20
PASS		
0	dma_dscrstr_chckn_bit	2002Aug20
PASS		
0	dma_dscrstr_flush1	2002Aug20
PASS		
0	dma_dscrstr_flush2	2002Aug20
PASS		
0	dma_dscrstr_flush3	2002Aug20
PASS		
0	dma_m2m_src00_dstXX	2002Aug20
PASS		
0	dma_m2m_src01_dstXX	2002Aug20
PASS		
0	dma_m2m_src10_dstXX	2002Aug20
PASS		
0	dma_m2m_src11_dstXX	2002Aug20
PASS		
0	dma_mem2mem	2002Aug20
PASS		
0	dma_mem2mem_abort	2002Aug20
PASS		
0	dma_mem2mem_alignments	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug20
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug20

PASS		
0	dma_mem2mem_byte_alignment7	2002Aug20
PASS		
0	dma_mem2mem_pio_abort	2002Aug20
PASS		
0	dma_mem2reg	2002Aug20
PASS		
0	dma_mem2reg_abort	2002Aug20
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug20
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug20
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug20
PASS		
0	dma_mem2reg_sync	2002Aug20
PASS		
0	dma_r2r_m2m_r2r	2002Aug20
PASS		
0	dma_r2r_m2r_r2r	2002Aug20
PASS		
0	dma_r2r_r2m_r2r	2002Aug20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug20
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug20
PASS		
0	dma_reg2mem	2002Aug20
PASS		
0	dma_reg2mem_abort	2002Aug20
PASS		
0	dma_reg2reg	2002Aug20
PASS		
0	dma_reg2reg_2dw	2002Aug20
PASS		
0	dma_reg2reg_daic	2002Aug20
PASS		
0	dma_reg2reg_mult	2002Aug20
PASS		
0	dma_reg2reg_mult2	2002Aug20
PASS		
0	dma_reg2reg_mult3	2002Aug20
PASS		
0	dma_reg2reg_saic	2002Aug20
PASS		
0	dma_reg2reg_sdaic	2002Aug20

PASS		
0	dma_reg2reg_sdaic_stress	2002Aug20
PASS		
0	dma_reg2reg_stress	2002Aug20
PASS		
0	draw_indx_a	2002Aug20
PASS		
0	draw_indx_b	2002Aug20
PASS		
0	draw_indx_c	2002Aug20
PASS		
0	draw_indx_indr1_a	2002Aug20
PASS		
0	draw_indx_indr1_b	2002Aug20
PASS		
0	draw_indx_indr1_short_a	2002Aug20
PASS		
0	draw_indx_indr1_short_b	2002Aug20
PASS		
0	draw_indx_indr1_stress	2002Aug20
PASS		
0	draw_indx_indr2_a	2002Aug20
PASS		
0	draw_indx_indr2_b	2002Aug20
PASS		
0	draw_indx_indr2_short_a	2002Aug20
PASS		
0	draw_indx_indr2_short_b	2002Aug20
PASS		
0	draw_indx_indr2_stress	2002Aug20
PASS		
0	draw_indx_ring1	2002Aug20
PASS		
0	draw_indx_ring2	2002Aug20
PASS		
0	draw_indx_ring_short1	2002Aug20
PASS		
0	draw_indx_ring_short2	2002Aug20
PASS		
0	draw_indx_ring_stress	2002Aug20
PASS		
0	draw_indx_short_a	2002Aug20
PASS		
0	draw_indx_short_b	2002Aug20
PASS		
0	draw_indx_short_c	2002Aug20
PASS		
0	draw_indx_stress_a	2002Aug20

PASS		
0	draw_indx_stress_b	2002Aug20
PASS		
0	draw_indx_stress_c	2002Aug20
PASS		
0	event_timestamp_write	2002Aug20
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Aug20
PASS		
0	ft_indr1_july22	2002Aug20
PASS		
0	ft_indr2_july22	2002Aug20
PASS		
0	ft_july22	2002Aug20
PASS		
0	header_dump	2002Aug20
PASS		
0	header_dump_rt	2002Aug20
PASS		
0	header_dump_small	2002Aug20
PASS		
0	hostdata_blt	2002Aug20
PASS		
0	im_load_a	2002Aug20
PASS		
0	im_load_b	2002Aug20
PASS		
0	im_load_cid_mismatch	2002Aug20
PASS		
0	im_load_immediate	2002Aug20
PASS		
0	im_load_immediate_nrt_me	2002Aug20
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug20
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug20
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug20
PASS		
0	im_load_indr1_a	2002Aug20

PASS		
0	im_load_indr1_b	2002Aug20
PASS		
0	im_load_indr2_a	2002Aug20
PASS		
0	im_load_indr2_b	2002Aug20
PASS		
0	im_load_invalidate	2002Aug20
PASS		
0	im_load_match	2002Aug20
PASS		
0	im_load_match2	2002Aug20
PASS		
0	im_load_nrt_me	2002Aug20
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug20
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug20
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug20
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug20
PASS		
0	im_load_pfmatch_disable	2002Aug20
PASS		
0	im_load_ring_a	2002Aug20
PASS		
0	im_load_ring_b	2002Aug20
PASS		
0	im_load_rt	2002Aug20
PASS		
0	im_load_rt2	2002Aug20
PASS		
0	im_load_rt3	2002Aug20
PASS		
0	im_load_rt4	2002Aug20
PASS		
0	im_load_sz_mismatch	2002Aug20
PASS		
0	incremental_update_const	2002Aug20
PASS		
0	incremental_update_instr	2002Aug20
PASS		
0	incremental_update_state	2002Aug20
PASS		
0	indirect_buffer_a	2002Aug20
PASS		
0	indirect_buffer_b	2002Aug20

PASS		
0	indirect_buffer_c	2002Aug20
PASS		
0	indirect_buffer_d	2002Aug20
PASS		
0	indirect_buffer_e	2002Aug20
PASS		
0	indirect_buffer_f	2002Aug20
PASS		
0	indirect_buffer_g	2002Aug20
PASS		
0	indirect_buffer_h	2002Aug20
PASS		
0	indirect_buffer_i	2002Aug20
PASS		
0	indirect_buffer_j	2002Aug20
PASS		
0	indirect_buffer_k	2002Aug20
PASS		
0	indirect_buffer_l	2002Aug20
PASS		
0	indirect_buffer_mp_a	2002Aug20
PASS		
0	indirect_buffer_mp_b	2002Aug20
PASS		
0	indirect_buffer_mp_c	2002Aug20
PASS		
0	indirect_buffer_mp_d	2002Aug20
PASS		
0	indirect_buffer_mp_e	2002Aug20
PASS		
0	indirect_buffer_mp_f	2002Aug20
PASS		
0	invalidate_on_me_init	2002Aug20
PASS		
0	invalidate_state_constants	2002Aug20
PASS		
0	invalidate_state_shaders	2002Aug20
PASS		
0	invalidate_state_shaders2	2002Aug20
PASS		
0	invalidate_state_sublks	2002Aug20
PASS		
0	invalidate_state_sublks2	2002Aug20
PASS		
0	invalid_opcode	2002Aug20
PASS		
0	iql_iq2_mult	2002Aug20

PASS		
0	iq1_iq2_push_wrap	2002Aug20
PASS		
214	iq2_csq_full	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	IQ2_Mult	2002Aug20
PASS		
0	IQ2_Wrap_Legacy	2002Aug20
PASS		
0	IQ_Wrap	2002Aug20
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Aug20
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug20
PASS		
0	ld_cnst_cntx_match	2002Aug20
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug20
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug20
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug20
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug20
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug20
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug20
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug20
PASS		
0	ld_cnst_cntx_replicate8	2002Aug20

PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug20
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug20
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug20
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug20
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug20
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug20
PASS		
0	ld_cnst_cntx_simple_alu	2002Aug20
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug20
PASS		
0	load_constant_context_rt	2002Aug20
PASS		
0	load_palette	2002Aug20
PASS		
0	mem_write	2002Aug20
PASS		
0	mem_write_rt	2002Aug20
PASS		
0	mpeg_index	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug20
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug20

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug20
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug20
PASS		
0	nop_eating_disabled	2002Aug20
PASS		
0	paint_multi	2002Aug20
PASS		
0	pitch_clip	2002Aug20
PASS		
0	ply_nextscan	2002Aug20
PASS		
0	rbbm_soft_reset	2002Aug20
PASS		
0	rbbm_WU_2D_idle_01	2002Aug20
PASS		
0	rbbm_WU_2D_idle_02	2002Aug20
PASS		
0	rbbm_WU_2D_idle_03	2002Aug20
PASS		
0	rbbm_WU_2D_idle_04	2002Aug20
PASS		
0	rbbm_WU_2D_idle_05	2002Aug20
PASS		
0	rbbm_WU_2D_idle_06	2002Aug20
PASS		
0	rbbm_WU_2D_idle_07	2002Aug20
PASS		
0	rbbm_WU_2D_idle_08	2002Aug20
PASS		
0	rbbm_WU_2D_idle_09	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Aug20

PASS		
0	rbbm_WU_2D_idle_clean_07	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Aug20
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Aug20
PASS		
0	rbbm_WU_3D_idle_01	2002Aug20
PASS		
0	rbbm_WU_3D_idle_02	2002Aug20
PASS		
0	rbbm_WU_3D_idle_03	2002Aug20
PASS		
0	rbbm_WU_3D_idle_04	2002Aug20
PASS		
0	rbbm_WU_3D_idle_05	2002Aug20
PASS		
0	rbbm_WU_3D_idle_06	2002Aug20
PASS		
0	rbbm_WU_3D_idle_07	2002Aug20
PASS		
0	rbbm_WU_3D_idle_08	2002Aug20
PASS		
0	rbbm_WU_3D_idle_09	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Aug20
PASS		
305	rbbm_WU_3D_idle_clean_06	2002Aug20
FAIL		
0	rbbm_WU_3D_idle_clean_07	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Aug20

PASS		
0	rbbm_WU_3D_idle_clean_10	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Aug20
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Aug20
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Aug20
PASS		
0	rbbm_WU_BOTH_GRP_H_update_pending_off	2002Aug20
PASS		
0	rbbm_WU_cmdfifo_entries	2002Aug20
PASS		
0	rbbm_WU_CP_dma_idle	2002Aug20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Aug20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Aug20
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Aug20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Aug20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Aug20
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Aug20
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRP_H_update_pending_off	2002Aug20
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRP_H_update_pending_off	2002Aug20
PASS		
0	rbbm_WU_idct_semaphore	2002Aug20
PASS		
0	rbbm_WU_MH_hdp_clean	2002Aug20
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Aug20
PASS		
327	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Aug20
FAIL		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Aug20
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Aug20
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Aug20

PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Aug20
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Aug20
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Aug20
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Aug20
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Aug20
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Aug20
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Aug20
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Aug20
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Aug20
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Aug20
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Aug20
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Aug20
PASS		
349	rbbm_WU_VIPH2_dma_idle	2002Aug20
FAIL		
0	rbbm_WU_VIPH3_dma_idle	2002Aug20
PASS		
0	RB_BUFSZ_0	2002Aug20
PASS		
0	RB_BUFSZ_1	2002Aug20
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0	RB_Wrap	2002Aug20
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0	reg_to_mem	2002Aug20
PASS		
0	reg_to_mem_rt	2002Aug20
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0	rop_cfd_wmd	2002Aug20

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0	rstr_ordr	2002Aug20
PASS		
0	rtee_discretes_falling_00_events_16	2002Aug20
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0	rtee_discretes_falling_01_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug20
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0	rtee_discretes_falling_04_events_16	2002Aug20
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PASS		
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0	rtee_discretes_falling_07_events_16	2002Aug20
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PASS		
0	rtee_discretes_falling_09_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug20
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0	rtee_discretes_falling_11_events_16	2002Aug20
PASS		
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PASS		
0	rtee_discretes_falling_13_events_16	2002Aug20
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0	rtee_discretes_falling_14_events_16	2002Aug20
PASS		
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PASS		
0	rtee_discretes_falling_16_events_16	2002Aug20
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0	rtee_discretes_falling_17_events_16	2002Aug20
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0	rtee_discretes_falling_18_events_16	2002Aug20
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0	rtee_discretes_falling_19_events_16	2002Aug20
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug20
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0	rtee_discretes_falling_21_events_16	2002Aug20
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0	rtee_discretes_falling_22_events_16	2002Aug20

PASS		
0	rtee_discretes_falling_23_events_16	2002Aug20
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0	rtee_discretes_falling_27_events_16	2002Aug20
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PASS		
0	rtee_discretes_falling_30_events_16	2002Aug20
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0	rtee_discretes_falling_31_events_16	2002Aug20
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PASS		
0	rtee_discretes_invert_01_events_16	2002Aug20
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0	rtee_discretes_invert_02_events_16	2002Aug20
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0	rtee_discretes_invert_03_events_16	2002Aug20
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug20
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0	rtee_discretes_invert_05_events_16	2002Aug20
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0	rtee_discretes_invert_06_events_16	2002Aug20
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0	rtee_discretes_invert_08_events_16	2002Aug20
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PASS		
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0	rtee_discretes_invert_12_events_16	2002Aug20
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0	rtee_discretes_invert_13_events_16	2002Aug20
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0	rtee_discretes_invert_19_events_16	2002Aug20
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PASS		
0	rtee_discretes_invert_21_events_16	2002Aug20
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0	rtee_discretes_invert_22_events_16	2002Aug20
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PASS		
0	rtee_discretes_invert_31_events_16	2002Aug20
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0	rtee_discretes_level_06_events_16	2002Aug20

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0	rtee_discretes_level_07_events_16	2002Aug20
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0	rtee_discretes_level_08_events_16	2002Aug20
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0	rtee_discretes_level_09_events_16	2002Aug20
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PASS		
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PASS		
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PASS		
0	rtee_discretes_level_13_events_16	2002Aug20
PASS		
0	rtee_discretes_level_14_events_16	2002Aug20
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0	rtee_discretes_level_16_events_16	2002Aug20
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0	rtee_discretes_level_22_events_16	2002Aug20
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0	rtee_discretes_level_28_events_16	2002Aug20
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0	rtee_discretes_level_29_events_16	2002Aug20
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0	rtee_discretes_level_30_events_16	2002Aug20

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0	rtee_discretes_level_31_events_16	2002Aug20
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0	rtee_discretes_manual_disarm_events_16	2002Aug20
PASS		
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0	rtee_discretes_rising_31_events_16	2002Aug20
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0	rtee_display2_comp_events_16	2002Aug20
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0	rtee_mem_linear_search_events_2	2002Aug20
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0	rtee_mem_poll_comp_000_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug20
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0	rtee_mem_poll_comp_010_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug20
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0	rtee_mem_poll_comp_100_events_16	2002Aug20
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0	rtee_mem_poll_comp_101_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug20
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug20
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug20

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0	rtee_prefetch_parse_nops	2002Aug20
PASS		
0	rtee_reg_linear_search_events_2	2002Aug20
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0	rtee_reg_poll_comp_000_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug20
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug20
PASS		
0	rtee_vip_comp_events_16	2002Aug20
PASS		
0	rt_invalid_opcode	2002Aug20
PASS		
0	scratch_mem_read	2002Aug20
PASS		
0	scratch_mem_read2	2002Aug20
PASS		
0	set_constant	2002Aug20
PASS		
0	set_constant2	2002Aug20
PASS		
0	set_constant_alu_max1	2002Aug20
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0	set_constant_alu_max2	2002Aug20
PASS		
0	set_constant_bool_max1	2002Aug20
PASS		
0	set_constant_bool_max2	2002Aug20
PASS		
0	set_constant_indr1_short	2002Aug20
PASS		
0	set_constant_indr1_short2	2002Aug20
PASS		
0	set_constant_indr1_write_enable	2002Aug20

PASS		
0	set_constant_indr1_write_enable_lcc	2002Aug20
PASS		
0	set_constant_indr2_short	2002Aug20
PASS		
0	set_constant_indr2_short2	2002Aug20
PASS		
0	set_constant_indr2_write_enable	2002Aug20
PASS		
0	set_constant_indr2_write_enable_lcc	2002Aug20
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0	set_constant_loop_max1	2002Aug20
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0	set_constant_loop_max2	2002Aug20
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0	set_constant_ring_short	2002Aug20
PASS		
0	set_constant_ring_short2	2002Aug20
PASS		
0	set_constant_ring_write_enable	2002Aug20
PASS		
0	set_constant_ring_write_enable_lcc	2002Aug20
PASS		
0	set_constant_short	2002Aug20
PASS		
0	set_constant_short2	2002Aug20
PASS		
0	set_constant_tex_max1	2002Aug20
PASS		
0	set_constant_tex_max2	2002Aug20
PASS		
0	set_constant_write_enable	2002Aug20
PASS		
0	set_constant_write_enable_lcc	2002Aug20
PASS		
0	set_constant_write_enable_lcc2	2002Aug20
PASS		
0	set_scissors	2002Aug20
PASS		
0	set_state_rt_a	2002Aug20
PASS		
545	set_state_rt_b	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
546	set_state_rt_c	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
547	set_state_rt_d	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_indr1_match	2002Aug20

PASS		
0	set_st_indr1_mismatch	2002Aug20
PASS		
0	set_st_indr1_mismatch2	2002Aug20
PASS		
0	set_st_indr1_mismatch3	2002Aug20
PASS		
0	set_st_indr1_mismatch4	2002Aug20
PASS		
0	set_st_indr1_ps	2002Aug20
PASS		
0	set_st_indr1_ps_only	2002Aug20
PASS		
0	set_st_indr1_sublk0	2002Aug20
PASS		
0	set_st_indr1_sublk1	2002Aug20
PASS		
0	set_st_indr1_sublk1_only	2002Aug20
PASS		
0	set_st_indr1_sublk2	2002Aug20
PASS		
0	set_st_indr1_sublk2_only	2002Aug20
PASS		
0	set_st_indr1_sublk3	2002Aug20
PASS		
0	set_st_indr1_sublk3_only	2002Aug20
PASS		
0	set_st_indr1_sublk4	2002Aug20
PASS		
0	set_st_indr1_sublk4_only	2002Aug20
PASS		
0	set_st_indr1_sublk5	2002Aug20
PASS		
0	set_st_indr1_sublk5_only	2002Aug20
PASS		
0	set_st_indr1_sublk6	2002Aug20
PASS		
0	set_st_indr1_sublk6_only	2002Aug20
PASS		
0	set_st_indr1_sublk7	2002Aug20
PASS		
0	set_st_indr1_sublk7_only	2002Aug20
PASS		
0	set_st_indr1_vs	2002Aug20
PASS		
0	set_st_indr1_vs_only	2002Aug20
PASS		
0	set_st_indr2_match	2002Aug20

PASS		
0	set_st_indr2_mismatch	2002Aug20
PASS		
0	set_st_indr2_mismatch2	2002Aug20
PASS		
0	set_st_indr2_mismatch3	2002Aug20
PASS		
0	set_st_indr2_mismatch4	2002Aug20
PASS		
0	set_st_indr2_ps	2002Aug20
PASS		
0	set_st_indr2_ps_only	2002Aug20
PASS		
0	set_st_indr2_sublk0	2002Aug20
PASS		
0	set_st_indr2_sublk1	2002Aug20
PASS		
0	set_st_indr2_sublk1_only	2002Aug20
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0	set_st_indr2_sublk2	2002Aug20
PASS		
0	set_st_indr2_sublk2_only	2002Aug20
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0	set_st_indr2_sublk3	2002Aug20
PASS		
0	set_st_indr2_sublk3_only	2002Aug20
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0	set_st_indr2_sublk4	2002Aug20
PASS		
0	set_st_indr2_sublk4_only	2002Aug20
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0	set_st_indr2_sublk5	2002Aug20
PASS		
0	set_st_indr2_sublk5_only	2002Aug20
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0	set_st_indr2_sublk6	2002Aug20
PASS		
0	set_st_indr2_sublk6_only	2002Aug20
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0	set_st_indr2_sublk7	2002Aug20
PASS		
0	set_st_indr2_sublk7_only	2002Aug20
PASS		
0	set_st_indr2_vs	2002Aug20
PASS		
0	set_st_indr2_vs_only	2002Aug20
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0	set_st_pfmismatch_disabled	2002Aug20

PASS		
0	set_st_ring_match	2002Aug20
PASS		
0	set_st_ring_mismatch	2002Aug20
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0	set_st_ring_mismatch2	2002Aug20
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0	set_st_ring_mismatch3	2002Aug20
PASS		
0	set_st_ring_mismatch4	2002Aug20
PASS		
0	set_st_ring_ps	2002Aug20
PASS		
0	set_st_ring_ps_only	2002Aug20
PASS		
0	set_st_ring_sublk0	2002Aug20
PASS		
0	set_st_ring_sublk1	2002Aug20
PASS		
0	set_st_ring_sublk1_only	2002Aug20
PASS		
0	set_st_ring_sublk2	2002Aug20
PASS		
0	set_st_ring_sublk2_only	2002Aug20
PASS		
0	set_st_ring_sublk3	2002Aug20
PASS		
0	set_st_ring_sublk3_only	2002Aug20
PASS		
0	set_st_ring_sublk4	2002Aug20
PASS		
0	set_st_ring_sublk4_only	2002Aug20
PASS		
0	set_st_ring_sublk5	2002Aug20
PASS		
0	set_st_ring_sublk5_only	2002Aug20
PASS		
0	set_st_ring_sublk6	2002Aug20
PASS		
0	set_st_ring_sublk6_only	2002Aug20
PASS		
0	set_st_ring_sublk7	2002Aug20
PASS		
0	set_st_ring_sublk7_only	2002Aug20
PASS		
0	set_st_ring_vs	2002Aug20
PASS		
0	set_st_ring_vs_only	2002Aug20

PASS		
0	set_st_stress_a	2002Aug20
PASS		
622	set_st_stress_b	2002Aug20
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug20
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0	set_st_stress_d	2002Aug20
PASS		
0	set_st_stress_e	2002Aug20
PASS		
0	set_st_stress_f	2002Aug20
PASS		
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PASS		
0	smoke_nrt	2002Aug20
PASS		
0	src_clip	2002Aug20
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0	srf_cohr	2002Aug20
PASS		
0	trans_bitblt	2002Aug20
PASS		
0	type0_2long_pckts	2002Aug20
PASS		
0	type0_3indr_buffs	2002Aug20
PASS		
0	type0_4pckts	2002Aug20
PASS		
0	type0_incr_updt_cnst	2002Aug20
PASS		
0	type0_incr_updt_instrmem	2002Aug20
PASS		
0	type0_incr_updt_st	2002Aug20
PASS		
0	type0_incr_updt_stress	2002Aug20
PASS		
0	type0_ind1_ind2_a	2002Aug20
PASS		
0	type0_ind1_ind2_b	2002Aug20
PASS		
0	type0_indr_buff	2002Aug20
PASS		
0	Type0_IQ2_BM	2002Aug20
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug20
PASS		
0	Type0_IQ_BM	2002Aug20

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PASS
0   Type0_IQ_BM_IB_Packet                2002Aug20
PASS
0   Type0_PQ_BM                          2002Aug20
PASS
0   Type0_PQ_BM_Pre_Write_Timer          2002Aug20
PASS
0   type1_4pckts                         2002Aug20
PASS
0   type1_indr_buff                      2002Aug20
PASS
0   type1_pckt                           2002Aug20
PASS
0   Type3_Bitblt                         2002Aug20
PASS
0   Type3_Load_Palette                   2002Aug20
PASS
0   Type3_NOP                            2002Aug20
PASS
654 uram_rd_wrt                          2002Aug20
**FAIL**
655 uram_rd_wrt_rt                       2002Aug20
**FAIL**
656 uram_wrt_rt_ucode                    2002Aug20
**FAIL**          rbbmWr.cmp
0   uram_wrt_ucode                       2002Aug20
PASS
0   wait_for_idle                        2002Aug20
PASS
0   wait_reg_mem_poll_mem0               2002Aug20
PASS
0   wait_reg_mem_poll_mem0_rt            2002Aug20
PASS
0   wait_reg_mem_poll_mem1               2002Aug20
PASS
0   wait_reg_mem_poll_mem2               2002Aug20
PASS
0   wait_reg_mem_poll_mem3               2002Aug20
PASS
0   wait_reg_mem_poll_reg0               2002Aug20
PASS
0   wait_reg_mem_poll_reg1               2002Aug20
PASS
0   wait_reg_mem_poll_reg2               2002Aug20
PASS
0   wait_reg_mem_poll_reg3               2002Aug20
PASS
=====

```


Tests_Run_count = 667 Pass_count = 655 Fail_count = 12 Incomplete_count = 0

=====

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Tue Oct 15 23:45:04 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          753        753        735         97
              RBBM        120        120        120        100
+++++
              TOTAL        873        873        855         97
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
1	2d_indr_buffer_a		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
2	2d_indr_buffer_b		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
3	2d_indr_buffer_c		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
4	2d_indr_buffer_d		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
5	2d_indr_buffer_e		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
6	2d_indr_buffer_f		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
7	2d_indr_buffer_g		2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp		
8	2d_indr_buffer_random		2002Sep18
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep18
	PASS		
0	3d_draw_indx_2_b		2002Sep18
	PASS		
0	IQ2_Mult		2002Sep18
	PASS		
0	IQ2_Wrap_Legacy		2002Sep18
	PASS		
0	IQ_Wrap		2002Sep18
	PASS		
0	Isync_Control		2002Sep18
	PASS		
0	Isync_Control_2D_3D		2002Sep18
	PASS		
0	Isync_Control_3D_2D		2002Sep18
	PASS		
0	RB_BUFSZ_0		2002Sep18
	PASS		
0	RB_BUFSZ_1		2002Sep18
	PASS		
0	RB_Wrap		2002Sep18
	PASS		
0	Small_Text_BM		2002Sep18

PASS		
0	Type0_IQ2_BM	2002Sep18
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep18
PASS		
0	Type0_IQ_BM	2002Sep18
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep18
PASS		
0	Type0_PQ_BM	2002Sep18
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep18
PASS		
0	Type3_Bitblt	2002Sep18
PASS		
0	Type3_Load_Palette	2002Sep18
PASS		
0	Type3_NOP	2002Sep18
PASS		
0	Type3_Polyline	2002Sep18
PASS		
0	Type3_Polyline_Large	2002Sep18
PASS		
0	bitblt_multi	2002Sep18
PASS		
0	brush_32x1mono	2002Sep18
PASS		
0	brush_8x8color_16	2002Sep18
PASS		
0	brush_8x8color_32	2002Sep18
PASS		
0	brush_8x8color_64	2002Sep18
PASS		
0	brush_8x8color_dst_0_1	2002Sep18
PASS		
0	brush_8x8mono	2002Sep18
PASS		
0	brush_color_mono_stress	2002Sep18
PASS		
0	brush_flag	2002Sep18
PASS		
0	brush_type0	2002Sep18
PASS		
0	brush_type1	2002Sep18
PASS		
0	brush_type10	2002Sep18
PASS		
0	brush_type13	2002Sep18

PASS		
0	brush_type14	2002Sep18
PASS		
0	brush_type6	2002Sep18
PASS		
0	brush_type7	2002Sep18
PASS		
0	brush_type_other	2002Sep18
PASS		
0	brush_types_13_14	2002Sep18
PASS		
0	clear_const_wr_en_on_me_init	2002Sep18
PASS		
0	clear_const_wr_en_with_lcc	2002Sep18
PASS		
0	cond_write_mem_poll_mem	2002Sep18
PASS		
0	cond_write_mem_poll_mem2	2002Sep18
PASS		
0	cond_write_mem_poll_mem3	2002Sep18
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep18
PASS		
0	cond_write_mem_poll_reg	2002Sep18
PASS		
0	cond_write_mem_poll_reg2	2002Sep18
PASS		
0	cond_write_mem_poll_reg3	2002Sep18
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep18
PASS		
0	cp_bios_scratch	2002Sep18
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep18
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep18

PASS		
0	cp_ib_base000_bufszXXX	2002Sep18
PASS		
0	cp_ib_base001_bufszXXX	2002Sep18
PASS		
0	cp_ib_base010_bufszXXX	2002Sep18
PASS		
0	cp_ib_base011_bufszXXX	2002Sep18
PASS		
0	cp_ib_base100_bufszXXX	2002Sep18
PASS		
0	cp_ib_base101_bufszXXX	2002Sep18
PASS		
0	cp_ib_base110_bufszXXX	2002Sep18
PASS		
0	cp_ib_base111_bufszXXX	2002Sep18
PASS		
0	cp_interrupt	2002Sep18
PASS		
0	cp_interrupt_rt_events_16	2002Sep18
PASS		
0	cp_iq2_alignment_000	2002Sep18
PASS		
0	cp_iq2_alignment_001	2002Sep18
PASS		
0	cp_iq2_alignment_010	2002Sep18
PASS		
0	cp_iq2_alignment_011	2002Sep18
PASS		
0	cp_iq2_alignment_100	2002Sep18
PASS		
0	cp_iq2_alignment_101	2002Sep18
PASS		
0	cp_iq2_alignment_110	2002Sep18
PASS		
0	cp_iq2_alignment_111	2002Sep18
PASS		
0	cp_iq_alignment_000	2002Sep18
PASS		
0	cp_iq_alignment_001	2002Sep18
PASS		
0	cp_iq_alignment_010	2002Sep18
PASS		
0	cp_iq_alignment_011	2002Sep18
PASS		
0	cp_iq_alignment_100	2002Sep18
PASS		
0	cp_iq_alignment_101	2002Sep18

PASS		
0	cp_iq_alignment_110	2002Sep18
PASS		
0	cp_iq_alignment_111	2002Sep18
PASS		
0	cp_max_size_packet	2002Sep18
PASS		
96	cp_max_size_packet_random	2002Sep18
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep18
PASS		
0	cp_output_stress	2002Sep18
PASS		
0	cp_output_stress_random	2002Sep18
PASS		
0	cp_protection_mode	2002Sep18
PASS		
0	cp_rb_alignment_000	2002Sep18
PASS		
0	cp_rb_alignment_001	2002Sep18
PASS		
0	cp_rb_alignment_010	2002Sep18
PASS		
0	cp_rb_alignment_011	2002Sep18
PASS		
0	cp_rb_alignment_100	2002Sep18
PASS		
0	cp_rb_alignment_101	2002Sep18
PASS		
0	cp_rb_alignment_110	2002Sep18
PASS		
0	cp_rb_alignment_111	2002Sep18
PASS		
0	cp_rb_wptrXXX_base000	2002Sep18
PASS		
0	cp_rb_wptrXXX_base001	2002Sep18
PASS		
0	cp_rb_wptrXXX_base010	2002Sep18
PASS		
0	cp_rb_wptrXXX_base011	2002Sep18
PASS		
0	cp_rb_wptrXXX_base100	2002Sep18
PASS		
0	cp_rb_wptrXXX_base101	2002Sep18
PASS		
0	cp_rb_wptrXXX_base110	2002Sep18
PASS		
0	cp_rb_wptrXXX_base111	2002Sep18

PASS		
117	cp_reg_rw_test	2002Sep18
FAIL		
0	cp_register_reads	2002Sep18
PASS		
0	cp_rt_protection_mode	2002Sep18
PASS		
0	cp_scratch	2002Sep18
PASS		
0	cp_scratch_multi	2002Sep18
PASS		
0	dma_cp_sync	2002Sep18
PASS		
0	dma_dscrstr_chckn_bit	2002Sep18
PASS		
0	dma_dscrstr_flush1	2002Sep18
PASS		
0	dma_dscrstr_flush2	2002Sep18
PASS		
0	dma_dscrstr_flush3	2002Sep18
PASS		
0	dma_m2m_src00_dstXX	2002Sep18
PASS		
0	dma_m2m_src01_dstXX	2002Sep18
PASS		
0	dma_m2m_src10_dstXX	2002Sep18
PASS		
0	dma_m2m_src11_dstXX	2002Sep18
PASS		
0	dma_mem2mem	2002Sep18
PASS		
0	dma_mem2mem_abort	2002Sep18
PASS		
0	dma_mem2mem_alignments	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep18
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep18

PASS		
0	dma_mem2mem_byte_alignment7	2002Sep18
PASS		
0	dma_mem2mem_pio_abort	2002Sep18
PASS		
0	dma_mem2mem_random	2002Sep18
PASS		
0	dma_mem2reg	2002Sep18
PASS		
0	dma_mem2reg_abort	2002Sep18
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep18
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep18
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep18
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep18
PASS		
0	dma_mem2reg_sync	2002Sep18
PASS		
0	dma_r2r_m2m_r2r	2002Sep18
PASS		
0	dma_r2r_m2r_r2r	2002Sep18
PASS		
0	dma_r2r_r2m_r2r	2002Sep18
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep18
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep18
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep18
PASS		
0	dma_reg2mem	2002Sep18
PASS		
0	dma_reg2mem_abort	2002Sep18
PASS		
0	dma_reg2reg	2002Sep18
PASS		
0	dma_reg2reg_2dw	2002Sep18
PASS		
0	dma_reg2reg_daic	2002Sep18
PASS		
0	dma_reg2reg_mult	2002Sep18
PASS		
0	dma_reg2reg_mult2	2002Sep18
PASS		
0	dma_reg2reg_mult3	2002Sep18

PASS		
0	dma_reg2reg_saic	2002Sep18
PASS		
0	dma_reg2reg_sdaic	2002Sep18
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep18
PASS		
0	dma_reg2reg_stress	2002Sep18
PASS		
0	draw_indx_a	2002Sep18
PASS		
0	draw_indx_b	2002Sep18
PASS		
0	draw_indx_c	2002Sep18
PASS		
0	draw_indx_indr1_a	2002Sep18
PASS		
0	draw_indx_indr1_b	2002Sep18
PASS		
0	draw_indx_indr1_short_a	2002Sep18
PASS		
0	draw_indx_indr1_short_b	2002Sep18
PASS		
0	draw_indx_indr1_stress	2002Sep18
PASS		
0	draw_indx_indr2_a	2002Sep18
PASS		
0	draw_indx_indr2_b	2002Sep18
PASS		
0	draw_indx_indr2_short_a	2002Sep18
PASS		
0	draw_indx_indr2_short_b	2002Sep18
PASS		
0	draw_indx_indr2_stress	2002Sep18
PASS		
0	draw_indx_ring1	2002Sep18
PASS		
0	draw_indx_ring2	2002Sep18
PASS		
0	draw_indx_ring_short1	2002Sep18
PASS		
0	draw_indx_ring_short2	2002Sep18
PASS		
0	draw_indx_ring_stress	2002Sep18
PASS		
0	draw_indx_short_a	2002Sep18
PASS		
0	draw_indx_short_b	2002Sep18

PASS		
0	draw_indx_short_c	2002Sep18
PASS		
0	draw_indx_stress_a	2002Sep18
PASS		
0	draw_indx_stress_b	2002Sep18
PASS		
0	draw_indx_stress_c	2002Sep18
PASS		
0	event_timestamp_write	2002Sep18
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep18
PASS		
0	ft_indr1_july22	2002Sep18
PASS		
0	ft_indr2_july22	2002Sep18
PASS		
0	ft_july22	2002Sep18
PASS		
198	ft_july22_random	2002Sep18
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	header_dump	2002Sep18
PASS		
0	header_dump_rt	2002Sep18
PASS		
0	header_dump_small	2002Sep18
PASS		
0	hostdata_blt	2002Sep18
PASS		
0	im_load_a	2002Sep18
PASS		
0	im_load_b	2002Sep18
PASS		
0	im_load_cid_mismatch	2002Sep18
PASS		
0	im_load_immediate	2002Sep18
PASS		
0	im_load_immediate_nrt_me	2002Sep18
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep18
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep18
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep18
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep18
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep18

PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep18
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep18
PASS		
0	im_load_indr1_a	2002Sep18
PASS		
0	im_load_indr1_b	2002Sep18
PASS		
0	im_load_indr2_a	2002Sep18
PASS		
0	im_load_indr2_b	2002Sep18
PASS		
0	im_load_invalidate	2002Sep18
PASS		
0	im_load_match	2002Sep18
PASS		
0	im_load_match2	2002Sep18
PASS		
0	im_load_nrt_me	2002Sep18
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep18
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep18
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep18
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep18
PASS		
0	im_load_pfmatch_disable	2002Sep18
PASS		
0	im_load_ring_a	2002Sep18
PASS		
0	im_load_ring_b	2002Sep18
PASS		
0	im_load_rt	2002Sep18
PASS		
0	im_load_rt2	2002Sep18
PASS		
0	im_load_rt3	2002Sep18
PASS		
0	im_load_rt4	2002Sep18
PASS		
0	im_load_sz_mismatch	2002Sep18
PASS		
0	incremental_update_const	2002Sep18
PASS		
236	incremental_update_instr	2002Sep18

```

**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  incremental_update_state                2002Sep18
PASS
0  indirect_buffer_a                       2002Sep18
PASS
0  indirect_buffer_b                       2002Sep18
PASS
0  indirect_buffer_c                       2002Sep18
PASS
0  indirect_buffer_d                       2002Sep18
PASS
0  indirect_buffer_e                       2002Sep18
PASS
0  indirect_buffer_f                       2002Sep18
PASS
0  indirect_buffer_g                       2002Sep18
PASS
0  indirect_buffer_h                       2002Sep18
PASS
0  indirect_buffer_i                       2002Sep18
PASS
0  indirect_buffer_j                       2002Sep18
PASS
0  indirect_buffer_k                       2002Sep18
PASS
0  indirect_buffer_l                       2002Sep18
PASS
0  indirect_buffer_mp_a                    2002Sep18
PASS
0  indirect_buffer_mp_b                    2002Sep18
PASS
0  indirect_buffer_mp_c                    2002Sep18
PASS
0  indirect_buffer_mp_d                    2002Sep18
PASS
0  indirect_buffer_mp_e                    2002Sep18
PASS
0  indirect_buffer_mp_f                    2002Sep18
PASS
0  invalid_opcode                          2002Sep18
PASS
257 invalidate_on_me_init                  2002Sep18
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  invalidate_state_constants              2002Sep18
PASS
0  invalidate_state_shaders                2002Sep18
PASS
260 invalidate_state_shaders2              2002Sep18

```

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**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  invalidate_state_sublks                2002Sep18
PASS
0  invalidate_state_sublks2              2002Sep18
PASS
0  iq1_iq2_mult                          2002Sep18
PASS
0  iq1_iq2_push_wrap                    2002Sep18
PASS
0  iq2_csq_full                          2002Sep18
PASS
0  ld_cnst_cntx_coherency_cntrl          2002Sep18
PASS
267 ld_cnst_cntx_coherency_cntrl2        2002Sep18
**FAIL**          cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp
0  ld_cnst_cntx_indr1_replicate           2002Sep18
PASS
0  ld_cnst_cntx_indr1_replicate4         2002Sep18
PASS
0  ld_cnst_cntx_indr1_replicate8         2002Sep18
PASS
0  ld_cnst_cntx_indr1_simple             2002Sep18
PASS
0  ld_cnst_cntx_indr2_replicate          2002Sep18
PASS
0  ld_cnst_cntx_indr2_replicate4        2002Sep18
PASS
0  ld_cnst_cntx_indr2_replicate8        2002Sep18
PASS
0  ld_cnst_cntx_indr2_simple             2002Sep18
PASS
0  ld_cnst_cntx_match                    2002Sep18
PASS
277 ld_cnst_cntx_match_valid_preserved   2002Sep18
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
0  ld_cnst_cntx_mismatch_alutex          2002Sep18
PASS
0  ld_cnst_cntx_mismatch_base            2002Sep18
PASS
0  ld_cnst_cntx_mismatch_force           2002Sep18
PASS
0  ld_cnst_cntx_pfmatch_disable          2002Sep18
PASS
0  ld_cnst_cntx_replicate4_alu           2002Sep18
PASS
0  ld_cnst_cntx_replicate4_tex           2002Sep18
PASS
0  ld_cnst_cntx_replicate8               2002Sep18

```

PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep18
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep18
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep18
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep18
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep18
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep18
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep18
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep18
PASS		
0	load_constant_context_rt	2002Sep18
PASS		
0	load_palette	2002Sep18
PASS		
0	lp_2d3d	2002Sep18
PASS		
0	mem_write	2002Sep18
PASS		
0	mem_write_rt	2002Sep18
PASS		
0	mpeg_index	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep18
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep18
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep18
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep18

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep18
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep18
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep18
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep18
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep18
PASS		
0	nop_eating_disabled	2002Sep18
PASS		
0	paint_multi	2002Sep18
PASS		
0	paint_packet	2002Sep18
PASS		
0	paint_packet_gui_control	2002Sep18
PASS		
0	pitch_clip	2002Sep18
PASS		
0	ply_nextscan	2002Sep18
PASS		
320	rbbm_WU_2D_idle_01	2002Sep18
FAIL		
321	rbbm_WU_2D_idle_02	2002Sep18
FAIL		
322	rbbm_WU_2D_idle_03	2002Sep18
FAIL		
323	rbbm_WU_2D_idle_04	2002Sep18
FAIL		
0	rbbm_WU_2D_idle_05	2002Sep18
PASS		
325	rbbm_WU_2D_idle_06	2002Sep18
FAIL		
326	rbbm_WU_2D_idle_07	2002Sep18
FAIL		
327	rbbm_WU_2D_idle_08	2002Sep18
FAIL		
328	rbbm_WU_2D_idle_09	2002Sep18
FAIL		
329	rbbm_WU_2D_idle_clean_01	2002Sep18
FAIL		
330	rbbm_WU_2D_idle_clean_02	2002Sep18
FAIL		
331	rbbm_WU_2D_idle_clean_03	2002Sep18
FAIL		
332	rbbm_WU_2D_idle_clean_04	2002Sep18

FAIL		
0	rbbm_WU_2D_idle_clean_05	2002Sep18
	PASS	
334	rbbm_WU_2D_idle_clean_06	2002Sep18
FAIL		
335	rbbm_WU_2D_idle_clean_07	2002Sep18
FAIL		
336	rbbm_WU_2D_idle_clean_08	2002Sep18
FAIL		
337	rbbm_WU_2D_idle_clean_09	2002Sep18
FAIL		
338	rbbm_WU_2D_idle_clean_10	2002Sep18
FAIL		
339	rbbm_WU_2D_idle_clean_11	2002Sep18
FAIL		
0	rbbm_WU_2D_idle_clean_12	2002Sep18
	PASS	
341	rbbm_WU_3D_idle_01	2002Sep18
FAIL		
342	rbbm_WU_3D_idle_02	2002Sep18
FAIL		
343	rbbm_WU_3D_idle_03	2002Sep18
FAIL		
344	rbbm_WU_3D_idle_04	2002Sep18
FAIL		
0	rbbm_WU_3D_idle_05	2002Sep18
	PASS	
346	rbbm_WU_3D_idle_06	2002Sep18
FAIL		
347	rbbm_WU_3D_idle_07	2002Sep18
FAIL		
348	rbbm_WU_3D_idle_08	2002Sep18
FAIL		
349	rbbm_WU_3D_idle_09	2002Sep18
FAIL		
350	rbbm_WU_3D_idle_clean_01	2002Sep18
FAIL		
351	rbbm_WU_3D_idle_clean_02	2002Sep18
FAIL		
352	rbbm_WU_3D_idle_clean_03	2002Sep18
FAIL		
353	rbbm_WU_3D_idle_clean_04	2002Sep18
FAIL		
0	rbbm_WU_3D_idle_clean_05	2002Sep18
	PASS	
355	rbbm_WU_3D_idle_clean_06	2002Sep18
FAIL		
356	rbbm_WU_3D_idle_clean_07	2002Sep18

```

**FAIL**
357  rbbm_WU_3D_idle_clean_08                2002Sep18
**FAIL**
358  rbbm_WU_3D_idle_clean_09                2002Sep18
**FAIL**
359  rbbm_WU_3D_idle_clean_10               2002Sep18
**FAIL**
360  rbbm_WU_3D_idle_clean_11               2002Sep18
**FAIL**
0    rbbm_WU_3D_idle_clean_12                2002Sep18
PASS
0    rbbm_WU_BIF_RBBM_agp_flush_off          2002Sep18
PASS
0    rbbm_WU_BOTH_GRP update_pending_off    2002Sep18
PASS
0    rbbm_WU_CP_dma_idle                     2002Sep18
PASS
0    rbbm_WU_CRTC_D1MODE_vline_fe           2002Sep18
PASS
0    rbbm_WU_CRTC_D1MODE_vline_on           2002Sep18
PASS
0    rbbm_WU_CRTC_D1MODE_vline_re           2002Sep18
PASS
0    rbbm_WU_CRTC_D2MODE_vline_fe           2002Sep18
PASS
0    rbbm_WU_CRTC_D2MODE_vline_on           2002Sep18
PASS
0    rbbm_WU_CRTC_D2MODE_vline_re           2002Sep18
PASS
0    rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep18
PASS
0    rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep18
PASS
0    rbbm_WU_MH_hdp_clean                    2002Sep18
PASS
0    rbbm_WU_OV_FLIP_D1OVL_update_pending_off 2002Sep18
PASS
0    rbbm_WU_OV_FLIP_D2OVL_update_pending_off 2002Sep18
PASS
0    rbbm_WU_VIPH0_dma_idle                  2002Sep18
PASS
0    rbbm_WU_VIPH1_dma_idle                  2002Sep18
PASS
0    rbbm_WU_VIPH2_dma_idle                  2002Sep18
PASS
0    rbbm_WU_VIPH3_dma_idle                  2002Sep18
PASS
0    rbbm_WU_cmdfifo_entries                 2002Sep18

```

PASS		
0	rbbm_WU_idct_semaphore	2002Sep18
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep18
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep18
PASS		
0	rbbm_WUrt_BOTH_GRP update_pending_off	2002Sep18
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRP update_pending_off	2002Sep18
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRP update_pending_off	2002Sep18
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep18
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL update_pending_off	2002Sep18
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL update_pending_off	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_clean_01	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep18
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep18

PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep18
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep18
PASS		
0	rbbm_WUrt_VIPH1_dma_idle	2002Sep18
PASS		
0	rbbm_WUrt_VIPH2_dma_idle	2002Sep18
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep18
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep18
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep18
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep18
PASS		
413	rbbm_cp_queued_holds_nonqueued	2002Sep18
FAIL	cp_rbbm.cmp	
414	rbbm_cp_queued_rdwr	2002Sep18
FAIL	cp_rbbm.cmp	
0	rbbm_isync_cpscratch_idlegui	2002Sep18
PASS		
0	rbbm_isync_wait_idlegui	2002Sep18
PASS		
0	rbbm_soft_reset	2002Sep18
PASS		
0	reg_to_mem	2002Sep18
PASS		
0	reg_to_mem_rt	2002Sep18
PASS		
0	rop_cfd_wmd	2002Sep18
PASS		
0	rstr_ordr	2002Sep18
PASS		
0	rt_invalid_opcode	2002Sep18
PASS		
423	rtee_arm_wait_idle_trigger_events_16	2002Sep18
FAIL		
0	rtee_discretes_arm_rearm_events_32	2002Sep18
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep18

PASS		
0	rtee_discretes_falling_04_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_12_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep18

PASS		
0	rtee_discretes_falling_28_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep18
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_04_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep18

PASS		
0	rtee_discretes_invert_20_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_28_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep18
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep18
PASS		
0	rtee_discretes_level_00_events_16	2002Sep18
PASS		
0	rtee_discretes_level_01_events_16	2002Sep18
PASS		
0	rtee_discretes_level_02_events_16	2002Sep18
PASS		
0	rtee_discretes_level_03_events_16	2002Sep18
PASS		
0	rtee_discretes_level_04_events_16	2002Sep18
PASS		
0	rtee_discretes_level_05_events_16	2002Sep18
PASS		
0	rtee_discretes_level_06_events_16	2002Sep18
PASS		
0	rtee_discretes_level_07_events_16	2002Sep18
PASS		
0	rtee_discretes_level_08_events_16	2002Sep18
PASS		
0	rtee_discretes_level_09_events_16	2002Sep18
PASS		
0	rtee_discretes_level_10_events_16	2002Sep18
PASS		
0	rtee_discretes_level_11_events_16	2002Sep18

PASS		
0	rtee_discretes_level_12_events_16	2002Sep18
PASS		
0	rtee_discretes_level_13_events_16	2002Sep18
PASS		
0	rtee_discretes_level_14_events_16	2002Sep18
PASS		
0	rtee_discretes_level_15_events_16	2002Sep18
PASS		
0	rtee_discretes_level_16_events_16	2002Sep18
PASS		
0	rtee_discretes_level_17_events_16	2002Sep18
PASS		
0	rtee_discretes_level_18_events_16	2002Sep18
PASS		
0	rtee_discretes_level_19_events_16	2002Sep18
PASS		
0	rtee_discretes_level_20_events_16	2002Sep18
PASS		
0	rtee_discretes_level_21_events_16	2002Sep18
PASS		
0	rtee_discretes_level_22_events_16	2002Sep18
PASS		
0	rtee_discretes_level_23_events_16	2002Sep18
PASS		
0	rtee_discretes_level_24_events_16	2002Sep18
PASS		
0	rtee_discretes_level_25_events_16	2002Sep18
PASS		
0	rtee_discretes_level_26_events_16	2002Sep18
PASS		
0	rtee_discretes_level_27_events_16	2002Sep18
PASS		
0	rtee_discretes_level_28_events_16	2002Sep18
PASS		
0	rtee_discretes_level_29_events_16	2002Sep18
PASS		
0	rtee_discretes_level_30_events_16	2002Sep18
PASS		
0	rtee_discretes_level_31_events_16	2002Sep18
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep18

PASS		
0	rtee_discretes_rising_03_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_11_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep18
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0	rtee_discretes_rising_20_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep18

PASS		
0	rtee_discretes_rising_27_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep18
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep18
PASS		
0	rtee_display1_comp_events_16	2002Sep18
PASS		
0	rtee_display2_comp_events_16	2002Sep18
PASS		
0	rtee_mem_linear_search_events_2	2002Sep18
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep18
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep18
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep18
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep18
PASS		
0	rtee_prefetch_parse_nops	2002Sep18
PASS		
0	rtee_reg_linear_search_events_2	2002Sep18
PASS		
571	rtee_reg_poll_comp_000_events_16	2002Sep18
FAIL		
572	rtee_reg_poll_comp_001_events_16	2002Sep18

```

**FAIL**
573 rtee_reg_poll_comp_010_events_16          2002Sep18
**FAIL**
574 rtee_reg_poll_comp_011_events_16          2002Sep18
**FAIL**
575 rtee_reg_poll_comp_100_events_16          2002Sep18
**FAIL**
576 rtee_reg_poll_comp_101_events_16          2002Sep18
**FAIL**
577 rtee_reg_poll_comp_110_events_16          2002Sep18
**FAIL**
578 rtee_reg_poll_comp_111_events_16          2002Sep18
**FAIL**
579 rtee_reg_poll_comp_queued_nonqueued_events_16 2002Sep18
**FAIL**
0 rtee_vip_comp_events_16                     2002Sep18
PASS
0 scratch_mem_read                            2002Sep18
PASS
0 scratch_mem_read2                          2002Sep18
PASS
0 set_constant                               2002Sep18
PASS
0 set_constant2                              2002Sep18
PASS
0 set_constant_alu_max1                      2002Sep18
PASS
0 set_constant_alu_max2                      2002Sep18
PASS
0 set_constant_bool_max1                    2002Sep18
PASS
0 set_constant_bool_max2                    2002Sep18
PASS
0 set_constant_indr1_short                   2002Sep18
PASS
0 set_constant_indr1_short2                  2002Sep18
PASS
0 set_constant_indr1_write_enable            2002Sep18
PASS
0 set_constant_indr1_write_enable_lcc        2002Sep18
PASS
0 set_constant_indr2_short                   2002Sep18
PASS
0 set_constant_indr2_short2                  2002Sep18
PASS
0 set_constant_indr2_write_enable            2002Sep18
PASS
0 set_constant_indr2_write_enable_lcc        2002Sep18

```

PASS		
0	set_constant_loop_max1	2002Sep18
PASS		
0	set_constant_loop_max2	2002Sep18
PASS		
0	set_constant_ring_short	2002Sep18
PASS		
0	set_constant_ring_short2	2002Sep18
PASS		
0	set_constant_ring_write_enable	2002Sep18
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep18
PASS		
0	set_constant_short	2002Sep18
PASS		
0	set_constant_short2	2002Sep18
PASS		
0	set_constant_tex_max1	2002Sep18
PASS		
0	set_constant_tex_max2	2002Sep18
PASS		
0	set_constant_write_enable	2002Sep18
PASS		
0	set_constant_write_enable_lcc	2002Sep18
PASS		
0	set_constant_write_enable_lcc2	2002Sep18
PASS		
0	set_scissors	2002Sep18
PASS		
0	set_st_indrl_match	2002Sep18
PASS		
0	set_st_indrl_mismatch	2002Sep18
PASS		
0	set_st_indrl_mismatch2	2002Sep18
PASS		
0	set_st_indrl_mismatch3	2002Sep18
PASS		
0	set_st_indrl_mismatch4	2002Sep18
PASS		
0	set_st_indrl_ps	2002Sep18
PASS		
0	set_st_indrl_ps_only	2002Sep18
PASS		
0	set_st_indrl_sublk0	2002Sep18
PASS		
0	set_st_indrl_sublk1	2002Sep18
PASS		
0	set_st_indrl_sublk1_only	2002Sep18

PASS		
0	set_st_indr1_sublk2	2002Sep18
PASS		
0	set_st_indr1_sublk2_only	2002Sep18
PASS		
0	set_st_indr1_sublk3	2002Sep18
PASS		
0	set_st_indr1_sublk3_only	2002Sep18
PASS		
0	set_st_indr1_sublk4	2002Sep18
PASS		
0	set_st_indr1_sublk4_only	2002Sep18
PASS		
0	set_st_indr1_sublk5	2002Sep18
PASS		
0	set_st_indr1_sublk5_only	2002Sep18
PASS		
0	set_st_indr1_sublk6	2002Sep18
PASS		
0	set_st_indr1_sublk6_only	2002Sep18
PASS		
0	set_st_indr1_sublk7	2002Sep18
PASS		
0	set_st_indr1_sublk7_only	2002Sep18
PASS		
0	set_st_indr1_vs	2002Sep18
PASS		
0	set_st_indr1_vs_only	2002Sep18
PASS		
0	set_st_indr2_match	2002Sep18
PASS		
0	set_st_indr2_mismatch	2002Sep18
PASS		
0	set_st_indr2_mismatch2	2002Sep18
PASS		
0	set_st_indr2_mismatch3	2002Sep18
PASS		
0	set_st_indr2_mismatch4	2002Sep18
PASS		
0	set_st_indr2_ps	2002Sep18
PASS		
0	set_st_indr2_ps_only	2002Sep18
PASS		
0	set_st_indr2_sublk0	2002Sep18
PASS		
0	set_st_indr2_sublk1	2002Sep18
PASS		
0	set_st_indr2_sublk1_only	2002Sep18

PASS		
0	set_st_indr2_sublk2	2002Sep18
PASS		
0	set_st_indr2_sublk2_only	2002Sep18
PASS		
0	set_st_indr2_sublk3	2002Sep18
PASS		
0	set_st_indr2_sublk3_only	2002Sep18
PASS		
0	set_st_indr2_sublk4	2002Sep18
PASS		
0	set_st_indr2_sublk4_only	2002Sep18
PASS		
0	set_st_indr2_sublk5	2002Sep18
PASS		
0	set_st_indr2_sublk5_only	2002Sep18
PASS		
0	set_st_indr2_sublk6	2002Sep18
PASS		
0	set_st_indr2_sublk6_only	2002Sep18
PASS		
0	set_st_indr2_sublk7	2002Sep18
PASS		
0	set_st_indr2_sublk7_only	2002Sep18
PASS		
0	set_st_indr2_vs	2002Sep18
PASS		
0	set_st_indr2_vs_only	2002Sep18
PASS		
0	set_st_pfmatch_disabled	2002Sep18
PASS		
0	set_st_ring_match	2002Sep18
PASS		
0	set_st_ring_mismatch	2002Sep18
PASS		
0	set_st_ring_mismatch2	2002Sep18
PASS		
0	set_st_ring_mismatch3	2002Sep18
PASS		
0	set_st_ring_mismatch4	2002Sep18
PASS		
0	set_st_ring_ps	2002Sep18
PASS		
0	set_st_ring_ps_only	2002Sep18
PASS		
0	set_st_ring_sublk0	2002Sep18
PASS		
0	set_st_ring_sublk1	2002Sep18

PASS		
0	set_st_ring_subblk1_only	2002Sep18
PASS		
0	set_st_ring_subblk2	2002Sep18
PASS		
0	set_st_ring_subblk2_only	2002Sep18
PASS		
0	set_st_ring_subblk3	2002Sep18
PASS		
0	set_st_ring_subblk3_only	2002Sep18
PASS		
0	set_st_ring_subblk4	2002Sep18
PASS		
0	set_st_ring_subblk4_only	2002Sep18
PASS		
0	set_st_ring_subblk5	2002Sep18
PASS		
0	set_st_ring_subblk5_only	2002Sep18
PASS		
0	set_st_ring_subblk6	2002Sep18
PASS		
0	set_st_ring_subblk6_only	2002Sep18
PASS		
0	set_st_ring_subblk7	2002Sep18
PASS		
0	set_st_ring_subblk7_only	2002Sep18
PASS		
0	set_st_ring_vs	2002Sep18
PASS		
0	set_st_ring_vs_only	2002Sep18
PASS		
0	set_st_stress_a	2002Sep18
PASS		
685	set_st_stress_b	2002Sep18
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Sep18
PASS		
0	set_st_stress_d	2002Sep18
PASS		
0	set_st_stress_e	2002Sep18
PASS		
0	set_st_stress_f	2002Sep18
PASS		
690	set_state_rt_a	2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp	
691	set_state_rt_b	2002Sep18
FAIL	cp_rbbm.cmp rbbmWr.cmp	
692	set_state_rt_c	2002Sep18

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
693  set_state_rt_d                                2002Sep18
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0    smoke_nrt                                     2002Sep18
PASS
0    src_clip                                       2002Sep18
PASS
0    srf_cohr                                       2002Sep18
PASS
0    trans_bitblt                                    2002Sep18
PASS
0    type0_2long_pckts                              2002Sep18
PASS
0    type0_3indr_buffs                              2002Sep18
PASS
0    type0_4pckts                                    2002Sep18
PASS
0    type0_incr_updt_cnst                            2002Sep18
PASS
0    type0_incr_updt_instrmem                        2002Sep18
PASS
0    type0_incr_updt_st                              2002Sep18
PASS
0    type0_incr_updt_stress                          2002Sep18
PASS
0    type0_ind1_ind2_a                               2002Sep18
PASS
0    type0_ind1_ind2_b                               2002Sep18
PASS
0    type0_indr_buff                                 2002Sep18
PASS
0    type1_4pckts                                    2002Sep18
PASS
0    type1_indr_buff                                 2002Sep18
PASS
0    type1_pckt                                      2002Sep18
PASS
711  type_random                                    2002Sep18
**FAIL**          cp_mh_req.cmp mh_cp_rd.cmp
712  uram_wrt_rt_ucode                              2002Sep18
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
713  uram_wrt_ucode                                 2002Sep18
**FAIL**          rbbmWr.cmp
714  viz_query_a                                     2002Sep18
**FAIL**          rbbmWr.cmp
715  viz_query_draw_indx                             2002Sep18
**FAIL**          rbbmWr.cmp
716  viz_query_draw_indx_indr1                      2002Sep18

```



```

**FAIL**      rbbmWr.cmp
717 viz_query_draw_indx_indr2      2002Sep18
**FAIL**      rbbmWr.cmp
718 viz_query_draw_indx_multipass  2002Sep18
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
719 viz_query_draw_indx_ring      2002Sep18
**FAIL**      rbbmWr.cmp
720 viz_query_indr1                2002Sep18
**FAIL**      rbbmWr.cmp
0 viz_query_indr1_simple           2002Sep18
PASS
722 viz_query_indr2                2002Sep18
**FAIL**      rbbmWr.cmp
0 viz_query_indr2_simple           2002Sep18
PASS
724 viz_query_ring                 2002Sep18
**FAIL**      rbbmWr.cmp
0 viz_query_ring_simple            2002Sep18
PASS
0 wait_for_idle                    2002Sep18
PASS
727 wait_reg_mem_poll_mem0         2002Sep18
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_mem0_rt        2002Sep18
PASS
729 wait_reg_mem_poll_mem1         2002Sep18
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
730 wait_reg_mem_poll_mem2         2002Sep18
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
731 wait_reg_mem_poll_mem3         2002Sep18
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0 wait_reg_mem_poll_reg0           2002Sep18
PASS
0 wait_reg_mem_poll_reg1           2002Sep18
PASS
0 wait_reg_mem_poll_reg2           2002Sep18
PASS
0 wait_reg_mem_poll_reg3           2002Sep18
PASS

```

```

=====
Tests_Run_count = 735 Pass_count = 650 Fail_count = 85 Incomplete_count = 0
=====

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Aug13
	PASS		
0	2d_indr_buffer_b		2002Aug13
	PASS		
0	2d_indr_buffer_c		2002Aug13
	PASS		
4	2d_indr_buffer_d		2002Aug13
	INCOMPLETE		
0	2d_indr_buffer_e		2002Aug13
	PASS		
0	2d_indr_buffer_f		2002Aug13
	PASS		
7	2d_indr_buffer_g		2002Aug13
	INCOMPLETE		
0	3d_draw_indx_2		2002Aug13
	PASS		
0	3d_draw_indx_2_b		2002Aug13
	PASS		
10	bitblt_multi		2002Aug13
	FAIL rbbmWr.cmp		
11	brush_flag		2002Aug13
	FAIL rbbmWr.cmp		
0	cond_write_mem_poll_mem		2002Aug13
	PASS		
0	cond_write_mem_poll_mem2		2002Aug13
	PASS		
0	cond_write_mem_poll_mem3		2002Aug13
	PASS		
0	cond_write_mem_poll_mem_rt		2002Aug13
	PASS		
0	cond_write_mem_poll_reg		2002Aug13
	PASS		
0	cond_write_mem_poll_reg2		2002Aug13
	PASS		
0	cond_write_mem_poll_reg3		2002Aug13
	PASS		
0	cp_acpi_recovery_soft_reset		2002Aug13
	PASS		
0	cp_bios_scratch		2002Aug13

PASS		
0	cp_ib2_base000_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug13
PASS		
0	cp_ib_base000_bufszXXX	2002Aug13
PASS		
0	cp_ib_base001_bufszXXX	2002Aug13
PASS		
0	cp_ib_base010_bufszXXX	2002Aug13
PASS		
0	cp_ib_base011_bufszXXX	2002Aug13
PASS		
0	cp_ib_base100_bufszXXX	2002Aug13
PASS		
0	cp_ib_base101_bufszXXX	2002Aug13
PASS		
35	cp_ib_base110_bufszXXX	2002Aug13
FAIL	cp_mh_req.cmp	
36	cp_ib_base111_bufszXXX	2002Aug13
FAIL	cp_mh_req.cmp	
0	cp_interrupt	2002Aug13
PASS		
0	cp_interrupt_rt_events_16	2002Aug13
PASS		
0	cp_iq2_alignment_000	2002Aug13
PASS		
0	cp_iq2_alignment_001	2002Aug13
PASS		
0	cp_iq2_alignment_010	2002Aug13
PASS		
0	cp_iq2_alignment_011	2002Aug13
PASS		
0	cp_iq2_alignment_100	2002Aug13
PASS		
0	cp_iq2_alignment_101	2002Aug13

PASS		
0	cp_iq2_alignment_110	2002Aug13
PASS		
0	cp_iq2_alignment_111	2002Aug13
PASS		
0	cp_iq_alignment_000	2002Aug13
PASS		
0	cp_iq_alignment_001	2002Aug13
PASS		
0	cp_iq_alignment_010	2002Aug13
PASS		
0	cp_iq_alignment_011	2002Aug13
PASS		
0	cp_iq_alignment_100	2002Aug13
PASS		
0	cp_iq_alignment_101	2002Aug13
PASS		
0	cp_iq_alignment_110	2002Aug13
PASS		
0	cp_iq_alignment_111	2002Aug13
PASS		
0	cp_max_size_packet	2002Aug13
PASS		
0	cp_nrt_rt_protection_mode	2002Aug13
PASS		
57	cp_output_stress	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_protection_mode	2002Aug13
PASS		
0	cp_rb_alignment_000	2002Aug13
PASS		
0	cp_rb_alignment_001	2002Aug13
PASS		
0	cp_rb_alignment_010	2002Aug13
PASS		
0	cp_rb_alignment_011	2002Aug13
PASS		
0	cp_rb_alignment_100	2002Aug13
PASS		
0	cp_rb_alignment_101	2002Aug13
PASS		
0	cp_rb_alignment_110	2002Aug13
PASS		
0	cp_rb_alignment_111	2002Aug13
PASS		
0	cp_register_reads	2002Aug13
PASS		
0	cp_reg_rw_test	2002Aug13

PASS		
0	cp_rt_protection_mode	2002Aug13
PASS		
0	cp_scratch	2002Aug13
PASS		
0	cp_scratch_multi	2002Aug13
PASS		
0	dma_cp_sync	2002Aug13
PASS		
0	dma_dscrstr_chckn_bit	2002Aug13
PASS		
0	dma_dscrstr_flush1	2002Aug13
PASS		
0	dma_dscrstr_flush2	2002Aug13
PASS		
0	dma_dscrstr_flush3	2002Aug13
PASS		
0	dma_m2m_src00_dstXX	2002Aug13
PASS		
0	dma_m2m_src01_dstXX	2002Aug13
PASS		
0	dma_m2m_src10_dstXX	2002Aug13
PASS		
0	dma_m2m_src11_dstXX	2002Aug13
PASS		
0	dma_mem2mem	2002Aug13
PASS		
0	dma_mem2mem_abort	2002Aug13
PASS		
0	dma_mem2mem_alignments	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug13
PASS		
0	dma_mem2mem_pio_abort	2002Aug13

PASS		
0	dma_mem2reg	2002Aug13
PASS		
0	dma_mem2reg_abort	2002Aug13
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug13
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug13
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug13
PASS		
0	dma_mem2reg_sync	2002Aug13
PASS		
0	dma_r2r_m2m_r2r	2002Aug13
PASS		
0	dma_r2r_m2r_r2r	2002Aug13
PASS		
0	dma_r2r_r2m_r2r	2002Aug13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug13
PASS		
0	dma_reg2mem	2002Aug13
PASS		
0	dma_reg2mem_abort	2002Aug13
PASS		
0	dma_reg2reg	2002Aug13
PASS		
0	dma_reg2reg_2dw	2002Aug13
PASS		
0	dma_reg2reg_daic	2002Aug13
PASS		
0	dma_reg2reg_mult	2002Aug13
PASS		
0	dma_reg2reg_mult2	2002Aug13
PASS		
0	dma_reg2reg_mult3	2002Aug13
PASS		
0	dma_reg2reg_saic	2002Aug13
PASS		
0	dma_reg2reg_sdaic	2002Aug13
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug13
PASS		
0	dma_reg2reg_stress	2002Aug13

PASS		
0	draw_indx_a	2002Aug13
PASS		
0	draw_indx_b	2002Aug13
PASS		
0	draw_indx_c	2002Aug13
PASS		
0	draw_indx_indr1_a	2002Aug13
PASS		
0	draw_indx_indr1_b	2002Aug13
PASS		
0	draw_indx_indr1_short_a	2002Aug13
PASS		
0	draw_indx_indr1_short_b	2002Aug13
PASS		
0	draw_indx_indr1_stress	2002Aug13
PASS		
0	draw_indx_indr2_a	2002Aug13
PASS		
0	draw_indx_indr2_b	2002Aug13
PASS		
0	draw_indx_indr2_short_a	2002Aug13
PASS		
0	draw_indx_indr2_short_b	2002Aug13
PASS		
0	draw_indx_indr2_stress	2002Aug13
PASS		
0	draw_indx_ring1	2002Aug13
PASS		
0	draw_indx_ring2	2002Aug13
PASS		
0	draw_indx_ring_short1	2002Aug13
PASS		
0	draw_indx_ring_short2	2002Aug13
PASS		
0	draw_indx_ring_stress	2002Aug13
PASS		
0	draw_indx_short_a	2002Aug13
PASS		
0	draw_indx_short_b	2002Aug13
PASS		
0	draw_indx_short_c	2002Aug13
PASS		
0	draw_indx_stress_a	2002Aug13
PASS		
0	draw_indx_stress_b	2002Aug13
PASS		
0	draw_indx_stress_c	2002Aug13

PASS		
0	event_timestamp_write	2002Aug13
PASS		
142	event_timestamp_write_multicontext_maxcontext_7	2002Aug13
FAIL		
0	ft_indr1_july22	2002Aug13
PASS		
0	ft_indr2_july22	2002Aug13
PASS		
145	ft_july22	2002Aug13
FAIL	cp_mh_req.cmp	
0	header_dump	2002Aug13
PASS		
0	header_dump_rt	2002Aug13
PASS		
0	header_dump_small	2002Aug13
PASS		
0	im_load	2002Aug12
PASS		
0	im_load_a	2002Aug13
PASS		
0	im_load_b	2002Aug13
PASS		
0	im_load_cid_mismatch	2002Aug13
PASS		
0	im_load_immediate	2002Aug13
PASS		
0	im_load_immediate_nrt_me	2002Aug13
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug13
PASS		
156	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug13
INCOMPLETE		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug13
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug13
PASS		
159	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug13
INCOMPLETE		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug13
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug13
PASS		
0	im_load_indr1_a	2002Aug13
PASS		
0	im_load_indr1_b	2002Aug13
PASS		
0	im_load_indr2_a	2002Aug13

PASS		
0	im_load_indr2_b	2002Aug13
PASS		
0	im_load_invalidate	2002Aug13
PASS		
0	im_load_match	2002Aug13
PASS		
0	im_load_match2	2002Aug13
PASS		
0	im_load_nrt_me	2002Aug13
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug13
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug13
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug13
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug13
PASS		
0	im_load_pfmatch_disable	2002Aug13
PASS		
0	im_load_rt	2002Aug13
PASS		
0	im_load_rt2	2002Aug13
PASS		
0	im_load_rt3	2002Aug13
PASS		
178	im_load_rt4	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	im_load_sz_mismatch	2002Aug13
PASS		
0	incremental_update_const	2002Aug13
PASS		
181	incremental_update_instr	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	incremental_update_state	2002Aug13
PASS		
0	indirect_buffer_a	2002Aug13
PASS		
0	indirect_buffer_b	2002Aug13
PASS		
0	indirect_buffer_c	2002Aug13
PASS		
0	indirect_buffer_d	2002Aug13
PASS		
0	indirect_buffer_e	2002Aug13
PASS		
0	indirect_buffer_f	2002Aug13

PASS		
189	indirect_buffer_g	2002Aug13
INCOMPLETE		
0	indirect_buffer_h	2002Aug13
PASS		
191	indirect_buffer_i	2002Aug13
INCOMPLETE		
192	indirect_buffer_j	2002Aug13
INCOMPLETE		
0	indirect_buffer_k	2002Aug13
PASS		
194	indirect_buffer_l	2002Aug13
INCOMPLETE		
0	indirect_buffer_mp_a	2002Aug13
PASS		
0	indirect_buffer_mp_b	2002Aug13
PASS		
0	indirect_buffer_mp_c	2002Aug13
PASS		
0	indirect_buffer_mp_d	2002Aug13
PASS		
0	indirect_buffer_mp_e	2002Aug13
PASS		
0	indirect_buffer_mp_f	2002Aug13
PASS		
0	invalidate_on_me_init	2002Aug13
PASS		
0	invalidate_state_constants	2002Aug13
PASS		
0	invalidate_state_shaders	2002Aug13
PASS		
0	invalidate_state_shaders2	2002Aug13
PASS		
0	invalidate_state_sublks	2002Aug13
PASS		
0	invalidate_state_sublks2	2002Aug13
PASS		
207	invalid_opcode	2002Aug13
FAIL	rbbmWr.cmp	
0	iq1_iq2_mult	2002Aug13
PASS		
209	iq1_iq2_push_wrap	2002Aug13
INCOMPLETE		
210	iq2_csq_full	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	IQ2_Mult	2002Aug13
PASS		
0	IQ2_Wrap_Legacy	2002Aug13

PASS		
0	IQ_Wrap	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug13
PASS		
0	ld_cnst_cntx_match	2002Aug13
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug13
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug13
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug13
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug13
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug13
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug13
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug13
PASS		
0	ld_cnst_cntx_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug13
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug13
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug13
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug13
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug13

PASS		
0	ld_cnst_cntx_simple_alu	2002Aug13
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug13
PASS		
0	load_constant_context_rt	2002Aug13
PASS		
240	load_palette	2002Aug13
FAIL	cp_mh_req.cmp rbbmWr.cmp	
0	mem_write	2002Aug13
PASS		
0	mem_write_rt	2002Aug13
PASS		
0	mpeg_index	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug13
PASS		
0	nop_eating_disabled	2002Aug13
PASS		
260	paint_multi	2002Aug13

```

**FAIL**      rbbmWr.cmp
261 pitch_clip                                2002Aug13
**FAIL**      rbbmWr.cmp
0   rbbm_soft_reset                          2002Aug13
PASS
0   rbbm_WU_BIF_RBBM_agp_flush_off           2002Aug13
PASS
0   rbbm_WU_BOTH_GRPX_update_pending_off     2002Aug13
PASS
0   rbbm_WU_CP_dma_idle                      2002Aug13
PASS
0   rbbm_WU_CRTC_D1MODE_vline_fe            2002Aug13
PASS
0   rbbm_WU_CRTC_D1MODE_vline_on            2002Aug13
PASS
0   rbbm_WU_CRTC_D1MODE_vline_re            2002Aug13
PASS
0   rbbm_WU_CRTC_D2MODE_vline_fe            2002Aug13
PASS
0   rbbm_WU_CRTC_D2MODE_vline_on            2002Aug13
PASS
0   rbbm_WU_CRTC_D2MODE_vline_re            2002Aug13
PASS
0   rbbm_WU_CRTC_PFLIP_D1GRPX_update_pending_off 2002Aug13
PASS
0   rbbm_WU_CRTC_PFLIP_D2GRPX_update_pending_off 2002Aug13
PASS
0   rbbm_WU_idct_semaphore                  2002Aug13
PASS
0   rbbm_WU_MH_hdp_clean                    2002Aug13
PASS
0   rbbm_WU_OV_FLIP_D1OVL_update_pending_off 2002Aug13
PASS
0   rbbm_WU_OV_FLIP_D2OVL_update_pending_off 2002Aug13
PASS
0   rbbm_WU_pad_ext_sig_rising_edge          2002Aug13
PASS
0   rbbm_WU_VIPH0_dma_idle                  2002Aug13
PASS
0   rbbm_WU_VIPH1_dma_idle                  2002Aug13
PASS
0   rbbm_WU_VIPH2_dma_idle                  2002Aug13
PASS
0   rbbm_WU_VIPH3_dma_idle                  2002Aug13
PASS
0   RB_BUFSZ_0                              2002Aug13
PASS
0   RB_BUFSZ_1                              2002Aug13

```

PASS		
0	RB_Wrap	2002Aug13
PASS		
0	reg_to_mem	2002Aug13
PASS		
0	reg_to_mem_rt	2002Aug13
PASS		
288	rop_cfd_wmd	2002Aug13
FAIL	rbbmWr.cmp	
289	rstr_ordr	2002Aug13
FAIL	rbbmWr.cmp	
0	rtee_discretes_falling_00_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug13

PASS		
0	rtee_discretes_falling_19_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug13

PASS		
0	rtee_discretes_invert_11_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug13
PASS		
0	rtee_discretes_level_00_events_16	2002Aug13
PASS		
0	rtee_discretes_level_01_events_16	2002Aug13
PASS		
0	rtee_discretes_level_02_events_16	2002Aug13

PASS		
0	rtee_discretes_level_03_events_16	2002Aug13
PASS		
0	rtee_discretes_level_04_events_16	2002Aug13
PASS		
0	rtee_discretes_level_05_events_16	2002Aug13
PASS		
0	rtee_discretes_level_06_events_16	2002Aug13
PASS		
0	rtee_discretes_level_07_events_16	2002Aug13
PASS		
0	rtee_discretes_level_08_events_16	2002Aug13
PASS		
0	rtee_discretes_level_09_events_16	2002Aug13
PASS		
0	rtee_discretes_level_10_events_16	2002Aug13
PASS		
0	rtee_discretes_level_11_events_16	2002Aug13
PASS		
0	rtee_discretes_level_12_events_16	2002Aug13
PASS		
0	rtee_discretes_level_13_events_16	2002Aug13
PASS		
0	rtee_discretes_level_14_events_16	2002Aug13
PASS		
0	rtee_discretes_level_15_events_16	2002Aug13
PASS		
0	rtee_discretes_level_16_events_16	2002Aug13
PASS		
0	rtee_discretes_level_17_events_16	2002Aug13
PASS		
0	rtee_discretes_level_18_events_16	2002Aug13
PASS		
0	rtee_discretes_level_19_events_16	2002Aug13
PASS		
0	rtee_discretes_level_20_events_16	2002Aug13
PASS		
0	rtee_discretes_level_21_events_16	2002Aug13
PASS		
0	rtee_discretes_level_22_events_16	2002Aug13
PASS		
0	rtee_discretes_level_23_events_16	2002Aug13
PASS		
0	rtee_discretes_level_24_events_16	2002Aug13
PASS		
0	rtee_discretes_level_25_events_16	2002Aug13
PASS		
0	rtee_discretes_level_26_events_16	2002Aug13

PASS		
0	rtee_discretes_level_27_events_16	2002Aug13
PASS		
0	rtee_discretes_level_28_events_16	2002Aug13
PASS		
0	rtee_discretes_level_29_events_16	2002Aug13
PASS		
0	rtee_discretes_level_30_events_16	2002Aug13
PASS		
0	rtee_discretes_level_31_events_16	2002Aug13
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug13

PASS		
0	rtee_discretes_rising_18_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug13
PASS		
0	rtee_display1_comp_events_16	2002Aug13
PASS		
0	rtee_display2_comp_events_16	2002Aug13
PASS		
0	rtee_mem_linear_search_events_2	2002Aug13
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug13

PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug13
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug13
PASS		
0	rtee_prefetch_parse_nops	2002Aug13
PASS		
0	rtee_reg_linear_search_events_2	2002Aug13
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug13
PASS		
0	rtee_vip_comp_events_16	2002Aug13
PASS		
445	rt_invalid_opcode	2002Aug13
FAIL	rbbmWr.cmp	
0	scratch_mem_read	2002Aug13
PASS		
0	scratch_mem_read2	2002Aug13
PASS		
0	set_constant	2002Aug13
PASS		
0	set_constant2	2002Aug13
PASS		
0	set_constant_indr1_short	2002Aug13
PASS		
0	set_constant_indr1_short2	2002Aug13
PASS		
0	set_constant_indr2_short	2002Aug13

PASS		
0	set_constant_indr2_short2	2002Aug13
PASS		
0	set_constant_ring_short	2002Aug13
PASS		
0	set_constant_ring_short2	2002Aug13
PASS		
0	set_constant_short	2002Aug13
PASS		
0	set_scissors	2002Aug13
PASS		
0	set_state_rt_a	2002Aug13
PASS		
0	set_state_rt_b	2002Aug13
PASS		
0	set_state_rt_c	2002Aug13
PASS		
0	set_state_rt_d	2002Aug13
PASS		
0	set_st_indr1_match	2002Aug13
PASS		
0	set_st_indr1_mismatch	2002Aug13
PASS		
0	set_st_indr1_mismatch2	2002Aug13
PASS		
0	set_st_indr1_mismatch3	2002Aug13
PASS		
0	set_st_indr1_mismatch4	2002Aug13
PASS		
0	set_st_indr1_ps	2002Aug13
PASS		
0	set_st_indr1_ps_only	2002Aug13
PASS		
0	set_st_indr1_sublk0	2002Aug13
PASS		
0	set_st_indr1_sublk1	2002Aug13
PASS		
0	set_st_indr1_sublk1_only	2002Aug13
PASS		
0	set_st_indr1_sublk2	2002Aug13
PASS		
0	set_st_indr1_sublk2_only	2002Aug13
PASS		
0	set_st_indr1_sublk3	2002Aug13
PASS		
0	set_st_indr1_sublk3_only	2002Aug13
PASS		
0	set_st_indr1_sublk4	2002Aug13

PASS		
0	set_st_indr1_sublk4_only	2002Aug13
PASS		
0	set_st_indr1_sublk5	2002Aug13
PASS		
0	set_st_indr1_sublk5_only	2002Aug13
PASS		
0	set_st_indr1_sublk6	2002Aug13
PASS		
0	set_st_indr1_sublk6_only	2002Aug13
PASS		
0	set_st_indr1_sublk7	2002Aug13
PASS		
0	set_st_indr1_sublk7_only	2002Aug13
PASS		
0	set_st_indr1_vs	2002Aug13
PASS		
0	set_st_indr1_vs_only	2002Aug13
PASS		
0	set_st_indr2_match	2002Aug13
PASS		
0	set_st_indr2_mismatch	2002Aug13
PASS		
0	set_st_indr2_mismatch2	2002Aug13
PASS		
0	set_st_indr2_mismatch3	2002Aug13
PASS		
0	set_st_indr2_mismatch4	2002Aug13
PASS		
0	set_st_indr2_ps	2002Aug13
PASS		
0	set_st_indr2_ps_only	2002Aug13
PASS		
0	set_st_indr2_sublk0	2002Aug13
PASS		
0	set_st_indr2_sublk1	2002Aug13
PASS		
0	set_st_indr2_sublk1_only	2002Aug13
PASS		
0	set_st_indr2_sublk2	2002Aug13
PASS		
0	set_st_indr2_sublk2_only	2002Aug13
PASS		
0	set_st_indr2_sublk3	2002Aug13
PASS		
0	set_st_indr2_sublk3_only	2002Aug13
PASS		
0	set_st_indr2_sublk4	2002Aug13

PASS		
0	set_st_indr2_sublk4_only	2002Aug13
PASS		
0	set_st_indr2_sublk5	2002Aug13
PASS		
0	set_st_indr2_sublk5_only	2002Aug13
PASS		
0	set_st_indr2_sublk6	2002Aug13
PASS		
0	set_st_indr2_sublk6_only	2002Aug13
PASS		
0	set_st_indr2_sublk7	2002Aug13
PASS		
0	set_st_indr2_sublk7_only	2002Aug13
PASS		
0	set_st_indr2_vs	2002Aug13
PASS		
0	set_st_indr2_vs_only	2002Aug13
PASS		
0	set_st_pfmatch_disabled	2002Aug13
PASS		
0	set_st_ring_match	2002Aug13
PASS		
0	set_st_ring_mismatch	2002Aug13
PASS		
0	set_st_ring_mismatch2	2002Aug13
PASS		
0	set_st_ring_mismatch3	2002Aug13
PASS		
0	set_st_ring_mismatch4	2002Aug13
PASS		
0	set_st_ring_ps	2002Aug13
PASS		
0	set_st_ring_ps_only	2002Aug13
PASS		
0	set_st_ring_sublk0	2002Aug13
PASS		
0	set_st_ring_sublk1	2002Aug13
PASS		
0	set_st_ring_sublk1_only	2002Aug13
PASS		
0	set_st_ring_sublk2	2002Aug13
PASS		
0	set_st_ring_sublk2_only	2002Aug13
PASS		
0	set_st_ring_sublk3	2002Aug13
PASS		
0	set_st_ring_sublk3_only	2002Aug13

PASS		
0	set_st_ring_sublk4	2002Aug13
PASS		
0	set_st_ring_sublk4_only	2002Aug13
PASS		
0	set_st_ring_sublk5	2002Aug13
PASS		
0	set_st_ring_sublk5_only	2002Aug13
PASS		
0	set_st_ring_sublk6	2002Aug13
PASS		
0	set_st_ring_sublk6_only	2002Aug13
PASS		
0	set_st_ring_sublk7	2002Aug13
PASS		
0	set_st_ring_sublk7_only	2002Aug13
PASS		
0	set_st_ring_vs	2002Aug13
PASS		
0	set_st_ring_vs_only	2002Aug13
PASS		
0	set_st_stress_a	2002Aug13
PASS		
536	set_st_stress_b	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug13
PASS		
0	set_st_stress_d	2002Aug13
PASS		
0	set_st_stress_e	2002Aug13
PASS		
0	set_st_stress_f	2002Aug13
PASS		
0	smoke_nrt	2002Aug13
PASS		
542	src_clip	2002Aug13
FAIL	rbbmWr.cmp	
543	srf_cohr	2002Aug13
FAIL	rbbmWr.cmp	
544	trans_bitblt	2002Aug13
FAIL	rbbmWr.cmp	
0	type0_2long_pckts	2002Aug13
PASS		
0	type0_3indr_buffs	2002Aug13
PASS		
0	type0_4pckts	2002Aug13
PASS		
0	type0_incr_updt_cnst	2002Aug13

PASS		
0	type0_incr_updt_instrmem	2002Aug13
PASS		
0	type0_incr_updt_st	2002Aug13
PASS		
0	type0_incr_updt_stress	2002Aug13
PASS		
0	type0_ind1_ind2_a	2002Aug13
PASS		
0	type0_ind1_ind2_b	2002Aug13
PASS		
0	type0_indr_buff	2002Aug13
PASS		
0	Type0_IQ2_BM	2002Aug13
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug13
PASS		
0	Type0_IQ_BM	2002Aug13
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug13
PASS		
0	Type0_PQ_BM	2002Aug13
PASS		
560	Type0_PQ_BM_Pre_Write_Timer	2002Aug13
FAIL	rbbmWr.cmp	
0	type1_4pckts	2002Aug13
PASS		
0	type1_indr_buff	2002Aug13
PASS		
0	type1_pckt	2002Aug13
PASS		
564	Type3_Bitblt	2002Aug13
FAIL	rbbmWr.cmp	
565	Type3_Load_Palette	2002Aug13
FAIL	rbbmWr.cmp	
0	Type3_NOP	2002Aug13
PASS		
567	uram_rd_wrt	2002Aug13
FAIL		
568	uram_rd_wrt_rt	2002Aug13
FAIL		
569	uram_wrt_rt_ucode	2002Aug13
FAIL	rbbmWr.cmp	
0	uram_wrt_ucode	2002Aug13
PASS		
0	wait_for_idle	2002Aug13
PASS		
0	wait_reg_mem_poll_mem0	2002Aug13

```
PASS
0    wait_reg_mem_poll_mem0_rt          2002Aug13
PASS
574  wait_reg_mem_poll_mem1            2002Aug13
**FAIL**
575  wait_reg_mem_poll_mem2            2002Aug13
**FAIL**
0    wait_reg_mem_poll_mem3            2002Aug13
PASS
577  wait_reg_mem_poll_reg0            2002Aug13
**FAIL**
0    wait_reg_mem_poll_reg1            2002Aug13
PASS
0    wait_reg_mem_poll_reg2            2002Aug13
PASS
0    wait_reg_mem_poll_reg3            2002Aug13
PASS
```

```
=====
Tests_Run_count = 580  Pass_count = 541  Fail_count = 30  Incomplete_count = 9
=====
```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Fri Oct  4 23:05:38 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          676        676        656         97
              RBBM        120        120        120        100
+++++
              TOTAL        796        796        776         97
+++++

```

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+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Thu Aug  8 21:54:31 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SU           0          0          0          0
              CP          538        538        507        94
+++++
              TEST          538        538        507        94
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sun Sep  8  8:08:25 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU              0          0          0          0
              CP             635         635         624         98
              RBBM           98          98          70          71
+++++
              TOTAL           733         733         694         94
+++++

```

```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Mon Sep  2  7:33:37 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC                0          0          0          0
              CL                0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU                0          0          0          0
              CP              628        628        619         98
              RBBM            96         96         41         42
+++++
              TOTAL            724        724        660         91
+++++

```

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+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Mon Oct  7 23:04:09 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT              0          0          0          0
              SC              0          0          0          0
              CL              0          0          0          0
              VTE              0          0          0          0
SPECIALTY
              SU              0          0          0          0
              CP             676         676         651         96
              RBBM           120         120         120         100
+++++
              TOTAL           796         796         771         96
+++++

```

=====
Regression Report For CP - Project = r400 Created on Tue Aug 13 21:54:18 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

0	2d_indr_buffer_a		2002Aug13
	PASS		
0	2d_indr_buffer_b		2002Aug13
	PASS		
0	2d_indr_buffer_c		2002Aug13
	PASS		
4	2d_indr_buffer_d		2002Aug13
	INCOMPLETE		
0	2d_indr_buffer_e		2002Aug13
	PASS		
0	2d_indr_buffer_f		2002Aug13
	PASS		
7	2d_indr_buffer_g		2002Aug13
	INCOMPLETE		
0	3d_draw_indx_2		2002Aug13
	PASS		
0	3d_draw_indx_2_b		2002Aug13
	PASS		
10	bitblt_multi		2002Aug13
	FAIL rbbmWr.cmp		
11	brush_flag		2002Aug13
	FAIL rbbmWr.cmp		
0	cond_write_mem_poll_mem		2002Aug13
	PASS		
0	cond_write_mem_poll_mem2		2002Aug13
	PASS		
0	cond_write_mem_poll_mem3		2002Aug13
	PASS		
0	cond_write_mem_poll_mem_rt		2002Aug13
	PASS		
0	cond_write_mem_poll_reg		2002Aug13
	PASS		
0	cond_write_mem_poll_reg2		2002Aug13
	PASS		
0	cond_write_mem_poll_reg3		2002Aug13
	PASS		
0	cp_acpi_recovery_soft_reset		2002Aug13
	PASS		
0	cp_bios_scratch		2002Aug13

PASS		
0	cp_ib2_base000_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base001_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base010_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base011_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base100_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base101_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base110_bufszXXX	2002Aug13
PASS		
0	cp_ib2_base111_bufszXXX	2002Aug13
PASS		
0	cp_ib_base000_bufszXXX	2002Aug13
PASS		
0	cp_ib_base001_bufszXXX	2002Aug13
PASS		
0	cp_ib_base010_bufszXXX	2002Aug13
PASS		
0	cp_ib_base011_bufszXXX	2002Aug13
PASS		
0	cp_ib_base100_bufszXXX	2002Aug13
PASS		
0	cp_ib_base101_bufszXXX	2002Aug13
PASS		
35	cp_ib_base110_bufszXXX	2002Aug13
FAIL	cp_mh_req.cmp	
36	cp_ib_base111_bufszXXX	2002Aug13
FAIL	cp_mh_req.cmp	
0	cp_interrupt	2002Aug13
PASS		
0	cp_interrupt_rt_events_16	2002Aug13
PASS		
0	cp_iq2_alignment_000	2002Aug13
PASS		
0	cp_iq2_alignment_001	2002Aug13
PASS		
0	cp_iq2_alignment_010	2002Aug13
PASS		
0	cp_iq2_alignment_011	2002Aug13
PASS		
0	cp_iq2_alignment_100	2002Aug13
PASS		
0	cp_iq2_alignment_101	2002Aug13

PASS		
0	cp_iq2_alignment_110	2002Aug13
PASS		
0	cp_iq2_alignment_111	2002Aug13
PASS		
0	cp_iq_alignment_000	2002Aug13
PASS		
0	cp_iq_alignment_001	2002Aug13
PASS		
0	cp_iq_alignment_010	2002Aug13
PASS		
0	cp_iq_alignment_011	2002Aug13
PASS		
0	cp_iq_alignment_100	2002Aug13
PASS		
0	cp_iq_alignment_101	2002Aug13
PASS		
0	cp_iq_alignment_110	2002Aug13
PASS		
0	cp_iq_alignment_111	2002Aug13
PASS		
0	cp_max_size_packet	2002Aug13
PASS		
0	cp_nrt_rt_protection_mode	2002Aug13
PASS		
57	cp_output_stress	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_protection_mode	2002Aug13
PASS		
0	cp_rb_alignment_000	2002Aug13
PASS		
0	cp_rb_alignment_001	2002Aug13
PASS		
0	cp_rb_alignment_010	2002Aug13
PASS		
0	cp_rb_alignment_011	2002Aug13
PASS		
0	cp_rb_alignment_100	2002Aug13
PASS		
0	cp_rb_alignment_101	2002Aug13
PASS		
0	cp_rb_alignment_110	2002Aug13
PASS		
0	cp_rb_alignment_111	2002Aug13
PASS		
0	cp_register_reads	2002Aug13
PASS		
0	cp_reg_rw_test	2002Aug13

PASS		
0	cp_rt_protection_mode	2002Aug13
PASS		
0	cp_scratch	2002Aug13
PASS		
0	cp_scratch_multi	2002Aug13
PASS		
0	dma_cp_sync	2002Aug13
PASS		
0	dma_dscrstr_chckn_bit	2002Aug13
PASS		
0	dma_dscrstr_flush1	2002Aug13
PASS		
0	dma_dscrstr_flush2	2002Aug13
PASS		
0	dma_dscrstr_flush3	2002Aug13
PASS		
0	dma_m2m_src00_dstXX	2002Aug13
PASS		
0	dma_m2m_src01_dstXX	2002Aug13
PASS		
0	dma_m2m_src10_dstXX	2002Aug13
PASS		
0	dma_m2m_src11_dstXX	2002Aug13
PASS		
0	dma_mem2mem	2002Aug13
PASS		
0	dma_mem2mem_abort	2002Aug13
PASS		
0	dma_mem2mem_alignments	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment0	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment1	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment2	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment3	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment4	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment5	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment6	2002Aug13
PASS		
0	dma_mem2mem_byte_alignment7	2002Aug13
PASS		
0	dma_mem2mem_pio_abort	2002Aug13

PASS		
0	dma_mem2reg	2002Aug13
PASS		
0	dma_mem2reg_abort	2002Aug13
PASS		
0	dma_mem2reg_dtaq_stress	2002Aug13
PASS		
0	dma_mem2reg_pio_stressbm	2002Aug13
PASS		
0	dma_mem2reg_pio_stresspio	2002Aug13
PASS		
0	dma_mem2reg_sync	2002Aug13
PASS		
0	dma_r2r_m2m_r2r	2002Aug13
PASS		
0	dma_r2r_m2r_r2r	2002Aug13
PASS		
0	dma_r2r_r2m_r2r	2002Aug13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Aug13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Aug13
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Aug13
PASS		
0	dma_reg2mem	2002Aug13
PASS		
0	dma_reg2mem_abort	2002Aug13
PASS		
0	dma_reg2reg	2002Aug13
PASS		
0	dma_reg2reg_2dw	2002Aug13
PASS		
0	dma_reg2reg_daic	2002Aug13
PASS		
0	dma_reg2reg_mult	2002Aug13
PASS		
0	dma_reg2reg_mult2	2002Aug13
PASS		
0	dma_reg2reg_mult3	2002Aug13
PASS		
0	dma_reg2reg_saic	2002Aug13
PASS		
0	dma_reg2reg_sdaic	2002Aug13
PASS		
0	dma_reg2reg_sdaic_stress	2002Aug13
PASS		
0	dma_reg2reg_stress	2002Aug13

PASS		
0	draw_indx_a	2002Aug13
PASS		
0	draw_indx_b	2002Aug13
PASS		
0	draw_indx_c	2002Aug13
PASS		
0	draw_indx_indr1_a	2002Aug13
PASS		
0	draw_indx_indr1_b	2002Aug13
PASS		
0	draw_indx_indr1_short_a	2002Aug13
PASS		
0	draw_indx_indr1_short_b	2002Aug13
PASS		
0	draw_indx_indr1_stress	2002Aug13
PASS		
0	draw_indx_indr2_a	2002Aug13
PASS		
0	draw_indx_indr2_b	2002Aug13
PASS		
0	draw_indx_indr2_short_a	2002Aug13
PASS		
0	draw_indx_indr2_short_b	2002Aug13
PASS		
0	draw_indx_indr2_stress	2002Aug13
PASS		
0	draw_indx_ring1	2002Aug13
PASS		
0	draw_indx_ring2	2002Aug13
PASS		
0	draw_indx_ring_short1	2002Aug13
PASS		
0	draw_indx_ring_short2	2002Aug13
PASS		
0	draw_indx_ring_stress	2002Aug13
PASS		
0	draw_indx_short_a	2002Aug13
PASS		
0	draw_indx_short_b	2002Aug13
PASS		
0	draw_indx_short_c	2002Aug13
PASS		
0	draw_indx_stress_a	2002Aug13
PASS		
0	draw_indx_stress_b	2002Aug13
PASS		
0	draw_indx_stress_c	2002Aug13

PASS		
0	event_timestamp_write	2002Aug13
PASS		
142	event_timestamp_write_multicontext_maxcontext_7	2002Aug13
FAIL		
0	ft_indr1_july22	2002Aug13
PASS		
0	ft_indr2_july22	2002Aug13
PASS		
145	ft_july22	2002Aug13
FAIL	cp_mh_req.cmp	
0	header_dump	2002Aug13
PASS		
0	header_dump_rt	2002Aug13
PASS		
0	header_dump_small	2002Aug13
PASS		
0	im_load	2002Aug12
PASS		
0	im_load_a	2002Aug13
PASS		
0	im_load_b	2002Aug13
PASS		
0	im_load_cid_mismatch	2002Aug13
PASS		
0	im_load_immediate	2002Aug13
PASS		
0	im_load_immediate_nrt_me	2002Aug13
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Aug13
PASS		
156	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Aug13
INCOMPLETE		
0	im_load_immediate_nrt_me_ps_wrap	2002Aug13
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Aug13
PASS		
159	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Aug13
INCOMPLETE		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Aug13
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Aug13
PASS		
0	im_load_indr1_a	2002Aug13
PASS		
0	im_load_indr1_b	2002Aug13
PASS		
0	im_load_indr2_a	2002Aug13

PASS		
0	im_load_indr2_b	2002Aug13
PASS		
0	im_load_invalidate	2002Aug13
PASS		
0	im_load_match	2002Aug13
PASS		
0	im_load_match2	2002Aug13
PASS		
0	im_load_nrt_me	2002Aug13
PASS		
0	im_load_nrt_me_ps_nowrap	2002Aug13
PASS		
0	im_load_nrt_me_ps_wrap	2002Aug13
PASS		
0	im_load_nrt_me_vs_nowrap	2002Aug13
PASS		
0	im_load_nrt_me_vs_wrap	2002Aug13
PASS		
0	im_load_pfmatch_disable	2002Aug13
PASS		
0	im_load_rt	2002Aug13
PASS		
0	im_load_rt2	2002Aug13
PASS		
0	im_load_rt3	2002Aug13
PASS		
178	im_load_rt4	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	im_load_sz_mismatch	2002Aug13
PASS		
0	incremental_update_const	2002Aug13
PASS		
181	incremental_update_instr	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	incremental_update_state	2002Aug13
PASS		
0	indirect_buffer_a	2002Aug13
PASS		
0	indirect_buffer_b	2002Aug13
PASS		
0	indirect_buffer_c	2002Aug13
PASS		
0	indirect_buffer_d	2002Aug13
PASS		
0	indirect_buffer_e	2002Aug13
PASS		
0	indirect_buffer_f	2002Aug13

PASS		
189	indirect_buffer_g	2002Aug13
INCOMPLETE		
0	indirect_buffer_h	2002Aug13
PASS		
191	indirect_buffer_i	2002Aug13
INCOMPLETE		
192	indirect_buffer_j	2002Aug13
INCOMPLETE		
0	indirect_buffer_k	2002Aug13
PASS		
194	indirect_buffer_l	2002Aug13
INCOMPLETE		
0	indirect_buffer_mp_a	2002Aug13
PASS		
0	indirect_buffer_mp_b	2002Aug13
PASS		
0	indirect_buffer_mp_c	2002Aug13
PASS		
0	indirect_buffer_mp_d	2002Aug13
PASS		
0	indirect_buffer_mp_e	2002Aug13
PASS		
0	indirect_buffer_mp_f	2002Aug13
PASS		
0	invalidate_on_me_init	2002Aug13
PASS		
0	invalidate_state_constants	2002Aug13
PASS		
0	invalidate_state_shaders	2002Aug13
PASS		
0	invalidate_state_shaders2	2002Aug13
PASS		
0	invalidate_state_sublks	2002Aug13
PASS		
0	invalidate_state_sublks2	2002Aug13
PASS		
207	invalid_opcode	2002Aug13
FAIL	rbbmWr.cmp	
0	iq1_iq2_mult	2002Aug13
PASS		
209	iq1_iq2_push_wrap	2002Aug13
INCOMPLETE		
210	iq2_csq_full	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	IQ2_Mult	2002Aug13
PASS		
0	IQ2_Wrap_Legacy	2002Aug13

PASS		
0	IQ_Wrap	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_indr1_simple	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_indr2_simple	2002Aug13
PASS		
0	ld_cnst_cntx_match	2002Aug13
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Aug13
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Aug13
PASS		
0	ld_cnst_cntx_mismatch_base	2002Aug13
PASS		
0	ld_cnst_cntx_mismatch_force	2002Aug13
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Aug13
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Aug13
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Aug13
PASS		
0	ld_cnst_cntx_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_replicate_alu	2002Aug13
PASS		
0	ld_cnst_cntx_replicate_tex	2002Aug13
PASS		
0	ld_cnst_cntx_ring_replicate	2002Aug13
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Aug13
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Aug13
PASS		
0	ld_cnst_cntx_ring_simple	2002Aug13

PASS		
0	ld_cnst_cntx_simple_alu	2002Aug13
PASS		
0	ld_cnst_cntx_simple_tex	2002Aug13
PASS		
0	load_constant_context_rt	2002Aug13
PASS		
240	load_palette	2002Aug13
FAIL	cp_mh_req.cmp rbbmWr.cmp	
0	mem_write	2002Aug13
PASS		
0	mem_write_rt	2002Aug13
PASS		
0	mpeg_index	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_0	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_1	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_2	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_3	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_4	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_5	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_6	2002Aug13
PASS		
0	mpeg_index_multi_context_max_context_7	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Aug13
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Aug13
PASS		
0	nop_eating_disabled	2002Aug13
PASS		
260	paint_multi	2002Aug13

```

**FAIL**      rbbmWr.cmp
261 pitch_clip                                2002Aug13
**FAIL**      rbbmWr.cmp
0   rbbm_soft_reset                            2002Aug13
PASS
0   rbbm_WU_BIF_RBBM_agp_flush_off            2002Aug13
PASS
0   rbbm_WU_BOTH_GRPX_update_pending_off      2002Aug13
PASS
0   rbbm_WU_CP_dma_idle                       2002Aug13
PASS
0   rbbm_WU_CRTC_D1MODE_vline_fe             2002Aug13
PASS
0   rbbm_WU_CRTC_D1MODE_vline_on             2002Aug13
PASS
0   rbbm_WU_CRTC_D1MODE_vline_re             2002Aug13
PASS
0   rbbm_WU_CRTC_D2MODE_vline_fe             2002Aug13
PASS
0   rbbm_WU_CRTC_D2MODE_vline_on             2002Aug13
PASS
0   rbbm_WU_CRTC_D2MODE_vline_re             2002Aug13
PASS
0   rbbm_WU_CRTC_PFLIP_D1GRPX_update_pending_off 2002Aug13
PASS
0   rbbm_WU_CRTC_PFLIP_D2GRPX_update_pending_off 2002Aug13
PASS
0   rbbm_WU_idct_semaphore                   2002Aug13
PASS
0   rbbm_WU_MH_hdp_clean                     2002Aug13
PASS
0   rbbm_WU_OV_FLIP_D1OVL_update_pending_off  2002Aug13
PASS
0   rbbm_WU_OV_FLIP_D2OVL_update_pending_off  2002Aug13
PASS
0   rbbm_WU_pad_ext_sig_rising_edge           2002Aug13
PASS
0   rbbm_WU_VIPH0_dma_idle                   2002Aug13
PASS
0   rbbm_WU_VIPH1_dma_idle                   2002Aug13
PASS
0   rbbm_WU_VIPH2_dma_idle                   2002Aug13
PASS
0   rbbm_WU_VIPH3_dma_idle                   2002Aug13
PASS
0   RB_BUFSZ_0                               2002Aug13
PASS
0   RB_BUFSZ_1                               2002Aug13

```

PASS		
0	RB_Wrap	2002Aug13
PASS		
0	reg_to_mem	2002Aug13
PASS		
0	reg_to_mem_rt	2002Aug13
PASS		
288	rop_cfd_wmd	2002Aug13
FAIL	rbbmWr.cmp	
289	rstr_ordr	2002Aug13
FAIL	rbbmWr.cmp	
0	rtee_discretes_falling_00_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_01_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_02_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_03_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_04_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_05_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_06_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_07_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_08_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_09_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_10_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_11_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_12_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_13_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_14_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_15_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_16_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_17_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_18_events_16	2002Aug13

PASS		
0	rtee_discretes_falling_19_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_20_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_21_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_22_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_23_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_24_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_25_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_26_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_27_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_28_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_29_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_30_events_16	2002Aug13
PASS		
0	rtee_discretes_falling_31_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_00_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_01_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_02_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_03_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_04_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_05_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_06_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_07_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_08_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_09_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_10_events_16	2002Aug13

PASS		
0	rtee_discretes_invert_11_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_12_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_13_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_14_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_15_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_16_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_17_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_18_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_19_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_20_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_21_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_22_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_23_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_24_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_25_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_26_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_27_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_28_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_29_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_30_events_16	2002Aug13
PASS		
0	rtee_discretes_invert_31_events_16	2002Aug13
PASS		
0	rtee_discretes_level_00_events_16	2002Aug13
PASS		
0	rtee_discretes_level_01_events_16	2002Aug13
PASS		
0	rtee_discretes_level_02_events_16	2002Aug13

PASS		
0	rtee_discretes_level_03_events_16	2002Aug13
PASS		
0	rtee_discretes_level_04_events_16	2002Aug13
PASS		
0	rtee_discretes_level_05_events_16	2002Aug13
PASS		
0	rtee_discretes_level_06_events_16	2002Aug13
PASS		
0	rtee_discretes_level_07_events_16	2002Aug13
PASS		
0	rtee_discretes_level_08_events_16	2002Aug13
PASS		
0	rtee_discretes_level_09_events_16	2002Aug13
PASS		
0	rtee_discretes_level_10_events_16	2002Aug13
PASS		
0	rtee_discretes_level_11_events_16	2002Aug13
PASS		
0	rtee_discretes_level_12_events_16	2002Aug13
PASS		
0	rtee_discretes_level_13_events_16	2002Aug13
PASS		
0	rtee_discretes_level_14_events_16	2002Aug13
PASS		
0	rtee_discretes_level_15_events_16	2002Aug13
PASS		
0	rtee_discretes_level_16_events_16	2002Aug13
PASS		
0	rtee_discretes_level_17_events_16	2002Aug13
PASS		
0	rtee_discretes_level_18_events_16	2002Aug13
PASS		
0	rtee_discretes_level_19_events_16	2002Aug13
PASS		
0	rtee_discretes_level_20_events_16	2002Aug13
PASS		
0	rtee_discretes_level_21_events_16	2002Aug13
PASS		
0	rtee_discretes_level_22_events_16	2002Aug13
PASS		
0	rtee_discretes_level_23_events_16	2002Aug13
PASS		
0	rtee_discretes_level_24_events_16	2002Aug13
PASS		
0	rtee_discretes_level_25_events_16	2002Aug13
PASS		
0	rtee_discretes_level_26_events_16	2002Aug13

PASS		
0	rtee_discretes_level_27_events_16	2002Aug13
PASS		
0	rtee_discretes_level_28_events_16	2002Aug13
PASS		
0	rtee_discretes_level_29_events_16	2002Aug13
PASS		
0	rtee_discretes_level_30_events_16	2002Aug13
PASS		
0	rtee_discretes_level_31_events_16	2002Aug13
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_00_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_01_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_02_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_03_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_04_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_05_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_06_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_07_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_08_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_09_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_10_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_11_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_12_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_13_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_14_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_15_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_16_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_17_events_16	2002Aug13

PASS		
0	rtee_discretes_rising_18_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_19_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_20_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_21_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_22_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_23_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_24_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_25_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_26_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_27_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_28_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_29_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_30_events_16	2002Aug13
PASS		
0	rtee_discretes_rising_31_events_16	2002Aug13
PASS		
0	rtee_display1_comp_events_16	2002Aug13
PASS		
0	rtee_display2_comp_events_16	2002Aug13
PASS		
0	rtee_mem_linear_search_events_2	2002Aug13
PASS		
0	rtee_mem_poll_comp_000_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Aug13

PASS		
0	rtee_mem_poll_comp_111_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Aug13
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Aug13
PASS		
0	rtee_mem_poll_intervals_events_16	2002Aug13
PASS		
0	rtee_prefetch_parse_nops	2002Aug13
PASS		
0	rtee_reg_linear_search_events_2	2002Aug13
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Aug13
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Aug13
PASS		
0	rtee_vip_comp_events_16	2002Aug13
PASS		
445	rt_invalid_opcode	2002Aug13
FAIL	rbbmWr.cmp	
0	scratch_mem_read	2002Aug13
PASS		
0	scratch_mem_read2	2002Aug13
PASS		
0	set_constant	2002Aug13
PASS		
0	set_constant2	2002Aug13
PASS		
0	set_constant_indr1_short	2002Aug13
PASS		
0	set_constant_indr1_short2	2002Aug13
PASS		
0	set_constant_indr2_short	2002Aug13

PASS		
0	set_constant_indr2_short2	2002Aug13
PASS		
0	set_constant_ring_short	2002Aug13
PASS		
0	set_constant_ring_short2	2002Aug13
PASS		
0	set_constant_short	2002Aug13
PASS		
0	set_scissors	2002Aug13
PASS		
0	set_state_rt_a	2002Aug13
PASS		
0	set_state_rt_b	2002Aug13
PASS		
0	set_state_rt_c	2002Aug13
PASS		
0	set_state_rt_d	2002Aug13
PASS		
0	set_st_indr1_match	2002Aug13
PASS		
0	set_st_indr1_mismatch	2002Aug13
PASS		
0	set_st_indr1_mismatch2	2002Aug13
PASS		
0	set_st_indr1_mismatch3	2002Aug13
PASS		
0	set_st_indr1_mismatch4	2002Aug13
PASS		
0	set_st_indr1_ps	2002Aug13
PASS		
0	set_st_indr1_ps_only	2002Aug13
PASS		
0	set_st_indr1_sublk0	2002Aug13
PASS		
0	set_st_indr1_sublk1	2002Aug13
PASS		
0	set_st_indr1_sublk1_only	2002Aug13
PASS		
0	set_st_indr1_sublk2	2002Aug13
PASS		
0	set_st_indr1_sublk2_only	2002Aug13
PASS		
0	set_st_indr1_sublk3	2002Aug13
PASS		
0	set_st_indr1_sublk3_only	2002Aug13
PASS		
0	set_st_indr1_sublk4	2002Aug13

PASS		
0	set_st_indr1_sublk4_only	2002Aug13
PASS		
0	set_st_indr1_sublk5	2002Aug13
PASS		
0	set_st_indr1_sublk5_only	2002Aug13
PASS		
0	set_st_indr1_sublk6	2002Aug13
PASS		
0	set_st_indr1_sublk6_only	2002Aug13
PASS		
0	set_st_indr1_sublk7	2002Aug13
PASS		
0	set_st_indr1_sublk7_only	2002Aug13
PASS		
0	set_st_indr1_vs	2002Aug13
PASS		
0	set_st_indr1_vs_only	2002Aug13
PASS		
0	set_st_indr2_match	2002Aug13
PASS		
0	set_st_indr2_mismatch	2002Aug13
PASS		
0	set_st_indr2_mismatch2	2002Aug13
PASS		
0	set_st_indr2_mismatch3	2002Aug13
PASS		
0	set_st_indr2_mismatch4	2002Aug13
PASS		
0	set_st_indr2_ps	2002Aug13
PASS		
0	set_st_indr2_ps_only	2002Aug13
PASS		
0	set_st_indr2_sublk0	2002Aug13
PASS		
0	set_st_indr2_sublk1	2002Aug13
PASS		
0	set_st_indr2_sublk1_only	2002Aug13
PASS		
0	set_st_indr2_sublk2	2002Aug13
PASS		
0	set_st_indr2_sublk2_only	2002Aug13
PASS		
0	set_st_indr2_sublk3	2002Aug13
PASS		
0	set_st_indr2_sublk3_only	2002Aug13
PASS		
0	set_st_indr2_sublk4	2002Aug13

PASS		
0	set_st_indr2_sublk4_only	2002Aug13
PASS		
0	set_st_indr2_sublk5	2002Aug13
PASS		
0	set_st_indr2_sublk5_only	2002Aug13
PASS		
0	set_st_indr2_sublk6	2002Aug13
PASS		
0	set_st_indr2_sublk6_only	2002Aug13
PASS		
0	set_st_indr2_sublk7	2002Aug13
PASS		
0	set_st_indr2_sublk7_only	2002Aug13
PASS		
0	set_st_indr2_vs	2002Aug13
PASS		
0	set_st_indr2_vs_only	2002Aug13
PASS		
0	set_st_pfmatch_disabled	2002Aug13
PASS		
0	set_st_ring_match	2002Aug13
PASS		
0	set_st_ring_mismatch	2002Aug13
PASS		
0	set_st_ring_mismatch2	2002Aug13
PASS		
0	set_st_ring_mismatch3	2002Aug13
PASS		
0	set_st_ring_mismatch4	2002Aug13
PASS		
0	set_st_ring_ps	2002Aug13
PASS		
0	set_st_ring_ps_only	2002Aug13
PASS		
0	set_st_ring_sublk0	2002Aug13
PASS		
0	set_st_ring_sublk1	2002Aug13
PASS		
0	set_st_ring_sublk1_only	2002Aug13
PASS		
0	set_st_ring_sublk2	2002Aug13
PASS		
0	set_st_ring_sublk2_only	2002Aug13
PASS		
0	set_st_ring_sublk3	2002Aug13
PASS		
0	set_st_ring_sublk3_only	2002Aug13

PASS		
0	set_st_ring_sublk4	2002Aug13
PASS		
0	set_st_ring_sublk4_only	2002Aug13
PASS		
0	set_st_ring_sublk5	2002Aug13
PASS		
0	set_st_ring_sublk5_only	2002Aug13
PASS		
0	set_st_ring_sublk6	2002Aug13
PASS		
0	set_st_ring_sublk6_only	2002Aug13
PASS		
0	set_st_ring_sublk7	2002Aug13
PASS		
0	set_st_ring_sublk7_only	2002Aug13
PASS		
0	set_st_ring_vs	2002Aug13
PASS		
0	set_st_ring_vs_only	2002Aug13
PASS		
0	set_st_stress_a	2002Aug13
PASS		
536	set_st_stress_b	2002Aug13
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	set_st_stress_c	2002Aug13
PASS		
0	set_st_stress_d	2002Aug13
PASS		
0	set_st_stress_e	2002Aug13
PASS		
0	set_st_stress_f	2002Aug13
PASS		
0	smoke_nrt	2002Aug13
PASS		
542	src_clip	2002Aug13
FAIL	rbbmWr.cmp	
543	srf_cohr	2002Aug13
FAIL	rbbmWr.cmp	
544	trans_bitblt	2002Aug13
FAIL	rbbmWr.cmp	
0	type0_2long_pckts	2002Aug13
PASS		
0	type0_3indr_buffs	2002Aug13
PASS		
0	type0_4pckts	2002Aug13
PASS		
0	type0_incr_updt_cnst	2002Aug13

PASS		
0	type0_incr_updt_instrmem	2002Aug13
PASS		
0	type0_incr_updt_st	2002Aug13
PASS		
0	type0_incr_updt_stress	2002Aug13
PASS		
0	type0_ind1_ind2_a	2002Aug13
PASS		
0	type0_ind1_ind2_b	2002Aug13
PASS		
0	type0_indr_buff	2002Aug13
PASS		
0	Type0_IQ2_BM	2002Aug13
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Aug13
PASS		
0	Type0_IQ_BM	2002Aug13
PASS		
0	Type0_IQ_BM_IB_Packet	2002Aug13
PASS		
0	Type0_PQ_BM	2002Aug13
PASS		
560	Type0_PQ_BM_Pre_Write_Timer	2002Aug13
FAIL	rbbmWr.cmp	
0	type1_4pckts	2002Aug13
PASS		
0	type1_indr_buff	2002Aug13
PASS		
0	type1_pckt	2002Aug13
PASS		
564	Type3_Bitblt	2002Aug13
FAIL	rbbmWr.cmp	
565	Type3_Load_Palette	2002Aug13
FAIL	rbbmWr.cmp	
0	Type3_NOP	2002Aug13
PASS		
567	uram_rd_wrt	2002Aug13
FAIL		
568	uram_rd_wrt_rt	2002Aug13
FAIL		
569	uram_wrt_rt_ucode	2002Aug13
FAIL	rbbmWr.cmp	
0	uram_wrt_ucode	2002Aug13
PASS		
0	wait_for_idle	2002Aug13
PASS		
0	wait_reg_mem_poll_mem0	2002Aug13

```
PASS
0    wait_reg_mem_poll_mem0_rt          2002Aug13
PASS
574  wait_reg_mem_poll_mem1            2002Aug13
**FAIL**
575  wait_reg_mem_poll_mem2            2002Aug13
**FAIL**
0    wait_reg_mem_poll_mem3            2002Aug13
PASS
577  wait_reg_mem_poll_reg0            2002Aug13
**FAIL**
0    wait_reg_mem_poll_reg1            2002Aug13
PASS
0    wait_reg_mem_poll_reg2            2002Aug13
PASS
0    wait_reg_mem_poll_reg3            2002Aug13
PASS
```

```
=====
Tests_Run_count = 580  Pass_count = 541  Fail_count = 30  Incomplete_count = 9
=====
```



```

+++++
++++      Regression Summary for CP UNIT Simulations in Orlando      +++++
++++                               Date: Sat Oct 12 23:44:26 2002
+++++
++++      UNIT      +++++ | Total Tests | Tests Run | Tests Pass | % Pass
+++++
              VGT          0          0          0          0
              SC           0          0          0          0
              CL           0          0          0          0
              VTE          0          0          0          0
SPECIALTY    SPECIALTY    0          0          0          0
              SU           0          0          0          0
              CP          752         752         662         88
              RBBM        120         120         118         98
+++++
              TOTAL        872         872         780         89
+++++

```

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep8
	PASS		
0	2d_indr_buffer_b		2002Sep8
	PASS		
0	2d_indr_buffer_c		2002Sep8
	PASS		
0	2d_indr_buffer_d		2002Sep8
	PASS		
0	2d_indr_buffer_e		2002Sep8
	PASS		
0	2d_indr_buffer_f		2002Sep8
	PASS		
0	2d_indr_buffer_g		2002Sep8
	PASS		
0	3d_draw_indx_2		2002Sep8
	PASS		
0	3d_draw_indx_2_b		2002Sep8
	PASS		
0	IQ2_Mult		2002Sep8
	PASS		
0	IQ2_Wrap_Legacy		2002Sep8
	PASS		
0	IQ_Wrap		2002Sep8
	PASS		
0	RB_BUFSZ_0		2002Sep8
	PASS		
0	RB_BUFSZ_1		2002Sep8
	PASS		
0	RB_Wrap		2002Sep8
	PASS		
16	Small_Text_BM		2002Sep8
	FAIL cp_rbbm.cmp rbbmWr.cmp		
0	Type0_IQ2_BM		2002Sep8
	PASS		
0	Type0_IQ2_BM_IB_Packet		2002Sep8
	PASS		
0	Type0_IQ_BM		2002Sep8
	PASS		
0	Type0_IQ_BM_IB_Packet		2002Sep8

PASS		
0	Type0_PQ_BM	2002Sep8
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep8
PASS		
0	Type3_Bitblt	2002Sep8
PASS		
0	Type3_Load_Palette	2002Sep8
PASS		
0	Type3_NOP	2002Sep8
PASS		
0	bitblt_multi	2002Sep8
PASS		
0	brush_32x1mono	2002Sep8
PASS		
0	brush_8x8color_16	2002Sep8
PASS		
29	brush_8x8color_32	2002Sep8
FAIL	cp_mh_req.cmp	
0	brush_8x8color_64	2002Sep8
PASS		
0	brush_8x8color_dst_0_1	2002Sep8
PASS		
0	brush_8x8mono	2002Sep8
PASS		
0	brush_color_mono_stress	2002Sep8
PASS		
34	brush_flag	2002Sep8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	brush_type0	2002Sep8
PASS		
0	brush_type1	2002Sep8
PASS		
0	brush_type10	2002Sep8
PASS		
0	brush_type13	2002Sep8
PASS		
0	brush_type14	2002Sep8
PASS		
0	brush_type6	2002Sep8
PASS		
0	brush_type7	2002Sep8
PASS		
0	brush_type_other	2002Sep8
PASS		
0	brush_types_13_14	2002Sep8
PASS		
0	clear_const_wr_en_on_me_init	2002Sep8

PASS		
0	clear_const_wr_en_with_lcc	2002Sep8
PASS		
0	cond_write_mem_poll_mem	2002Sep8
PASS		
0	cond_write_mem_poll_mem2	2002Sep8
PASS		
0	cond_write_mem_poll_mem3	2002Sep8
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep8
PASS		
0	cond_write_mem_poll_reg	2002Sep8
PASS		
0	cond_write_mem_poll_reg2	2002Sep8
PASS		
0	cond_write_mem_poll_reg3	2002Sep8
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep8
PASS		
0	cp_bios_scratch	2002Sep8
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep8
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep8
PASS		
0	cp_ib_base000_bufszXXX	2002Sep8
PASS		
0	cp_ib_base001_bufszXXX	2002Sep8
PASS		
0	cp_ib_base010_bufszXXX	2002Sep8
PASS		
0	cp_ib_base011_bufszXXX	2002Sep8
PASS		
0	cp_ib_base100_bufszXXX	2002Sep8
PASS		
0	cp_ib_base101_bufszXXX	2002Sep8

PASS		
0	cp_ib_base110_bufszXXX	2002Sep8
PASS		
0	cp_ib_base111_bufszXXX	2002Sep8
PASS		
0	cp_interrupt	2002Sep8
PASS		
0	cp_interrupt_rt_events_16	2002Sep8
PASS		
0	cp_iq2_alignment_000	2002Sep8
PASS		
0	cp_iq2_alignment_001	2002Sep8
PASS		
0	cp_iq2_alignment_010	2002Sep8
PASS		
0	cp_iq2_alignment_011	2002Sep8
PASS		
0	cp_iq2_alignment_100	2002Sep8
PASS		
0	cp_iq2_alignment_101	2002Sep8
PASS		
0	cp_iq2_alignment_110	2002Sep8
PASS		
0	cp_iq2_alignment_111	2002Sep8
PASS		
0	cp_iq_alignment_000	2002Sep8
PASS		
0	cp_iq_alignment_001	2002Sep8
PASS		
0	cp_iq_alignment_010	2002Sep8
PASS		
0	cp_iq_alignment_011	2002Sep8
PASS		
0	cp_iq_alignment_100	2002Sep8
PASS		
0	cp_iq_alignment_101	2002Sep8
PASS		
0	cp_iq_alignment_110	2002Sep8
PASS		
0	cp_iq_alignment_111	2002Sep8
PASS		
0	cp_max_size_packet	2002Sep8
PASS		
0	cp_max_size_packet_random	2002Sep8
PASS		
0	cp_nrt_rt_protection_mode	2002Sep8
PASS		
0	cp_output_stress	2002Sep8

PASS		
0	cp_output_stress_random	2002Sep8
PASS		
0	cp_protection_mode	2002Sep8
PASS		
0	cp_rb_alignment_000	2002Sep8
PASS		
0	cp_rb_alignment_001	2002Sep8
PASS		
0	cp_rb_alignment_010	2002Sep8
PASS		
0	cp_rb_alignment_011	2002Sep8
PASS		
0	cp_rb_alignment_100	2002Sep8
PASS		
0	cp_rb_alignment_101	2002Sep8
PASS		
0	cp_rb_alignment_110	2002Sep8
PASS		
0	cp_rb_alignment_111	2002Sep8
PASS		
0	cp_rb_wptrXXX_base000	2002Sep8
PASS		
0	cp_rb_wptrXXX_base001	2002Sep8
PASS		
0	cp_rb_wptrXXX_base010	2002Sep8
PASS		
0	cp_rb_wptrXXX_base011	2002Sep8
PASS		
0	cp_rb_wptrXXX_base100	2002Sep8
PASS		
0	cp_rb_wptrXXX_base101	2002Sep8
PASS		
0	cp_rb_wptrXXX_base110	2002Sep8
PASS		
0	cp_rb_wptrXXX_base111	2002Sep8
PASS		
0	cp_reg_rw_test	2002Sep8
PASS		
0	cp_register_reads	2002Sep8
PASS		
0	cp_rt_protection_mode	2002Sep8
PASS		
0	cp_scratch	2002Sep8
PASS		
0	cp_scratch_multi	2002Sep8
PASS		
0	dma_cp_sync	2002Sep8

PASS		
0	dma_dscrstr_chckn_bit	2002Sep8
PASS		
0	dma_dscrstr_flush1	2002Sep8
PASS		
0	dma_dscrstr_flush2	2002Sep8
PASS		
0	dma_dscrstr_flush3	2002Sep8
PASS		
0	dma_m2m_src00_dstXX	2002Sep8
PASS		
0	dma_m2m_src01_dstXX	2002Sep8
PASS		
0	dma_m2m_src10_dstXX	2002Sep8
PASS		
0	dma_m2m_src11_dstXX	2002Sep8
PASS		
0	dma_mem2mem	2002Sep8
PASS		
0	dma_mem2mem_abort	2002Sep8
PASS		
0	dma_mem2mem_alignments	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep8
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep8
PASS		
0	dma_mem2mem_pio_abort	2002Sep8
PASS		
0	dma_mem2mem_random	2002Sep8
PASS		
0	dma_mem2reg	2002Sep8
PASS		
0	dma_mem2reg_abort	2002Sep8
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep8

PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep8
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep8
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep8
PASS		
0	dma_mem2reg_sync	2002Sep8
PASS		
0	dma_r2r_m2m_r2r	2002Sep8
PASS		
0	dma_r2r_m2r_r2r	2002Sep8
PASS		
0	dma_r2r_r2m_r2r	2002Sep8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep8
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep8
PASS		
0	dma_reg2mem	2002Sep8
PASS		
0	dma_reg2mem_abort	2002Sep8
PASS		
0	dma_reg2reg	2002Sep8
PASS		
0	dma_reg2reg_2dw	2002Sep8
PASS		
0	dma_reg2reg_daic	2002Sep8
PASS		
0	dma_reg2reg_mult	2002Sep8
PASS		
0	dma_reg2reg_mult2	2002Sep8
PASS		
0	dma_reg2reg_mult3	2002Sep8
PASS		
0	dma_reg2reg_saic	2002Sep8
PASS		
0	dma_reg2reg_sdaic	2002Sep8
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep8
PASS		
0	dma_reg2reg_stress	2002Sep8
PASS		
0	draw_indx_a	2002Sep8
PASS		
0	draw_indx_b	2002Sep8

PASS		
0	draw_indx_c	2002Sep8
PASS		
0	draw_indx_indr1_a	2002Sep8
PASS		
0	draw_indx_indr1_b	2002Sep8
PASS		
0	draw_indx_indr1_short_a	2002Sep8
PASS		
0	draw_indx_indr1_short_b	2002Sep8
PASS		
0	draw_indx_indr1_stress	2002Sep8
PASS		
0	draw_indx_indr2_a	2002Sep8
PASS		
0	draw_indx_indr2_b	2002Sep8
PASS		
0	draw_indx_indr2_short_a	2002Sep8
PASS		
0	draw_indx_indr2_short_b	2002Sep8
PASS		
0	draw_indx_indr2_stress	2002Sep8
PASS		
0	draw_indx_ring1	2002Sep8
PASS		
0	draw_indx_ring2	2002Sep8
PASS		
0	draw_indx_ring_short1	2002Sep8
PASS		
0	draw_indx_ring_short2	2002Sep8
PASS		
0	draw_indx_ring_stress	2002Sep8
PASS		
0	draw_indx_short_a	2002Sep8
PASS		
0	draw_indx_short_b	2002Sep8
PASS		
0	draw_indx_short_c	2002Sep8
PASS		
0	draw_indx_stress_a	2002Sep8
PASS		
0	draw_indx_stress_b	2002Sep8
PASS		
0	draw_indx_stress_c	2002Sep8
PASS		
0	event_timestamp_write	2002Sep8
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep8

PASS		
0	ft_indr1_july22	2002Sep8
PASS		
0	ft_indr2_july22	2002Sep8
PASS		
0	ft_july22	2002Sep8
PASS		
0	ft_july22_random	2002Sep8
PASS		
0	header_dump	2002Sep8
PASS		
0	header_dump_rt	2002Sep8
PASS		
0	header_dump_small	2002Sep8
PASS		
196	hostdata_blt	2002Sep8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep8
PASS		
0	im_load_b	2002Sep8
PASS		
0	im_load_cid_mismatch	2002Sep8
PASS		
0	im_load_immediate	2002Sep8
PASS		
0	im_load_immediate_nrt_me	2002Sep8
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep8
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep8
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep8
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep8
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep8
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep8
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep8
PASS		
0	im_load_indr1_a	2002Sep8
PASS		
0	im_load_indr1_b	2002Sep8
PASS		
0	im_load_indr2_a	2002Sep8
PASS		
0	im_load_indr2_b	2002Sep8

PASS		
0	im_load_invalidate	2002Sep8
PASS		
0	im_load_match	2002Sep8
PASS		
0	im_load_match2	2002Sep8
PASS		
0	im_load_nrt_me	2002Sep8
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep8
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep8
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep8
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep8
PASS		
0	im_load_pfmatch_disable	2002Sep8
PASS		
0	im_load_ring_a	2002Sep8
PASS		
0	im_load_ring_b	2002Sep8
PASS		
0	im_load_rt	2002Sep8
PASS		
0	im_load_rt2	2002Sep8
PASS		
0	im_load_rt3	2002Sep8
PASS		
0	im_load_rt4	2002Sep8
PASS		
0	im_load_sz_mismatch	2002Sep8
PASS		
0	incremental_update_const	2002Sep8
PASS		
0	incremental_update_instr	2002Sep8
PASS		
0	incremental_update_state	2002Sep8
PASS		
0	indirect_buffer_a	2002Sep8
PASS		
0	indirect_buffer_b	2002Sep8
PASS		
0	indirect_buffer_c	2002Sep8
PASS		
0	indirect_buffer_d	2002Sep8
PASS		
0	indirect_buffer_e	2002Sep8

PASS		
0	indirect_buffer_f	2002Sep8
PASS		
0	indirect_buffer_g	2002Sep8
PASS		
0	indirect_buffer_h	2002Sep8
PASS		
0	indirect_buffer_i	2002Sep8
PASS		
0	indirect_buffer_j	2002Sep8
PASS		
0	indirect_buffer_k	2002Sep8
PASS		
0	indirect_buffer_l	2002Sep8
PASS		
0	indirect_buffer_mp_a	2002Sep8
PASS		
0	indirect_buffer_mp_b	2002Sep8
PASS		
0	indirect_buffer_mp_c	2002Sep8
PASS		
0	indirect_buffer_mp_d	2002Sep8
PASS		
0	indirect_buffer_mp_e	2002Sep8
PASS		
0	indirect_buffer_mp_f	2002Sep8
PASS		
0	invalid_opcode	2002Sep8
PASS		
0	invalidate_on_me_init	2002Sep8
PASS		
0	invalidate_state_constants	2002Sep8
PASS		
0	invalidate_state_shaders	2002Sep8
PASS		
0	invalidate_state_shaders2	2002Sep8
PASS		
0	invalidate_state_sublks	2002Sep8
PASS		
0	invalidate_state_sublks2	2002Sep8
PASS		
0	iq1_iq2_mult	2002Sep8
PASS		
0	iq1_iq2_push_wrap	2002Sep8
PASS		
0	iq2_csq_full	2002Sep8
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep8

PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep8
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep8
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep8
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep8
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep8
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep8
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep8
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep8
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep8
PASS		
0	ld_cnst_cntx_match	2002Sep8
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep8
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep8
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep8
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep8
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep8
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep8
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep8
PASS		
0	ld_cnst_cntx_replicate8	2002Sep8
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep8
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep8
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep8
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep8
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep8
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep8

PASS		
0	ld_cnst_cntx_simple_alu	2002Sep8
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep8
PASS		
0	load_constant_context_rt	2002Sep8
PASS		
0	load_palette	2002Sep8
PASS		
0	lp_2d3d	2002Sep8
PASS		
0	mem_write	2002Sep8
PASS		
0	mem_write_rt	2002Sep8
PASS		
0	mpeg_index	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep8
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep8
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep8
PASS		
0	nop_eating_disabled	2002Sep8

PASS		
0	paint_multi	2002Sep8
PASS		
0	pitch_clip	2002Sep8
PASS		
0	ply_nextscan	2002Sep8
PASS		
0	rbbm_WU_2D_idle_01	2002Sep8
PASS		
0	rbbm_WU_2D_idle_02	2002Sep8
PASS		
0	rbbm_WU_2D_idle_03	2002Sep8
PASS		
0	rbbm_WU_2D_idle_04	2002Sep8
PASS		
0	rbbm_WU_2D_idle_05	2002Sep8
PASS		
0	rbbm_WU_2D_idle_06	2002Sep8
PASS		
0	rbbm_WU_2D_idle_07	2002Sep8
PASS		
0	rbbm_WU_2D_idle_08	2002Sep8
PASS		
0	rbbm_WU_2D_idle_09	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep8
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep8

PASS		
0	rbbm_WU_3D_idle_01	2002Sep8
PASS		
0	rbbm_WU_3D_idle_02	2002Sep8
PASS		
0	rbbm_WU_3D_idle_03	2002Sep8
PASS		
0	rbbm_WU_3D_idle_04	2002Sep8
PASS		
0	rbbm_WU_3D_idle_05	2002Sep8
PASS		
0	rbbm_WU_3D_idle_06	2002Sep8
PASS		
0	rbbm_WU_3D_idle_07	2002Sep8
PASS		
0	rbbm_WU_3D_idle_08	2002Sep8
PASS		
0	rbbm_WU_3D_idle_09	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep8
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep8
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep8
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep8
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep8

PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep8
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep8
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep8
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep8
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep8
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep8
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep8
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep8
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep8
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep8
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep8
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep8
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep8
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep8
PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep8
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep8
PASS		
0	rbbm_WU_idct_semaphore	2002Sep8
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep8
PASS		
375	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep8
FAIL	cp_rbbm.cmp	
376	rbbm_WUrt_BOTH_GRPH_update_pending_off	2002Sep8
FAIL	cp_rbbm.cmp	
377	rbbm_WUrt_CP_dma_idle	2002Sep8
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp	
378	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep8
FAIL	cp_rbbm.cmp	
379	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep8
FAIL	cp_rbbm.cmp	
380	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep8

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**FAIL**      cp_rbbm.cmp
381  rbbm_WUrt_CRTC_D2MODE_vline_fe                2002Sep8
**FAIL**      cp_rbbm.cmp
382  rbbm_WUrt_CRTC_D2MODE_vline_on                2002Sep8
**FAIL**      cp_rbbm.cmp
383  rbbm_WUrt_CRTC_D2MODE_vline_re                2002Sep8
**FAIL**      cp_rbbm.cmp
384  rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep8
**FAIL**      cp_rbbm.cmp
385  rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep8
**FAIL**      cp_rbbm.cmp
386  rbbm_WUrt_MH_hdp_clean                          2002Sep8
**FAIL**      cp_rbbm.cmp
387  rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off      2002Sep8
**FAIL**      cp_rbbm.cmp
388  rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off      2002Sep8
**FAIL**      cp_rbbm.cmp
389  rbbm_WUrt_RT_idle_01                            2002Sep8
**FAIL**      cp_rbbm.cmp
390  rbbm_WUrt_RT_idle_02                            2002Sep8
**FAIL**      cp_rbbm.cmp
391  rbbm_WUrt_RT_idle_03                            2002Sep8
**FAIL**      cp_rbbm.cmp
392  rbbm_WUrt_RT_idle_04                            2002Sep8
**FAIL**      cp_rbbm.cmp
393  rbbm_WUrt_RT_idle_clean_01                       2002Sep8
**FAIL**      cp_rbbm.cmp
394  rbbm_WUrt_RT_idle_clean_02                       2002Sep8
**FAIL**      cp_rbbm.cmp
395  rbbm_WUrt_RT_idle_clean_03                       2002Sep8
**FAIL**      cp_rbbm.cmp
396  rbbm_WUrt_RT_idle_clean_04                       2002Sep8
**FAIL**      cp_rbbm.cmp
397  rbbm_WUrt_RT_idle_clean_05                       2002Sep8
**FAIL**      cp_rbbm.cmp
398  rbbm_WUrt_VIPH0_dma_idle                          2002Sep8
**FAIL**      cp_rbbm.cmp
399  rbbm_WUrt_VIPH1_dma_idle                          2002Sep8
**FAIL**      cp_rbbm.cmp
400  rbbm_WUrt_VIPH2_dma_idle                          2002Sep8
**FAIL**      cp_rbbm.cmp
401  rbbm_WUrt_VIPH3_dma_idle                          2002Sep8
**FAIL**      cp_rbbm.cmp
402  rbbm_WUrt_pad_ext_sig_rising_edge                2002Sep8
**FAIL**      cp_rbbm.cmp
0    rbbm_cp_non_queued_rdwr_01                       2002Sep8
PASS
0    rbbm_cp_non_queued_rdwr_02                       2002Sep8

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PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep8
PASS		
0	rbbm_cp_queued_rdwr	2002Sep8
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep8
PASS		
0	rbbm_isync_wait_idlegui	2002Sep8
PASS		
0	rbbm_soft_reset	2002Sep8
PASS		
0	reg_to_mem	2002Sep8
PASS		
0	reg_to_mem_rt	2002Sep8
PASS		
0	rop_cfd_wmd	2002Sep8
PASS		
0	rstr_ordr	2002Sep8
PASS		
0	rt_invalid_opcode	2002Sep8
PASS		
0	rtee_arm_wait_idle_trigger_events_16	2002Sep8
PASS		
0	rtee_discretes_arm_rearm_events_32	2002Sep8
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_01_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_02_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_03_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_04_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_05_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_06_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_07_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_08_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_09_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_10_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_11_events_16	2002Sep8

PASS		
0	rtee_discretes_falling_12_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_13_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_14_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_15_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_16_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_17_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_18_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_19_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_20_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_21_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_22_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_23_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_24_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_25_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_26_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_27_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_28_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_29_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_30_events_16	2002Sep8
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep8

PASS		
0	rtee_discretes_invert_04_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_05_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_06_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_07_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_08_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_09_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_10_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_11_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_12_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_13_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_14_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_15_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_16_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_17_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_18_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_19_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_20_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_21_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_22_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_23_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_24_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_25_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_26_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_27_events_16	2002Sep8

PASS		
0	rtee_discretes_invert_28_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_29_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_30_events_16	2002Sep8
PASS		
0	rtee_discretes_invert_31_events_16	2002Sep8
PASS		
0	rtee_discretes_level_00_events_16	2002Sep8
PASS		
0	rtee_discretes_level_01_events_16	2002Sep8
PASS		
0	rtee_discretes_level_02_events_16	2002Sep8
PASS		
0	rtee_discretes_level_03_events_16	2002Sep8
PASS		
0	rtee_discretes_level_04_events_16	2002Sep8
PASS		
0	rtee_discretes_level_05_events_16	2002Sep8
PASS		
0	rtee_discretes_level_06_events_16	2002Sep8
PASS		
0	rtee_discretes_level_07_events_16	2002Sep8
PASS		
0	rtee_discretes_level_08_events_16	2002Sep8
PASS		
0	rtee_discretes_level_09_events_16	2002Sep8
PASS		
0	rtee_discretes_level_10_events_16	2002Sep8
PASS		
0	rtee_discretes_level_11_events_16	2002Sep8
PASS		
0	rtee_discretes_level_12_events_16	2002Sep8
PASS		
0	rtee_discretes_level_13_events_16	2002Sep8
PASS		
0	rtee_discretes_level_14_events_16	2002Sep8
PASS		
0	rtee_discretes_level_15_events_16	2002Sep8
PASS		
0	rtee_discretes_level_16_events_16	2002Sep8
PASS		
0	rtee_discretes_level_17_events_16	2002Sep8
PASS		
0	rtee_discretes_level_18_events_16	2002Sep8
PASS		
0	rtee_discretes_level_19_events_16	2002Sep8

PASS		
0	rtee_discretes_level_20_events_16	2002Sep8
PASS		
0	rtee_discretes_level_21_events_16	2002Sep8
PASS		
0	rtee_discretes_level_22_events_16	2002Sep8
PASS		
0	rtee_discretes_level_23_events_16	2002Sep8
PASS		
0	rtee_discretes_level_24_events_16	2002Sep8
PASS		
0	rtee_discretes_level_25_events_16	2002Sep8
PASS		
0	rtee_discretes_level_26_events_16	2002Sep8
PASS		
0	rtee_discretes_level_27_events_16	2002Sep8
PASS		
0	rtee_discretes_level_28_events_16	2002Sep8
PASS		
0	rtee_discretes_level_29_events_16	2002Sep8
PASS		
0	rtee_discretes_level_30_events_16	2002Sep8
PASS		
0	rtee_discretes_level_31_events_16	2002Sep8
PASS		
0	rtee_discretes_manual_disarm_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_00_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_01_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_02_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_03_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_04_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_05_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_06_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_07_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_08_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_09_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_10_events_16	2002Sep8

PASS		
0	rtee_discretes_rising_11_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_12_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_13_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_14_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_15_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_16_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_17_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_18_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_19_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_20_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_21_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_22_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_23_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_24_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_25_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_26_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_27_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_28_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_29_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_30_events_16	2002Sep8
PASS		
0	rtee_discretes_rising_31_events_16	2002Sep8
PASS		
0	rtee_display1_comp_events_16	2002Sep8
PASS		
0	rtee_display2_comp_events_16	2002Sep8
PASS		
0	rtee_mem_linear_search_events_2	2002Sep8

PASS		
0	rtee_mem_poll_comp_000_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_010_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_011_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_100_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_110_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_111_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep8
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep8
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep8
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep8
PASS		
0	rtee_prefetch_parse_nops	2002Sep8
PASS		
0	rtee_reg_linear_search_events_2	2002Sep8
PASS		
0	rtee_reg_poll_comp_000_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_010_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_011_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_100_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_101_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep8
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep8
PASS		
0	rtee_vip_comp_events_16	2002Sep8

PASS		
0	scratch_mem_read	2002Sep8
PASS		
0	scratch_mem_read2	2002Sep8
PASS		
0	set_constant	2002Sep8
PASS		
0	set_constant2	2002Sep8
PASS		
0	set_constant_alu_max1	2002Sep8
PASS		
0	set_constant_alu_max2	2002Sep8
PASS		
0	set_constant_bool_max1	2002Sep8
PASS		
0	set_constant_bool_max2	2002Sep8
PASS		
0	set_constant_indr1_short	2002Sep8
PASS		
0	set_constant_indr1_short2	2002Sep8
PASS		
0	set_constant_indr1_write_enable	2002Sep8
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep8
PASS		
0	set_constant_indr2_short	2002Sep8
PASS		
0	set_constant_indr2_short2	2002Sep8
PASS		
0	set_constant_indr2_write_enable	2002Sep8
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep8
PASS		
0	set_constant_loop_max1	2002Sep8
PASS		
0	set_constant_loop_max2	2002Sep8
PASS		
0	set_constant_ring_short	2002Sep8
PASS		
0	set_constant_ring_short2	2002Sep8
PASS		
0	set_constant_ring_write_enable	2002Sep8
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep8
PASS		
0	set_constant_short	2002Sep8
PASS		
0	set_constant_short2	2002Sep8

PASS		
0	set_constant_tex_max1	2002Sep8
PASS		
0	set_constant_tex_max2	2002Sep8
PASS		
0	set_constant_write_enable	2002Sep8
PASS		
0	set_constant_write_enable_lcc	2002Sep8
PASS		
0	set_constant_write_enable_lcc2	2002Sep8
PASS		
0	set_scissors	2002Sep8
PASS		
0	set_st_indrl_match	2002Sep8
PASS		
0	set_st_indrl_mismatch	2002Sep8
PASS		
0	set_st_indrl_mismatch2	2002Sep8
PASS		
0	set_st_indrl_mismatch3	2002Sep8
PASS		
0	set_st_indrl_mismatch4	2002Sep8
PASS		
0	set_st_indrl_ps	2002Sep8
PASS		
0	set_st_indrl_ps_only	2002Sep8
PASS		
0	set_st_indrl_sublk0	2002Sep8
PASS		
0	set_st_indrl_sublk1	2002Sep8
PASS		
0	set_st_indrl_sublk1_only	2002Sep8
PASS		
0	set_st_indrl_sublk2	2002Sep8
PASS		
0	set_st_indrl_sublk2_only	2002Sep8
PASS		
0	set_st_indrl_sublk3	2002Sep8
PASS		
0	set_st_indrl_sublk3_only	2002Sep8
PASS		
0	set_st_indrl_sublk4	2002Sep8
PASS		
0	set_st_indrl_sublk4_only	2002Sep8
PASS		
0	set_st_indrl_sublk5	2002Sep8
PASS		
0	set_st_indrl_sublk5_only	2002Sep8

PASS		
0	set_st_indr1_sublk6	2002Sep8
PASS		
0	set_st_indr1_sublk6_only	2002Sep8
PASS		
0	set_st_indr1_sublk7	2002Sep8
PASS		
0	set_st_indr1_sublk7_only	2002Sep8
PASS		
0	set_st_indr1_vs	2002Sep8
PASS		
0	set_st_indr1_vs_only	2002Sep8
PASS		
0	set_st_indr2_match	2002Sep8
PASS		
0	set_st_indr2_mismatch	2002Sep8
PASS		
0	set_st_indr2_mismatch2	2002Sep8
PASS		
0	set_st_indr2_mismatch3	2002Sep8
PASS		
0	set_st_indr2_mismatch4	2002Sep8
PASS		
0	set_st_indr2_ps	2002Sep8
PASS		
0	set_st_indr2_ps_only	2002Sep8
PASS		
0	set_st_indr2_sublk0	2002Sep8
PASS		
0	set_st_indr2_sublk1	2002Sep8
PASS		
0	set_st_indr2_sublk1_only	2002Sep8
PASS		
0	set_st_indr2_sublk2	2002Sep8
PASS		
0	set_st_indr2_sublk2_only	2002Sep8
PASS		
0	set_st_indr2_sublk3	2002Sep8
PASS		
0	set_st_indr2_sublk3_only	2002Sep8
PASS		
0	set_st_indr2_sublk4	2002Sep8
PASS		
0	set_st_indr2_sublk4_only	2002Sep8
PASS		
0	set_st_indr2_sublk5	2002Sep8
PASS		
0	set_st_indr2_sublk5_only	2002Sep8

PASS		
0	set_st_indr2_sublk6	2002Sep8
PASS		
0	set_st_indr2_sublk6_only	2002Sep8
PASS		
0	set_st_indr2_sublk7	2002Sep8
PASS		
0	set_st_indr2_sublk7_only	2002Sep8
PASS		
0	set_st_indr2_vs	2002Sep8
PASS		
0	set_st_indr2_vs_only	2002Sep8
PASS		
0	set_st_pfmacth_disabled	2002Sep8
PASS		
0	set_st_ring_match	2002Sep8
PASS		
0	set_st_ring_mismatch	2002Sep8
PASS		
0	set_st_ring_mismatch2	2002Sep8
PASS		
0	set_st_ring_mismatch3	2002Sep8
PASS		
0	set_st_ring_mismatch4	2002Sep8
PASS		
0	set_st_ring_ps	2002Sep8
PASS		
0	set_st_ring_ps_only	2002Sep8
PASS		
0	set_st_ring_sublk0	2002Sep8
PASS		
0	set_st_ring_sublk1	2002Sep8
PASS		
0	set_st_ring_sublk1_only	2002Sep8
PASS		
0	set_st_ring_sublk2	2002Sep8
PASS		
0	set_st_ring_sublk2_only	2002Sep8
PASS		
0	set_st_ring_sublk3	2002Sep8
PASS		
0	set_st_ring_sublk3_only	2002Sep8
PASS		
0	set_st_ring_sublk4	2002Sep8
PASS		
0	set_st_ring_sublk4_only	2002Sep8
PASS		
0	set_st_ring_sublk5	2002Sep8

PASS		
0	set_st_ring_sublk5_only	2002Sep8
PASS		
0	set_st_ring_sublk6	2002Sep8
PASS		
0	set_st_ring_sublk6_only	2002Sep8
PASS		
0	set_st_ring_sublk7	2002Sep8
PASS		
0	set_st_ring_sublk7_only	2002Sep8
PASS		
0	set_st_ring_vs	2002Sep8
PASS		
0	set_st_ring_vs_only	2002Sep8
PASS		
0	set_st_stress_a	2002Sep8
PASS		
0	set_st_stress_b	2002Sep8
PASS		
0	set_st_stress_c	2002Sep8
PASS		
0	set_st_stress_d	2002Sep8
PASS		
0	set_st_stress_e	2002Sep8
PASS		
0	set_st_stress_f	2002Sep8
PASS		
0	set_state_rt_a	2002Sep8
PASS		
0	set_state_rt_b	2002Sep8
PASS		
0	set_state_rt_c	2002Sep8
PASS		
0	set_state_rt_d	2002Sep8
PASS		
0	smoke_nrt	2002Sep8
PASS		
0	src_clip	2002Sep8
PASS		
0	srf_cohr	2002Sep8
PASS		
0	trans_bitblt	2002Sep8
PASS		
0	type0_2long_pckts	2002Sep8
PASS		
0	type0_3indr_buffs	2002Sep8
PASS		
0	type0_4pckts	2002Sep8

PASS		
0	type0_incr_updt_cnst	2002Sep8
PASS		
0	type0_incr_updt_instrmem	2002Sep8
PASS		
0	type0_incr_updt_st	2002Sep8
PASS		
0	type0_incr_updt_stress	2002Sep8
PASS		
0	type0_ind1_ind2_a	2002Sep8
PASS		
0	type0_ind1_ind2_b	2002Sep8
PASS		
0	type0_indr_buff	2002Sep8
PASS		
0	type1_4pckts	2002Sep8
PASS		
0	type1_indr_buff	2002Sep8
PASS		
0	type1_pckt	2002Sep8
PASS		
0	type_random	2002Sep8
PASS		
704	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
705	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep8
PASS		
0	uram_wrt_ucode	2002Sep8
PASS		
0	viz_query_a	2002Sep8
PASS		
0	viz_query_draw_indx	2002Sep8
PASS		
0	viz_query_draw_indx_indr1	2002Sep8
PASS		
0	viz_query_draw_indx_indr2	2002Sep8
PASS		
712	viz_query_draw_indx_multipass	2002Sep8
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_draw_indx_ring	2002Sep8
PASS		
0	viz_query_indr1	2002Sep8
PASS		
0	viz_query_indr1_simple	2002Sep8
PASS		
0	viz_query_indr2	2002Sep8

PASS
0 viz_query_indr2_simple 2002Sep8
PASS
0 viz_query_ring 2002Sep8
PASS
0 viz_query_ring_simple 2002Sep8
PASS
0 wait_for_idle 2002Sep8
PASS
0 wait_reg_mem_poll_mem0 2002Sep8
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep8
PASS
0 wait_reg_mem_poll_mem1 2002Sep8
PASS
0 wait_reg_mem_poll_mem2 2002Sep8
PASS
0 wait_reg_mem_poll_mem3 2002Sep8
PASS
0 wait_reg_mem_poll_reg0 2002Sep8
PASS
0 wait_reg_mem_poll_reg1 2002Sep8
PASS
0 wait_reg_mem_poll_reg2 2002Sep8
PASS
0 wait_reg_mem_poll_reg3 2002Sep8
PASS

=====
Tests_Run_count = 729 Pass_count = 694 Fail_count = 35 Incomplete_count = 0
=====

=====
Regression Report For CP - Project = r400 Created on Sat Sep 21 16:04:09 2002
=====

=====
No Test_name RunTime Baseline
Test_Status Probable_Cause_of_Failures LastPass
=====

0	2d_indr_buffer_a		2002Sep21
PASS			
0	2d_indr_buffer_b		2002Sep21
PASS			
0	2d_indr_buffer_c		2002Sep21
PASS			
0	2d_indr_buffer_d		2002Sep21
PASS			
0	2d_indr_buffer_e		2002Sep21
PASS			
0	2d_indr_buffer_f		2002Sep21
PASS			
0	2d_indr_buffer_g		2002Sep21
PASS			
8	2d_indr_buffer_random		2002Sep21
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep21
PASS			
0	3d_draw_indx_2_b		2002Sep21
PASS			
0	IQ2_Mult		2002Sep21
PASS			
0	IQ2_Wrap_Legacy		2002Sep21
PASS			
0	IQ_Wrap		2002Sep21
PASS			
0	Isync_Control		2002Sep21
PASS			
0	Isync_Control_2D_3D		2002Sep21
PASS			
0	Isync_Control_3D_2D		2002Sep21
PASS			
0	RB_BUFSZ_0		2002Sep21
PASS			
0	RB_BUFSZ_1		2002Sep21
PASS			
0	RB_Wrap		2002Sep21
PASS			
20	Small_Text_BM		2002Sep21

```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type0_IQ2_BM                                     2002Sep21
PASS
0   Type0_IQ2_BM_IB_Packet                           2002Sep21
PASS
0   Type0_IQ_BM                                       2002Sep21
PASS
0   Type0_IQ_BM_IB_Packet                             2002Sep21
PASS
0   Type0_PQ_BM                                       2002Sep21
PASS
0   Type0_PQ_BM_Pre_Write_Timer                       2002Sep21
PASS
0   Type3_Bitblt                                       2002Sep21
PASS
0   Type3_Load_Palette                               2002Sep21
PASS
0   Type3_NOP                                         2002Sep21
PASS
0   Type3_Polyline                                    2002Sep21
PASS
0   Type3_Polyline_Large                             2002Sep21
PASS
0   bitblt_multi                                      2002Sep21
PASS
0   brush_32x1mono                                    2002Sep21
PASS
0   brush_8x8color_16                                2002Sep21
PASS
0   brush_8x8color_32                                2002Sep21
PASS
0   brush_8x8color_64                                2002Sep21
PASS
0   brush_8x8color_dst_0_1                           2002Sep21
PASS
0   brush_8x8mono                                      2002Sep21
PASS
0   brush_color_mono_stress                           2002Sep21
PASS
0   brush_flag                                        2002Sep21
PASS
41  brush_type0                                       2002Sep21
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
42  brush_type1                                       2002Sep21
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
43  brush_type10                                      2002Sep21
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
44  brush_type13                                      2002Sep21

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
45  brush_type14                                2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
46  brush_type6                                  2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
47  brush_type7                                  2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
48  brush_type_other                             2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   brush_types_13_14                           2002Sep21
PASS
0   clear_const_wr_en_on_me_init                 2002Sep21
PASS
0   clear_const_wr_en_with_lcc                  2002Sep21
PASS
0   cond_write_mem_poll_mem                     2002Sep21
PASS
0   cond_write_mem_poll_mem2                    2002Sep21
PASS
0   cond_write_mem_poll_mem3                    2002Sep21
PASS
0   cond_write_mem_poll_mem_rt                  2002Sep21
PASS
0   cond_write_mem_poll_reg                     2002Sep21
PASS
0   cond_write_mem_poll_reg2                    2002Sep21
PASS
0   cond_write_mem_poll_reg3                    2002Sep21
PASS
59  cp_acpi_recovery_soft_reset                 2002Sep21
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   cp_bios_scratch                             2002Sep21
PASS
0   cp_ib2_base000_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base001_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base010_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base011_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base100_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base101_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base110_bufszXXX                      2002Sep21
PASS
0   cp_ib2_base111_bufszXXX                      2002Sep21

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PASS		
0	cp_ib_base000_bufszXXX	2002Sep21
PASS		
0	cp_ib_base001_bufszXXX	2002Sep21
PASS		
0	cp_ib_base010_bufszXXX	2002Sep21
PASS		
0	cp_ib_base011_bufszXXX	2002Sep21
PASS		
0	cp_ib_base100_bufszXXX	2002Sep21
PASS		
0	cp_ib_base101_bufszXXX	2002Sep21
PASS		
0	cp_ib_base110_bufszXXX	2002Sep21
PASS		
0	cp_ib_base111_bufszXXX	2002Sep21
PASS		
0	cp_interrupt	2002Sep21
PASS		
0	cp_interrupt_rt_events_16	2002Sep21
PASS		
0	cp_iq2_alignment_000	2002Sep21
PASS		
0	cp_iq2_alignment_001	2002Sep21
PASS		
0	cp_iq2_alignment_010	2002Sep21
PASS		
0	cp_iq2_alignment_011	2002Sep21
PASS		
0	cp_iq2_alignment_100	2002Sep21
PASS		
0	cp_iq2_alignment_101	2002Sep21
PASS		
0	cp_iq2_alignment_110	2002Sep21
PASS		
0	cp_iq2_alignment_111	2002Sep21
PASS		
0	cp_iq_alignment_000	2002Sep21
PASS		
0	cp_iq_alignment_001	2002Sep21
PASS		
0	cp_iq_alignment_010	2002Sep21
PASS		
0	cp_iq_alignment_011	2002Sep21
PASS		
0	cp_iq_alignment_100	2002Sep21
PASS		
0	cp_iq_alignment_101	2002Sep21

PASS		
0	cp_iq_alignment_110	2002Sep21
PASS		
0	cp_iq_alignment_111	2002Sep21
PASS		
0	cp_max_size_packet	2002Sep21
PASS		
96	cp_max_size_packet_random	2002Sep21
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep21
PASS		
0	cp_output_stress	2002Sep21
PASS		
0	cp_output_stress_random	2002Sep21
PASS		
0	cp_protection_mode	2002Sep21
PASS		
0	cp_rb_alignment_000	2002Sep21
PASS		
0	cp_rb_alignment_001	2002Sep21
PASS		
0	cp_rb_alignment_010	2002Sep21
PASS		
0	cp_rb_alignment_011	2002Sep21
PASS		
0	cp_rb_alignment_100	2002Sep21
PASS		
0	cp_rb_alignment_101	2002Sep21
PASS		
0	cp_rb_alignment_110	2002Sep21
PASS		
0	cp_rb_alignment_111	2002Sep21
PASS		
0	cp_rb_wptrXXX_base000	2002Sep21
PASS		
0	cp_rb_wptrXXX_base001	2002Sep21
PASS		
0	cp_rb_wptrXXX_base010	2002Sep21
PASS		
0	cp_rb_wptrXXX_base011	2002Sep21
PASS		
0	cp_rb_wptrXXX_base100	2002Sep21
PASS		
0	cp_rb_wptrXXX_base101	2002Sep21
PASS		
0	cp_rb_wptrXXX_base110	2002Sep21
PASS		
0	cp_rb_wptrXXX_base111	2002Sep21

PASS		
0	cp_reg_rw_test	2002Sep21
PASS		
0	cp_register_reads	2002Sep21
PASS		
0	cp_rt_protection_mode	2002Sep21
PASS		
0	cp_scratch	2002Sep21
PASS		
0	cp_scratch_multi	2002Sep21
PASS		
0	csf_reg_rd_default	2002Sep21
PASS		
0	csf_reg_wr_rd	2002Sep21
PASS		
0	dma_cp_sync	2002Sep21
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep21
PASS		
0	dma_dscrstr_flush1	2002Sep21
PASS		
0	dma_dscrstr_flush2	2002Sep21
PASS		
0	dma_dscrstr_flush3	2002Sep21
PASS		
0	dma_m2m_src00_dstXX	2002Sep21
PASS		
0	dma_m2m_src01_dstXX	2002Sep21
PASS		
0	dma_m2m_src10_dstXX	2002Sep21
PASS		
0	dma_m2m_src11_dstXX	2002Sep21
PASS		
0	dma_mem2mem	2002Sep21
PASS		
0	dma_mem2mem_abort	2002Sep21
PASS		
0	dma_mem2mem_alignments	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep21

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep21
PASS		
0	dma_mem2mem_pio_abort	2002Sep21
PASS		
0	dma_mem2mem_random	2002Sep21
PASS		
0	dma_mem2reg	2002Sep21
PASS		
0	dma_mem2reg_abort	2002Sep21
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep21
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep21
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep21
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep21
PASS		
0	dma_mem2reg_sync	2002Sep21
PASS		
0	dma_r2r_m2m_r2r	2002Sep21
PASS		
0	dma_r2r_m2r_r2r	2002Sep21
PASS		
0	dma_r2r_r2m_r2r	2002Sep21
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep21
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep21
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep21
PASS		
0	dma_reg2mem	2002Sep21
PASS		
0	dma_reg2mem_abort	2002Sep21
PASS		
0	dma_reg2reg	2002Sep21
PASS		
0	dma_reg2reg_2dw	2002Sep21
PASS		
0	dma_reg2reg_daic	2002Sep21
PASS		
0	dma_reg2reg_mult	2002Sep21

PASS		
0	dma_reg2reg_mult2	2002Sep21
PASS		
0	dma_reg2reg_mult3	2002Sep21
PASS		
0	dma_reg2reg_saic	2002Sep21
PASS		
0	dma_reg2reg_sdaic	2002Sep21
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep21
PASS		
0	dma_reg2reg_stress	2002Sep21
PASS		
0	draw_indx_a	2002Sep21
PASS		
0	draw_indx_b	2002Sep21
PASS		
0	draw_indx_c	2002Sep21
PASS		
0	draw_indx_indr1_a	2002Sep21
PASS		
0	draw_indx_indr1_b	2002Sep21
PASS		
0	draw_indx_indr1_short_a	2002Sep21
PASS		
0	draw_indx_indr1_short_b	2002Sep21
PASS		
0	draw_indx_indr1_stress	2002Sep21
PASS		
0	draw_indx_indr2_a	2002Sep21
PASS		
0	draw_indx_indr2_b	2002Sep21
PASS		
0	draw_indx_indr2_short_a	2002Sep21
PASS		
0	draw_indx_indr2_short_b	2002Sep21
PASS		
0	draw_indx_indr2_stress	2002Sep21
PASS		
0	draw_indx_ring1	2002Sep21
PASS		
0	draw_indx_ring2	2002Sep21
PASS		
0	draw_indx_ring_short1	2002Sep21
PASS		
0	draw_indx_ring_short2	2002Sep21
PASS		
0	draw_indx_ring_stress	2002Sep21

PASS		
0	draw_indx_short_a	2002Sep21
PASS		
0	draw_indx_short_b	2002Sep21
PASS		
0	draw_indx_short_c	2002Sep21
PASS		
0	draw_indx_stress_a	2002Sep21
PASS		
0	draw_indx_stress_b	2002Sep21
PASS		
0	draw_indx_stress_c	2002Sep21
PASS		
0	event_timestamp_write	2002Sep21
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep21
PASS		
0	ft_indr1_july22	2002Sep21
PASS		
0	ft_indr2_july22	2002Sep21
PASS		
0	ft_july22	2002Sep21
PASS		
0	ft_july22_random	2002Sep21
PASS		
0	header_dump	2002Sep21
PASS		
0	header_dump_rt	2002Sep21
PASS		
0	header_dump_small	2002Sep21
PASS		
204	hostdata_blt	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep21
PASS		
0	im_load_b	2002Sep21
PASS		
0	im_load_cid_mismatch	2002Sep21
PASS		
0	im_load_immediate	2002Sep21
PASS		
0	im_load_immediate_nrt_me	2002Sep21
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep21
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep21
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep21

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep21
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep21
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep21
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep21
PASS		
0	im_load_indr1_a	2002Sep21
PASS		
0	im_load_indr1_b	2002Sep21
PASS		
0	im_load_indr2_a	2002Sep21
PASS		
0	im_load_indr2_b	2002Sep21
PASS		
0	im_load_invalidate	2002Sep21
PASS		
0	im_load_match	2002Sep21
PASS		
0	im_load_match2	2002Sep21
PASS		
0	im_load_nrt_me	2002Sep21
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep21
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep21
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep21
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep21
PASS		
0	im_load_pfmacth_disable	2002Sep21
PASS		
0	im_load_ring_a	2002Sep21
PASS		
0	im_load_ring_b	2002Sep21
PASS		
0	im_load_rt	2002Sep21
PASS		
0	im_load_rt2	2002Sep21
PASS		
0	im_load_rt3	2002Sep21
PASS		
0	im_load_rt4	2002Sep21
PASS		
0	im_load_sz_mismatch	2002Sep21

PASS		
0	incremental_update_const	2002Sep21
PASS		
0	incremental_update_instr	2002Sep21
PASS		
0	incremental_update_state	2002Sep21
PASS		
0	indirect_buffer_a	2002Sep21
PASS		
0	indirect_buffer_b	2002Sep21
PASS		
0	indirect_buffer_c	2002Sep21
PASS		
0	indirect_buffer_d	2002Sep21
PASS		
0	indirect_buffer_e	2002Sep21
PASS		
0	indirect_buffer_f	2002Sep21
PASS		
0	indirect_buffer_g	2002Sep21
PASS		
0	indirect_buffer_h	2002Sep21
PASS		
0	indirect_buffer_i	2002Sep21
PASS		
0	indirect_buffer_j	2002Sep21
PASS		
0	indirect_buffer_k	2002Sep21
PASS		
0	indirect_buffer_l	2002Sep21
PASS		
0	indirect_buffer_mp_a	2002Sep21
PASS		
0	indirect_buffer_mp_b	2002Sep21
PASS		
0	indirect_buffer_mp_c	2002Sep21
PASS		
0	indirect_buffer_mp_d	2002Sep21
PASS		
0	indirect_buffer_mp_e	2002Sep21
PASS		
0	indirect_buffer_mp_f	2002Sep21
PASS		
258	invalid_opcode	2002Sep21
INCOMPLETE		
0	invalidate_on_me_init	2002Sep21
PASS		
0	invalidate_state_constants	2002Sep21

PASS		
0	invalidate_state_shaders	2002Sep21
PASS		
0	invalidate_state_shaders2	2002Sep21
PASS		
0	invalidate_state_sublks	2002Sep21
PASS		
0	invalidate_state_sublks2	2002Sep21
PASS		
0	iq1_iq2_mult	2002Sep21
PASS		
0	iq1_iq2_push_wrap	2002Sep21
PASS		
0	iq2_csq_full	2002Sep21
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep21
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep21
PASS		
0	ld_cnst_cntx_match	2002Sep21
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep21
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep21
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep21
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep21
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep21
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep21

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep21
PASS		
0	ld_cnst_cntx_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep21
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep21
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep21
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep21
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep21
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep21
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep21
PASS		
0	load_constant_context_rt	2002Sep21
PASS		
0	load_palette	2002Sep21
PASS		
0	lp_2d3d	2002Sep21
PASS		
0	mem_write	2002Sep21
PASS		
0	mem_write_rt	2002Sep21
PASS		
0	mpeg_index	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep21

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep21
PASS		
0	nop_eating_disabled	2002Sep21
PASS		
0	paint_multi	2002Sep21
PASS		
0	paint_packet	2002Sep21
PASS		
319	paint_packet_gui_control	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	pitch_clip	2002Sep21
PASS		
0	ply_nextscan	2002Sep21
PASS		
0	rbbm_WU_2D_idle_01	2002Sep21
PASS		
0	rbbm_WU_2D_idle_02	2002Sep21
PASS		
0	rbbm_WU_2D_idle_03	2002Sep21
PASS		
0	rbbm_WU_2D_idle_04	2002Sep21
PASS		
0	rbbm_WU_2D_idle_05	2002Sep21
PASS		
0	rbbm_WU_2D_idle_06	2002Sep21
PASS		
0	rbbm_WU_2D_idle_07	2002Sep21
PASS		
0	rbbm_WU_2D_idle_08	2002Sep21
PASS		
0	rbbm_WU_2D_idle_09	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep21

PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep21
PASS		
0	rbbm_WU_3D_idle_01	2002Sep21
PASS		
0	rbbm_WU_3D_idle_02	2002Sep21
PASS		
0	rbbm_WU_3D_idle_03	2002Sep21
PASS		
0	rbbm_WU_3D_idle_04	2002Sep21
PASS		
0	rbbm_WU_3D_idle_05	2002Sep21
PASS		
0	rbbm_WU_3D_idle_06	2002Sep21
PASS		
0	rbbm_WU_3D_idle_07	2002Sep21
PASS		
0	rbbm_WU_3D_idle_08	2002Sep21
PASS		
0	rbbm_WU_3D_idle_09	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_01	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep21

PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_07	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_08	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_09	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_10	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep21
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep21
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep21
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep21
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep21
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep21
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep21
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep21
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep21
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep21
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep21
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep21
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep21
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep21


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PASS
0   rbbm_WU_VIPH3_dma_idle           2002Sep21
PASS
0   rbbm_WU_cmdfifo_entries         2002Sep21
PASS
0   rbbm_WU_idct_semaphore          2002Sep21
PASS
0   rbbm_WU_pad_ext_sig_rising_edge 2002Sep21
PASS
0   rbbm_WUrt_BIF_RBBM_agp_flush_off 2002Sep21
PASS
0   rbbm_WUrt_BOTH_GRP_H_update_pending_off 2002Sep21
PASS
0   rbbm_WUrt_CP_dma_idle           2002Sep21
PASS
0   rbbm_WUrt_CRTC_D1MODE_vline_fe  2002Sep21
PASS
0   rbbm_WUrt_CRTC_D1MODE_vline_on  2002Sep21
PASS
0   rbbm_WUrt_CRTC_D1MODE_vline_re  2002Sep21
PASS
0   rbbm_WUrt_CRTC_D2MODE_vline_fe  2002Sep21
PASS
0   rbbm_WUrt_CRTC_D2MODE_vline_on  2002Sep21
PASS
0   rbbm_WUrt_CRTC_D2MODE_vline_re  2002Sep21
PASS
0   rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off 2002Sep21
PASS
0   rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off 2002Sep21
PASS
0   rbbm_WUrt_MH_hdp_clean          2002Sep21
PASS
0   rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off 2002Sep21
PASS
0   rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off 2002Sep21
PASS
0   rbbm_WUrt_RT_idle_01            2002Sep21
PASS
0   rbbm_WUrt_RT_idle_02            2002Sep21
PASS
0   rbbm_WUrt_RT_idle_03            2002Sep21
PASS
0   rbbm_WUrt_RT_idle_04            2002Sep21
PASS
403 rbbm_WUrt_RT_idle_clean_01      2002Sep21
**FAIL**
0   rbbm_WUrt_RT_idle_clean_02      2002Sep21

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PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep21
PASS		
0	rbbm_WUrt_VIPH0_dma_idle	2002Sep21
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0	rbbm_WUrt_VIPH1_dma_idle	2002Sep21
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0	rbbm_WUrt_VIPH2_dma_idle	2002Sep21
PASS		
0	rbbm_WUrt_VIPH3_dma_idle	2002Sep21
PASS		
0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep21
PASS		
0	rbbm_clk_active_deassert_min_max	2002Sep21
PASS		
0	rbbm_clk_active_each_pipe	2002Sep21
PASS		
0	rbbm_clk_active_for_read_error	2002Sep21
PASS		
0	rbbm_clk_active_for_read_request	2002Sep21
PASS		
0	rbbm_clk_active_on_reset	2002Sep21
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep21
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep21
PASS		
420	rbbm_cp_queued_holds_nonqueued	2002Sep21
FAIL	cp_rbbm.cmp	
0	rbbm_cp_queued_rdwr	2002Sep21
PASS		
0	rbbm_int_ack	2002Sep21
PASS		
0	rbbm_int_each_client	2002Sep21
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Sep21
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep21
PASS		
0	rbbm_int_rderr	2002Sep21
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep21
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep21

PASS		
0	rbbm_isync_wait_idlegui	2002Sep21
PASS		
0	rbbm_soft_reset	2002Sep21
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0	reg_to_mem	2002Sep21
PASS		
0	reg_to_mem_rt	2002Sep21
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0	rop_cfd_wmd	2002Sep21
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0	rstr_ordr	2002Sep21
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0	rt_invalid_opcode	2002Sep21
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0	rtee_arm_wait_idle_trigger_events_16	2002Sep21
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0	rtee_discretes_arm_rearm_events_32	2002Sep21
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0	rtee_discretes_falling_00_events_16	2002Sep21
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0	rtee_discretes_falling_01_events_16	2002Sep21
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0	rtee_discretes_falling_02_events_16	2002Sep21
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0	rtee_discretes_falling_03_events_16	2002Sep21
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0	rtee_discretes_falling_28_events_16	2002Sep21
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0	rtee_discretes_rising_31_events_16	2002Sep21
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0	rtee_display2_comp_events_16	2002Sep21
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0	rtee_mem_poll_comp_000_events_16	2002Sep21
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0	rtee_mem_poll_comp_001_events_16	2002Sep21
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0	rtee_mem_poll_comp_010_events_16	2002Sep21

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0	rtee_mem_poll_comp_011_events_16	2002Sep21
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0	rtee_mem_poll_comp_100_events_16	2002Sep21
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0	rtee_mem_poll_comp_101_events_16	2002Sep21
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0	rtee_mem_poll_comp_110_events_16	2002Sep21
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0	rtee_mem_poll_comp_111_events_16	2002Sep21
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0	rtee_mem_poll_comp_masks_events_16	2002Sep21
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0	rtee_mem_poll_comp_masks_events_16_random	2002Sep21
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0	rtee_mem_poll_comp_swaps_events_16	2002Sep21
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0	rtee_mem_poll_intervals_events_16	2002Sep21
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0	rtee_reg_linear_search_events_2	2002Sep21
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0	rtee_reg_poll_comp_000_events_16	2002Sep21
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0	rtee_reg_poll_comp_001_events_16	2002Sep21
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0	rtee_reg_poll_comp_010_events_16	2002Sep21
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0	rtee_reg_poll_comp_011_events_16	2002Sep21
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0	rtee_reg_poll_comp_100_events_16	2002Sep21
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0	rtee_reg_poll_comp_101_events_16	2002Sep21
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0	rtee_reg_poll_comp_110_events_16	2002Sep21
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0	rtee_reg_poll_comp_111_events_16	2002Sep21
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0	rtee_vip_comp_events_16	2002Sep21
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PASS		
0	scratch_mem_read2	2002Sep21
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0	set_constant	2002Sep21

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0	set_constant_alu_max1	2002Sep21
PASS		
0	set_constant_alu_max2	2002Sep21
PASS		
0	set_constant_bool_max1	2002Sep21
PASS		
0	set_constant_bool_max2	2002Sep21
PASS		
0	set_constant_indr1_short	2002Sep21
PASS		
0	set_constant_indr1_short2	2002Sep21
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0	set_constant_indr1_write_enable	2002Sep21
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep21
PASS		
0	set_constant_indr2_short	2002Sep21
PASS		
0	set_constant_indr2_short2	2002Sep21
PASS		
0	set_constant_indr2_write_enable	2002Sep21
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep21
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PASS		
0	set_constant_loop_max2	2002Sep21
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PASS		
0	set_constant_ring_short2	2002Sep21
PASS		
0	set_constant_ring_write_enable	2002Sep21
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep21
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PASS		
0	set_constant_short2	2002Sep21
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PASS		
0	set_constant_tex_max2	2002Sep21
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0	set_constant_write_enable_lcc	2002Sep21
PASS		
0	set_constant_write_enable_lcc2	2002Sep21
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0	set_scissors	2002Sep21
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0	set_st_indrl_mismatch	2002Sep21
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0	set_st_indrl_mismatch2	2002Sep21
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0	set_st_indrl_mismatch3	2002Sep21
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0	set_st_indrl_mismatch4	2002Sep21
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0	set_st_indrl_ps	2002Sep21
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0	set_st_indrl_sublk0	2002Sep21
PASS		
0	set_st_indrl_sublk1	2002Sep21
PASS		
0	set_st_indrl_sublk1_only	2002Sep21
PASS		
0	set_st_indrl_sublk2	2002Sep21
PASS		
0	set_st_indrl_sublk2_only	2002Sep21
PASS		
0	set_st_indrl_sublk3	2002Sep21
PASS		
0	set_st_indrl_sublk3_only	2002Sep21
PASS		
0	set_st_indrl_sublk4	2002Sep21
PASS		
0	set_st_indrl_sublk4_only	2002Sep21
PASS		
0	set_st_indrl_sublk5	2002Sep21
PASS		
0	set_st_indrl_sublk5_only	2002Sep21
PASS		
0	set_st_indrl_sublk6	2002Sep21
PASS		
0	set_st_indrl_sublk6_only	2002Sep21
PASS		
0	set_st_indrl_sublk7	2002Sep21

PASS		
0	set_st_indr1_sublk7_only	2002Sep21
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0	set_st_indr1_vs	2002Sep21
PASS		
0	set_st_indr1_vs_only	2002Sep21
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PASS		
0	set_st_indr2_mismatch	2002Sep21
PASS		
0	set_st_indr2_mismatch2	2002Sep21
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0	set_st_indr2_mismatch3	2002Sep21
PASS		
0	set_st_indr2_mismatch4	2002Sep21
PASS		
0	set_st_indr2_ps	2002Sep21
PASS		
0	set_st_indr2_ps_only	2002Sep21
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0	set_st_indr2_sublk0	2002Sep21
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0	set_st_indr2_sublk1	2002Sep21
PASS		
0	set_st_indr2_sublk1_only	2002Sep21
PASS		
0	set_st_indr2_sublk2	2002Sep21
PASS		
0	set_st_indr2_sublk2_only	2002Sep21
PASS		
0	set_st_indr2_sublk3	2002Sep21
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0	set_st_indr2_sublk3_only	2002Sep21
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0	set_st_indr2_sublk4	2002Sep21
PASS		
0	set_st_indr2_sublk4_only	2002Sep21
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0	set_st_indr2_sublk5	2002Sep21
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0	set_st_indr2_sublk5_only	2002Sep21
PASS		
0	set_st_indr2_sublk6	2002Sep21
PASS		
0	set_st_indr2_sublk6_only	2002Sep21
PASS		
0	set_st_indr2_sublk7	2002Sep21

PASS		
0	set_st_indr2_sublk7_only	2002Sep21
PASS		
0	set_st_indr2_vs	2002Sep21
PASS		
0	set_st_indr2_vs_only	2002Sep21
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0	set_st_pfmatch_disabled	2002Sep21
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0	set_st_ring_match	2002Sep21
PASS		
0	set_st_ring_mismatch	2002Sep21
PASS		
0	set_st_ring_mismatch2	2002Sep21
PASS		
0	set_st_ring_mismatch3	2002Sep21
PASS		
0	set_st_ring_mismatch4	2002Sep21
PASS		
0	set_st_ring_ps	2002Sep21
PASS		
0	set_st_ring_ps_only	2002Sep21
PASS		
0	set_st_ring_sublk0	2002Sep21
PASS		
0	set_st_ring_sublk1	2002Sep21
PASS		
0	set_st_ring_sublk1_only	2002Sep21
PASS		
0	set_st_ring_sublk2	2002Sep21
PASS		
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PASS		
0	set_st_ring_sublk3	2002Sep21
PASS		
0	set_st_ring_sublk3_only	2002Sep21
PASS		
0	set_st_ring_sublk4	2002Sep21
PASS		
0	set_st_ring_sublk4_only	2002Sep21
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0	set_st_ring_sublk5	2002Sep21
PASS		
0	set_st_ring_sublk5_only	2002Sep21
PASS		
0	set_st_ring_sublk6	2002Sep21
PASS		
0	set_st_ring_sublk6_only	2002Sep21

PASS		
0	set_st_ring_sublk7	2002Sep21
PASS		
0	set_st_ring_sublk7_only	2002Sep21
PASS		
0	set_st_ring_vs	2002Sep21
PASS		
0	set_st_ring_vs_only	2002Sep21
PASS		
0	set_st_stress_a	2002Sep21
PASS		
0	set_st_stress_b	2002Sep21
PASS		
0	set_st_stress_c	2002Sep21
PASS		
0	set_st_stress_d	2002Sep21
PASS		
0	set_st_stress_e	2002Sep21
PASS		
0	set_st_stress_f	2002Sep21
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703	set_state_rt_a	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
704	set_state_rt_b	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
705	set_state_rt_c	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
706	set_state_rt_d	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep21
PASS		
708	src_clip	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	srf_cohr	2002Sep21
PASS		
0	trans_bitblt	2002Sep21
PASS		
0	type0_2long_pckts	2002Sep21
PASS		
0	type0_3indr_buffs	2002Sep21
PASS		
0	type0_4pckts	2002Sep21
PASS		
0	type0_incr_updt_cnst	2002Sep21
PASS		
0	type0_incr_updt_instrmem	2002Sep21
PASS		
0	type0_incr_updt_st	2002Sep21

PASS		
0	type0_incr_updt_stress	2002Sep21
PASS		
0	type0_ind1_ind2_a	2002Sep21
PASS		
0	type0_ind1_ind2_b	2002Sep21
PASS		
0	type0_indr_buff	2002Sep21
PASS		
0	type1_4pckts	2002Sep21
PASS		
0	type1_indr_buff	2002Sep21
PASS		
0	type1_pckt	2002Sep21
PASS		
724	type_random	2002Sep21
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
725	uram_wrt_rt_ucode	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
726	uram_wrt_ucode	2002Sep21
FAIL	rbbmWr.cmp	
727	viz_query_a	2002Sep21
FAIL	rbbmWr.cmp	
728	viz_query_draw_indx	2002Sep21
FAIL	rbbmWr.cmp	
729	viz_query_draw_indx_indr1	2002Sep21
FAIL	rbbmWr.cmp	
730	viz_query_draw_indx_indr2	2002Sep21
FAIL	rbbmWr.cmp	
731	viz_query_draw_indx_multipass	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
732	viz_query_draw_indx_ring	2002Sep21
FAIL	rbbmWr.cmp	
733	viz_query_indr1	2002Sep21
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep21
PASS		
735	viz_query_indr2	2002Sep21
FAIL	rbbmWr.cmp	
0	viz_query_indr2_simple	2002Sep21
PASS		
737	viz_query_ring	2002Sep21
FAIL	rbbmWr.cmp	
0	viz_query_ring_simple	2002Sep21
PASS		
0	wait_for_idle	2002Sep21
PASS		
0	wait_reg_mem_poll_mem0	2002Sep21

PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep21
PASS
0 wait_reg_mem_poll_mem1 2002Sep21
PASS
0 wait_reg_mem_poll_mem2 2002Sep21
PASS
0 wait_reg_mem_poll_mem3 2002Sep21
PASS
0 wait_reg_mem_poll_reg0 2002Sep21
PASS
0 wait_reg_mem_poll_reg1 2002Sep21
PASS
0 wait_reg_mem_poll_reg2 2002Sep21
PASS
0 wait_reg_mem_poll_reg3 2002Sep21
PASS

=====
Tests_Run_count = 748 Pass_count = 714 Fail_count = 33 Incomplete_count = 1
=====

=====
Regression Report For CP - Project = r400 Created on Tue Sep 10 7:21:06 2002
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep10
PASS			
0	2d_indr_buffer_b		2002Sep10
PASS			
0	2d_indr_buffer_c		2002Sep10
PASS			
0	2d_indr_buffer_d		2002Sep10
PASS			
0	2d_indr_buffer_e		2002Sep10
PASS			
0	2d_indr_buffer_f		2002Sep10
PASS			
0	2d_indr_buffer_g		2002Sep10
PASS			
0	3d_draw_indx_2		2002Sep10
PASS			
0	3d_draw_indx_2_b		2002Sep10
PASS			
0	bitblt_multi		2002Sep10
PASS			
0	brush_32x1mono		2002Sep10
PASS			
0	brush_8x8color_16		2002Sep10
PASS			
13	brush_8x8color_32		2002Sep10
FAIL	cp_mh_req.cmp		
0	brush_8x8color_64		2002Sep10
PASS			
0	brush_8x8color_dst_0_1		2002Sep10
PASS			
0	brush_8x8mono		2002Sep10
PASS			
0	brush_color_mono_stress		2002Sep10
PASS			
18	brush_flag		2002Sep10
FAIL	cp_rbbm.cmp rbbmWr.cmp		
0	brush_type0		2002Sep10
PASS			
0	brush_type1		2002Sep10

PASS		
0	brush_type10	2002Sep10
PASS		
0	brush_type13	2002Sep10
PASS		
0	brush_type14	2002Sep10
PASS		
0	brush_type6	2002Sep10
PASS		
0	brush_type7	2002Sep10
PASS		
0	brush_type_other	2002Sep10
PASS		
0	brush_types_13_14	2002Sep10
PASS		
0	clear_const_wr_en_on_me_init	2002Sep10
PASS		
0	clear_const_wr_en_with_lcc	2002Sep10
PASS		
0	cond_write_mem_poll_mem	2002Sep10
PASS		
0	cond_write_mem_poll_mem2	2002Sep10
PASS		
0	cond_write_mem_poll_mem3	2002Sep10
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep10
PASS		
0	cond_write_mem_poll_reg	2002Sep10
PASS		
0	cond_write_mem_poll_reg2	2002Sep10
PASS		
0	cond_write_mem_poll_reg3	2002Sep10
PASS		
0	cp_acpi_recovery_soft_reset	2002Sep9
PASS		
0	cp_bios_scratch	2002Sep9
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep10
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep10
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep10
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep10
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep10
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep10

PASS		
0	cp_ib2_base110_bufszXXX	2002Sep10
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep10
PASS		
0	cp_ib_base000_bufszXXX	2002Sep10
PASS		
0	cp_ib_base001_bufszXXX	2002Sep10
PASS		
0	cp_ib_base010_bufszXXX	2002Sep10
PASS		
0	cp_ib_base011_bufszXXX	2002Sep10
PASS		
0	cp_ib_base100_bufszXXX	2002Sep10
PASS		
0	cp_ib_base101_bufszXXX	2002Sep10
PASS		
0	cp_ib_base110_bufszXXX	2002Sep10
PASS		
0	cp_ib_base111_bufszXXX	2002Sep10
PASS		
0	cp_interrupt	2002Sep10
PASS		
0	cp_interrupt_rt_events_16	2002Sep10
PASS		
0	cp_iq2_alignment_000	2002Sep10
PASS		
0	cp_iq2_alignment_001	2002Sep10
PASS		
0	cp_iq2_alignment_010	2002Sep10
PASS		
0	cp_iq2_alignment_011	2002Sep10
PASS		
0	cp_iq2_alignment_100	2002Sep10
PASS		
0	cp_iq2_alignment_101	2002Sep10
PASS		
0	cp_iq2_alignment_110	2002Sep10
PASS		
0	cp_iq2_alignment_111	2002Sep10
PASS		
0	cp_iq_alignment_000	2002Sep10
PASS		
0	cp_iq_alignment_001	2002Sep10
PASS		
0	cp_iq_alignment_010	2002Sep10
PASS		
0	cp_iq_alignment_011	2002Sep10

PASS		
0	cp_iq_alignment_100	2002Sep10
PASS		
0	cp_iq_alignment_101	2002Sep10
PASS		
0	cp_iq_alignment_110	2002Sep10
PASS		
0	cp_iq_alignment_111	2002Sep10
PASS		
0	cp_max_size_packet	2002Sep10
PASS		
0	cp_max_size_packet_random	2002Sep10
PASS		
0	cp_nrt_rt_protection_mode	2002Sep10
PASS		
0	cp_output_stress	2002Sep10
PASS		
0	cp_output_stress_random	2002Sep10
PASS		
0	cp_protection_mode	2002Sep10
PASS		
0	cp_rb_alignment_000	2002Sep10
PASS		
0	cp_rb_alignment_001	2002Sep10
PASS		
0	cp_rb_alignment_010	2002Sep10
PASS		
0	cp_rb_alignment_011	2002Sep10
PASS		
0	cp_rb_alignment_100	2002Sep10
PASS		
0	cp_rb_alignment_101	2002Sep10
PASS		
0	cp_rb_alignment_110	2002Sep10
PASS		
0	cp_rb_alignment_111	2002Sep10
PASS		
0	cp_rb_wptrXXX_base000	2002Sep10
PASS		
0	cp_rb_wptrXXX_base001	2002Sep10
PASS		
0	cp_rb_wptrXXX_base010	2002Sep10
PASS		
0	cp_rb_wptrXXX_base011	2002Sep10
PASS		
0	cp_rb_wptrXXX_base100	2002Sep10
PASS		
0	cp_rb_wptrXXX_base101	2002Sep10

PASS		
0	cp_rb_wptrXXX_base10	2002Sep10
PASS		
0	cp_rb_wptrXXX_base11	2002Sep10
PASS		
0	cp_register_reads	2002Sep9
PASS		
0	cp_reg_rw_test	2002Sep9
PASS		
0	cp_rt_protection_mode	2002Sep10
PASS		
0	cp_scratch	2002Sep9
PASS		
0	cp_scratch_multi	2002Sep9
PASS		
0	dma_cp_sync	2002Sep9
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep9
PASS		
0	dma_dscrstr_flush1	2002Sep9
PASS		
0	dma_dscrstr_flush2	2002Sep9
PASS		
0	dma_dscrstr_flush3	2002Sep9
PASS		
0	dma_m2m_src00_dstXX	2002Sep9
PASS		
0	dma_m2m_src01_dstXX	2002Sep9
PASS		
0	dma_m2m_src10_dstXX	2002Sep9
PASS		
0	dma_m2m_src11_dstXX	2002Sep9
PASS		
0	dma_mem2mem	2002Sep9
PASS		
0	dma_mem2mem_abort	2002Sep9
PASS		
0	dma_mem2mem_alignments	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep9

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep9
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep9
PASS		
0	dma_mem2mem_pio_abort	2002Sep9
PASS		
0	dma_mem2mem_random	2002Sep9
PASS		
0	dma_mem2reg	2002Sep9
PASS		
0	dma_mem2reg_abort	2002Sep9
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep9
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep9
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep9
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep9
PASS		
0	dma_mem2reg_sync	2002Sep9
PASS		
0	dma_r2r_m2m_r2r	2002Sep9
PASS		
0	dma_r2r_m2r_r2r	2002Sep9
PASS		
0	dma_r2r_r2m_r2r	2002Sep9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep9
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep9
PASS		
0	dma_reg2mem	2002Sep9
PASS		
0	dma_reg2mem_abort	2002Sep9
PASS		
0	dma_reg2reg	2002Sep9
PASS		
0	dma_reg2reg_2dw	2002Sep9
PASS		
0	dma_reg2reg_daic	2002Sep9
PASS		
0	dma_reg2reg_mult	2002Sep9

PASS		
0	dma_reg2reg_mult2	2002Sep9
PASS		
0	dma_reg2reg_mult3	2002Sep9
PASS		
0	dma_reg2reg_saic	2002Sep9
PASS		
0	dma_reg2reg_sdaic	2002Sep9
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep9
PASS		
0	dma_reg2reg_stress	2002Sep9
PASS		
0	draw_indx_a	2002Sep10
PASS		
0	draw_indx_b	2002Sep10
PASS		
0	draw_indx_c	2002Sep10
PASS		
0	draw_indx_indr1_a	2002Sep10
PASS		
0	draw_indx_indr1_b	2002Sep10
PASS		
0	draw_indx_indr1_short_a	2002Sep10
PASS		
0	draw_indx_indr1_short_b	2002Sep10
PASS		
0	draw_indx_indr1_stress	2002Sep10
PASS		
0	draw_indx_indr2_a	2002Sep10
PASS		
0	draw_indx_indr2_b	2002Sep10
PASS		
0	draw_indx_indr2_short_a	2002Sep10
PASS		
0	draw_indx_indr2_short_b	2002Sep10
PASS		
0	draw_indx_indr2_stress	2002Sep10
PASS		
0	draw_indx_ring1	2002Sep10
PASS		
0	draw_indx_ring2	2002Sep10
PASS		
0	draw_indx_ring_short1	2002Sep10
PASS		
0	draw_indx_ring_short2	2002Sep10
PASS		
0	draw_indx_ring_stress	2002Sep10

PASS		
0	draw_indx_short_a	2002Sep10
PASS		
0	draw_indx_short_b	2002Sep10
PASS		
0	draw_indx_short_c	2002Sep10
PASS		
0	draw_indx_stress_a	2002Sep10
PASS		
0	draw_indx_stress_b	2002Sep10
PASS		
0	draw_indx_stress_c	2002Sep10
PASS		
0	event_timestamp_write	2002Sep10
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep10
PASS		
0	ft_indr1_july22	2002Sep10
PASS		
0	ft_indr2_july22	2002Sep10
PASS		
0	ft_july22	2002Sep10
PASS		
0	ft_july22_random	2002Sep10
PASS		
0	header_dump	2002Sep10
PASS		
0	header_dump_rt	2002Sep10
PASS		
0	header_dump_small	2002Sep10
PASS		
180	hostdata_blt	2002Sep10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep10
PASS		
0	im_load_b	2002Sep10
PASS		
0	im_load_cid_mismatch	2002Sep10
PASS		
0	im_load_immediate	2002Sep10
PASS		
0	im_load_immediate_nrt_me	2002Sep10
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep10
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep10
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep10

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep10
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep10
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep10
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep10
PASS		
0	im_load_indr1_a	2002Sep10
PASS		
0	im_load_indr1_b	2002Sep10
PASS		
0	im_load_indr2_a	2002Sep10
PASS		
0	im_load_indr2_b	2002Sep10
PASS		
0	im_load_invalidate	2002Sep10
PASS		
0	im_load_match	2002Sep10
PASS		
0	im_load_match2	2002Sep10
PASS		
0	im_load_nrt_me	2002Sep10
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep10
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep10
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep10
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep10
PASS		
205	im_load_pfmatch_disable	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_ring_a	2002Sep10
PASS		
0	im_load_ring_b	2002Sep10
PASS		
208	im_load_rt	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
209	im_load_rt2	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
210	im_load_rt3	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
211	im_load_rt4	2002Sep9
FAIL	cp_rbbm.cmp rbbmWr.cmp	
212	im_load_sz_mismatch	2002Sep9

```

**FAIL**      cp_rbbm.cmp rbbmWr.cmp
213 incremental_update_const                2002Sep9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
214 incremental_update_instr                2002Sep9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
215 incremental_update_state                2002Sep9
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
216 indirect_buffer_a                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
217 indirect_buffer_b                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
218 indirect_buffer_c                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
219 indirect_buffer_d                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
220 indirect_buffer_e                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
221 indirect_buffer_f                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
222 indirect_buffer_g                      2002Sep9
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0 indirect_buffer_h                        2002Sep10
PASS
0 indirect_buffer_i                        2002Sep10
PASS
0 indirect_buffer_j                        2002Sep10
PASS
0 indirect_buffer_k                        2002Sep10
PASS
0 indirect_buffer_l                        2002Sep10
PASS
0 indirect_buffer_mp_a                     2002Sep10
PASS
0 indirect_buffer_mp_b                     2002Sep10
PASS
0 indirect_buffer_mp_c                     2002Sep10
PASS
0 indirect_buffer_mp_d                     2002Sep10
PASS
0 indirect_buffer_mp_e                     2002Sep10
PASS
0 indirect_buffer_mp_f                     2002Sep10
PASS
0 invalidate_on_me_init                    2002Sep10
PASS
0 invalidate_state_constants                2002Sep10
PASS
0 invalidate_state_shaders                  2002Sep10

```

PASS		
0	invalidate_state_shaders2	2002Sep10
PASS		
0	invalidate_state_sublks	2002Sep10
PASS		
0	invalidate_state_sublks2	2002Sep10
PASS		
0	invalid_opcode	2002Sep10
PASS		
0	iq1_iq2_mult	2002Sep10
PASS		
0	iq1_iq2_push_wrap	2002Sep10
PASS		
0	iq2_csq_full	2002Sep10
PASS		
0	IQ2_Mult	2002Sep10
PASS		
0	IQ2_Wrap_Legacy	2002Sep10
PASS		
0	IQ_Wrap	2002Sep10
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep10
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep10
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep10
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep10
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep10
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep10
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep10
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep10
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep10
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep10
PASS		
0	ld_cnst_cntx_match	2002Sep10
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep10
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep10
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep10

PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep10
PASS		
0	ld_cnst_cntx_pfmatch_disable	2002Sep10
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep10
PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep10
PASS		
0	ld_cnst_cntx_replicate8	2002Sep10
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep10
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep10
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep10
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep10
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep10
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep10
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep10
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep10
PASS		
0	load_constant_context_rt	2002Sep10
PASS		
0	load_palette	2002Sep10
PASS		
0	lp_2d3d	2002Sep10
PASS		
0	mem_write	2002Sep10
PASS		
0	mem_write_rt	2002Sep10
PASS		
0	mpeg_index	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep10

PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep10
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep10
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep10
PASS		
0	nop_eating_disabled	2002Sep10
PASS		
0	paint_multi	2002Sep10
PASS		
0	pitch_clip	2002Sep10
PASS		
0	ply_nextscan	2002Sep10
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep9
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep9
PASS		
0	rbbm_cp_queued_holds_nonqueued	2002Sep9
PASS		
0	rbbm_cp_queued_rdwr	2002Sep9
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep9
PASS		
0	rbbm_isync_wait_idlegui	2002Sep9
PASS		
0	rbbm_soft_reset	2002Sep9
PASS		
0	rbbm_WU_2D_idle_01	2002Sep9
PASS		
0	rbbm_WU_2D_idle_02	2002Sep9
PASS		
0	rbbm_WU_2D_idle_03	2002Sep9

PASS		
0	rbbm_WU_2D_idle_04	2002Sep9
PASS		
0	rbbm_WU_2D_idle_05	2002Sep9
PASS		
0	rbbm_WU_2D_idle_06	2002Sep9
PASS		
0	rbbm_WU_2D_idle_07	2002Sep9
PASS		
0	rbbm_WU_2D_idle_08	2002Sep9
PASS		
0	rbbm_WU_2D_idle_09	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_08	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_10	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_11	2002Sep9
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep9
PASS		
0	rbbm_WU_3D_idle_01	2002Sep9
PASS		
0	rbbm_WU_3D_idle_02	2002Sep9
PASS		
0	rbbm_WU_3D_idle_03	2002Sep9
PASS		
0	rbbm_WU_3D_idle_04	2002Sep9
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0	rbbm_WU_3D_idle_05	2002Sep9
PASS		
0	rbbm_WU_3D_idle_06	2002Sep9

PASS		
0	rbbm_WU_3D_idle_07	2002Sep9
PASS		
0	rbbm_WU_3D_idle_08	2002Sep9
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0	rbbm_WU_3D_idle_09	2002Sep9
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0	rbbm_WU_3D_idle_clean_01	2002Sep9
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0	rbbm_WU_3D_idle_clean_02	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep9
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0	rbbm_WU_3D_idle_clean_04	2002Sep9
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0	rbbm_WU_3D_idle_clean_10	2002Sep9
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0	rbbm_WU_3D_idle_clean_11	2002Sep9
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep9
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0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep9
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep9
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep9
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep9
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0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep9
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep9
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep9

PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep9
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep9
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep9
PASS		
0	rbbm_WU_idct_semaphore	2002Sep9
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0	rbbm_WU_MH_hdp_clean	2002Sep9
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep9
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep9
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep9
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0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep9
PASS		
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0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep9
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0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep9
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0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep9
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep9
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0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep9
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep9
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PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep9
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PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep9
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0	rbbm_WUrt_RT_idle_clean_03	2002Sep9
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0	rbbm_WUrt_RT_idle_clean_04	2002Sep9
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0	rbbm_WUrt_RT_idle_clean_05	2002Sep9
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0	rbbm_WUrt_VIPH2_dma_idle	2002Sep9
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0	rbbm_WUrt_VIPH3_dma_idle	2002Sep9
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0	rbbm_WU_VIPH1_dma_idle	2002Sep9
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0	rbbm_WU_VIPH2_dma_idle	2002Sep9
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0	rbbm_WU_VIPH3_dma_idle	2002Sep9
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0	reg_to_mem_rt	2002Sep10
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0	rtee_display2_comp_events_16	2002Sep10
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0	rtee_mem_poll_comp_001_events_16	2002Sep10
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0	rtee_mem_poll_comp_010_events_16	2002Sep10
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0	rtee_mem_poll_comp_011_events_16	2002Sep10
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0	rtee_mem_poll_comp_100_events_16	2002Sep10
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0	rtee_mem_poll_comp_101_events_16	2002Sep10
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0	rtee_mem_poll_comp_110_events_16	2002Sep10
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0	rtee_mem_poll_comp_111_events_16	2002Sep10
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0	rtee_mem_poll_comp_masks_events_16	2002Sep10
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0	rtee_mem_poll_comp_masks_events_16_random	2002Sep10
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0	rtee_mem_poll_comp_swaps_events_16	2002Sep10

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0	rtee_reg_poll_comp_110_events_16	2002Sep9
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0	rtee_vip_comp_events_16	2002Sep9
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0	set_constant_alu_max2	2002Sep10
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0	set_constant_bool_max2	2002Sep10
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PASS		
0	set_constant_indr1_short2	2002Sep10

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0	set_constant_indr1_write_enable_lcc	2002Sep10
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0	set_constant_write_enable_lcc2	2002Sep10
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0	set_state_rt_c	2002Sep10
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0	set_state_rt_d	2002Sep10

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0	set_st_indrl_mismatch4	2002Sep10
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0	set_st_indrl_sublk2_only	2002Sep10
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0	set_st_indrl_sublk3_only	2002Sep10
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0	set_st_indrl_sublk6	2002Sep10
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0	set_st_indrl_sublk6_only	2002Sep10
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0	set_st_indrl_sublk7	2002Sep10
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PASS		
0	set_st_indrl_vs_only	2002Sep10

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0	set_st_indr2_sublk2	2002Sep10
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0	set_st_indr2_sublk2_only	2002Sep10
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0	set_st_indr2_sublk3	2002Sep10
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0	set_st_indr2_sublk3_only	2002Sep10
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0	set_st_indr2_sublk4	2002Sep10
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0	set_st_indr2_sublk4_only	2002Sep10
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0	set_st_indr2_sublk5	2002Sep10
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0	set_st_indr2_sublk5_only	2002Sep10
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0	set_st_indr2_sublk6	2002Sep10
PASS		
0	set_st_indr2_sublk6_only	2002Sep10
PASS		
0	set_st_indr2_sublk7	2002Sep10
PASS		
0	set_st_indr2_sublk7_only	2002Sep10
PASS		
0	set_st_indr2_vs	2002Sep10
PASS		
0	set_st_indr2_vs_only	2002Sep10

PASS		
0	set_st_pfmatch_disabled	2002Sep10
PASS		
0	set_st_ring_match	2002Sep10
PASS		
0	set_st_ring_mismatch	2002Sep10
PASS		
0	set_st_ring_mismatch2	2002Sep10
PASS		
0	set_st_ring_mismatch3	2002Sep10
PASS		
0	set_st_ring_mismatch4	2002Sep10
PASS		
0	set_st_ring_ps	2002Sep10
PASS		
0	set_st_ring_ps_only	2002Sep10
PASS		
0	set_st_ring_subblk0	2002Sep10
PASS		
0	set_st_ring_subblk1	2002Sep10
PASS		
0	set_st_ring_subblk1_only	2002Sep10
PASS		
0	set_st_ring_subblk2	2002Sep10
PASS		
0	set_st_ring_subblk2_only	2002Sep10
PASS		
0	set_st_ring_subblk3	2002Sep10
PASS		
0	set_st_ring_subblk3_only	2002Sep10
PASS		
0	set_st_ring_subblk4	2002Sep10
PASS		
0	set_st_ring_subblk4_only	2002Sep10
PASS		
0	set_st_ring_subblk5	2002Sep10
PASS		
0	set_st_ring_subblk5_only	2002Sep10
PASS		
0	set_st_ring_subblk6	2002Sep10
PASS		
0	set_st_ring_subblk6_only	2002Sep10
PASS		
0	set_st_ring_subblk7	2002Sep10
PASS		
0	set_st_ring_subblk7_only	2002Sep10
PASS		
0	set_st_ring_vs	2002Sep10

PASS		
0	set_st_ring_vs_only	2002Sep10
PASS		
0	set_st_stress_a	2002Sep10
PASS		
0	set_st_stress_b	2002Sep10
PASS		
0	set_st_stress_c	2002Sep10
PASS		
0	set_st_stress_d	2002Sep10
PASS		
0	set_st_stress_e	2002Sep10
PASS		
0	set_st_stress_f	2002Sep10
PASS		
676	Small_Text_BM	2002Sep10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep10
PASS		
0	src_clip	2002Sep10
PASS		
0	srf_cohr	2002Sep10
PASS		
0	trans_bitblt	2002Sep10
PASS		
0	type0_2long_pckts	2002Sep10
PASS		
0	type0_3indr_buffs	2002Sep10
PASS		
0	type0_4pckts	2002Sep10
PASS		
0	type0_incr_updt_cnst	2002Sep10
PASS		
0	type0_incr_updt_instrmem	2002Sep10
PASS		
0	type0_incr_updt_st	2002Sep10
PASS		
0	type0_incr_updt_stress	2002Sep10
PASS		
0	type0_ind1_ind2_a	2002Sep10
PASS		
0	type0_ind1_ind2_b	2002Sep10
PASS		
0	type0_indr_buff	2002Sep10
PASS		
0	Type0_IQ2_BM	2002Sep10
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep10

PASS		
0	Type0_IQ_BM	2002Sep10
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep10
PASS		
0	Type0_PQ_BM	2002Sep10
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep10
PASS		
0	type1_4pckts	2002Sep10
PASS		
0	type1_indr_buff	2002Sep10
PASS		
0	type1_pckt	2002Sep10
PASS		
0	Type3_Bitblt	2002Sep10
PASS		
0	Type3_Load_Palette	2002Sep10
PASS		
0	Type3_NOP	2002Sep10
PASS		
0	Type3_Polyline	2002Sep10
PASS		
0	Type3_Polyline_Large	2002Sep10
PASS		
0	type_random	2002Sep10
PASS		
706	uram_rd_wrt	2002Aug23
FAIL	rbbmWr.cmp	
707	uram_rd_wrt_rt	2002Aug23
FAIL	rbbmWr.cmp	
0	uram_wrt_rt_ucode	2002Sep10
PASS		
709	uram_wrt_ucode	2002Sep10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_a	2002Sep10
PASS		
0	viz_query_draw_indx	2002Sep10
PASS		
0	viz_query_draw_indx_indr1	2002Sep10
PASS		
0	viz_query_draw_indx_indr2	2002Sep10
PASS		
714	viz_query_draw_indx_multipass	2002Sep10
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	viz_query_draw_indx_ring	2002Sep10
PASS		
0	viz_query_indr1	2002Sep10

PASS
0 viz_query_indr1_simple 2002Sep10
PASS
0 viz_query_indr2 2002Sep10
PASS
0 viz_query_indr2_simple 2002Sep10
PASS
0 viz_query_ring 2002Sep10
PASS
0 viz_query_ring_simple 2002Sep10
PASS
0 wait_for_idle 2002Sep10
PASS
0 wait_reg_mem_poll_mem0 2002Sep10
PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep10
PASS
0 wait_reg_mem_poll_mem1 2002Sep10
PASS
0 wait_reg_mem_poll_mem2 2002Sep10
PASS
0 wait_reg_mem_poll_mem3 2002Sep10
PASS
0 wait_reg_mem_poll_reg0 2002Sep10
PASS
0 wait_reg_mem_poll_reg1 2002Sep10
PASS
0 wait_reg_mem_poll_reg2 2002Sep10
PASS
0 wait_reg_mem_poll_reg3 2002Sep10
PASS

=====
Tests_Run_count = 731 Pass_count = 707 Fail_count = 24 Incomplete_count = 0
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep21
PASS			
0	2d_indr_buffer_b		2002Sep21
PASS			
0	2d_indr_buffer_c		2002Sep21
PASS			
0	2d_indr_buffer_d		2002Sep21
PASS			
0	2d_indr_buffer_e		2002Sep21
PASS			
0	2d_indr_buffer_f		2002Sep21
PASS			
0	2d_indr_buffer_g		2002Sep21
PASS			
8	2d_indr_buffer_random		2002Sep21
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep21
PASS			
0	3d_draw_indx_2_b		2002Sep21
PASS			
0	IQ2_Mult		2002Sep21
PASS			
0	IQ2_Wrap_Legacy		2002Sep21
PASS			
0	IQ_Wrap		2002Sep21
PASS			
0	Isync_Control		2002Sep21
PASS			
0	Isync_Control_2D_3D		2002Sep21
PASS			
0	Isync_Control_3D_2D		2002Sep21
PASS			
0	RB_BUFSZ_0		2002Sep21
PASS			
0	RB_BUFSZ_1		2002Sep21
PASS			
0	RB_Wrap		2002Sep21
PASS			
20	Small_Text_BM		2002Sep21


```

**FAIL**          cp_rbbm.cmp rbbmWr.cmp
0   Type0_IQ2_BM                                     2002Sep21
PASS
0   Type0_IQ2_BM_IB_Packet                           2002Sep21
PASS
0   Type0_IQ_BM                                       2002Sep21
PASS
0   Type0_IQ_BM_IB_Packet                             2002Sep21
PASS
0   Type0_PQ_BM                                       2002Sep21
PASS
0   Type0_PQ_BM_Pre_Write_Timer                       2002Sep21
PASS
0   Type3_Bitblt                                       2002Sep21
PASS
0   Type3_Load_Palette                               2002Sep21
PASS
0   Type3_NOP                                         2002Sep21
PASS
0   Type3_Polyline                                    2002Sep21
PASS
0   Type3_Polyline_Large                             2002Sep21
PASS
0   bitblt_multi                                      2002Sep21
PASS
0   brush_32x1mono                                    2002Sep21
PASS
0   brush_8x8color_16                                2002Sep21
PASS
0   brush_8x8color_32                                2002Sep21
PASS
0   brush_8x8color_64                                2002Sep21
PASS
0   brush_8x8color_dst_0_1                           2002Sep21
PASS
0   brush_8x8mono                                     2002Sep21
PASS
0   brush_color_mono_stress                           2002Sep21
PASS
0   brush_flag                                        2002Sep21
PASS
41  brush_type0                                       2002Sep21
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
42  brush_type1                                       2002Sep21
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
43  brush_type10                                      2002Sep21
**FAIL**          cp_rbbm.cmp rbbmWr.cmp
44  brush_type13                                      2002Sep21

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**FAIL**      cp_rbbm.cmp rbbmWr.cmp
45  brush_type14                                2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
46  brush_type6                                  2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
47  brush_type7                                  2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
48  brush_type_other                             2002Sep21
**FAIL**      cp_rbbm.cmp rbbmWr.cmp
0   brush_types_13_14                           2002Sep21
PASS
0   clear_const_wr_en_on_me_init                 2002Sep21
PASS
0   clear_const_wr_en_with_lcc                   2002Sep21
PASS
0   cond_write_mem_poll_mem                      2002Sep21
PASS
0   cond_write_mem_poll_mem2                    2002Sep21
PASS
0   cond_write_mem_poll_mem3                    2002Sep21
PASS
0   cond_write_mem_poll_mem_rt                  2002Sep21
PASS
0   cond_write_mem_poll_reg                     2002Sep21
PASS
0   cond_write_mem_poll_reg2                    2002Sep21
PASS
0   cond_write_mem_poll_reg3                    2002Sep21
PASS
59  cp_acpi_recovery_soft_reset                 2002Sep21
**FAIL**      cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp
0   cp_bios_scratch                              2002Sep21
PASS
0   cp_ib2_base000_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base001_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base010_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base011_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base100_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base101_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base110_bufszXXX                     2002Sep21
PASS
0   cp_ib2_base111_bufszXXX                     2002Sep21

```

PASS		
0	cp_ib_base000_bufszXXX	2002Sep21
PASS		
0	cp_ib_base001_bufszXXX	2002Sep21
PASS		
0	cp_ib_base010_bufszXXX	2002Sep21
PASS		
0	cp_ib_base011_bufszXXX	2002Sep21
PASS		
0	cp_ib_base100_bufszXXX	2002Sep21
PASS		
0	cp_ib_base101_bufszXXX	2002Sep21
PASS		
0	cp_ib_base110_bufszXXX	2002Sep21
PASS		
0	cp_ib_base111_bufszXXX	2002Sep21
PASS		
0	cp_interrupt	2002Sep21
PASS		
0	cp_interrupt_rt_events_16	2002Sep21
PASS		
0	cp_iq2_alignment_000	2002Sep21
PASS		
0	cp_iq2_alignment_001	2002Sep21
PASS		
0	cp_iq2_alignment_010	2002Sep21
PASS		
0	cp_iq2_alignment_011	2002Sep21
PASS		
0	cp_iq2_alignment_100	2002Sep21
PASS		
0	cp_iq2_alignment_101	2002Sep21
PASS		
0	cp_iq2_alignment_110	2002Sep21
PASS		
0	cp_iq2_alignment_111	2002Sep21
PASS		
0	cp_iq_alignment_000	2002Sep21
PASS		
0	cp_iq_alignment_001	2002Sep21
PASS		
0	cp_iq_alignment_010	2002Sep21
PASS		
0	cp_iq_alignment_011	2002Sep21
PASS		
0	cp_iq_alignment_100	2002Sep21
PASS		
0	cp_iq_alignment_101	2002Sep21

PASS		
0	cp_iq_alignment_110	2002Sep21
PASS		
0	cp_iq_alignment_111	2002Sep21
PASS		
0	cp_max_size_packet	2002Sep21
PASS		
96	cp_max_size_packet_random	2002Sep21
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
0	cp_nrt_rt_protection_mode	2002Sep21
PASS		
0	cp_output_stress	2002Sep21
PASS		
0	cp_output_stress_random	2002Sep21
PASS		
0	cp_protection_mode	2002Sep21
PASS		
0	cp_rb_alignment_000	2002Sep21
PASS		
0	cp_rb_alignment_001	2002Sep21
PASS		
0	cp_rb_alignment_010	2002Sep21
PASS		
0	cp_rb_alignment_011	2002Sep21
PASS		
0	cp_rb_alignment_100	2002Sep21
PASS		
0	cp_rb_alignment_101	2002Sep21
PASS		
0	cp_rb_alignment_110	2002Sep21
PASS		
0	cp_rb_alignment_111	2002Sep21
PASS		
0	cp_rb_wptrXXX_base000	2002Sep21
PASS		
0	cp_rb_wptrXXX_base001	2002Sep21
PASS		
0	cp_rb_wptrXXX_base010	2002Sep21
PASS		
0	cp_rb_wptrXXX_base011	2002Sep21
PASS		
0	cp_rb_wptrXXX_base100	2002Sep21
PASS		
0	cp_rb_wptrXXX_base101	2002Sep21
PASS		
0	cp_rb_wptrXXX_base110	2002Sep21
PASS		
0	cp_rb_wptrXXX_base111	2002Sep21

PASS		
0	cp_reg_rw_test	2002Sep21
PASS		
0	cp_register_reads	2002Sep21
PASS		
0	cp_rt_protection_mode	2002Sep21
PASS		
0	cp_scratch	2002Sep21
PASS		
0	cp_scratch_multi	2002Sep21
PASS		
0	csf_reg_rd_default	2002Sep21
PASS		
0	csf_reg_wr_rd	2002Sep21
PASS		
0	dma_cp_sync	2002Sep21
PASS		
0	dma_dscrstr_chkcn_bit	2002Sep21
PASS		
0	dma_dscrstr_flush1	2002Sep21
PASS		
0	dma_dscrstr_flush2	2002Sep21
PASS		
0	dma_dscrstr_flush3	2002Sep21
PASS		
0	dma_m2m_src00_dstXX	2002Sep21
PASS		
0	dma_m2m_src01_dstXX	2002Sep21
PASS		
0	dma_m2m_src10_dstXX	2002Sep21
PASS		
0	dma_m2m_src11_dstXX	2002Sep21
PASS		
0	dma_mem2mem	2002Sep21
PASS		
0	dma_mem2mem_abort	2002Sep21
PASS		
0	dma_mem2mem_alignments	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment4	2002Sep21

PASS		
0	dma_mem2mem_byte_alignment5	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep21
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep21
PASS		
0	dma_mem2mem_pio_abort	2002Sep21
PASS		
0	dma_mem2mem_random	2002Sep21
PASS		
0	dma_mem2reg	2002Sep21
PASS		
0	dma_mem2reg_abort	2002Sep21
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep21
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep21
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep21
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep21
PASS		
0	dma_mem2reg_sync	2002Sep21
PASS		
0	dma_r2r_m2m_r2r	2002Sep21
PASS		
0	dma_r2r_m2r_r2r	2002Sep21
PASS		
0	dma_r2r_r2m_r2r	2002Sep21
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep21
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep21
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep21
PASS		
0	dma_reg2mem	2002Sep21
PASS		
0	dma_reg2mem_abort	2002Sep21
PASS		
0	dma_reg2reg	2002Sep21
PASS		
0	dma_reg2reg_2dw	2002Sep21
PASS		
0	dma_reg2reg_daic	2002Sep21
PASS		
0	dma_reg2reg_mult	2002Sep21

PASS		
0	dma_reg2reg_mult2	2002Sep21
PASS		
0	dma_reg2reg_mult3	2002Sep21
PASS		
0	dma_reg2reg_saic	2002Sep21
PASS		
0	dma_reg2reg_sdaic	2002Sep21
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep21
PASS		
0	dma_reg2reg_stress	2002Sep21
PASS		
0	draw_indx_a	2002Sep21
PASS		
0	draw_indx_b	2002Sep21
PASS		
0	draw_indx_c	2002Sep21
PASS		
0	draw_indx_indr1_a	2002Sep21
PASS		
0	draw_indx_indr1_b	2002Sep21
PASS		
0	draw_indx_indr1_short_a	2002Sep21
PASS		
0	draw_indx_indr1_short_b	2002Sep21
PASS		
0	draw_indx_indr1_stress	2002Sep21
PASS		
0	draw_indx_indr2_a	2002Sep21
PASS		
0	draw_indx_indr2_b	2002Sep21
PASS		
0	draw_indx_indr2_short_a	2002Sep21
PASS		
0	draw_indx_indr2_short_b	2002Sep21
PASS		
0	draw_indx_indr2_stress	2002Sep21
PASS		
0	draw_indx_ring1	2002Sep21
PASS		
0	draw_indx_ring2	2002Sep21
PASS		
0	draw_indx_ring_short1	2002Sep21
PASS		
0	draw_indx_ring_short2	2002Sep21
PASS		
0	draw_indx_ring_stress	2002Sep21

PASS		
0	draw_indx_short_a	2002Sep21
PASS		
0	draw_indx_short_b	2002Sep21
PASS		
0	draw_indx_short_c	2002Sep21
PASS		
0	draw_indx_stress_a	2002Sep21
PASS		
0	draw_indx_stress_b	2002Sep21
PASS		
0	draw_indx_stress_c	2002Sep21
PASS		
0	event_timestamp_write	2002Sep21
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep21
PASS		
0	ft_indr1_july22	2002Sep21
PASS		
0	ft_indr2_july22	2002Sep21
PASS		
0	ft_july22	2002Sep21
PASS		
0	ft_july22_random	2002Sep21
PASS		
0	header_dump	2002Sep21
PASS		
0	header_dump_rt	2002Sep21
PASS		
0	header_dump_small	2002Sep21
PASS		
204	hostdata_blt	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	im_load_a	2002Sep21
PASS		
0	im_load_b	2002Sep21
PASS		
0	im_load_cid_mismatch	2002Sep21
PASS		
0	im_load_immediate	2002Sep21
PASS		
0	im_load_immediate_nrt_me	2002Sep21
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep21
PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep21
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep21

PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep21
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep21
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep21
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep21
PASS		
0	im_load_indr1_a	2002Sep21
PASS		
0	im_load_indr1_b	2002Sep21
PASS		
0	im_load_indr2_a	2002Sep21
PASS		
0	im_load_indr2_b	2002Sep21
PASS		
0	im_load_invalidate	2002Sep21
PASS		
0	im_load_match	2002Sep21
PASS		
0	im_load_match2	2002Sep21
PASS		
0	im_load_nrt_me	2002Sep21
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep21
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep21
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep21
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep21
PASS		
0	im_load_pfmacth_disable	2002Sep21
PASS		
0	im_load_ring_a	2002Sep21
PASS		
0	im_load_ring_b	2002Sep21
PASS		
0	im_load_rt	2002Sep21
PASS		
0	im_load_rt2	2002Sep21
PASS		
0	im_load_rt3	2002Sep21
PASS		
0	im_load_rt4	2002Sep21
PASS		
0	im_load_sz_mismatch	2002Sep21

PASS		
0	incremental_update_const	2002Sep21
PASS		
0	incremental_update_instr	2002Sep21
PASS		
0	incremental_update_state	2002Sep21
PASS		
0	indirect_buffer_a	2002Sep21
PASS		
0	indirect_buffer_b	2002Sep21
PASS		
0	indirect_buffer_c	2002Sep21
PASS		
0	indirect_buffer_d	2002Sep21
PASS		
0	indirect_buffer_e	2002Sep21
PASS		
0	indirect_buffer_f	2002Sep21
PASS		
0	indirect_buffer_g	2002Sep21
PASS		
0	indirect_buffer_h	2002Sep21
PASS		
0	indirect_buffer_i	2002Sep21
PASS		
0	indirect_buffer_j	2002Sep21
PASS		
0	indirect_buffer_k	2002Sep21
PASS		
0	indirect_buffer_l	2002Sep21
PASS		
0	indirect_buffer_mp_a	2002Sep21
PASS		
0	indirect_buffer_mp_b	2002Sep21
PASS		
0	indirect_buffer_mp_c	2002Sep21
PASS		
0	indirect_buffer_mp_d	2002Sep21
PASS		
0	indirect_buffer_mp_e	2002Sep21
PASS		
0	indirect_buffer_mp_f	2002Sep21
PASS		
258	invalid_opcode	2002Sep21
INCOMPLETE		
0	invalidate_on_me_init	2002Sep21
PASS		
0	invalidate_state_constants	2002Sep21

PASS		
0	invalidate_state_shaders	2002Sep21
PASS		
0	invalidate_state_shaders2	2002Sep21
PASS		
0	invalidate_state_sublks	2002Sep21
PASS		
0	invalidate_state_sublks2	2002Sep21
PASS		
0	iq1_iq2_mult	2002Sep21
PASS		
0	iq1_iq2_push_wrap	2002Sep21
PASS		
0	iq2_csq_full	2002Sep21
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep21
PASS		
0	ld_cnst_cntx_coherency_cntr2	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep21
PASS		
0	ld_cnst_cntx_match	2002Sep21
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep21
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep21
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep21
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep21
PASS		
0	ld_cnst_cntx_pfmacth_disable	2002Sep21
PASS		
0	ld_cnst_cntx_replicate4_alu	2002Sep21

PASS		
0	ld_cnst_cntx_replicate4_tex	2002Sep21
PASS		
0	ld_cnst_cntx_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_replicate_alu	2002Sep21
PASS		
0	ld_cnst_cntx_replicate_tex	2002Sep21
PASS		
0	ld_cnst_cntx_ring_replicate	2002Sep21
PASS		
0	ld_cnst_cntx_ring_replicate4	2002Sep21
PASS		
0	ld_cnst_cntx_ring_replicate8	2002Sep21
PASS		
0	ld_cnst_cntx_ring_simple	2002Sep21
PASS		
0	ld_cnst_cntx_simple_alu	2002Sep21
PASS		
0	ld_cnst_cntx_simple_tex	2002Sep21
PASS		
0	load_constant_context_rt	2002Sep21
PASS		
0	load_palette	2002Sep21
PASS		
0	lp_2d3d	2002Sep21
PASS		
0	mem_write	2002Sep21
PASS		
0	mem_write_rt	2002Sep21
PASS		
0	mpeg_index	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_0	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_1	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_2	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_3	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_4	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_5	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_6	2002Sep21
PASS		
0	mpeg_index_multi_context_max_context_7	2002Sep21

PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_1	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_2	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_3	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_4	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_5	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_6	2002Sep21
PASS		
0	mpeg_index_multi_context_rt_enabled_max_context_7	2002Sep21
PASS		
0	nop_eating_disabled	2002Sep21
PASS		
0	paint_multi	2002Sep21
PASS		
0	paint_packet	2002Sep21
PASS		
319	paint_packet_gui_control	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	pitch_clip	2002Sep21
PASS		
0	ply_nextscan	2002Sep21
PASS		
0	rbbm_WU_2D_idle_01	2002Sep21
PASS		
0	rbbm_WU_2D_idle_02	2002Sep21
PASS		
0	rbbm_WU_2D_idle_03	2002Sep21
PASS		
0	rbbm_WU_2D_idle_04	2002Sep21
PASS		
0	rbbm_WU_2D_idle_05	2002Sep21
PASS		
0	rbbm_WU_2D_idle_06	2002Sep21
PASS		
0	rbbm_WU_2D_idle_07	2002Sep21
PASS		
0	rbbm_WU_2D_idle_08	2002Sep21
PASS		
0	rbbm_WU_2D_idle_09	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_01	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_02	2002Sep21

PASS		
0	rbbm_WU_2D_idle_clean_03	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_04	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_05	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_06	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_07	2002Sep21
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0	rbbm_WU_2D_idle_clean_08	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_09	2002Sep21
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0	rbbm_WU_2D_idle_clean_10	2002Sep21
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0	rbbm_WU_2D_idle_clean_11	2002Sep21
PASS		
0	rbbm_WU_2D_idle_clean_12	2002Sep21
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0	rbbm_WU_3D_idle_01	2002Sep21
PASS		
0	rbbm_WU_3D_idle_02	2002Sep21
PASS		
0	rbbm_WU_3D_idle_03	2002Sep21
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0	rbbm_WU_3D_idle_08	2002Sep21
PASS		
0	rbbm_WU_3D_idle_09	2002Sep21
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0	rbbm_WU_3D_idle_clean_01	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_02	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_03	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_04	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_05	2002Sep21

PASS		
0	rbbm_WU_3D_idle_clean_06	2002Sep21
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0	rbbm_WU_3D_idle_clean_07	2002Sep21
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0	rbbm_WU_3D_idle_clean_08	2002Sep21
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0	rbbm_WU_3D_idle_clean_09	2002Sep21
PASS		
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PASS		
0	rbbm_WU_3D_idle_clean_11	2002Sep21
PASS		
0	rbbm_WU_3D_idle_clean_12	2002Sep21
PASS		
0	rbbm_WU_BIF_RBBM_agp_flush_off	2002Sep21
PASS		
0	rbbm_WU_BOTH_GRP update_pending_off	2002Sep21
PASS		
0	rbbm_WU_CP_dma_idle	2002Sep21
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_fe	2002Sep21
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_on	2002Sep21
PASS		
0	rbbm_WU_CRTC_D1MODE_vline_re	2002Sep21
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_fe	2002Sep21
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_on	2002Sep21
PASS		
0	rbbm_WU_CRTC_D2MODE_vline_re	2002Sep21
PASS		
0	rbbm_WU_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_MH_hdp_clean	2002Sep21
PASS		
0	rbbm_WU_OV_FLIP_D1OVL_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_OV_FLIP_D2OVL_update_pending_off	2002Sep21
PASS		
0	rbbm_WU_VIPH0_dma_idle	2002Sep21
PASS		
0	rbbm_WU_VIPH1_dma_idle	2002Sep21
PASS		
0	rbbm_WU_VIPH2_dma_idle	2002Sep21

PASS		
0	rbbm_WU_VIPH3_dma_idle	2002Sep21
PASS		
0	rbbm_WU_cmdfifo_entries	2002Sep21
PASS		
0	rbbm_WU_idct_semaphore	2002Sep21
PASS		
0	rbbm_WU_pad_ext_sig_rising_edge	2002Sep21
PASS		
0	rbbm_WUrt_BIF_RBBM_agp_flush_off	2002Sep21
PASS		
0	rbbm_WUrt_BOTH_GRP_H_update_pending_off	2002Sep21
PASS		
0	rbbm_WUrt_CP_dma_idle	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_fe	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_on	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_D1MODE_vline_re	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_fe	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_on	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_D2MODE_vline_re	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D1GRPH_update_pending_off	2002Sep21
PASS		
0	rbbm_WUrt_CRTC_PFLIP_D2GRPH_update_pending_off	2002Sep21
PASS		
0	rbbm_WUrt_MH_hdp_clean	2002Sep21
PASS		
0	rbbm_WUrt_OV_FLIP_D1OVL_update_pending_off	2002Sep21
PASS		
0	rbbm_WUrt_OV_FLIP_D2OVL_update_pending_off	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_01	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_02	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_03	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_04	2002Sep21
PASS		
403	rbbm_WUrt_RT_idle_clean_01	2002Sep21
FAIL		
0	rbbm_WUrt_RT_idle_clean_02	2002Sep21

PASS		
0	rbbm_WUrt_RT_idle_clean_03	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_clean_04	2002Sep21
PASS		
0	rbbm_WUrt_RT_idle_clean_05	2002Sep21
PASS		
0	rbbm_WUrt_VIPh0_dma_idle	2002Sep21
PASS		
0	rbbm_WUrt_VIPh1_dma_idle	2002Sep21
PASS		
0	rbbm_WUrt_VIPh2_dma_idle	2002Sep21
PASS		
0	rbbm_WUrt_VIPh3_dma_idle	2002Sep21
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0	rbbm_WUrt_pad_ext_sig_rising_edge	2002Sep21
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0	rbbm_clk_active_deassert_min_max	2002Sep21
PASS		
0	rbbm_clk_active_each_pipe	2002Sep21
PASS		
0	rbbm_clk_active_for_read_error	2002Sep21
PASS		
0	rbbm_clk_active_for_read_request	2002Sep21
PASS		
0	rbbm_clk_active_on_reset	2002Sep21
PASS		
0	rbbm_cp_non_queued_rdwr_01	2002Sep21
PASS		
0	rbbm_cp_non_queued_rdwr_02	2002Sep21
PASS		
420	rbbm_cp_queued_holds_nonqueued	2002Sep21
FAIL	cp_rbbm.cmp	
0	rbbm_cp_queued_rdwr	2002Sep21
PASS		
0	rbbm_int_ack	2002Sep21
PASS		
0	rbbm_int_each_client	2002Sep21
PASS		
0	rbbm_int_nrt_gfxpipe_idle	2002Sep21
PASS		
0	rbbm_int_nrt_gfxpipe_idle_rderr	2002Sep21
PASS		
0	rbbm_int_rderr	2002Sep21
PASS		
0	rbbm_int_st_restore_nrt_gfxpipe_idle_rderr	2002Sep21
PASS		
0	rbbm_isync_cpscratch_idlegui	2002Sep21

PASS		
0	rbbm_isync_wait_idlegui	2002Sep21
PASS		
0	rbbm_soft_reset	2002Sep21
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0	reg_to_mem	2002Sep21
PASS		
0	reg_to_mem_rt	2002Sep21
PASS		
0	rop_cfd_wmd	2002Sep21
PASS		
0	rstr_ordr	2002Sep21
PASS		
0	rt_invalid_opcode	2002Sep21
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0	rtee_arm_wait_idle_trigger_events_16	2002Sep21
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0	rtee_discretes_arm_rearm_events_32	2002Sep21
PASS		
0	rtee_discretes_falling_00_events_16	2002Sep21
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0	rtee_discretes_falling_01_events_16	2002Sep21
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0	rtee_discretes_falling_02_events_16	2002Sep21
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0	rtee_discretes_falling_08_events_16	2002Sep21
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0	rtee_discretes_falling_14_events_16	2002Sep21

PASS		
0	rtee_discretes_falling_15_events_16	2002Sep21
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0	rtee_discretes_falling_17_events_16	2002Sep21
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0	rtee_discretes_falling_19_events_16	2002Sep21
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0	rtee_discretes_falling_30_events_16	2002Sep21
PASS		
0	rtee_discretes_falling_31_events_16	2002Sep21
PASS		
0	rtee_discretes_invert_00_events_16	2002Sep21
PASS		
0	rtee_discretes_invert_01_events_16	2002Sep21
PASS		
0	rtee_discretes_invert_02_events_16	2002Sep21
PASS		
0	rtee_discretes_invert_03_events_16	2002Sep21
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0	rtee_discretes_invert_04_events_16	2002Sep21
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0	rtee_discretes_invert_06_events_16	2002Sep21

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0	rtee_discretes_level_06_events_16	2002Sep21
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0	rtee_discretes_level_09_events_16	2002Sep21
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0	rtee_discretes_level_31_events_16	2002Sep21
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0	rtee_discretes_manual_disarm_events_16	2002Sep21
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0	rtee_discretes_rising_00_events_16	2002Sep21
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0	rtee_discretes_rising_31_events_16	2002Sep21
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0	rtee_display1_comp_events_16	2002Sep21
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0	rtee_display2_comp_events_16	2002Sep21
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0	rtee_mem_linear_search_events_2	2002Sep21
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0	rtee_mem_poll_comp_000_events_16	2002Sep21
PASS		
0	rtee_mem_poll_comp_001_events_16	2002Sep21
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0	rtee_mem_poll_comp_010_events_16	2002Sep21

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0	rtee_mem_poll_comp_011_events_16	2002Sep21
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0	rtee_mem_poll_comp_100_events_16	2002Sep21
PASS		
0	rtee_mem_poll_comp_101_events_16	2002Sep21
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0	rtee_mem_poll_comp_110_events_16	2002Sep21
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0	rtee_mem_poll_comp_111_events_16	2002Sep21
PASS		
0	rtee_mem_poll_comp_masks_events_16	2002Sep21
PASS		
0	rtee_mem_poll_comp_masks_events_16_random	2002Sep21
PASS		
0	rtee_mem_poll_comp_swaps_events_16	2002Sep21
PASS		
0	rtee_mem_poll_intervals_events_16	2002Sep21
PASS		
0	rtee_prefetch_parse_nops	2002Sep21
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0	rtee_reg_linear_search_events_2	2002Sep21
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0	rtee_reg_poll_comp_000_events_16	2002Sep21
PASS		
0	rtee_reg_poll_comp_001_events_16	2002Sep21
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0	rtee_reg_poll_comp_010_events_16	2002Sep21
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0	rtee_reg_poll_comp_011_events_16	2002Sep21
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0	rtee_reg_poll_comp_100_events_16	2002Sep21
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0	rtee_reg_poll_comp_101_events_16	2002Sep21
PASS		
0	rtee_reg_poll_comp_110_events_16	2002Sep21
PASS		
0	rtee_reg_poll_comp_111_events_16	2002Sep21
PASS		
0	rtee_reg_poll_comp_queued_nonqueued_events_16	2002Sep21
PASS		
0	rtee_vip_comp_events_16	2002Sep21
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0	scratch_mem_read	2002Sep21
PASS		
0	scratch_mem_read2	2002Sep21
PASS		
0	set_constant	2002Sep21

PASS		
0	set_constant2	2002Sep21
PASS		
0	set_constant_alu_max1	2002Sep21
PASS		
0	set_constant_alu_max2	2002Sep21
PASS		
0	set_constant_bool_max1	2002Sep21
PASS		
0	set_constant_bool_max2	2002Sep21
PASS		
0	set_constant_indr1_short	2002Sep21
PASS		
0	set_constant_indr1_short2	2002Sep21
PASS		
0	set_constant_indr1_write_enable	2002Sep21
PASS		
0	set_constant_indr1_write_enable_lcc	2002Sep21
PASS		
0	set_constant_indr2_short	2002Sep21
PASS		
0	set_constant_indr2_short2	2002Sep21
PASS		
0	set_constant_indr2_write_enable	2002Sep21
PASS		
0	set_constant_indr2_write_enable_lcc	2002Sep21
PASS		
0	set_constant_loop_max1	2002Sep21
PASS		
0	set_constant_loop_max2	2002Sep21
PASS		
0	set_constant_ring_short	2002Sep21
PASS		
0	set_constant_ring_short2	2002Sep21
PASS		
0	set_constant_ring_write_enable	2002Sep21
PASS		
0	set_constant_ring_write_enable_lcc	2002Sep21
PASS		
0	set_constant_short	2002Sep21
PASS		
0	set_constant_short2	2002Sep21
PASS		
0	set_constant_tex_max1	2002Sep21
PASS		
0	set_constant_tex_max2	2002Sep21
PASS		
0	set_constant_write_enable	2002Sep21

PASS		
0	set_constant_write_enable_lcc	2002Sep21
PASS		
0	set_constant_write_enable_lcc2	2002Sep21
PASS		
0	set_scissors	2002Sep21
PASS		
0	set_st_indrl_match	2002Sep21
PASS		
0	set_st_indrl_mismatch	2002Sep21
PASS		
0	set_st_indrl_mismatch2	2002Sep21
PASS		
0	set_st_indrl_mismatch3	2002Sep21
PASS		
0	set_st_indrl_mismatch4	2002Sep21
PASS		
0	set_st_indrl_ps	2002Sep21
PASS		
0	set_st_indrl_ps_only	2002Sep21
PASS		
0	set_st_indrl_sublk0	2002Sep21
PASS		
0	set_st_indrl_sublk1	2002Sep21
PASS		
0	set_st_indrl_sublk1_only	2002Sep21
PASS		
0	set_st_indrl_sublk2	2002Sep21
PASS		
0	set_st_indrl_sublk2_only	2002Sep21
PASS		
0	set_st_indrl_sublk3	2002Sep21
PASS		
0	set_st_indrl_sublk3_only	2002Sep21
PASS		
0	set_st_indrl_sublk4	2002Sep21
PASS		
0	set_st_indrl_sublk4_only	2002Sep21
PASS		
0	set_st_indrl_sublk5	2002Sep21
PASS		
0	set_st_indrl_sublk5_only	2002Sep21
PASS		
0	set_st_indrl_sublk6	2002Sep21
PASS		
0	set_st_indrl_sublk6_only	2002Sep21
PASS		
0	set_st_indrl_sublk7	2002Sep21

PASS		
0	set_st_indr1_sublk7_only	2002Sep21
PASS		
0	set_st_indr1_vs	2002Sep21
PASS		
0	set_st_indr1_vs_only	2002Sep21
PASS		
0	set_st_indr2_match	2002Sep21
PASS		
0	set_st_indr2_mismatch	2002Sep21
PASS		
0	set_st_indr2_mismatch2	2002Sep21
PASS		
0	set_st_indr2_mismatch3	2002Sep21
PASS		
0	set_st_indr2_mismatch4	2002Sep21
PASS		
0	set_st_indr2_ps	2002Sep21
PASS		
0	set_st_indr2_ps_only	2002Sep21
PASS		
0	set_st_indr2_sublk0	2002Sep21
PASS		
0	set_st_indr2_sublk1	2002Sep21
PASS		
0	set_st_indr2_sublk1_only	2002Sep21
PASS		
0	set_st_indr2_sublk2	2002Sep21
PASS		
0	set_st_indr2_sublk2_only	2002Sep21
PASS		
0	set_st_indr2_sublk3	2002Sep21
PASS		
0	set_st_indr2_sublk3_only	2002Sep21
PASS		
0	set_st_indr2_sublk4	2002Sep21
PASS		
0	set_st_indr2_sublk4_only	2002Sep21
PASS		
0	set_st_indr2_sublk5	2002Sep21
PASS		
0	set_st_indr2_sublk5_only	2002Sep21
PASS		
0	set_st_indr2_sublk6	2002Sep21
PASS		
0	set_st_indr2_sublk6_only	2002Sep21
PASS		
0	set_st_indr2_sublk7	2002Sep21

PASS		
0	set_st_indr2_sublk7_only	2002Sep21
PASS		
0	set_st_indr2_vs	2002Sep21
PASS		
0	set_st_indr2_vs_only	2002Sep21
PASS		
0	set_st_pfmatch_disabled	2002Sep21
PASS		
0	set_st_ring_match	2002Sep21
PASS		
0	set_st_ring_mismatch	2002Sep21
PASS		
0	set_st_ring_mismatch2	2002Sep21
PASS		
0	set_st_ring_mismatch3	2002Sep21
PASS		
0	set_st_ring_mismatch4	2002Sep21
PASS		
0	set_st_ring_ps	2002Sep21
PASS		
0	set_st_ring_ps_only	2002Sep21
PASS		
0	set_st_ring_sublk0	2002Sep21
PASS		
0	set_st_ring_sublk1	2002Sep21
PASS		
0	set_st_ring_sublk1_only	2002Sep21
PASS		
0	set_st_ring_sublk2	2002Sep21
PASS		
0	set_st_ring_sublk2_only	2002Sep21
PASS		
0	set_st_ring_sublk3	2002Sep21
PASS		
0	set_st_ring_sublk3_only	2002Sep21
PASS		
0	set_st_ring_sublk4	2002Sep21
PASS		
0	set_st_ring_sublk4_only	2002Sep21
PASS		
0	set_st_ring_sublk5	2002Sep21
PASS		
0	set_st_ring_sublk5_only	2002Sep21
PASS		
0	set_st_ring_sublk6	2002Sep21
PASS		
0	set_st_ring_sublk6_only	2002Sep21

PASS		
0	set_st_ring_sublk7	2002Sep21
PASS		
0	set_st_ring_sublk7_only	2002Sep21
PASS		
0	set_st_ring_vs	2002Sep21
PASS		
0	set_st_ring_vs_only	2002Sep21
PASS		
0	set_st_stress_a	2002Sep21
PASS		
0	set_st_stress_b	2002Sep21
PASS		
0	set_st_stress_c	2002Sep21
PASS		
0	set_st_stress_d	2002Sep21
PASS		
0	set_st_stress_e	2002Sep21
PASS		
0	set_st_stress_f	2002Sep21
PASS		
703	set_state_rt_a	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
704	set_state_rt_b	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
705	set_state_rt_c	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
706	set_state_rt_d	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	smoke_nrt	2002Sep21
PASS		
708	src_clip	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
0	srf_cohr	2002Sep21
PASS		
0	trans_bitblt	2002Sep21
PASS		
0	type0_2long_pckts	2002Sep21
PASS		
0	type0_3indr_buffs	2002Sep21
PASS		
0	type0_4pckts	2002Sep21
PASS		
0	type0_incr_updt_cnst	2002Sep21
PASS		
0	type0_incr_updt_instrmem	2002Sep21
PASS		
0	type0_incr_updt_st	2002Sep21

PASS		
0	type0_incr_updt_stress	2002Sep21
PASS		
0	type0_ind1_ind2_a	2002Sep21
PASS		
0	type0_ind1_ind2_b	2002Sep21
PASS		
0	type0_indr_buff	2002Sep21
PASS		
0	type1_4pckts	2002Sep21
PASS		
0	type1_indr_buff	2002Sep21
PASS		
0	type1_pckt	2002Sep21
PASS		
724	type_random	2002Sep21
FAIL	cp_mh_req.cmp mh_cp_rd.cmp	
725	uram_wrt_rt_ucode	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
726	uram_wrt_ucode	2002Sep21
FAIL	rbbmWr.cmp	
727	viz_query_a	2002Sep21
FAIL	rbbmWr.cmp	
728	viz_query_draw_indx	2002Sep21
FAIL	rbbmWr.cmp	
729	viz_query_draw_indx_indr1	2002Sep21
FAIL	rbbmWr.cmp	
730	viz_query_draw_indx_indr2	2002Sep21
FAIL	rbbmWr.cmp	
731	viz_query_draw_indx_multipass	2002Sep21
FAIL	cp_rbbm.cmp rbbmWr.cmp	
732	viz_query_draw_indx_ring	2002Sep21
FAIL	rbbmWr.cmp	
733	viz_query_indr1	2002Sep21
FAIL	rbbmWr.cmp	
0	viz_query_indr1_simple	2002Sep21
PASS		
735	viz_query_indr2	2002Sep21
FAIL	rbbmWr.cmp	
0	viz_query_indr2_simple	2002Sep21
PASS		
737	viz_query_ring	2002Sep21
FAIL	rbbmWr.cmp	
0	viz_query_ring_simple	2002Sep21
PASS		
0	wait_for_idle	2002Sep21
PASS		
0	wait_reg_mem_poll_mem0	2002Sep21

PASS
0 wait_reg_mem_poll_mem0_rt 2002Sep21
PASS
0 wait_reg_mem_poll_mem1 2002Sep21
PASS
0 wait_reg_mem_poll_mem2 2002Sep21
PASS
0 wait_reg_mem_poll_mem3 2002Sep21
PASS
0 wait_reg_mem_poll_reg0 2002Sep21
PASS
0 wait_reg_mem_poll_reg1 2002Sep21
PASS
0 wait_reg_mem_poll_reg2 2002Sep21
PASS
0 wait_reg_mem_poll_reg3 2002Sep21
PASS

=====
Tests_Run_count = 748 Pass_count = 714 Fail_count = 33 Incomplete_count = 1
=====

No	Test_name	RunTime	Baseline
Test_Status	Probable_Cause_of_Failures		LastPass
0	2d_indr_buffer_a		2002Sep24
	PASS		
0	2d_indr_buffer_b		2002Sep24
	PASS		
0	2d_indr_buffer_c		2002Sep24
	PASS		
0	2d_indr_buffer_d		2002Sep24
	PASS		
0	2d_indr_buffer_e		2002Sep24
	PASS		
0	2d_indr_buffer_f		2002Sep24
	PASS		
0	2d_indr_buffer_g		2002Sep24
	PASS		
8	2d_indr_buffer_random		2002Sep24
	FAIL cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp		
0	3d_draw_indx_2		2002Sep24
	PASS		
0	3d_draw_indx_2_b		2002Sep24
	PASS		
0	IQ2_Mult		2002Sep24
	PASS		
0	IQ2_Wrap_Legacy		2002Sep24
	PASS		
0	IQ_Wrap		2002Sep24
	PASS		
0	Isync_Control		2002Sep24
	PASS		
0	Isync_Control_2D_3D		2002Sep24
	PASS		
0	Isync_Control_3D_2D		2002Sep24
	PASS		
0	RB_BUFSZ_0		2002Sep24
	PASS		
0	RB_BUFSZ_1		2002Sep24
	PASS		
0	RB_Wrap		2002Sep24
	PASS		
0	Small_Text_BM		2002Sep24

PASS		
0	Type0_IQ2_BM	2002Sep24
PASS		
0	Type0_IQ2_BM_IB_Packet	2002Sep24
PASS		
0	Type0_IQ_BM	2002Sep24
PASS		
0	Type0_IQ_BM_IB_Packet	2002Sep24
PASS		
0	Type0_PQ_BM	2002Sep24
PASS		
0	Type0_PQ_BM_Pre_Write_Timer	2002Sep24
PASS		
0	Type3_Bitblt	2002Sep24
PASS		
0	Type3_Load_Palette	2002Sep24
PASS		
0	Type3_NOP	2002Sep24
PASS		
0	Type3_Polyline	2002Sep24
PASS		
0	Type3_Polyline_Large	2002Sep24
PASS		
0	bitblt_multi	2002Sep24
PASS		
0	brush_32x1mono	2002Sep24
PASS		
0	brush_8x8color_16	2002Sep24
PASS		
0	brush_8x8color_32	2002Sep24
PASS		
0	brush_8x8color_64	2002Sep24
PASS		
0	brush_8x8color_dst_0_1	2002Sep24
PASS		
0	brush_8x8mono	2002Sep24
PASS		
0	brush_color_mono_stress	2002Sep24
PASS		
0	brush_flag	2002Sep24
PASS		
0	brush_type0	2002Sep24
PASS		
0	brush_type1	2002Sep24
PASS		
0	brush_type10	2002Sep24
PASS		
0	brush_type13	2002Sep24

PASS		
0	brush_type14	2002Sep24
PASS		
0	brush_type6	2002Sep24
PASS		
0	brush_type7	2002Sep24
PASS		
0	brush_type_other	2002Sep24
PASS		
0	brush_types_13_14	2002Sep24
PASS		
0	clear_const_wr_en_on_me_init	2002Sep24
PASS		
0	clear_const_wr_en_with_lcc	2002Sep24
PASS		
0	cond_write_mem_poll_mem	2002Sep24
PASS		
0	cond_write_mem_poll_mem2	2002Sep24
PASS		
0	cond_write_mem_poll_mem3	2002Sep24
PASS		
0	cond_write_mem_poll_mem_rt	2002Sep24
PASS		
0	cond_write_mem_poll_reg	2002Sep24
PASS		
0	cond_write_mem_poll_reg2	2002Sep24
PASS		
0	cond_write_mem_poll_reg3	2002Sep24
PASS		
59	cp_acpi_recovery_soft_reset	2002Sep24
FAIL	cp_mh_req.cmp cp_rbbm.cmp mh_cp_rd.cmp rbbmWr.cmp	
0	cp_bios_scratch	2002Sep24
PASS		
0	cp_ib2_base000_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base001_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base010_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base011_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base100_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base101_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base110_bufszXXX	2002Sep24
PASS		
0	cp_ib2_base111_bufszXXX	2002Sep24

PASS		
0	cp_ib_base000_bufszXXX	2002Sep24
PASS		
0	cp_ib_base001_bufszXXX	2002Sep24
PASS		
0	cp_ib_base010_bufszXXX	2002Sep24
PASS		
0	cp_ib_base011_bufszXXX	2002Sep24
PASS		
0	cp_ib_base100_bufszXXX	2002Sep24
PASS		
0	cp_ib_base101_bufszXXX	2002Sep24
PASS		
0	cp_ib_base110_bufszXXX	2002Sep24
PASS		
0	cp_ib_base111_bufszXXX	2002Sep24
PASS		
0	cp_interrupt	2002Sep24
PASS		
0	cp_interrupt_rt_events_16	2002Sep24
PASS		
0	cp_iq2_alignment_000	2002Sep24
PASS		
0	cp_iq2_alignment_001	2002Sep24
PASS		
0	cp_iq2_alignment_010	2002Sep24
PASS		
0	cp_iq2_alignment_011	2002Sep24
PASS		
0	cp_iq2_alignment_100	2002Sep24
PASS		
0	cp_iq2_alignment_101	2002Sep24
PASS		
0	cp_iq2_alignment_110	2002Sep24
PASS		
0	cp_iq2_alignment_111	2002Sep24
PASS		
0	cp_iq_alignment_000	2002Sep24
PASS		
0	cp_iq_alignment_001	2002Sep24
PASS		
0	cp_iq_alignment_010	2002Sep24
PASS		
0	cp_iq_alignment_011	2002Sep24
PASS		
0	cp_iq_alignment_100	2002Sep24
PASS		
0	cp_iq_alignment_101	2002Sep24

PASS		
0	cp_iq_alignment_110	2002Sep24
PASS		
0	cp_iq_alignment_111	2002Sep24
PASS		
0	cp_max_size_packet	2002Sep24
PASS		
0	cp_max_size_packet_random	2002Sep24
PASS		
0	cp_nrt_rt_protection_mode	2002Sep24
PASS		
0	cp_output_stress	2002Sep24
PASS		
0	cp_output_stress_random	2002Sep24
PASS		
0	cp_protection_mode	2002Sep24
PASS		
0	cp_rb_alignment_000	2002Sep24
PASS		
0	cp_rb_alignment_001	2002Sep24
PASS		
0	cp_rb_alignment_010	2002Sep24
PASS		
0	cp_rb_alignment_011	2002Sep24
PASS		
0	cp_rb_alignment_100	2002Sep24
PASS		
0	cp_rb_alignment_101	2002Sep24
PASS		
0	cp_rb_alignment_110	2002Sep24
PASS		
0	cp_rb_alignment_111	2002Sep24
PASS		
0	cp_rb_wptrXXX_base000	2002Sep24
PASS		
0	cp_rb_wptrXXX_base001	2002Sep24
PASS		
0	cp_rb_wptrXXX_base010	2002Sep24
PASS		
0	cp_rb_wptrXXX_base011	2002Sep24
PASS		
0	cp_rb_wptrXXX_base100	2002Sep24
PASS		
0	cp_rb_wptrXXX_base101	2002Sep24
PASS		
0	cp_rb_wptrXXX_base110	2002Sep24
PASS		
0	cp_rb_wptrXXX_base111	2002Sep24

PASS		
0	cp_reg_rw_test	2002Sep24
PASS		
0	cp_register_reads	2002Sep24
PASS		
0	cp_rt_protection_mode	2002Sep24
PASS		
0	cp_scratch	2002Sep24
PASS		
0	cp_scratch_multi	2002Sep24
PASS		
0	csf_reg_rd_default	2002Sep24
PASS		
0	csf_reg_rd_only	2002Sep24
PASS		
0	csf_reg_wr_rd	2002Sep24
PASS		
0	dma_cp_sync	2002Sep24
PASS		
0	dma_dscrstr_chckn_bit	2002Sep24
PASS		
0	dma_dscrstr_flush1	2002Sep24
PASS		
0	dma_dscrstr_flush2	2002Sep24
PASS		
0	dma_dscrstr_flush3	2002Sep24
PASS		
0	dma_m2m_src00_dstXX	2002Sep24
PASS		
0	dma_m2m_src01_dstXX	2002Sep24
PASS		
0	dma_m2m_src10_dstXX	2002Sep24
PASS		
0	dma_m2m_src11_dstXX	2002Sep24
PASS		
0	dma_mem2mem	2002Sep24
PASS		
0	dma_mem2mem_abort	2002Sep24
PASS		
0	dma_mem2mem_alignments	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment0	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment1	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment2	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment3	2002Sep24

PASS		
0	dma_mem2mem_byte_alignment4	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment5	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment6	2002Sep24
PASS		
0	dma_mem2mem_byte_alignment7	2002Sep24
PASS		
0	dma_mem2mem_pio_abort	2002Sep24
PASS		
0	dma_mem2mem_random	2002Sep24
PASS		
0	dma_mem2reg	2002Sep24
PASS		
0	dma_mem2reg_abort	2002Sep24
PASS		
0	dma_mem2reg_dtaq_stress	2002Sep24
PASS		
0	dma_mem2reg_dtaq_stress_random	2002Sep24
PASS		
0	dma_mem2reg_pio_stressbm	2002Sep24
PASS		
0	dma_mem2reg_pio_stresspio	2002Sep24
PASS		
0	dma_mem2reg_sync	2002Sep24
PASS		
0	dma_r2r_m2m_r2r	2002Sep24
PASS		
0	dma_r2r_m2r_r2r	2002Sep24
PASS		
0	dma_r2r_r2m_r2r	2002Sep24
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m	2002Sep24
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_queued	2002Sep24
PASS		
0	dma_r2r_r2m_r2r_m2r_m2m_table	2002Sep24
PASS		
0	dma_reg2mem	2002Sep24
PASS		
0	dma_reg2mem_abort	2002Sep24
PASS		
0	dma_reg2reg	2002Sep24
PASS		
0	dma_reg2reg_2dw	2002Sep24
PASS		
0	dma_reg2reg_daic	2002Sep24

PASS		
0	dma_reg2reg_mult	2002Sep24
PASS		
0	dma_reg2reg_mult2	2002Sep24
PASS		
0	dma_reg2reg_mult3	2002Sep24
PASS		
0	dma_reg2reg_saic	2002Sep24
PASS		
0	dma_reg2reg_sdaic	2002Sep24
PASS		
0	dma_reg2reg_sdaic_stress	2002Sep24
PASS		
0	dma_reg2reg_stress	2002Sep24
PASS		
0	draw_indx_a	2002Sep24
PASS		
0	draw_indx_b	2002Sep24
PASS		
0	draw_indx_c	2002Sep24
PASS		
0	draw_indx_indr1_a	2002Sep24
PASS		
0	draw_indx_indr1_b	2002Sep24
PASS		
0	draw_indx_indr1_short_a	2002Sep24
PASS		
0	draw_indx_indr1_short_b	2002Sep24
PASS		
0	draw_indx_indr1_stress	2002Sep24
PASS		
0	draw_indx_indr2_a	2002Sep24
PASS		
0	draw_indx_indr2_b	2002Sep24
PASS		
0	draw_indx_indr2_short_a	2002Sep24
PASS		
0	draw_indx_indr2_short_b	2002Sep24
PASS		
0	draw_indx_indr2_stress	2002Sep24
PASS		
0	draw_indx_ring1	2002Sep24
PASS		
0	draw_indx_ring2	2002Sep24
PASS		
0	draw_indx_ring_short1	2002Sep24
PASS		
0	draw_indx_ring_short2	2002Sep24

PASS		
0	draw_indx_ring_stress	2002Sep24
PASS		
0	draw_indx_short_a	2002Sep24
PASS		
0	draw_indx_short_b	2002Sep24
PASS		
0	draw_indx_short_c	2002Sep24
PASS		
0	draw_indx_stress_a	2002Sep24
PASS		
0	draw_indx_stress_b	2002Sep24
PASS		
0	draw_indx_stress_c	2002Sep24
PASS		
0	event_timestamp_write	2002Sep24
PASS		
0	event_timestamp_write_multicontext_maxcontext_7	2002Sep24
PASS		
0	fix2flt_reg	2002Sep24
PASS		
0	ft_indr1_july22	2002Sep24
PASS		
0	ft_indr2_july22	2002Sep24
PASS		
0	ft_july22	2002Sep24
PASS		
0	ft_july22_random	2002Sep24
PASS		
0	header_dump	2002Sep24
PASS		
0	header_dump_rt	2002Sep24
PASS		
0	header_dump_small	2002Sep24
PASS		
0	hostdata_blt	2002Sep24
PASS		
0	im_load_a	2002Sep24
PASS		
0	im_load_b	2002Sep24
PASS		
0	im_load_cid_mismatch	2002Sep24
PASS		
0	im_load_immediate	2002Sep24
PASS		
0	im_load_immediate_nrt_me	2002Sep24
PASS		
0	im_load_immediate_nrt_me_ps_nowrap	2002Sep24

PASS		
0	im_load_immediate_nrt_me_ps_nowrap_large_count	2002Sep24
PASS		
0	im_load_immediate_nrt_me_ps_wrap	2002Sep24
PASS		
0	im_load_immediate_nrt_me_vs_nowrap	2002Sep24
PASS		
0	im_load_immediate_nrt_me_vs_nowrap_large_count	2002Sep24
PASS		
0	im_load_immediate_nrt_me_vs_ps_nowrap	2002Sep24
PASS		
0	im_load_immediate_nrt_me_vs_wrap	2002Sep24
PASS		
0	im_load_indr1_a	2002Sep24
PASS		
0	im_load_indr1_b	2002Sep24
PASS		
0	im_load_indr2_a	2002Sep24
PASS		
0	im_load_indr2_b	2002Sep24
PASS		
0	im_load_invalidate	2002Sep24
PASS		
0	im_load_match	2002Sep24
PASS		
0	im_load_match2	2002Sep24
PASS		
0	im_load_nrt_me	2002Sep24
PASS		
0	im_load_nrt_me_ps_nowrap	2002Sep24
PASS		
0	im_load_nrt_me_ps_wrap	2002Sep24
PASS		
0	im_load_nrt_me_vs_nowrap	2002Sep24
PASS		
0	im_load_nrt_me_vs_wrap	2002Sep24
PASS		
0	im_load_pfmatch_disable	2002Sep24
PASS		
0	im_load_ring_a	2002Sep24
PASS		
0	im_load_ring_b	2002Sep24
PASS		
0	im_load_rt	2002Sep24
PASS		
0	im_load_rt2	2002Sep24
PASS		
0	im_load_rt3	2002Sep24

PASS		
0	im_load_rt4	2002Sep24
PASS		
0	im_load_sz_mismatch	2002Sep24
PASS		
0	incremental_update_const	2002Sep24
PASS		
0	incremental_update_instr	2002Sep24
PASS		
0	incremental_update_state	2002Sep24
PASS		
0	indirect_buffer_a	2002Sep24
PASS		
0	indirect_buffer_b	2002Sep24
PASS		
0	indirect_buffer_c	2002Sep24
PASS		
0	indirect_buffer_d	2002Sep24
PASS		
0	indirect_buffer_e	2002Sep24
PASS		
0	indirect_buffer_f	2002Sep24
PASS		
0	indirect_buffer_g	2002Sep24
PASS		
0	indirect_buffer_h	2002Sep24
PASS		
0	indirect_buffer_i	2002Sep24
PASS		
0	indirect_buffer_j	2002Sep24
PASS		
0	indirect_buffer_k	2002Sep24
PASS		
0	indirect_buffer_l	2002Sep24
PASS		
0	indirect_buffer_mp_a	2002Sep24
PASS		
0	indirect_buffer_mp_b	2002Sep24
PASS		
0	indirect_buffer_mp_c	2002Sep24
PASS		
0	indirect_buffer_mp_d	2002Sep24
PASS		
0	indirect_buffer_mp_e	2002Sep24
PASS		
0	indirect_buffer_mp_f	2002Sep24
PASS		
0	invalid_opcode	2002Sep24

PASS		
0	invalidate_on_me_init	2002Sep24
PASS		
0	invalidate_state_constants	2002Sep24
PASS		
0	invalidate_state_shaders	2002Sep24
PASS		
0	invalidate_state_shaders2	2002Sep24
PASS		
0	invalidate_state_sublks	2002Sep24
PASS		
0	invalidate_state_sublks2	2002Sep24
PASS		
0	iq1_iq2_mult	2002Sep24
PASS		
0	iq1_iq2_push_wrap	2002Sep24
PASS		
0	iq2_csq_full	2002Sep24
PASS		
0	ld_cnst_cntx_coherency_cntrl	2002Sep24
PASS		
0	ld_cnst_cntx_coherency_cntrl2	2002Sep24
PASS		
0	ld_cnst_cntx_indr1_replicate	2002Sep24
PASS		
0	ld_cnst_cntx_indr1_replicate4	2002Sep24
PASS		
0	ld_cnst_cntx_indr1_replicate8	2002Sep24
PASS		
0	ld_cnst_cntx_indr1_simple	2002Sep24
PASS		
0	ld_cnst_cntx_indr2_replicate	2002Sep24
PASS		
0	ld_cnst_cntx_indr2_replicate4	2002Sep24
PASS		
0	ld_cnst_cntx_indr2_replicate8	2002Sep24
PASS		
0	ld_cnst_cntx_indr2_simple	2002Sep24
PASS		
0	ld_cnst_cntx_match	2002Sep24
PASS		
0	ld_cnst_cntx_match_valid_preserved	2002Sep24
PASS		
0	ld_cnst_cntx_mismatch_alutex	2002Sep24
PASS		
0	ld_cnst_cntx_mismatch_base	2002Sep24
PASS		
0	ld_cnst_cntx_mismatch_force	2002Sep24