

Change 227650 on 2005/07/12 by smoss@smoss_xenos_linux_orl

some gate changes from c2

Change 226410 on 2005/06/28 by smoss@smoss_xenos_linux_orl

testbench clean-up

Change 226128 on 2005/06/24 by neelimam@neelimam_xenos_linux_orl

<1. Changed path for the technology libraries to generic in tb_gates.f for PA, HZ, VC and CP and in tb_pd.f for SC.

2. Updated README files for SC, VC and BIF.

3. Added technology library ati_90n.v to tb_gates_spi.f.

4. Added +define+UNIT

Change 224808 on 2005/06/13 by smoss@smoss_xenos_linux_orl

updates

Change 223796 on 2005/06/01 by smoss@smoss_xenos_linux_orl

also corrected

Change 223792 on 2005/06/01 by smoss@smoss_xenos_linux_orl

corrected

Change 222642 on 2005/05/20 by rramsey@rramsey_xenos3_linux_orl

get rid of extra include

Change 222616 on 2005/05/20 by rramsey@rramsey_xenos3_linux_orl

Add headers for synth

Change 222529 on 2005/05/19 by neelimam@neelimam_xenos_linux_orl

<edited .f files to point to the correct netlist. Edited tbtrk_vc_out.v to remove the #4 delay in the clk.>

Change 221262 on 2005/05/08 by smoss@smoss_crayola_linux_orl_regress

update for sx gates

Change 221171 on 2005/05/06 by smoss@smoss_crayola_linux_orl_regress

1. remove gates from tb.f

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2. update tb_gates
3. update tb_sqsp.v for spi

Change 221071 on 2005/05/06 by neelimam@neelimam_xenos_linux_orl

<updated README files for gate simulations>

Change 220567 on 2005/05/03 by neelimam@neelimam_xenos_linux_orl

Edited files to point to the right netlist for A22

Change 219797 on 2005/04/27 by rramsey@rramsey_xenos3_linux_orl

submit SQ_ECO_6 changes to fix a performance issue in pix_ctl

Change 219761 on 2005/04/27 by smoss@smoss_xenos_linux_orl

remove gate references

Change 219409 on 2005/04/25 by neelimam@neelimam_xenos_linux_orl

<Edited tb_gates.f for blocks HZ, PA, SC, SQ, VC to point to the PD netlist>

Change 216633 on 2005/04/07 by smoss@smoss_crayola_linux_orl_regress

enable dynamic clocking by default

Change 216453 on 2005/04/06 by smoss@smoss_xenos_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: <lame excuse inserted here>

updates for gate sims

Change 215790 on 2005/04/01 by smoss@smoss_crayola_linux_orl_regress

I did not pass release_pl_xenos.pl, but must submit because: <lame excuse inserted here>

get testbenches working for dft_eco_20

Change 215379 on 2005/03/31 by hartogs@hartogs_xenos_linux_orl

Change to SQ so that the RBBM can send transactions on the bus to other clients while the SQ fifo is full.

Prior to this change, all RBBM transactions went thru the skid FIFO. After this change, only the RBBM

transactions that are decoded on th

Change 208531 on 2005/02/15 by rramsey@rramsey_xenos3_linux_orl

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Changes for SQ_ECO_4 and SQ_ECO_5

Change 203527 on 2005/01/14 by viviana@viviana_xenos2_linux_orl

Added the clock adjuster deserializer inputs.

Change 202408 on 2005/01/07 by viviana@viviana_xenos2_linux_orl

Added DFT ECO's to the RTL code.

Change 200981 on 2004/12/21 by danh@danh_xenos5_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: this is just a text file

Change 200354 on 2004/12/15 by danh@danh_xenos2_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: these files are only related to gate level simulation.

Change 200339 on 2004/12/15 by danh@danh_xenos2_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: This is only a text file, it is a README file that explains how to run tb_sqsp gate level simulations

Change 200318 on 2004/12/15 by danh@danh_xenos2_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: these files are only used for gate level simulation. Initial release of tb_sqsp gate level ".f" files.

Change 194627 on 2004/11/03 by viviana@viviana_xenos2_linux_orl

I did not pass release_pl_xenos.pl, but must submit because:
These files are the TetraMAX models for the RAMS.

Change 192659 on 2004/10/21 by rramsey@rramsey_xenos3_linux_orl

state load change for tb_sqsp to fix suppress_eop_04

Change 190504 on 2004/10/06 by rramsey@rramsey_xenos3_linux_orl

Add SQ_ECO_3 to fix gpr dynamic/pool size bugs. This ECO is not enabled since it will not be fixed.

Change 188818 on 2004/09/22 by bhankins@bhankins_real_xenos_linux_orl

1. sx_export_control_common.v - add hook for future implemetation of
SX_ECO_3 for properly generating the sx busy signal. Note that this

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new code is hidden inside an `ifdef SX_ECO_3, which is not defined.
2. modify tb_sqsp.v and tbmod

Change 183607 on 2004/08/12 by smoss@smoss_xenos_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: these are virage testbench files

Change 183350 on 2004/08/10 by rramsey@rramsey_xenos3_linux_orl

Add SQ_ECO_2 implementation. Fixes a phase generation problem in the SQ when power management is enabled.

Change 183248 on 2004/08/10 by llefebvr@llefebvr_xenos_linux_marlboro

This change adds is the code fix for SQ_ECO1 by tying the SQ_CONSTANT_SENT_SP_SIMD2 wire. This change is currently DISABLED.

Change 179788 on 2004/07/21 by rramsey@rramsey_xenos3_linux_orl

modify testbench flush/bad_pipe synchronization to fix uber_rand timeout problem

Change 177928 on 2004/07/12 by rramsey@rramsey_xenos3_linux_orl

Check in Laurent's fixes for RSP fetch return problems in the emulator.
Add definition of SQ_ECO_0 and ifdef previous rtl change to use it.

Change 177331 on 2004/07/08 by rramsey@rramsey_xenos3_linux_orl

I did not pass release_pl_xenos.pl, but must submit because:
these testbenches are not used in the sanity run

add sq virage testbenches

Change 176392 on 2004/06/30 by rramsey@rramsey_xenos3_linux_orl

Fix bug that happens when we get a map_copy with a const_write pending due to a full phys memory

Change 176308 on 2004/06/30 by llefebvr@llefebvr_xenos_linux_marlboro

This is fixing the RSP expand_enable bit.

Change 176084 on 2004/06/29 by donaldl@donaldl_xenos2_linux_orl

Commented out ifdef GCTEST so rsp compares work on tb_sqsp testbench.

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Change 175630 on 2004/06/25 by rramsey@rramsey_xenos3_linux_orl

filter out vs_fetch_done events so they don't go to the sx
add a transaction count for sx-bc events
Fix serialize in vtx shader

Change 175531 on 2004/06/25 by llefebvr@llefebvr_xenos_linux_marlboro

Fixing max number of inputs+ param gen bug and the NO_CEXEC_OPT, NO_LOOP_EXIT bugs.

Change 175005 on 2004/06/22 by danh@danh_xenos2_linux_orl

Fixed SX_GATES section (ul_SX_SQ_* outputs are now only driven by sx_gates)

Change 174919 on 2004/06/22 by rramsey@rramsey_xenos3_linux_orl

I did not pass release_pl_xenos.pl, but must submit because:
these files are only used in tb_sqsp
trying to fix sx_bc event tracking issues

Change 174567 on 2004/06/18 by rramsey@rramsey_xenos3_linux_orl

I did not pass release_pl_xenos.pl, but must submit because: this testbench is
not part
of the sanity run
fix sx_bc event count mismatch error

Change 173554 on 2004/06/14 by donaldl@donaldl_xenos2_linux_orl

1. Changed DEBUSSY_PATH to VERDI_ROOT in buildtb_gate.
2. Updated vgt random scripts to use VCS and changed link requirement for tbfiles.
3. Updated sq to sp trackers to not compare the shader pipe defined by ROM_SIMD_SEL[1:0] and ROM_PIPE_SEL[

Change 172977 on 2004/06/10 by rramsey@rramsey_xenos3_linux_orl

fix tpsp drivers in tb_sqsp to use post-steered pixel masks
simd id was off by a clock in the sqtp and sqvc trackers

Change 170316 on 2004/05/26 by llefebvr@llefebvr_xenos_linux_marlboro

This is fixing the problem with the reduced size reservation stations.

Change 170313 on 2004/05/26 by llefebvr@llefebvr_xenos_linux_marlboro

This is fixing the tb_sqsp for predicated texture fetches.

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