

Marlboro Hardware Design Block Status

Block	% Feature Complete	Estimated completion date
MC	100%	
MH	99%	10/18
RB	95%	10/31
RC	99%	10/18
SX	98%	10/18
SP	100%	
SQ	95%	10/31
TP	99%	10/18
TC	99%	10/18
CG/CGM DBG/ROM	100%	

1
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Marlboro Hardware Design Block Status

- All blocks need to add
 - performance counters
 - power
 - debug registers
 - Star memories
- MC
 - Pad Interface
 - At speed sw generated commands
 - Synthesis target of 600MHz
- MH
 - Register updates in progress
 - Tiled surfaces in system memory support not started



2
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Marlboro Hardware Design Block Status

- RB
 - Depth and multi-sample logic still in design
 - Resolve and memory export logic not yet started
- SQ
 - Control flow in progress
 - (conditionals, loops, jumps, ...)
- SP/ SX
 - Memory export



3
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Marlboro Hardware Design Block Status

- TP / TC
 - 512 bit AGP requests
 - degamma with DXTC texels
- CG / CGM / DBG / ROM / IO
 - DFT logic in progress
 - Temperature sensor logic



4

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Block Synthesis

- Update ...



5
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