

Change 140380 on 2003/12/31 by vromaker@vromaker_r400_linux_marlboro

- fix for dropped real_time flag: moved export_pos bit into the "flags" field
(it was using a bit that was set aside for the extra_in field)

Change 140350 on 2003/12/31 by rramsey@rramsey_xenos_linux_orl

Fix a bug that was allowing the texconst mem to be written when full (no phys addr available)

Change 140331 on 2003/12/31 by mearl@mearl_xenos_linux_orl

Fixed bug; was using thread type instead of fetch type.

Change 140330 on 2003/12/31 by tien@tien_r500_emu

Bug fix for sec dim calcs

Change 140316 on 2003/12/31 by mmantor@FL_mmantorLT_r400_win

<special build with a sq_alu.h provided by tom to address dot lsb mismatches>

Change 140313 on 2003/12/31 by jcarroll@jcarroll_r400_win

added latest status; picked new test

Change 140284 on 2003/12/30 by dclifton@dclifton_xenos_linux_orl

Fixed scalar MOVA_FLOOR opcode;

Change 140270 on 2003/12/30 by danh@danh_r400_win

Status update.

Change 140266 on 2003/12/30 by smoss@smoss_crayola_linux_orl_regress

changes for simple_register_indirect test (register read)

Change 140247 on 2003/12/30 by vbhatia@vbhatia_r400_linux_marlboro

Reverted fetch multisample test clamp, for the Team believes that it might be best to support it

Change 140219 on 2003/12/30 by mdesai@mdesai_r400_linux

During tfetch, using msb of texel_offset_pre

Change 140218 on 2003/12/30 by vbhatia@vbhatia_r400_linux_marlboro

Optimised away fetch multi sample tests to be overridden as tfetches, not supported

Change 140205 on 2003/12/30 by rramsey@RRAMSEY_P4_r400_win

update test status, pick another one

Change 140203 on 2003/12/30 by danh@danh_r400_win

Status update.

Change 140180 on 2003/12/30 by vbhatia@vbhatia_r400_linux_marlboro

Added clamp so as to test only point sampling when doing vertex fetches

Change 140174 on 2003/12/30 by rramsey@RRAMSEY_P4_r400_win

update status

Change 140155 on 2003/12/30 by rramsey@RRAMSEY_P4_r400_win

update with 12/30 regression results

Change 140117 on 2003/12/30 by danh@danh_r400_win

Status update.

Change 140102 on 2003/12/30 by mmantor@FL_mmantorLT_r400_win

<special build with sq_alu.h#79 for debug>

Change 140051 on 2003/12/29 by mearl@mearl_r400_win

update status

Change 140050 on 2003/12/29 by vromaker@vromaker_r400_linux_marlboro

status update - emulator fix was made for scalar const opcodes

Change 140036 on 2003/12/29 by jcarroll@jcarroll_r400_win

Updated jcarroll status

Change 139967 on 2003/12/29 by tien@tien_r400_devel_marlboro

Added another delay on compare to mirror added pipestage in RTL

Change 139957 on 2003/12/29 by tien@tien_r500_emu

Recoded log pitches going to TCF for better timing
Closing GetBorderColorFraction bug .. verified tests passing
Added stage to tp_addresser and refreshed .bvrls to be safe (tp_parameters dependency)

Change 139909 on 2003/12/28 by mmantor@mmantor_xenos_linux_orl

<timing fixes>

Change 139836 on 2003/12/24 by jayw@jayw_r400_linux_marlboro

Depth code clean up part 1/38103. Fix for ignoring SX1 rtr. Non-hiz depth still has bug with expansion shown by rare gc r400rb_zwave failures.

Change 139795 on 2003/12/23 by danh@danh_r400_win

Status update.

Change 139793 on 2003/12/23 by vbhatia@vbhatia_r400_linux_marlboro

Added clamp for no lod grad when vertex fetches

Change 139789 on 2003/12/23 by mdesai@mdesai_r400_linux

Fixed Addresser random test genRand11 (large denorms with offsets)

Change 139383 on 2003/12/23 by ctaylor@ctaylor_xenos_linux_orl

Fixed bug in control flow sequencer where when thread was put back onto thread buffer due to alloc cfi, the no-serialize bit was being taken from bit 40 of the cfs opcode instead of the execute state machine opcode so it was the right bit from the wrong instruction. Things have been working mostly due to the fact that bit 40 of most of the other CFI opcodes is reserved and therefore 0.

Change 139373 on 2003/12/23 by llefebvr@llefebvr_r400_emu_montreal

updated status for SX->PA mismatches.

Change 139356 on 2003/12/23 by bhankins@bhankins_xenos_linux_orl

Move some assignments out of process where they shouldn't be. No functional change.

Change 139338 on 2003/12/23 by vromaker@vromaker_r400_linux_marlboro

- fix for scalar const ops: y and x swizzle fields used for gpr address bits [5:4] and [3:2] were swapped

Change 139327 on 2003/12/23 by rramsey@rramsey_xenos_linux_orl

add simd_id to sp_out mismatch message
make sx_rb_color tracker multi-threaded per sx/rb interface

Change 139310 on 2003/12/23 by vromaker@vromaker_r400_linux_marlboro

status update - took another test

Change 139296 on 2003/12/23 by tien@tien_r500_emu

Fixed GetBorderColorFraction DATA_FORMAT override

Change 139280 on 2003/12/23 by rmanapat@rmanapat_r400_release

Fix for bug 3126

Change 139223 on 2003/12/22 by llefebvr@llefebvr_r400_emu_montreal

Now working on SX->PA mismatches.

Change 139193 on 2003/12/22 by rramsey@RRAMSEY_P4_r400_win

update status

Change 139142 on 2003/12/22 by llefebvr@llefebvr_r400_emu_montreal

The SX->RB tracker is bad. Added a comment to explain the situation.

Change 139097 on 2003/12/22 by ctaylor@ctaylor_xenos_linux_orl

Fixed bug related to clamping of GPR addresses which are out of range. Old code clamped to absolute zero instead of the base for the current thread.

Change 139066 on 2003/12/22 by bhankins@bhankins_xenos_linux_orl

Add tbtrk_sx_bc_quad tracker (C1 version only)

Change 139057 on 2003/12/22 by rramsey@RRAMSEY_P4_r400_win

update more tests, pick another one

Change 139050 on 2003/12/22 by rramsey@RRAMSEY_P4_r400_win

Update with new regression results, pick a test

Change 139045 on 2003/12/22 by rramsey@rramsey_xenos_linux_orl

Fix phasing of thread_count in sq_ais_output.

Fix o_gprsm_busy from sq_vtx_ctl and change thread counter to only reset on RST_VTX_CNT event.

Change 139044 on 2003/12/22 by bhankins@bhankins_xenos_linux_orl

Initial add to r400 branch

Change 138986 on 2003/12/20 by mmantor@mmantor_xenos_linux_orl

<changed csim to only make one pass for param gen and gen index and write the dump files correctly, fixed a timing loop in pix tthread buffer >

Change 138964 on 2003/12/20 by donaldl@donaldl_xenos_linux_orl

Redundancy bug fixes --

1. Added RSP output muxes to TP_SP_data_valid[3:0] in tp_output.v.
2. Created I/O port TPC_TP_simd_id[1:0]. Needed so simd_id lines up with TP outputs going to SPs and RSP.

Change 138960 on 2003/12/20 by bhankins@bhankins_xenos_linux_orl

Forgot to make the same change to the BCl side. Has no affect on r400 version.

Change 138958 on 2003/12/20 by bhankins@bhankins_xenos_linux_orl

fix miswiring of quad_x and quad_y outputs of detailed quad fifo

Change 138921 on 2003/12/19 by tien@tien_r400_devel_marlboro

Man if this breaks anything I'll freak.

Changed the format_comp_* mux control for data_format=61 (FMT...AS_1_1_1_1)

Change 138662 on 2003/12/19 by mearl@mearl_r400_win

update status

Change 138650 on 2003/12/19 by danh@danh_r400_win

Status update.

Change 138647 on 2003/12/19 by bhankins@bhankins_xenos_linux_orl

Add include of sx_defines.v. No affect on r400 version.

Change 138609 on 2003/12/19 by tien@tien_r500_emu

Fixes location of ANISO_BIAS and DIM fields in emu and rtl

Closing out GetWeights bug, which I fixed last checkin

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