

Change 140380 on 2003/12/31 by vromaker@vromaker\_r400\_linux\_marlboro

- fix for dropped real\_time flag: moved export\_pos bit into the "flags" field  
(it was using a bit that was set aside for the extra\_in field)

Change 140350 on 2003/12/31 by rramsey@rramsey\_xenos\_linux\_orl

Fix a bug that was allowing the texconst mem to be written when full (no phys addr available)

Change 140331 on 2003/12/31 by mearl@mearl\_xenos\_linux\_orl

Fixed bug; was using thread type instead of fetch type.

Change 140330 on 2003/12/31 by tien@tien\_r500\_emu

Bug fix for sec dim calcs

Change 140316 on 2003/12/31 by mmantor@FL\_mmantorLT\_r400\_win

<special build with a sq\_alu.h provided by tom to address dot lsb mismatches>

Change 140313 on 2003/12/31 by jcarroll@jcarroll\_r400\_win

added latest status; picked new test

Change 140284 on 2003/12/30 by dclifton@dclifton\_xenos\_linux\_orl

Fixed scalar MOVA\_FLOOR opcode;

Change 140270 on 2003/12/30 by danh@danh\_r400\_win

Status update.

Change 140266 on 2003/12/30 by smoss@smoss\_crayola\_linux\_orl\_regress

changes for simple\_register\_indirect test (register read)

Change 140247 on 2003/12/30 by vbhatia@vbhatia\_r400\_linux\_marlboro

Reverted fetch multisample test clamp, for the Team believes that it might be best to support it

Change 140219 on 2003/12/30 by mdesai@mdesai\_r400\_linux

During tfetch, using msb of texel\_offset\_pre

Change 140218 on 2003/12/30 by vbhatia@vbhatia\_r400\_linux\_marlboro

Optimised away fetch multi sample tests to be overridden as tfetches, not supported

Change 140205 on 2003/12/30 by rramsey@RRAMSEY\_P4\_r400\_win

update test status, pick another one

Change 140203 on 2003/12/30 by danh@danh\_r400\_win

Status update.

Change 140180 on 2003/12/30 by vbhatia@vbhatia\_r400\_linux\_marlboro

Added clamp so as to test only point sampling when doing vertex fetches

Change 140174 on 2003/12/30 by rramsey@RRAMSEY\_P4\_r400\_win

update status

Change 140155 on 2003/12/30 by rramsey@RRAMSEY\_P4\_r400\_win

update with 12/30 regression results

Change 140117 on 2003/12/30 by danh@danh\_r400\_win

Status update.

Change 140102 on 2003/12/30 by mmantor@FL\_mmantorLT\_r400\_win

<special build with sq\_alu.h#79 for debug>

Change 140051 on 2003/12/29 by mearl@mearl\_r400\_win

update status

Change 140050 on 2003/12/29 by vromaker@vromaker\_r400\_linux\_marlboro

status update - emulator fix was made for scalar const opcodes

Change 140036 on 2003/12/29 by jcarroll@jcarroll\_r400\_win

Updated jcarroll status

Change 139967 on 2003/12/29 by tien@tien\_r400\_devel\_marlboro

Added another delay on compare to mirror added pipestage in RTL

Change 139957 on 2003/12/29 by tien@tien\_r500\_emu

Recoded log pitches going to TCF for better timing  
Closing GetBorderColorFraction bug .. verified tests passing  
Added stage to tp\_addresser and refreshed .bvrls to be safe (tp\_parameters dependency)

Change 139909 on 2003/12/28 by mmantor@mmantor\_xenos\_linux\_orl

<timing fixes>

Change 139836 on 2003/12/24 by jayw@jayw\_r400\_linux\_marlboro

Depth code clean up part 1/38103. Fix for ignoring SX1 rtr. Non-hiz depth still has bug with expansion shown by rare gc r400rb\_zwave failures.

Change 139795 on 2003/12/23 by danh@danh\_r400\_win

Status update.

Change 139793 on 2003/12/23 by vbhatia@vbhatia\_r400\_linux\_marlboro

Added clamp for no lod grad when vertex fetches

Change 139789 on 2003/12/23 by mdesai@mdesai\_r400\_linux

Fixed Addresser random test genRand11 (large denorms with offsets)

Change 139383 on 2003/12/23 by ctaylor@ctaylor\_xenos\_linux\_orl

Fixed bug in control flow sequencer where when thread was put back onto thread buffer due to alloc cfi, the no-serialize bit was being taken from bit 40 of the cfs opcode instead of the execute state machine opcode so it was the right bit from the wrong instruction. Things have been working mostly due to the fact that bit 40 of most of the other CFI opcodes is reserved and therefore 0.

Change 139373 on 2003/12/23 by llefebvr@llefebvr\_r400\_emu\_montreal

updated status for SX->PA mismatches.

Change 139356 on 2003/12/23 by bhankins@bhankins\_xenos\_linux\_orl

Move some assignments out of process where they shouldn't be. No functional change.

Change 139338 on 2003/12/23 by vromaker@vromaker\_r400\_linux\_marlboro

- fix for scalar const ops: y and x swizzle fields used for gpr address bits [5:4] and [3:2] were swapped

Change 139327 on 2003/12/23 by rramsey@rramsey\_xenos\_linux\_orl

add simd\_id to sp\_out mismatch message  
make sx\_rb\_color tracker multi-threaded per sx/rb interface

Change 139310 on 2003/12/23 by vromaker@vromaker\_r400\_linux\_marlboro

status update - took another test

Change 139296 on 2003/12/23 by tien@tien\_r500\_emu

Fixed GetBorderColorFraction DATA\_FORMAT override

Change 139280 on 2003/12/23 by rmanapat@rmanapat\_r400\_release

Fix for bug 3126

Change 139223 on 2003/12/22 by llefebvr@llefebvr\_r400\_emu\_montreal

Now working on SX->PA mismatches.

Change 139193 on 2003/12/22 by rramsey@RRAMSEY\_P4\_r400\_win

update status

Change 139142 on 2003/12/22 by llefebvr@llefebvr\_r400\_emu\_montreal

The SX->RB tracker is bad. Added a comment to explain the situation.

Change 139097 on 2003/12/22 by ctaylor@ctaylor\_xenos\_linux\_orl

Fixed bug related to clamping of GPR addresses which are out of range. Old code clamped to absolute zero instead of the base for the current thread.

Change 139066 on 2003/12/22 by bhankins@bhankins\_xenos\_linux\_orl

Add tbtrk\_sx\_bc\_quad tracker (C1 version only)

Change 139057 on 2003/12/22 by rramsey@RRAMSEY\_P4\_r400\_win

update more tests, pick another one

Change 139050 on 2003/12/22 by rramsey@RRAMSEY\_P4\_r400\_win

Update with new regression results, pick a test

Change 139045 on 2003/12/22 by rramsey@rramsey\_xenos\_linux\_orl

Fix phasing of thread\_count in sq\_ais\_output.

Fix o\_gprsm\_busy from sq\_vtx\_ctl and change thread counter to only reset on RST\_VTX\_CNT event.

Change 139044 on 2003/12/22 by bhankins@bhankins\_xenos\_linux\_orl

Initial add to r400 branch

Change 138986 on 2003/12/20 by mmantor@mmantor\_xenos\_linux\_orl

<changed csim to only make one pass for param gen and gen index and write the dump files correctly, fixed a timing loop in pix tthread buffer >

Change 138964 on 2003/12/20 by donaldl@donaldl\_xenos\_linux\_orl

Redundancy bug fixes --

1. Added RSP output muxes to TP\_SP\_data\_valid[3:0] in tp\_output.v.
2. Created I/O port TPC\_TP\_simd\_id[1:0]. Needed so simd\_id lines up with TP outputs going to SPs and RSP.

Change 138960 on 2003/12/20 by bhankins@bhankins\_xenos\_linux\_orl

Forgot to make the same change to the BCl side. Has no affect on r400 version.

Change 138958 on 2003/12/20 by bhankins@bhankins\_xenos\_linux\_orl

fix miswiring of quad\_x and quad\_y outputs of detailed quad fifo

Change 138921 on 2003/12/19 by tien@tien\_r400\_devel\_marlboro

Man if this breaks anything I'll freak.

Changed the format\_comp\_\* mux control for data\_format=61 (FMT...AS\_1\_1\_1\_1)

Change 138662 on 2003/12/19 by mearl@mearl\_r400\_win

update status

Change 138650 on 2003/12/19 by danh@danh\_r400\_win

Status update.

Change 138647 on 2003/12/19 by bhankins@bhankins\_xenos\_linux\_orl

Add include of sx\_defines.v. No affect on r400 version.

Change 138609 on 2003/12/19 by tien@tien\_r500\_emu

Fixes location of ANISO\_BIAS and DIM fields in emu and rtl

Closing out GetWeights bug, which I fixed last checkin

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