Change 140352 on 2003/12/31 by jgibney@jgibney_c1_linux_marlboro

Updates to enable az_edram_nec tb.

Change 140329 on 2003/12/31 by jgibney@jgibney_c1_linux_marlboro

Update routines.

Change 140274 on 2003/12/30 by kmeekins@kmeekins_xenos_linux_orl

Correcting inadvertent check-in of new RAM models that have three new input ports.
   Using these new models results in sim failures if the ports are not connected.
   Fixed by retrieving rev #2 of these files and submitting as latest version.

Change 140193 on 2003/12/30 by askende@askende_r400_linux_marlboro

integrating from the branch into TOTT

Change 140169 on 2003/12/30 by jgibney@jgibney_c1_linux_marlboro

Renamed az_surface.xxx to az_surfaceio.xxx

Change 140166 on 2003/12/30 by jgibney@jgibney_c1_linux_marlboro

Bug fixes, and changed some function names.

Change 139994 on 2003/12/29 by kmeekins@kmeekins_xenos_linux_orl

Added VCS coverage pragmas to exclude from coverage results.

Change 139962 on 2003/12/29 by kmeekins@kmeekins_xenos_linux_orl

Expanded mask bits for generating L2 FIFO depth to include 256.

Change 139332 on 2003/12/23 by jgibney@jgibney_c1_linux_marlboro

az_tst changes to enable NEC EDRAM testing in AZ_EDRAM. 2 new modules get compiled
into emulator, but nothing is enabled yet.

Change 139283 on 2003/12/23 by jgibney@jgibney_c1_linux_marlboro

Fix new surface routines.

Change 139269 on 2003/12/23 by grayc@grayc_xenos_test2

changes for preloading hz memory

Change 139249 on 2003/12/22 by jgibney@jgibney_c1_linux_marlboro

incremental checkin.

Change 139173 on 2003/12/22 by jgibney@jgibney_c1_linux_marlboro

Add routines for AZ_EDRAM sims using NEC model. Not connected to environment yet.

Change 139148 on 2003/12/22 by grayc@grayc_xenos_test2

checked in by accident

Change 139070 on 2003/12/22 by askende@askende_r400_linux_marlboro

integrating into TOTT

Change 138969 on 2003/12/20 by markf@markf_xenos_linux_marlboro

Temp fix to BC_HZ_tile_event

Change 138961 on 2003/12/20 by bhankins@bhankins_real_xenos_linux_orl

Initial checkin

Change 138959 on 2003/12/20 by bhankins@bhankins_real_xenos_linux_orl

Forgot to make the same change to the bc1 side. Has no affect on r400 version

Change 138912 on 2003/12/19 by jgibney@jgibney2_c1_linux_marlboro

Fix depth clear when resolving 64bpp color buffer. Now the depth side macro is not
accessed on the second phase of 64bpp copies. Also add test to verify fix.

Change 138843 on 2003/12/19 by subad@subad_c1_linux_marlboro

added sbi_soft_reset as another input to the test. So now we can
        drive all the SBI inputs from the test and compare the SBI output.

Change 138817 on 2003/12/19 by jgibney@jgibney2_c1_linux_marlboro

Fix problem with SBI read outputs not being held stable after read enable.

Change 138793 on 2003/12/19 by mzini@mzini_xenos_linux_orl

 - HZ preload module now preloads behavioral memories from a file
 - Dump the contents of the HZ memories at the emd of the test in the TB_HZ emulator

Change 138696 on 2003/12/19 by mzini@mzini_xenos_linux_orl

– Made new hz_preload.v module work with behavioral memories
 – Certain HZ dumps were enabled even when registry key was disabled
 – Fixed HZ WRITE cache bug where an address hit happened during a cache flush

Change 138669 on 2003/12/19 by aashkar@aashkar2_xenos_linux

Integrating VC changes from R400 into Xenos.

Change 138659 on 2003/12/19 by aashkar@aashkar2_xenos_linux

R400 GFX Integration into Xenos

Change 138645 on 2003/12/19 by subad@subad_c1_linux_marlboro

added sbi support to az_edram testbench

Change 138606 on 2003/12/19 by jgibney@jgibney2_c1_linux_marlboro

Add AZ_SBI_RD_DLY

Change 138592 on 2003/12/19 by amys@amys_xenos1_linux_orl

modified SC_PATH for gc testbench

Change 138551 on 2003/12/18 by markf@markf_xenos_linux_marlboro

Fixed packing vector to hold it's value

Change 138511 on 2003/12/18 by paulv@paulv_xenos

Integrating bc branch into TOTT.

Change 138488 on 2003/12/18 by bhankins@bhankins_real_xenos_linux_orl

Integraged file

Change 138451 on 2003/12/18 by mzini@mzini_xenos_linux_orl

 – Changes to get the HZ memories to preload from a file.
 – Created a module to preload the HZ memories that can be instatiated for TB_HZ, GC or
   fullchip
 – Reverted the Virage memory mode to its original

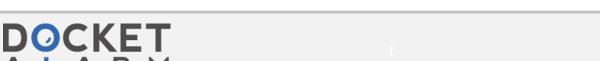Change 138418 on 2003/12/18 by chammer@chammer_xenos_linux

Left aligned all valid quads so quad fifo packer gets expected valid quads.

Change 138402 on 2003/12/18 by nkociuk@nkociuk_c1_linux_marl

add timestamp to read/write debug output

Change 138374 on 2003/12/18 by kmeekins@kmeekins_xenos_linux_orl

Grabbed the RAM .lib files generated by Moe Villalba for the 90 nm parts in an
   effort to remove the 130 nm scaled tables to fix timing issues.

Change 138357 on 2003/12/18 by mdoggett@mdoggett_xenos_linux

Change to AZ emulator to honor mask for color clear. This is
needed to match the fact that the BC emulator works on a quad
basis and doesn't have a reordering queue for accumulation.
        AZ testbench doesn't ignores the mask by setting a boolean
that directs the DS to use the hardware scheme.

Change 138356 on 2003/12/18 by subad@subad_c1_linux_marlboro

fixes in pb randoms to change sample_select every cycle and not to flush
        the pb pipe when changing sample_select

Change 138331 on 2003/12/18 by aashkar@aashkar2_xenos_linux

R400 GFX Integration into Xenos

Change 138324 on 2003/12/18 by jgibney@jgibney_c1_linux_marlboro

        add test.

Change 138311 on 2003/12/18 by jgibney@jgibney2_c1_linux_marlboro

Fix for AZ copy z bug: z_enable and stencil_enable are now forced to 0 on copies. Also
adding AZ_EDRAM test to verify fix.

Change 138255 on 2003/12/17 by subad@subad_c1_linux_marlboro

fixes to pipeline sample_select

Change 138249 on 2003/12/17 by paulv@paulv_xenos

Integrating BC branch into TOTT.

Change 138180 on 2003/12/17 by jgibney@jgibney2_c1_linux_marlboro

Fixed problem with color_clear for 64bpp surfaces.

Change 138155 on 2003/12/17 by aashkar@aashkar2_xenos_linux

R400 --> Xenos GFX Integration

        This contains the new SX to BC interface changes

Change 138110 on 2003/12/17 by tsnyder@tsnyder_xenos_linux_marlboro

Fixing verilog syntax errors.
Updated the hierarchy description in the iom.syn file.

Change 138083 on 2003/12/17 by nkociuk@nkociuk_c1_linux_marl_syn

add missing wire declaration

Change 138075 on 2003/12/17 by jcarroll@jcarroll_xenos_linux_orl

            Fixes for preloading and using real memories.

Change 138074 on 2003/12/17 by aashkar@aashkar2_xenos_linux

latest R400 --> Xenos GFX integration

Change 138068 on 2003/12/17 by chammer@chammer_xenos_linux

Changed XENOS flag to C1 to be consistent with other blocks

Change 137985 on 2003/12/16 by jcarroll@jcarroll_xenos_linux_orl

            Updated FIFO memories.

Change 137822 on 2003/12/16 by askende@askende_r400_linux_marlboro

 integrating the new BC-SX interface

Change 137802 on 2003/12/16 by aashkar@aashkar2_xenos_linux

Latest R400 GFX Integration into Xenos

Change 137790 on 2003/12/16 by enewman@enewman_xenos_linux_marlboro

fixed calibrate circuits

Change 137670 on 2003/12/15 by jgibney@jgibney2_c1_linux_marlboro

Fix copy_sample_select pipeline timing bug. This was causing a failure in BCAZ resolve
test, bc_copy_src_format_little_bcaz_colorx_10_11_11_float, due to sample_select being
incorrect for one of the read_accumulates. Adding new test to AZ_EDRAM which recreated
this bug (and the fix).

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