

# Executive Review – R400

October 28, 2002

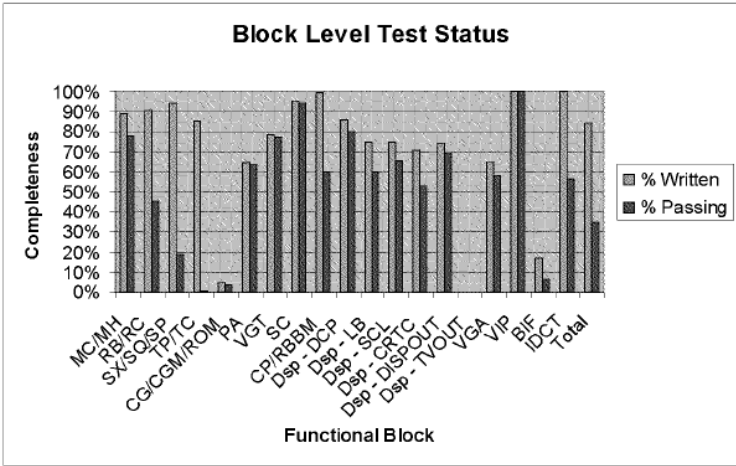
AMD1044 0051169

# Block Logic Design and Timing

Block	Percent Feature Complete	Forecast Feature Complete	Emulator Complete	Directed Tests Written	Synthesis W/C		TNS (nS)
					SCLK (2.5nS)	MCLK (1.67nS), PIXCLK (2.5nS), BCLK (14nS), AGP(3.5nS), TVCLK(10nS)	
MC/MH	100%	Done	N/A	89%	MC:2.5; MH: 3.08	MC - MCLK: 1.71	0 / 1295
RB/RC	97% / 99%	10/31	95%	91%	RB: 6.3; RC: 2.5		6211 / 0
SX/SQ	Done / 97%	Done, 10/31	100%	94%	SX: 2.5; SQ: 2.51		0 / 0
SP	100%	Done	100%		SP: 2.5		0
TP/TC	99%	10/12	95%	85%	TP: 2.5; TC: 7.5		0 / ?
CG/CGM/ROM	100%	Done	N/A	5%	CG: 2.5; CGM:2.5; DBG:2.5		0
PA	100%	Done	100%	65%	PA: 2.79		109
VGT	100%	Done	100%	78%	VGT: 2.50		0
SC	99.44%	10/30	100%	95%	SC:3.4		35
RBBM	100%	Done	95%	99%	RBBM: 2.5		0
CP	100%	Done	95%		CP: 3.1		408
Display - DCP	100%	Done	95%	86%	Display: 4.46	PIXCLK: 2.66	3608
Display - LB	100%	Done	100%	75%	2.6		
Display - SCL	100%	Done	90%	75%	5.01		
Display - CRTIC	100%	Done	100%	71%	2.72		
Display - DISPOUT	100%	Done	90%	74%	2.29		
Display - TVOUT	100%	Done	40%	0%		TVCLK:22.11	
VGA	100%	Done	99%	65%	4.8		
VIP	100%	Done	100%	100%	3.47		
BIF	100%	Done	90%	17%	BIF: 2.63	BCLK: 14.53nS; AGP:3.49nS	242
IDCT	100%	Done	97%	100%	IDCT: 2.83		

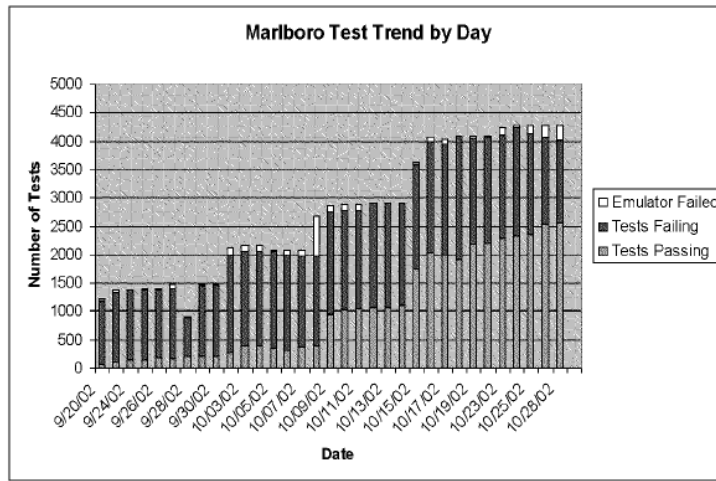
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# Block Level Test Completeness



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# Marlboro Blocks



AII and Proprietary

# General Status

- **Full chip**
  - Full chip environment now passing tests
- **Synthesis**
  - Working through issues with netlist “N”
    - Flow and process issues are being resolved for next netlist “N+1”
  - Next netlist ...
    - STAR memories and DFT are intended to be included
    - STAR processor functionality not arriving till 10/30/02
  - Synthesis experts continually reviewing code for area optimization
    - Have identified potential areas in RB that are being pursued
- **Block Design**
  - RB currently exceeds 300K instance
    - Split into color blend unit and remainder of RB
    - Allows early delivery of RB blend for Physical Design
  - MC synthesized at 600 MHz

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