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**Liao**

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(54) **POWER-EFFICIENT OPERATION FOR WIDER BANDWIDTH**

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(51) **Int. Cl.**

**H04L 12/24** (2006.01)

**H04W 52/14** (2009.01)

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(52) **U.S. Cl.**

CPC ..... **H04L 41/0833** (2013.01); **H04L 5/001** (2013.01); **H04L 5/0048** (2013.01); **H04L 5/0053** (2013.01); **H04L 5/0092** (2013.01); **H04W 48/00** (2013.01); **H04W 52/146** (2013.01); **H04W 48/12** (2013.01); **H04W 72/042** (2013.01)

(58) **Field of Classification Search**

None

See application file for complete search history.

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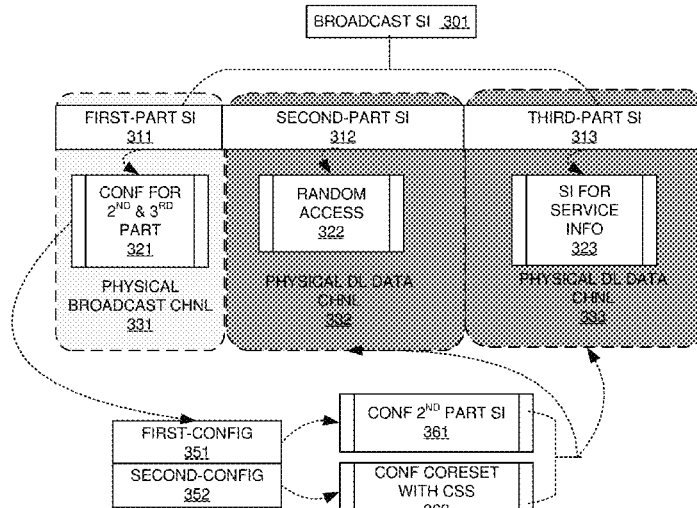
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(57) **ABSTRACT**

Apparatus and methods are provided for power-efficient operation for wide bandwidth. In one novel aspect, the configuration of the CORESET with CSS includes at least one of the time duration of the CORESET within a slot and the periodicity of the CORESET burst. In another embodiment, each configured BWP includes at least one CORESET with UE-specific search space and one CORESET with CSS. In another embodiment, at least one of the configured BWPs includes one CORESET with type-1 CSS for PCell. Each configured BWPs includes one CORESET with type-2 CSS for both PCell and SCell and at least one CORESET with UE-specific search space for PCell and SCell. In another novel aspect, a timer-triggered transmission of activating DL BWP indication in all DL BWP configured is used when the UE mis-detects or false-alarms the explicit signaling for BWP switch.

**28 Claims, 7 Drawing Sheets**



- (51) **Int. Cl.**  
*H04L 5/00* (2006.01)  
*H04W 48/00* (2009.01)  
*H04W 72/04* (2009.01)  
*H04W 48/12* (2009.01)

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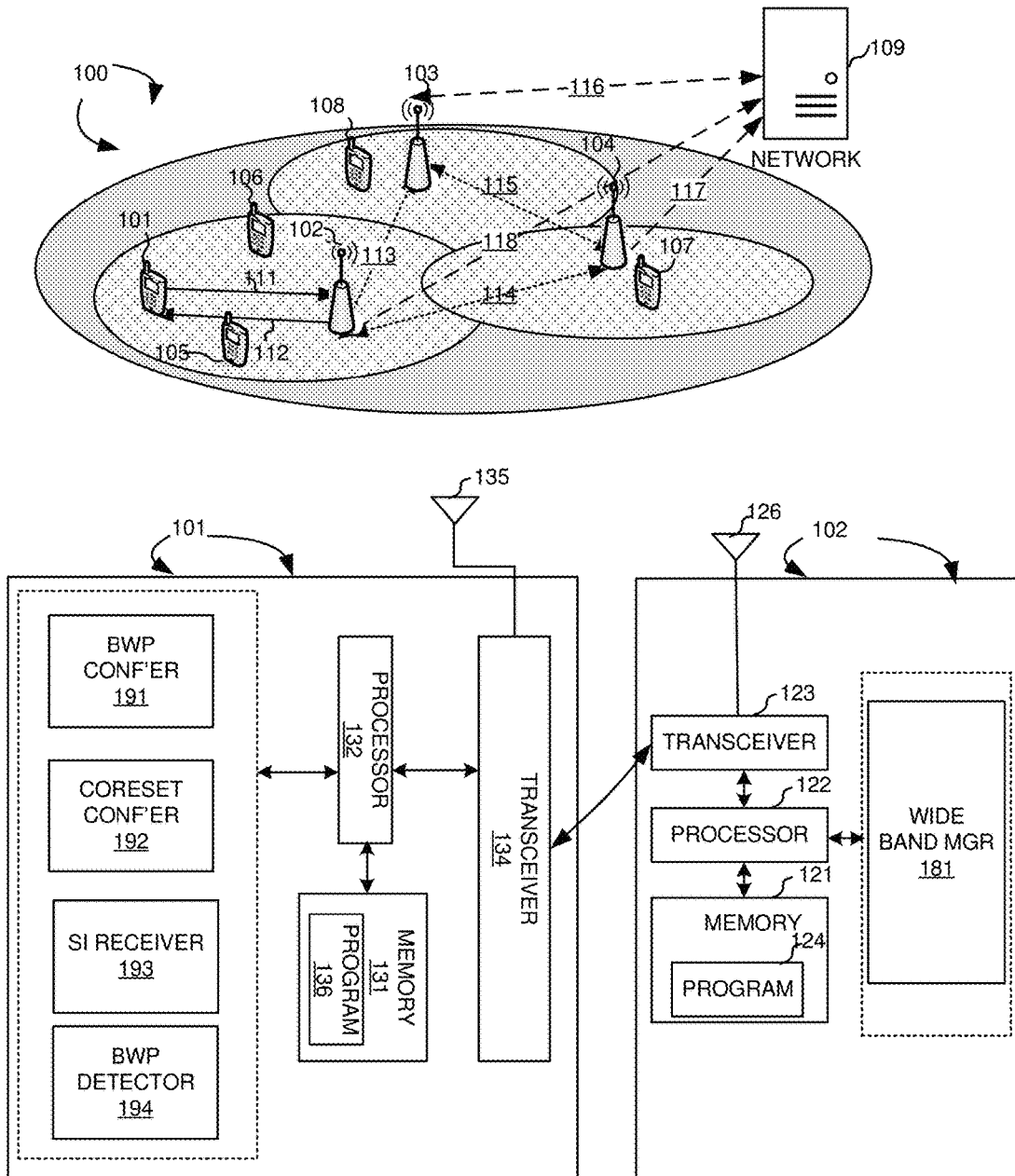


FIG. 1

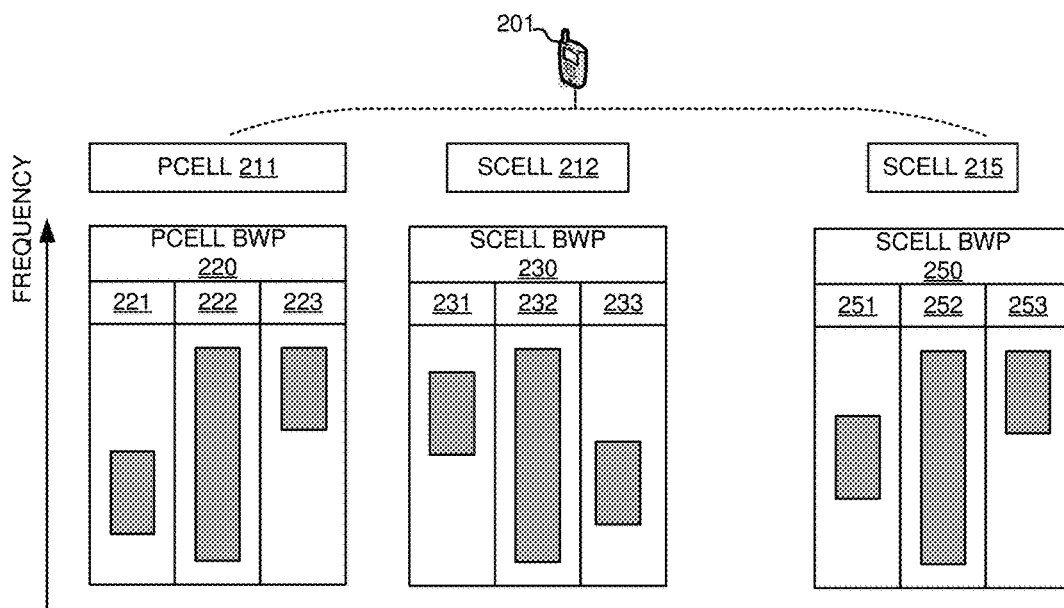


FIG. 2

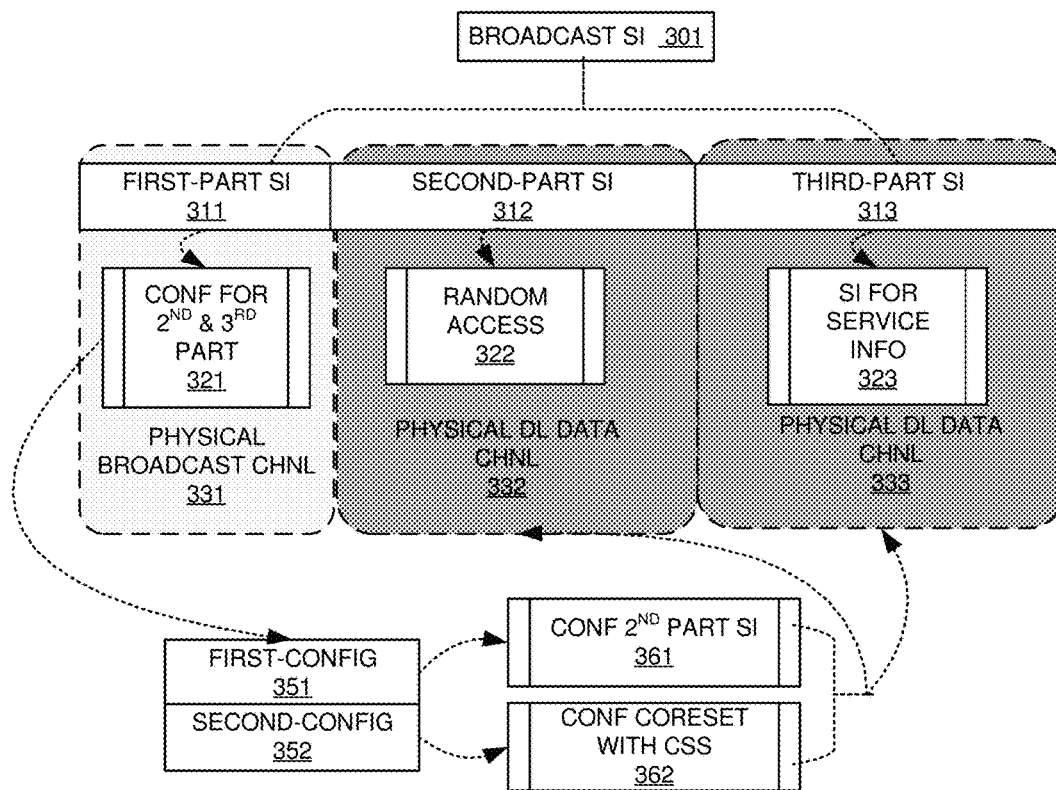


FIG. 3

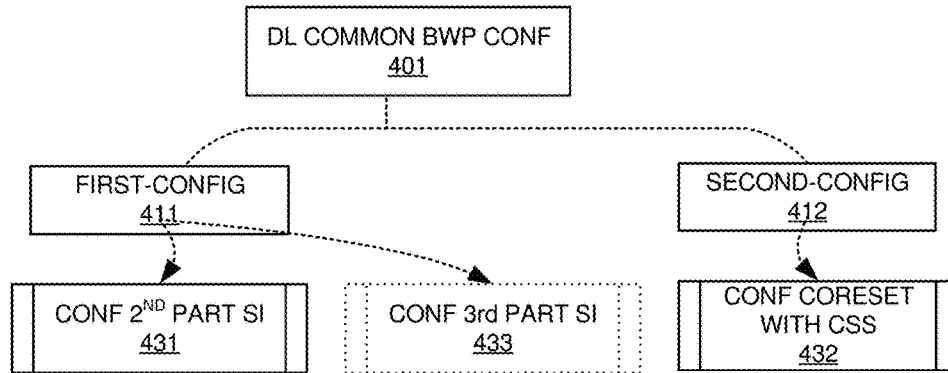


FIG. 4

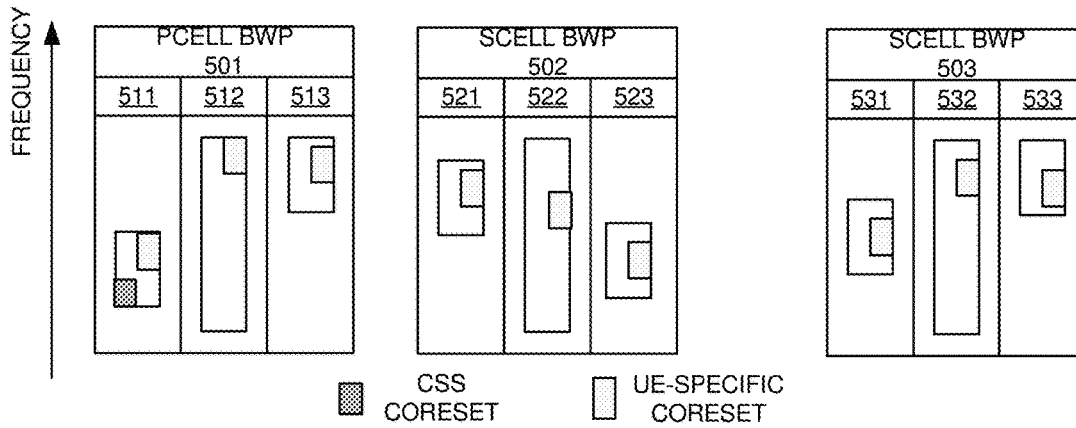


FIG. 5A

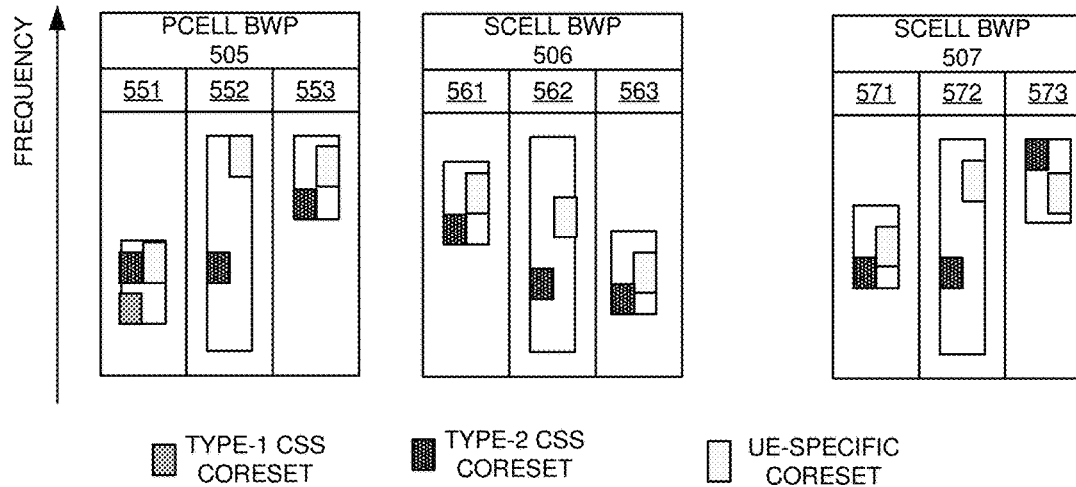


FIG. 5B

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