

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE PATENT TRIAL AND APPEAL BOARD**

*Inter Partes* Review of

IPR2023-00567

U.S. Patent No. 10,049,080

Filed: February 13, 2017

Issued: August 14, 2018

Inventor: Varghese George; Sanjeev S. Jahagirdar; Deborah T. Marr

Title: Asymmetric performance multicore architecture with same instruction set architecture

**POWER OF ATTORNEY PURSUANT TO 37 C.F.R. § 42.10(b)**

Qualcomm Incorporated hereby revokes its prior appointment of attorneys at O'Melveny & Myers LLP as counsel in this proceeding.

Qualcomm Incorporated hereby affirms its appointment of the attorneys associated with Customer Number 32425 to represent the undersigned and transact all business in the United States Patent and Trademark Office in connection with the above-referenced proceeding involving U.S. Patent No. 10,049,080.

Date: 10/17/2023 | 10:39:49 AM PDT DocuSigned by:  
Byron Yafuso  
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Name: Byron Yafuso

Title: Patent Counsel, Senior  
Qualcomm Incorporated