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(54) **DISTRIBUTION OF TASKS AMONG ASYMMETRIC PROCESSING ELEMENTS**

Related U.S. Application Data

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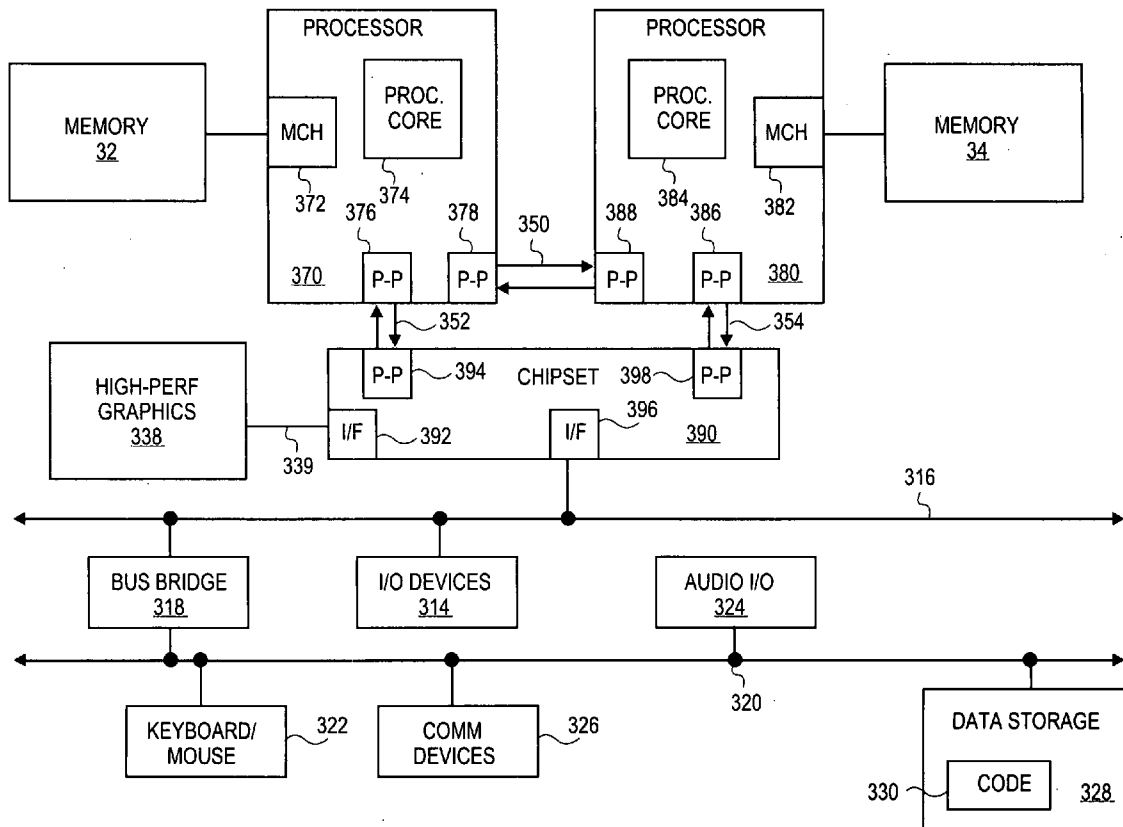
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(57) **ABSTRACT**

Techniques to control power and processing among a plurality of asymmetric cores. In one embodiment, one or more asymmetric cores are power managed to migrate processes or threads among a plurality of cores according to the performance and power needs of the system.

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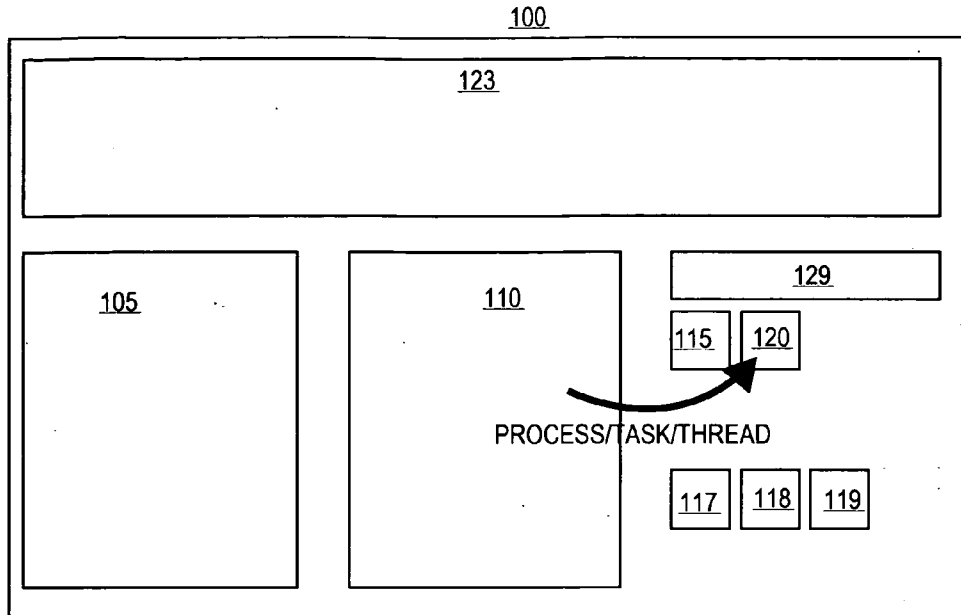


FIG. 1

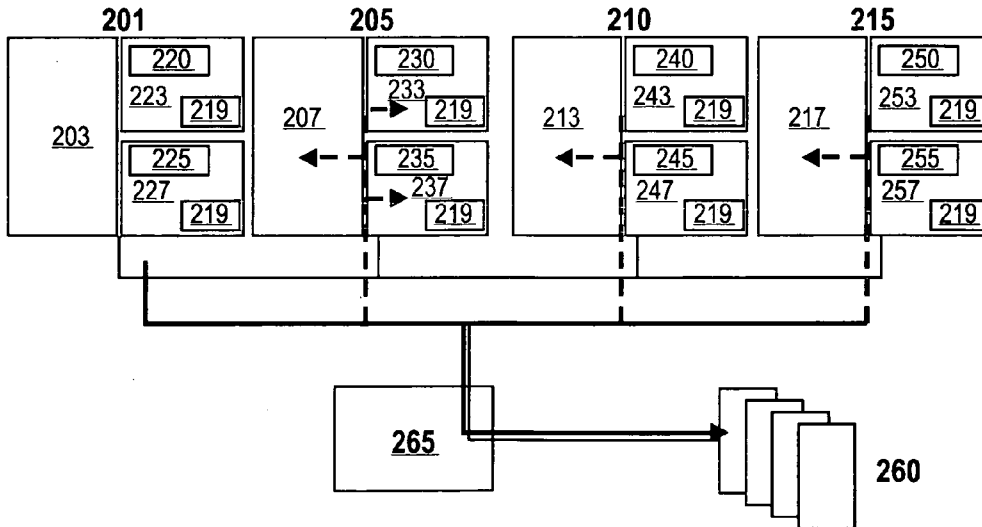


FIG. 2

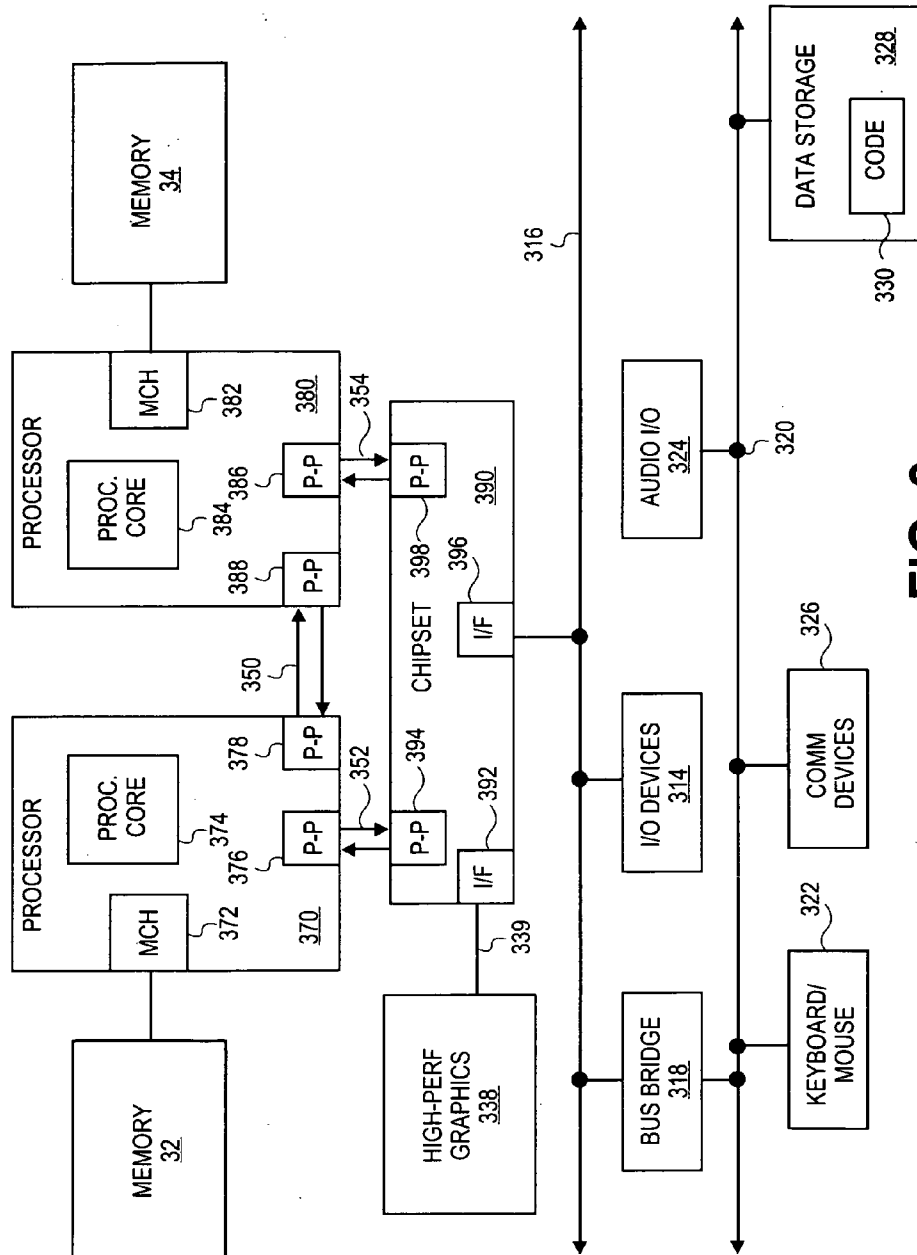


FIG. 3

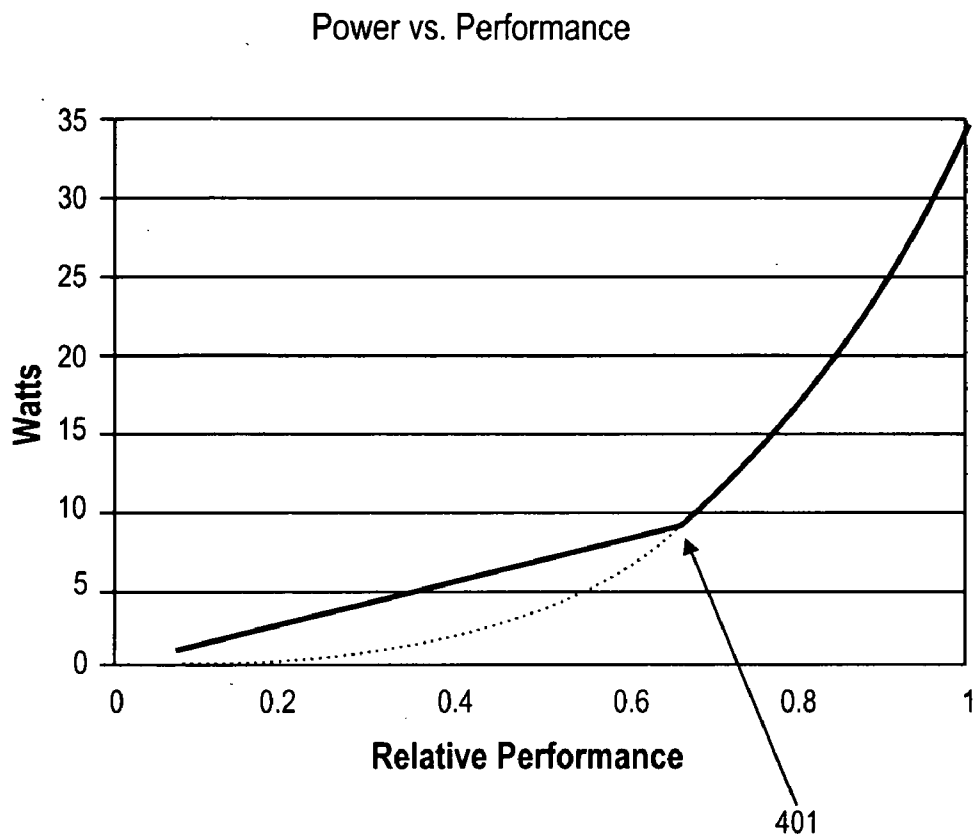


FIG. 4

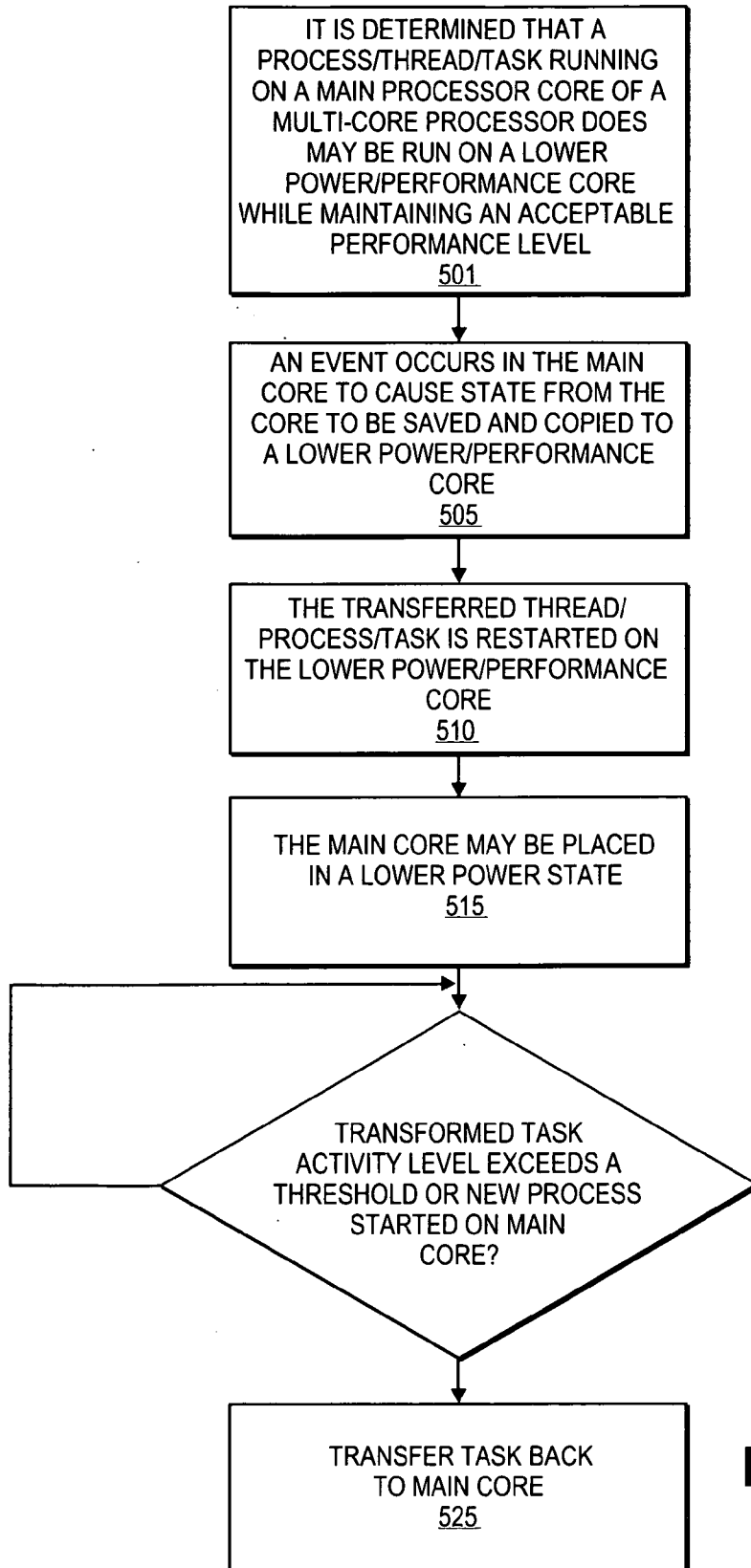


FIG. 5

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