

EXHIBIT E-1
Defendant's First Amended Invalidity Contentions
Orckit Corporation v. Cisco Systems, Inc., 2:22-cv-00276-JRG-RSP

Chart for U.S. Patent 6,680,904 (“the ’904 Patent”)
35 U.S.C. § 103

In this chart, “The Reference” refers to each of the following references and/or systems:

- Cisco Catalyst 2900 Series XL and 3500 Series XL Switches (collectively, the “Catalyst XL Switches”)
- BayStack 450 Series Switches (the “BayStack 450 Switches”)
- TRENDnet Stackable Hubs (“TRENDnet Stackable Hubs”)
- U.S. Patent No. 6,314,102 to Czerwic (“Czerwic”)
- U.S. Patent No. 6,092,214 to Quoc (“Quoc”)
- PCT Application No. WO 91/14324 to Vink (“Vink”)
- U.S. Patent No. 6,600,727 to Mackay (“Mackay”)
- U.S. Patent No. 6,663,499 to Dowling (“Dowling”)
- U.S. Patent No. 5,953,318 to Nattkemper (“Nattkemper”)

The following additional references are identified individually:

- U.S. Patent No. 5,313,456 to Sugawara (“Sugawara”)
- U.S. Patent No. 6,654,796 to Slater et al. (“Slater ’796”)
- U.S. Patent No. 6,895,433 to Slater & Chennapragada (“Slater ’433”)
- U.S. Patent No. 6,917,626 to Duvvury (“Duvvury ’626”)
- U.S. Patent No. 7,545,820 to Duvvury (“Duuvury ’820”)
- U.S. Patent No. 6,269,452 to Daruwalla et al. (“Daruwalla”)
- U.S. Patent No. 6,853,623 to Nederveen & King (“Nederveen”)
- U.S. Patent No. 6,952,421 to Slater (“Slater ’421”)
- U.S. Patent No. 6,463,065 to Petersen et al. (“Petersen”)

- *Cisco Introduces Next-Generation Stacking with New Catalyst 3500 Series XL*, CISCO: THE NEWSROOM, May 24, 1999, <https://newsroom.cisco.com/c/r/newsroom/en/us/a/y1999/m05/cisco-introduces-next-generation-stacking-with-new-catalyst-3500-series-xl.html> (“Cisco Catalyst Press Release”)

As shown in the chart below, all Asserted Claims of the ’904 Patent are invalid under 35 U.S.C. § 103 because The Reference renders those claims obvious either alone, or in combination with the knowledge of a person having ordinary skill in the art, and in further combination with the references specifically identified below and in the following claim chart and/or one or more references identified in Defendant’s Preliminary Invalidity Contentions.

Motivations to combine include at least the similarity in subject matter between the references to the extent they concern methods of stackable switching. Insofar as the references cite other patents or publications, or suggest additional changes, one of ordinary skill in the art would look beyond a single reference to other references in the field.

These invalidity contentions are based on Defendant’s present understanding of the Asserted Claims, and Orkit’s apparent construction of the claims in its November 3, 2022 Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1, and Orkit’s January 19, 2023 First Amended Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1 (Orkit’s “Infringement Disclosures”), which is deficient at least insofar as it fails to cite any documents or identify accused structures, acts, or materials in the Accused Products with particularity. Defendant does not agree with Orkit’s application of the claims, or that the claims satisfy the requirements of 35 U.S.C. § 112. Defendant’s contentions herein are not, and should in no way be seen as, admissions or adoptions as to any particular claim scope or construction, or as any admission that any particular element is met by any accused product in any particular way. Defendant objects to any attempt to imply claim construction from this chart. Defendant’s prior art invalidity contentions are made in a variety of alternatives and do not represent Defendant’s agreement or view as to the meaning, definiteness, written description support for, or enablement of any claim contained therein.

The following contentions are subject to revision and amendment pursuant to Federal Rule of Civil Procedure 26(e), the Local Rules, and the Orders of record in this matter subject to further investigation and discovery regarding the prior art and the Court’s construction of the claims at issue.

No.	'904 Patent Claim 1	The Reference
1[preamble]	Network access apparatus, comprising:	<p>The Reference discloses network access apparatus, comprising.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>
1[a]	first and second master units, each comprising a physical interface to a packet-switched network;	<p>The Reference discloses first and second master units, each comprising a physical interface to a packet-switched network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orkit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626 • Duuvury ’820

No.	'904 Patent Claim 1	The Reference
		<p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don't have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 1	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:¹</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP</p>

¹ Duvvury '626 is the parent to the CON Duvvury '820 and contains a substantially identical specification and reads on the Asserted Claims of the '904 patent for substantially the same reasons as Duvvury '626.

No.	'904 Patent Claim 1	The Reference
		<p>address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p> <p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury '626, 16:11-31.</p>

No.	'904 Patent Claim 1	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p style="text-align: center;">FIG. 17</p> <p>Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810,</p>

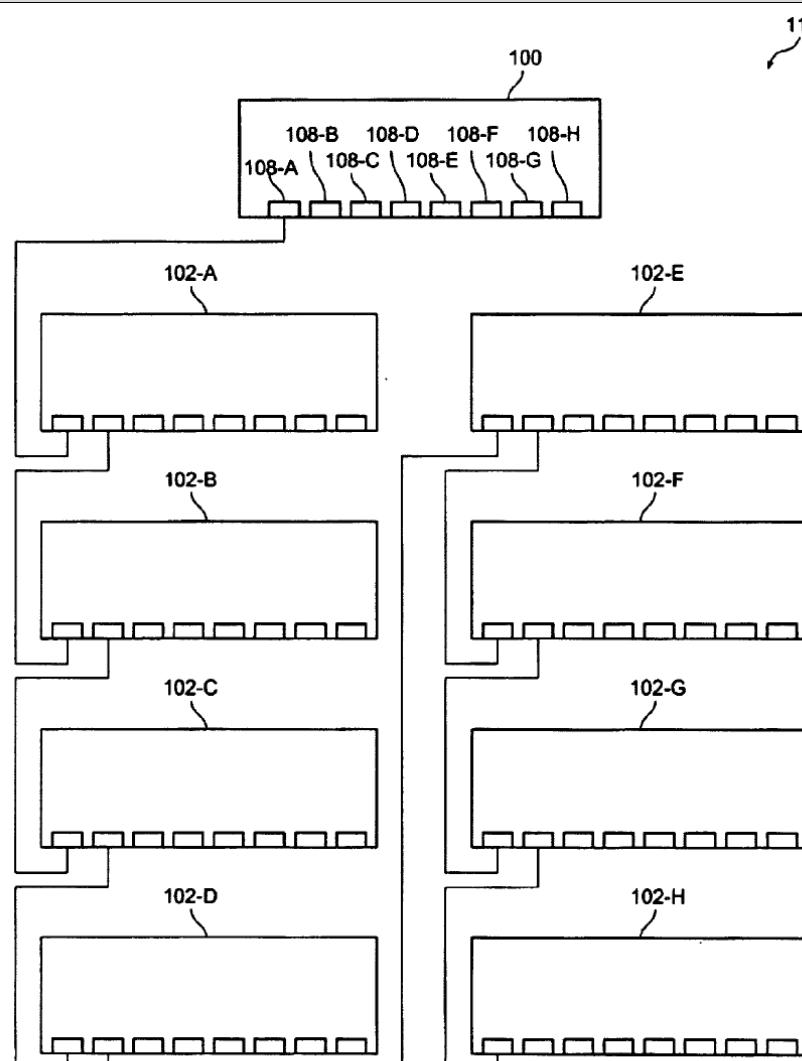
No.	'904 Patent Claim 1	The Reference
		<p>the second byte of the IP address is incremented by one, “modulo 256,” such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899.” Duvvury '626, 17:5-16.</p> <pre> graph TD 1800([1800 START]) --> 1805{1805 IS 2ND BYTE COUNTER < 255?} 1805 -- YES --> 1810[1810 INCREMENT 2ND BYTE BY ONE (MODULO 256)] 1810 --> 1820[1820 ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] 1820 --> 1830{1830 CONFLICT?} 1830 -- NO --> 1840{1840 IS 3RD BYTE COUNTER < 255?} 1840 -- YES --> 1850[1850 INCREMENT 3RD BYTE BY ONE (MODULO 256)] 1850 --> 1860[1860 ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] 1860 --> 1870{1870 CONFLICT?} 1870 -- NO --> 1880{1880 IS 4TH BYTE COUNTER < 255?} 1880 -- YES --> 1885[1885 INCREMENT 4TH BYTE BY ONE (MODULO 256)] 1885 --> 1890[1890 ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] 1890 --> 1895{1895 CONFLICT?} 1895 -- NO --> 1899([1899 END]) 1895 -- YES --> 1805 1900[1900 SIGNAL AN ERROR CONDITION] --- 1805 </pre> <p>FIG. 18 Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 1	The Reference
		<p><u>Slater '796 discloses:</u>²</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

² Slater '433 is the parent to the CON Slater '796 and contains a substantially identical specification and reads on the Asserted Claims of the '904 patent for substantially the same reasons as Slater '796.

No.	'904 Patent Claim 1	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 1	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 1	The Reference
		 <p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 1	The Reference
		<p>FIG. 10 Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Slater ’796, 12:44-56.</p> <p>“One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections.” Slater ’796, 13:8-14.</p>
1[b]	a plurality of slave units,	<p>The Reference discloses a plurality of slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orckit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626

No.	'904 Patent Claim 1	The Reference
		<ul style="list-style-type: none"> • Duuvury '820 <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don't have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

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		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

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		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury '626, 16:11-31.</p>

No.	'904 Patent Claim 1	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17</p> <p>Duvvury '626, FIG. 17.</p>

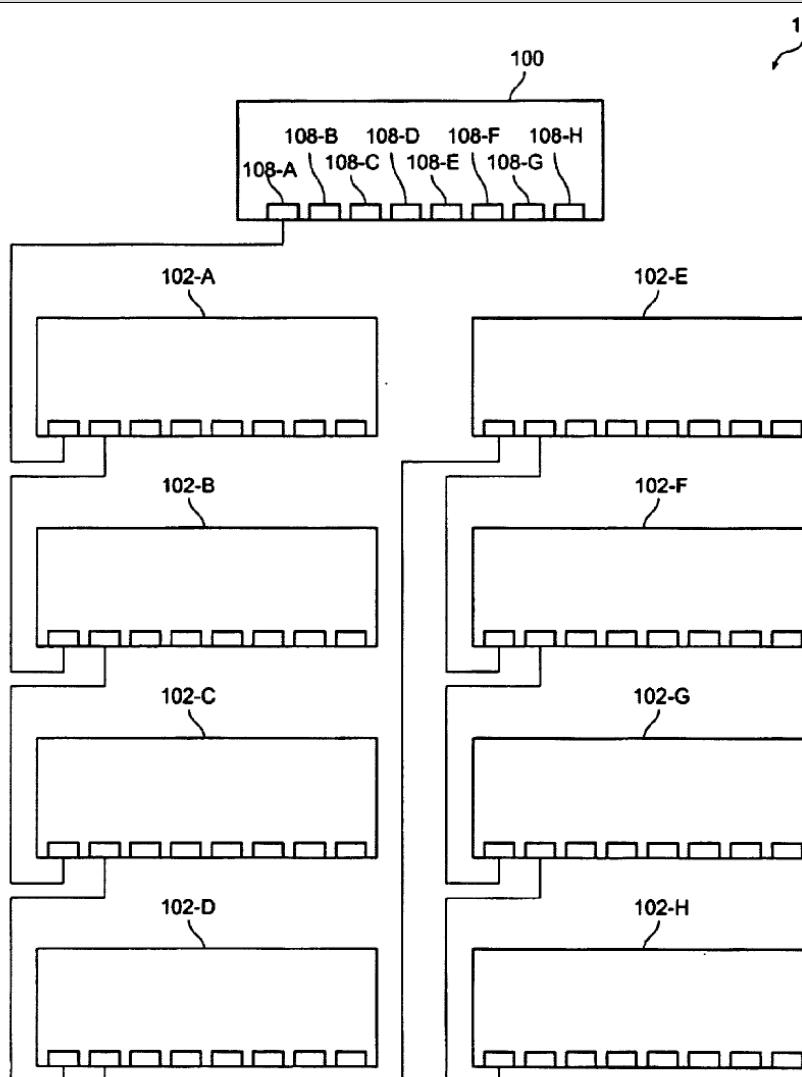
No.	'904 Patent Claim 1	The Reference
		<p>“FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, “modulo 256,” such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899.” Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 1	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 1	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

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		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 1	The Reference
		 <p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 1	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 1	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 1	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
1[c]	each slave unit comprising one or more ports to respective subscriber lines; and	<p>The Reference discloses each slave unit comprising one or more ports to respective subscriber lines.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>
1[d]	a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by	<p>The Reference discloses a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

No.	'904 Patent Claim 1	The Reference
	the physical interface lines therebetween,	<p>the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling and Sugawara.</p> <p>Below are examples of such references.</p> <p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p> <p style="text-align: center;">FIG. 2</p> <p style="text-align: right;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p>

No.	'904 Patent Claim 1	The Reference
		<p>Sugawara, 3:24-50 (“When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>

No.	'904 Patent Claim 1	The Reference
1[e]	each daisy chain comprising at least a first slave unit connected one of the physical interface lines to the first master unit, a second slave unit connected to the first slave unit but not to the first or second master unit, and a last slave unit connected by another of the physical interface lines to the second master unit.	<p>The Reference discloses each daisy chain comprising at least a first slave unit connected one of the physical interface lines to the first master unit, a second slave unit connected to the first slave unit but not to the first or second master unit, and a last slave unit connected by another of the physical interface lines to the second master unit.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 2	The Reference
2	Apparatus according to claim 1, wherein the network comprises an asynchronous transfer mode (ATM) network.	<p>The Reference discloses Apparatus according to claim 1, wherein the network comprises an asynchronous transfer mode (ATM) network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 3	The Reference
3	Apparatus according to claim 1, wherein the network comprises an Internet protocol (IP) network.	<p>The Reference discloses apparatus according to claim 1, wherein the network comprises an Internet protocol (IP) network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 4	The Reference
4[preamble]	Network access apparatus, comprising:	<p>The Reference discloses network access apparatus, comprising.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> at 1[preamble].</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orkit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626

No.	'904 Patent Claim 4	The Reference
		<ul style="list-style-type: none"> • Duuvury '820 <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don't have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 4	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 4	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Duvvury '626, 12:18-30.</p> <p>"FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number "10.0.0.0." Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number "10.0.0.0." Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is "00-e0-1e-01-02-03," then the generated CMIP address will be "10.01.02.03." At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other." Duvvury '626, 16:11-31.</p>

No.	'904 Patent Claim 4	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>The flowchart consists of five rectangular boxes connected by downward arrows. Each box contains a numbered reference identifier in the top right corner:</p> <ul style="list-style-type: none"> Box 1700: READ MAC ADDRESS OF EXPANSION SWITCH Box 1710: ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0" Box 1720: ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH Box 1730: COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH Box 1740: ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES

FIG. 17

Duvvury '626, FIG. 17.

No.	'904 Patent Claim 4	The Reference
		<p>“FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, “modulo 256,” such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899.” Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 4	The Reference
		<pre> graph TD START([START]) -- 1805 --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> SIGNAL_ERROR[SIGNAL AN ERROR CONDITION] Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 -- 1810 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 -- 1820 --> Q2{CONFLICT?} Q2 -- NO --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> SIGNAL_ERROR Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 -- 1850 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 -- 1860 --> Q4{CONFLICT?} Q4 -- NO --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> SIGNAL_ERROR Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 -- 1885 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 -- 1890 --> Q6{CONFLICT?} Q6 -- NO --> END([END]) Q6 -- YES --> SIGNAL_ERROR </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 4	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 4	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 4	The Reference
		<p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 4	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 4	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
4[a]	first and second master units, each comprising a physical interface to a packet-switched network;	<p>The Reference discloses first and second master units, each comprising a physical interface to a packet-switched network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra at 1[a].</i></p>

No.	'904 Patent Claim 4	The Reference
4[b]	a plurality of slave units,	<p>The Reference discloses a plurality of slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> at 1[b].</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orckit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626 • Duuvury ’820 <p>Below are examples of such references.</p> <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically</p>

No.	'904 Patent Claim 4	The Reference
		<p>dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don’t have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p> <p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p>

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		<p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the</p>

No.	'904 Patent Claim 4	The Reference
		<p>Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p> <p style="text-align: center;">FIG. 2</p> <p style="text-align: right; color: red;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p>

No.	'904 Patent Claim 4	The Reference
		<p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.").</p> <p>Sugawara, Abstract ("A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.")</p>

No.	'904 Patent Claim 4	The Reference
		<p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p> <p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then,</p>

No.	'904 Patent Claim 4	The Reference
		<p>at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury ’626, 16:11-31.</p> <pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17</p> <p>Duvvury ’626, FIG. 17.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, “modulo 256,” such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899.” Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 4	The Reference
		<pre> graph TD START([START]) -- 1800 --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 -- 1810 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 -- 1820 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 -- 1850 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 -- 1860 --> Q3{CONFLICT?} Q3 -- NO --> END Q3 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 -- 1885 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 -- 1890 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> S1[SIGNAL AN ERROR CONDITION] S1 -- 1900 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 4	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p> <p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented</p>

No.	'904 Patent Claim 4	The Reference
		<p>in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater '796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater '796, 11:1-10.</p>

No.	'904 Patent Claim 4	The Reference
		<p>FIG. 9</p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 4	The Reference
		<p><i>FIG. 10</i></p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 4	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 4	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
4[c]	each slave unit comprising one or more ports to respective subscriber lines; and	<p>The Reference discloses each slave unit comprising one or more ports to respective subscriber lines.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra at 1[c].</i></p>

No.	'904 Patent Claim 4	The Reference
4[d]	a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween,	<p>The Reference discloses a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling and Sugawara.</p> <p><i>See supra at 1[d].</i></p>
4[e]	each daisy chain comprising at least a first slave unit connected by one of the physical interface lines to the first master unit and a last slave unit connected by another of the physical interface lines to the second master unit	<p>The Reference discloses each daisy chain comprising at least a first slave unit connected by one of the physical interface lines to the first master unit and a last slave unit connected by another of the physical interface lines to the second master unit.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra at 1[e].</i></p> <p>Below are examples of such references.</p>

No.	'904 Patent Claim 4	The Reference
		<p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p> <p>FIG. 2 (annotation added)</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1</p>

No.	'904 Patent Claim 4	The Reference
		<p>not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>

No.	'904 Patent Claim 4	The Reference
4[f]	<p>wherein in normal operation, downstream data packets received from the network are passed from the first master unit to each of the daisy chains via the first slave unit in each chain, and upstream data packets received by the slaves in each chain from the subscriber lines are passed via the first slave unit in the chain to the first master unit for transmission over the network.</p>	<p>The Reference discloses wherein in normal operation, downstream data packets received from the network are passed from the first master unit to each of the daisy chains via the first slave unit in each chain, and upstream data packets received by the slaves in each chain from the subscriber lines are passed via the first slave unit in the chain to the first master unit for transmission over the network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 5	The Reference
5	Apparatus according to claim 4, and comprising a protection interface, which couples the second master unit to the first master unit, and over which interface data packets are conveyed between the first and second master units in case of a fault.	<p>The Reference discloses apparatus according to claim 4, and comprising a protection interface, which couples the second master unit to the first master unit, and over which interface data packets are conveyed between the first and second master units in case of a fault.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 6	The Reference
6	Apparatus according to claim 5, wherein the first master unit bicasts the upstream data packets that it receives from the slave units to the network and, via the protection interface, to the second master unit, which transmits the upstream data packets to the network.	<p>The Reference discloses apparatus according to claim 5, wherein the first master unit bicasts the upstream data packets that it receives from the slave units to the network and, via the protection interface, to the second master unit, which transmits the upstream data packets to the network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 7	The Reference
7	<p>Apparatus according to claim 4, wherein in case of a fault at a location in one of the daisy chains, data flow in a portion of the daisy chain between the location of the fault and the second master unit is reversed, so that the downstream data packets are passed from the second master unit to the slave units in the portion of the daisy chain via the last slave unit in the chain, and the upstream data packets are passed by the last slave unit to the second master unit.</p>	<p>The Reference discloses apparatus according to claim 4, wherein in case of a fault at a location in one of the daisy chains, data flow in a portion of the daisy chain between the location of the fault and the second master unit is reversed, so that the downstream data packets are passed from the second master unit to the slave units in the portion of the daisy chain via the last slave unit in the chain, and the upstream data packets are passed by the last slave unit to the second master unit.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Below are examples of such references.</p> <p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p>

No.	'904 Patent Claim 7	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.")</p>

No.	'904 Patent Claim 7	The Reference
		<p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line Is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p> <p>Cisco commercialized and patented technology relating to monitoring, detecting, and resolving faults without requiring a network reconfiguration before Orkit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Daruwalla • Nederveen • Slater ’421 • Petersen

No.	'904 Patent Claim 7	The Reference
		<p>Daruwalla discloses:</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. The present invention provides a protection protocol which simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol will appropriately remove a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, would remove protection for a low priority failure, such as a signal degrade, on another link.” Daruwalla, Abstract.</p>

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		<p style="text-align: center;"><u>100</u></p> <p>Daruwalla, FIG. 1.</p>

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		<p style="text-align: right;">200</p>

Daruwalla, FIG. 2.

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Daruwalla, FIG. 11.

"The present invention relates to computer networks. In particular, the present invention relates to a system and method for providing a protection protocol for fault recovery for a two line bi-directional ring network." Daruwalla, 1:8-11.

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		<p>“FIG. 1 shows an example of a two line bi-directional ring network. The ring network 100 is shown to include nodes 102 a-102 g. Each node is typically a computer with embedded processors and at least one network connection. Each node 102 a-102 g is shown to be bidirectionally coupled to two neighboring nodes 102 a-102 g via an inner connection ring 110 a-110 g and an outer connection ring 108 a-108 g. For instance, node 102 a is bidirectionally coupled to nodes 102 b and 102 g. The example of FIG. 1 also shows a problem 104 in the connection between node 102 b and node 102 c. When a problem is detected (such as a bi-directional line cut), the connection between nodes 102 b and 102 d wraps back upon itself, as shown by wraps 106 a and 106 b. In this manner, the connection problem 104 can be avoided.” Daruwalla, 1:17-30.</p> <p>“In a conventional SONET network, each message sent by a sending node to a receiving node typically needs the identification and location of the receiving node to arrive at the proper destination. Accordingly, manual configuration is typically needed in each node to store the identity and location of each other node in the ring network in order to provide for communication between the nodes in the network.” Daruwalla, 1:31-44.</p> <p>“In summary, for the protection mechanism to operate, each node needs to know the current ring map (current ring topology). What is needed is a system and method for providing fault recovery for two line bi-directional ring network that minimizes the need to keep track of other nodes in the ring network. Preferably, the system would not require reconfiguration of an internal map of the network when a new node is added to, or existing nodes are removed from the network. The present invention addresses such a need.” Daruwalla, 2:23-31.</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. According to the embodiment, when the problem is identified, the node sends a message identifying the problem to a second neighbor which is located at least one node away from the problem. The second neighbor then forwards the message to a third neighbor, unless the second neighbor is dealing with a situation that is higher in a hierarchy of situations than the problem described</p>

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		<p>in the message by the original node. In general, if the second neighbor's situation has a higher priority than the situation described by the original node, then the message is ignored and not forwarded. If, however, the message sent by the original node describes a situation with a higher priority than the situation being dealt with by the second neighbor, then, in general, the second neighbor's situation is ignored, at least for the moment, and the original node's message is forwarded to the next neighbor. In general, a higher priority request preempts a lower priority request within the ring. Exceptions are noted as rules of the protection protocol." Daruwalla, 2:35-58.</p> <p>"The present invention provides a protection protocol that simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol automatically appropriately removes a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, removes protection for a low priority failure, such as a signal degrade, on another link." Daruwalla, 2:59-3:3.</p> <p>"A method according to an embodiment of the present invention for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and initiating a fault recovery procedure when the second node receives the first message." Daruwalla, 3:4-12.</p> <p>"In another aspect of an embodiment of the present invention, a method for adding a new node to a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node prior to an addition of the new node; initiating a fault recovery procedure when the second node receives the first message; receiving a second</p>

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		<p>message from the new node; and entering an idle state when the second message is received.” Daruwalla, 3:13-24.</p> <p>“In yet another aspect of an embodiment of the present invention, a system for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The system comprises means for detecting a situation by a first node, wherein the first node is one of the plurality of nodes; means for sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and means for initiating a fault recovery procedure when the second node receives the first message.” Daruwalla, 3:25-35.</p> <p>“FIG. 2 is block diagram of a ring network utilizing a protection protocol according to an embodiment of the present invention.” Daruwalla, 3:40-42.</p> <p>“FIGS. 4-6 are flow diagrams illustrating various rules within the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:46-48.</p> <p>“FIGS. 8-12 are flow diagrams and a system diagram illustrating further rules of the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:52-54.</p> <p>“FIG. 2 is a block diagram showing a ring network system utilizing a method of fault recovery according to an embodiment of the present invention. The ring network 200 is shown to include nodes 202 a-202 g. The nodes 202 a-202 g are shown to be coupled via an inner ring 210 in which the data flows in one direction, such as a clockwise direction. Additionally, the nodes 202 a-202 g are also shown to be coupled by an outer ring 212 in which data can flow in the opposite direction to the inner ring 210, such as in a counter-clockwise direction. The ring network 200 is shown to have a situation 204 a that requires protection, such as a ring wrap 206.” Daruwalla, 5:35-45.</p>

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		<p>“FIG. 4 is a flow diagram of an example of a method according to an embodiment of the present invention implied by Rules 1-22. An APS packet is received via step 400. It is determined whether the APS packet has been sent along a long path via step 402. If the packet was not sent via a long path, then the APS packet is not forwarded via step 406. Accordingly, if the APS packet was sent via the short path, then the packet is not forwarded via step 406. If, however, the packet was sent through the long path via step 402, then the APS packet may be forwarded via step 404. Note that for this example of Rule (1), it is assumed that the long path message does not have to pass through a wrapped connection in order to be forwarded. Otherwise, if the long path message needs to pass through a wrapped connection in order to be forwarded, then the message will simply not be forwarded.” Daruwalla, 6:21-36.</p> <p>“FIG. 6 is a flow diagram illustrating Rules 4 and 5. A node detects a problem between the node and a first neighbor via step 600. The node performs a wrap away from the side on which the problem exists via step 602. A short path message is then sent to the first neighbor informing it of the problem via step 604. Additionally, a long path message is also sent to a second neighbor informing the second neighbor of the problem via step 604. The first neighbor then performs a wrap away from the side of the problem via step 606. The first neighbor also sends an IDLE message, indicating a wrapped status, on a short path to the node that detected the problem via step 608. This message is sent across the failed span. Note that IDLE messages do not get wrapped and are sent across failed spans if possible. Additionally, the first neighbor also sends a message on a long path toward the side without the problem via step 608.” Daruwalla, 6:64-11.</p> <p>“An example of the method described in FIG. 6 can be seen in FIG. 2. Node 202 b has detected a problem 204 a and performs a wrap 206 on the side on which the problem exists. Node 202 b also sends a short path message to the neighbor on the other side of the problem 204 a, which is node 202 c. Node 202 b also sends a long path message to its other neighbor node 202 a informing it of the problem. Node 202 c performs a wrap 206 on the side of the problem and sends an IDLE message on a short path to node 202 b. Node 202 c also sent a message on a long path toward the side without the problem to its neighbor 202 d.” Daruwalla, 7:12-21.</p>

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		<p>“FIG. 7 lists the hierarchy of priorities of Rule (8). For ease of reference, the hierarchy is separated into Class I-III. Class I is the highest priority, while Class III is the lowest priority. An example of a highest priority message in Class I is lockout. Lockout is an order stating that the ring network is not to wrap under any circumstances.” Daruwalla, 7:22-26.</p> <p>“Examples of the next priority listed in Class II are forced switch and signal fail. Forced switch indicates that the ring network is configured to wrap at the point of the forced switch. Signal fail is a situation where either two nodes cannot communicate with each other, or one node cannot hear the other node. An example of a signal fail is a physical break in the communication lines between two nodes.” Daruwalla, 7:27-33.</p> <p>“Note that members of Class II can co-exist (Rule 9). For example, multiple forced switches and signal fails can co-exist. Additionally, members of Class I can co-exist (Rule 10). For example, multiple lockouts in a single ring network can co-exist. However, situations in Class III cannot co-exist with other situations (Rule 11). For example, a signal degrade cannot co-exist with a wait-to-restore.” Daruwalla, 7:52-58.</p> <p>“When there are multiple requests of the same priority within Class III, the first request to complete a long path signaling will take priority (Rule 13). For example, if there are two signal degrades located on the same ring network, then the first signal degrade which completes the long path signaling will take priority over the other signal degrade. By not allowing members of Class III to co-exist with one another, partitioning of the ring network is avoided.” Daruwalla, 7:59-65.</p> <p>“In case of two equal requests within Class III on both inner and outer rings of the ring network, the tie is broken by choosing a request on one of the rings, such as the outer ring request (Rule 14). For example, if a signal degrade occurs both on the inner and outer rings, then a request on a predetermined ring, such as the outer ring, will take priority over the other requests.” Daruwalla, 7:66-8:5.</p>

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		<p>“FIG. 8 is a flow diagram illustrating Rules (9), (10), (11), (13), and (15). Note that the flow diagram described in FIG. 8 is merely an example of one way in which the rules of the method according to the embodiment of the present invention can be executed. For example, the determination of whether the long path message is a Class I request via step 802 or a Class II request via step 810 can be in reverse order.” Daruwalla, 8:6-11.</p> <p>“A wrapped node receives a long path message via step 800. It is then determined if the long path message is a Class I request via step 802. The classes used in FIG. 8 are meant to correspond with the example of classes defined in FIG. 7. If the long path message is a Class I request, then it is determined if a local situation also has a Class I request via step 804. A local situation includes scenarios such as when a node detects a situation or problem, or when a node is made aware of a problem or situation via a short path message from its neighbor. If a local situation is not a Class I request via step 804, then any existing local wraps are unwrapped and the long path message is forwarded via step 806. If, however, a local situation is a Class I request via step 804, then the connections are already unwrapped or was never wrapped, and the long path message is forwarded via step 808.” Daruwalla, 8:12-26.</p> <p>“FIG. 12 is a flow diagram illustrating rules (20) and (21) of the method according to the embodiment of the present invention. A wrapped node determines that a problem has been cleared via step 1200. It then enters a wait-to-restore state via step 1202. It is then determined if its neighbor is the same neighbor as previously noted via step 1204. The node can save the source of a short path message at the time of wrap initiation to note the identity of its neighbor. If the current neighbor is not the same as the previous neighbor via step 1204, then an IDLE state is entered via step 1206. If, however, the current neighbor is the same as the previous neighbor via step 1204, then it is determined whether a pre-determined wait-to-restore time has expired via step 1208. Once the pre-determined wait-to-restore time has expired, then the node enters an IDLE state via step 1206.” Daruwalla, 12:60-13:6.</p> <p>“A method and system for fault recovery for a two line bi-directional network has been disclosed. Software written according to the present invention may be stored in some form of computer-readable medium, such as memory or CD-ROM, or transmitted over a network, and executed by a processor.” Daruwalla, 13:7-19.</p>

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		<p>Nederveen discloses:</p> <p>“A technique for use in gathering network activity-related information from cascaded network switches is provided. Using this technique, the information can be gathered without substantially reducing performance of the cascaded switches. In one embodiment, a single remote monitoring probe is connected via respective connections to each of the switches so as to receive the information from the switches. In another embodiment, only one of the switches is connected to the probe, and the other switches transmit their respective portions of the information to the switch connected to probe. The switch connected to the probe provides these portions of the information, as well as, any of its respective activity-related information to the probe. In this latter embodiment, the switches may be connected by dedicated connections and switch ports that are used solely for communicating the activity-related information.” Nederveen, Abstract.</p>

No.	'904 Patent Claim 7	The Reference
		<p>FIG. 3</p> <p>Nederveen, FIG. 3.</p>

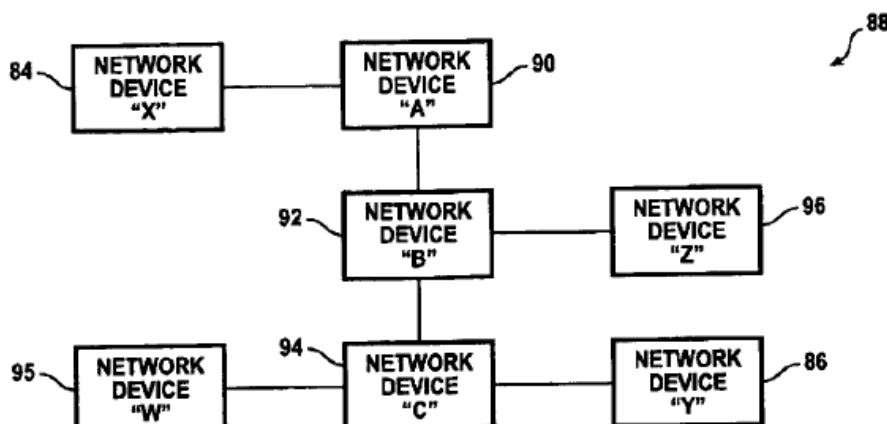
No.	'904 Patent Claim 7	The Reference
		<p>The diagram illustrates a stacked switch monitoring architecture. Three switches, labeled A, B, and C, are stacked vertically. Each switch contains a Buffer Processor (BP), a Bridge Logic (BL), and a MAC layer with Tx and Rx components. - Switch A (bottom) has a BP (~504), BL (~600B), and MAC (~302). It receives data from the segment below (~222) and transmits data to the segment above (~228). - Switch B (middle) has a BP (~501), BL (~600A), and MAC (~310B). It receives data from the segment below (~224) and transmits data to the segment above (~226). - Switch C (top) has a BP (~500), BL (~600A), and MAC (~306). It receives data from the segment below (~226) and transmits data to the segment above (~230) and to a Router (~250). A dashed line encloses the three switches. An external connection labeled '900' connects the MAC layer of Switch C to an 'RMON PROBE' block (402). The probe block then connects to a 'MANAGEMENT PROCESS' (408).</p> <p style="text-align: center;">FIG. 5</p> <p style="text-align: center;">Nederveen, FIG. 5.</p> <p>“Thus, it would be desirable to provide a stacked switch monitoring technique that permits efficient offloading of raw data processing from the stacked switches, requires only a minimal number of specialized network entities to gather and process such raw data, and does not result in substantial degradation of stacked switch performance.” Nederveen, 4:38-43.</p>

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		<p>“Accordingly, the present invention provides a technique for remote monitoring of a switch network that overcomes the aforesaid and other disadvantages and drawbacks of the prior art. More specifically, in one aspect of the present invention, a technique is provided for gathering information that may be useful in network management (e.g., switch port activity-related information), from switches in the network that are in a stacked configuration. The information is gathered from the stacked switches by a single network entity (e.g., an SNMP remote monitoring probe) in such a way that it does not substantially degrade the performance of the switches. This is accomplished, in one embodiment of the technique of the present invention, by connecting the switches via respective connections to a multiplexer that selectively connects the switches, according to an arbitration scheme, to the single network entity. The entity gathers respective portions of the information from switches when it is connected to the switches by the multiplexer. The information gathered by the entity may be provided to another network entity (e.g., an SNMP management node) in order to permit the other entity to use that information in managing the network.” Nederveen, 4:46-67.</p> <p>“In another embodiment of the technique of the present invention, only one of the switches is connected to the single information gathering entity. The switches that are not connected to the entity transmit, via respective dedicated ports and connections (i.e., ports and connections that are used solely for network information gathering activities), their respective portions of the information to the switch that is connected to the entity. The switch that is connected to the entity transmits, via a respective dedicated port and connection, the information received from the other switches, as well as, its own information to the entity.” Nederveen, 5:1-11.</p> <p>“FIG. 3 is a schematic, functional block diagram illustrating in greater detail the construction of the stacked switch network shown in FIG. 2.” Nederveen, 5:26-28.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network configured to employ another embodiment of the present invention.” Nederveen, 5:32-34.</p>

No.	'904 Patent Claim 7	The Reference
		<p>“FIGS. 2-5 illustrate features of a computer network 200 wherein embodiments of the present invention may be advantageously practiced. Network 200 comprises a stacked switch network 300 which interconnects a plurality of network segments 228, 232, 240, and 251. Each segment 228, 232, 240 comprises one or more local area networks having computer endstations (not shown). Segment 251 is a network router segment that comprises network router 250. Each segment 228, 232, 240 is coupled via a respective communications link 222, 224, 226 to a respective port 302 (i.e., port A), 304 (i.e., port B), 312 (i.e., port C) of the switch network 300. Likewise, the router 250 of router segment 251 is coupled via a respective trunk line 230 to router port 306 (i.e., port R).” Nederveen, 5:46-59.</p> <p>“Stacked switch network 300 comprises a plurality of data network switches 300A, 300B, 300C (e.g., Catalyst 3900™ series switches of the type commercially available from the Assignee of the subject application) coupled together via conventional stack link bus connection logic 600A, 600B. More specifically, logic 600A couples a stack link bus port and associated logic 500 in switch 300A to a stack link bus link port and associated logic 502 in switch 300B. Similarly, logic 600B couples another stack link bus port and associated logic 501 in switch 300B to a stack link bus port and associated logic 504 in switch 300C. It should be understood that although, as is shown in FIG. 3, switches 300A and 300B, and switches 300B and 300C, may be coupled serially together by separate respective logic elements 600A, 600B, each of the switches 300A, 300B, 300C may be coupled together via a single respective stack link bus port in the switch to a single stack link bus connection logic block (not shown, e.g., of the type that is commercially available under the tradename Catalyst Matrix™ from the Assignee of the subject application). Further alternatively, depending upon the particular design and functionality of the ports 500, 501, 502, and 504, and the control and forwarding logic (whose operation will be described more fully below) in the switches 300A, 300B, 300C, the circuitry in logic 600A, 600B may instead be comprised in the ports 500, 501, 502, and 504 and/or control and forwarding logic, and therefore, in this alternative configuration, the logic 600A, 600B in the network 300 may be replaced by simple connection means (e.g., cable connectors).” Nederveen, 6:29-57.</p>

No.	'904 Patent Claim 7	The Reference
		<p>“Each switch 300A, 300B, 300C includes a respective internal bus (e.g., element 800 in switch 300C) that is coupled via at least one stack link bus port and associated interface logic (e.g., 504 in switch 300C) to external stack link bus connection logic (e.g., element 600B in switch 300C). Each switch 300A, 300B, 300C also includes respective programmable control and forwarding logic (e.g., element 802 in switch 300C) comprising processing, memory, and other circuitry for storing and learning configuration information (e.g., source and destination MAC addresses of messages received by the switch, switch bridging table, switch segments' spanning tree and virtual local area network information, etc.), and for providing appropriate commands to other elements (e.g., the switch ports) to cause data messages received by the switch to be forwarded to appropriate network segments coupled to the switch based upon this configuration information. In each switch, the switch's port logic circuitry (e.g., port A logic 302 and port P logic 310C in switch 300C) and control and forwarding logic are coupled to each other via that switch's respective internal bus. The stack link bus port and associated logic in each switch 300A, 300B, 300C may comprise a Catalyst™ stack port line interface card (commercially available from the Assignee of the subject application) inserted into a bus expansion slot (not shown) in the switch. Although not shown in the Figures for purposes of clarity of illustration, each switch 300A, 300B, 300C in network 300 typically will include tens or hundreds of ports coupled to network segments.” Nederveen, 6:58-7:19.</p> <p>“The control and forwarding logic and stack link bus port and associated logic in each switch, and the logic 600A, 600B, are configured to together implement conventional techniques for permitting the switches 300A, 300B, 300C to function together as a single logical/virtual switch. More specifically, when configured in the stacked arrangement 300, after the switches 300A, 300B, 300C and logic 600A, 600B are initially activated, they execute initial power-on self-diagnostics, and thereafter, enter a “stack discovery” mode of operation.” Nederveen, 7:20-29.</p> <p>“In the stack discovery mode of operation, the control and forwarding logic in each switch 300A, 300B, 300C first “senses” that its switch is coupled to logic 600A and/or 600B, and then determines the particular configuration of the stacked switch network 300, using suitable conventional autosensing/autoconfiguration techniques. The control and 96forwarding logic in the switches 300A, 300B, 300C then assigns to the switches respective unique</p>

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		<p>identification numbers (e.g., based upon unique identification numbers of respective ports of the logic 600A, 600B to which the switches are coupled).” Nederveen, 7:30-40.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network 300’ configured to employ another embodiment of the present invention. It should be understood that unless specifically stated to the contrary, the structure and operation of the network 300’ are substantially the same as the structure and operation of network 300. In network 300’, each of the dedicated ports 310A, 310B, 310C comprises a respective transmit portion and receive portion, referenced in FIG. 5 as RX and TX, respectively.” Nederveen, 11:20-29.</p> <p><u>Slater '421 discloses:</u></p> <p>“A method and apparatus for discovering paths to other network devices includes a protocol and network management application that can be executed on network devices. The Ethernet protocol is used to detect paths to other network devices, knowing only the Ethernet address of the destination. A discovery protocol is extended to add hop probe and hop probe reply Type-Length-Value fields in a variable-length list. The hop probe fields contain a hop count, a destination Ethernet address, and a source Ethernet address. When a hop probe is received by a network device, the hop count field is decremented by one and the hop probe is forwarded. Packet received with a hop count of one are not forwarded and a hop probe reply is sent back to the Ethernet source address of the hop probe. The hop probe reply fields contain a destination Ethernet address and a source Ethernet address.” Slater '421, Abstract.</p>

No.	'904 Patent Claim 7	The Reference
		 <p>FIG. 6</p> <p>Slater '421, FIG. 6.</p> <p>"Partly as a result of the increased complexity of networks, network administrators must often troubleshoot problems with their network. Two classes of network problems often faced by network administrators are "reachability" problems and performance slowdowns. Reachability problems occur when one or more network devices cannot be accessed through a network, and can be caused by hardware or software failures, cabling problems, or any of several other types of difficult-to-diagnose problems that can occur in a network." Slater '421, 7:11-20.</p> <p>"Some of the world's largest networks today rely on the TCP/IP suite of networking protocols. With a relatively small kit of basic tools, network administrators can learn much about an internetwork. "Ping" and "traceroute" commands, "show" commands, and "debug" commands (all of which are typically available via the basic management interface on a network device) form the core of the network administrator's internetwork toolkit. Ping and traceroute commands can be useful tools in determining where failures are occurring, but they are cumbersome to use, and require knowledge of the IP address or host name of the</p>

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		<p>destination network device. The show commands provide information about interface conditions, protocol status, neighbor reachability, router configuration and status, level of traffic, errors and drops, and other network data. Finally, debug commands provide clues about the status of a network device and other network devices directly or indirectly connected to it. Because debug commands can create performance slowdowns, they must be used with great care, and using the wrong debug command at the wrong time can exacerbate problems in already poorly performing networks.” Slater ’421, 7:55-8:8.</p> <p>“Embodiments of the present invention as illustrated herein use the Cisco™ Discovery Protocol (“CDP”) to automatically detect paths to specified network devices in Ethernet LANs. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task.” Slater ’421, 9:10-15.</p> <p>“CDP is a media-independent device discovery protocol which can be used by a network administrator to view information about other network devices directly attached to a particular network device. In addition, network management applications can retrieve the device type and SNMP-agent address of neighboring network devices. This enables applications to send SNMP queries to neighboring devices. CDP thus allows network management applications to discover devices that are neighbors of already known devices, such as neighbors running lower-layer, transparent protocols.” Slater ’421, 9:16-26.</p> <p>“It is to be understood that the present invention is not limited to devices that are compatible with CDP. CDP runs on all media that support the Subnetwork Access Protocol (“SNAP”), including LAN and Frame Relay. CDP runs over the data link layer only. Each network device sends periodic messages to a multicast address and listens to the periodic messages sent by others in order to learn about neighboring devices and determine when their interfaces to the media go up or down. Each device also advertises at least one address at which it can receive SNMP messages. CDP messages, or “advertisements,” contain holdtime information, which indicates the period of time a receiving device should hold CDP information from a neighbor before discarding it. With CDP, network management applications can learn the device type and the SNMP-agent address of neighboring devices. This process enables applications to send SNMP queries to neighboring devices.” Slater ’421, 9:27-43.</p>

No.	'904 Patent Claim 7	The Reference
		<p>“It should be noted that-normally, CDP packets according to aspects of the present invention are transmitted at regular intervals (e.g. once every 60 seconds). However, in embodiments of the present invention, when a Hop Probe or Hop Probe Reply needs to be forwarded by a network device, the network device is commanded to send a CDP packet immediately.” Slater '421, 16:66-17:5.</p> <p>“The present invention is much faster than the previous method that involved logging in to each intermediate network device, entering the “show cdp neighbors” command, and interpreting the output to find the next hop along the path to the destination network device. Also, the present invention allows individual users, such as network administrators, to execute a tool to manually discover paths through a network of Ethernet switches. The present invention can be used by network management software to automatically map the topology of clusters of network devices, such as Ethernet switches. Finally, the present invention is useful in loop detection. Enhancements to Spanning Tree and other bridge-level routing protocols can test proposed changes to switch topology prior to making them.” Slater '421, 17:6-20.</p> <p>Petersen discloses:</p> <p>“Methods and apparatus for enabling communication between a source network device and one or more destination network devices are disclosed. A system enabling communication between a source network device and one or more destination network devices includes a switch and a ring interconnect. The switch is adapted for connecting to the source network device and the one or more destination network devices. More particularly, the switch is capable of storing data provided by the source network device and retrieving the data for the one or more destination network devices. The ring interconnect is adapted for connecting the source network device and the one or more destination network devices to one another. In addition, the ring interconnect is capable of passing one or more free slot symbols along the ring interconnect. Thus, the ring interconnect is capable of expanding when one or more of the free slot symbols are each replaced by a frame notify message indicating that the data has been stored by the switch for retrieval by the one or more destination network devices.” Petersen, Abstract.</p>

No.	'904 Patent Claim 7	The Reference
		<pre> sequenceDiagram participant SourceSat as Source Sat. participant Switch participant DestSat as Dest. Sat. Note left of SourceSat: Receive frame and break into cells Note right of SourceSat: 208 Note right of SourceSat: 202 Note right of Switch: 204 Note right of DestSat: 206 Note right of SourceSat: Send cells inc. data and header to switch (inc. retrieval count) Note right of SourceSat: 210 Note right of Switch: Switch stores cell data in Data Buffer and cell header in Buffer Table (inc. retrieval count) Note right of Switch: 212 Note right of DestSat: Copy and store the Notify msg. (inc. buffer #) in notify queue Note right of DestSat: 218 Note right of SourceSat: Send Frame Storage message indicating frame stored and location of frame Note right of SourceSat: 214 Note right of Switch: Send Frame Notify msg. (inc. buffer # in Data Buffer) on notify ring Note right of Switch: 216 Note right of SourceSat: - Decrement retrieval count Note right of SourceSat: - If retrieval count = 0, remove frame from storage upon completion of removal Note right of SourceSat: 224 Note right of SourceSat: 226 Note right of DestSat: Modify Frame Notify message to indicate whether able to accept and store the Frame Notify msg. Note right of DestSat: 220 Note right of SourceSat: Send Frame Retrieval msg. for buffer # at head of notify queue to retrieve frame Note right of SourceSat: 222 Note right of Switch: Send Frame cells to Dest. Sat. Note right of Switch: 228 Note right of SourceSat: Reconstruct frame for transmission Note right of SourceSat: 230 Note right of SourceSat: 232 Note right of SourceSat: Send modified Frame Notify message on notify ring Note right of SourceSat: 234 Note right of SourceSat: Removes Notify msg. from ring and discards Note right of SourceSat: 236 Note right of SourceSat: Send Buffer Table subtract msg. corr. to the modified frame Notify msg. (e.g., subtract # destinations unable to copy frame Notify msg. from retrieval count) Note right of SourceSat: 238 </pre>

FIG. 2

Petersen, FIG. 2.

No.	'904 Patent Claim 7	The Reference
		<p>“The present invention relates to a mixed topology data switching system that combines a radial interconnect with a ring interconnect. More particularly, the radial interconnect permits devices to store and retrieve data using a switch, while the ring interconnect permits devices along the ring interconnect to provide notification that data has been stored for retrieval, as well as provide feedback regarding the ability or inability to retrieve such data.” Petersen, 1:34-41.</p> <p>“In controlling the flow of network traffic through a switching system, it is often desirable to provide feedback to the source of the data. For instance, although a transmitting device, hereinafter referred to as a “source device,” may transmit or forward data to a receiving device, hereinafter referred to as a “destination device,” the destination device may be incapable of handling the data. In these circumstances, the source device is often unaware that the data was not accepted by the destination device, complicating switch management. Common solutions to the problem of switch traffic management have included ensuring that all intended destination devices are “ready to receive” prior to transmitting data on a ring or bus interconnect, or insisting that each intended destination device send an explicit acknowledgement back to the source device. Both of these approaches result in reduced efficiency of the interconnect scheme. By way of example, in a ring network, such acknowledgment is typically provided in the data frame being transmitted. As another example, in other interconnect schemes, each such device may send a separate acknowledgment, therefore adding to the traffic on the network. Accordingly, it would be desirable if a traffic management scheme were established which could provide such feedback to the source of the data while minimizing traffic management overhead.” Petersen, 2:8-32.</p> <p>“According to one embodiment, the present invention combines the use of two data transport methods: a point-to-point radial interconnect and a ring interconnect. The radial interconnect connects interface devices to each other through the services of a central switch device to permit the transport of data. Typically, a single interface has a single dedicated radial interconnect to the central switch. These interface devices are further connected to one another via a ring interconnect to convey retrieval notifications regarding forwarding of the data (by source devices) and receipt of the data (by destination devices).” Petersen, 2:36-46.</p>

No.	'904 Patent Claim 7	The Reference
		<p>“Each radial interconnect provides a narrow, high baud-rate connection to convey to the actual data from and to the associated interface without being burdened by the unrelated traffic for the remaining interfaces in the system. This is accomplished through the use of a central switch device, which stores and retrieves data for the various interfaces in the system. As will be apparent from the following description, this architecture provides numerous advantages over a wide parallel bus or ring.” Petersen, 2:47-55.</p> <p>“The ring interconnect may be used to convey a “retrieval notification”(i.e., retrieval message) that may be observed by all potential retrieving interfaces. The retrieval notification notifies specific devices (“destination devices”) or interfaces that one or more frames addressed to them are available from the switch device. Moreover, the ring interconnect permits each destination device to provide feedback to the source device letting the source know whether the destination has accepted the notification provided by the source device and therefore whether the destination can retrieve the data intended for it. The feedback is particularly useful in buffer management applications. In this manner, an efficient and flexible data transport and retrieval notification system that includes a feedback path to the source of the data is provided.” Petersen, 2:56-3:3.</p> <p>“FIG. 2 is a process flow diagram illustrating a method of providing network communication according to an embodiment of the invention.” Petersen, 3:9-11.</p> <p>“FIG. 2 is a process flow diagram illustrating in further detail a method of providing network communication in the above-described system according to an embodiment of the invention. As shown, process steps performed by a source device 202 are illustrated along an associated vertical line, steps performed by a switch 204 are illustrated along another vertical line, and steps performed by a destination device 206 are illustrated along still another vertical line.” Petersen, 4:50-57.</p> <p>“When the frame is stored by the switch 418, the source device preferably receives an acknowledgment that the data has been stored. Thus, to provide this feedback, a frame storage message (i.e., storage reply) is sent from the switch 418 on the channel 416 to the channel interface 414. The frame storage message is then provided to the notify ring interface as</p>

No.	'904 Patent Claim 7	The Reference
		<p>shown at 430 and sent on the notify ring. Once this acknowledgment is received by the interface device 402, the designated destination devices may be notified via notify ring interface 424. As described above, a Frame Notify message may be sent via the notify ring interface 424 to the destination devices. More particularly, the Frame Notify message may identify one or more destination devices for the frame and specify the location of the frame to be retrieved. By way of example, the location of the frame to be retrieved by the destination devices may be designated by a buffer number 430. In addition, the destination devices for the frame may be specified in the Frame Notify message through a notify queue map 426. More particularly, the notify queue map 426 may specify a notify queue associated with a particular destination device. The notify queue may be expressly designated through the use of one or more bits as well as implied through the specification of a priority level for the data. The notify queue map 426 will be described in further detail with reference to FIG. 13. The notify ring interface 424 then creates a Frame Notify message including the notify queue map 426 and the buffer number 430 which is then sent on an outbound interface of the notify ring 432.” Petersen, 7:55-8:16.</p> <p>“As described above, the notify ring may be expanded to accommodate communication between interface devices. The communication between the interface devices and the switch is therefore performed on one or more channels rather than the notify ring. As a result, the flexibility of the notify ring does not effect the speed with which the interface devices may communicate with the switch. Thus, where a single port operates at a faster speed than the channels, multiple channels may be grouped together. In this manner, the speed with which the switch may communicate with the interface devices may be maximized.” Petersen, 20:27-36.</p> <p>“The present invention provides a mixed topology data switching system that combines a point-to-point radial interconnect with a ring interconnect to maximize the speed of network traffic. The radial interconnect provides a narrow, high baud-rate connection to convey the data traffic for just the interface in question, without being burdened by all of the unrelated traffic for the remaining interfaces in the system. At the same time, the ring interconnect permits retrieval notifications to be observed by all potential retrieving interfaces. The ring topology further permits each destination interface to provide feedback to the source</p>

No.	'904 Patent Claim 7	The Reference
		interface, which is valuable for buffer management applications. Moreover, the point-to-point ring topology bus employs a variable latency access method that enables messages to be passed across the bus with low latency when the system is quiet and with increased latency when the system is busy. In addition, since control messaging around the ring interconnect and across the channel interconnects are embedded in the data stream, the number of pins required and manufacturing costs are reduced.” Petersen, 20:38-57.

No.	'904 Patent Claim 8	The Reference
8	Apparatus according to claim 7, wherein the downstream packets for the slave units in the portion of the daisy chain between the location of the fault and the second master unit are conveyed to the second master unit from the first master unit via another one of the daisy chains.	<p>The Reference discloses apparatus according to claim 7, wherein the downstream packets for the slave units in the portion of the daisy chain between the location of the fault and the second master unit are conveyed to the second master unit from the first master unit via another one of the daisy chains.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling and Sugawara.</p> <p>Below are examples of such references.</p> <p>Sugawara discloses:</p> <p>Sugawara, 1:26-45 (“FIG. 1 illustrates a conventional data link protecting system. A through D designate normal lines and P designates a backup line. LIUA through LIUD and LIUa through LIUd denote line interface circuits having functions of communicating with opposed devices, detecting the error rate of received signals, detecting abnormalities in main signals, such as signal interruption, and feeding intermediate repeaters. MUXA through MUXD and MUXa through MUXd designate multiplexing/demultiplexing circuits, and SW1 and SW2 denote switch circuits adapted to switch an abnormal line to the backup line. MPU1 and MPU2 denote supervisory and control units which control their associated devices, supervise</p>

No.	'904 Patent Claim 8	The Reference
		<p>the line interface circuits LIUs to switch the switch circuits SW as needed and perform data link communication with opposed devices. Note that the FIG. 1 illustrates only the arrangement for data transmission in one direction. The arrangement for data transmission in the opposite direction is the same as above.”).</p> <p style="text-align: center;">FIG. 1 PRIOR ART</p> <p style="text-align: center;">SUPervisory AND CONTROL UNIT</p> <p style="text-align: center;">LINE INTERFACE CIRCUIT</p> <p style="text-align: center;">MULTIPLEXING/DEMULITPLEXING CIRCUIT</p> <p style="text-align: center;">MULTIPLEXING/DEMULITPLEXING CIRCUIT</p>

FIG. 1 (annotated).

No.	'904 Patent Claim 8	The Reference
		<p>Sugawara, 3:24-39 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.").</p> <p>FIG. 2</p> <p>Master 1</p> <p>Master 2</p> <p>SUPERVISING AND CONTROL UNIT</p> <p>MPU 1</p> <p>MPU 2</p> <p>SWITCH CIRCUIT</p> <p>LINE INTERFACE CIRCUIT</p> <p>MUX A</p> <p>LIU A</p> <p>LIU a</p> <p>MUX a</p> <p>A</p> <p>MUX B</p> <p>LIU B</p> <p>LIU b</p> <p>MUX b</p> <p>B</p> <p>MUX C</p> <p>LIU C</p> <p>LIU c</p> <p>MUX c</p> <p>C</p> <p>MUX D</p> <p>LIU D</p> <p>LIU d</p> <p>MUX d</p> <p>D</p> <p>SW 1</p> <p>SW 2</p> <p>MULTIPLEXING/DEMULITPLEXING CIRCUIT</p> <p>MULTIPLEXING/DEMULITPLEXING CIRCUIT</p> <p>Plurality of daisy chains</p>

FIG. 2 (annotation added).

No.	'904 Patent Claim 9	The Reference
9[preamble]	Network access apparatus, comprising:	<p>The Reference discloses network access apparatus, comprising.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> at 1[preamble].</p>
9[a]	first and second master units, each comprising a physical interface to a packet-switched network;	<p>The Reference discloses first and second master units, each comprising a physical interface to a packet-switched network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> at 1[a].</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orkit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626

No.	'904 Patent Claim 9	The Reference
		<ul style="list-style-type: none"> • Duuvury '820 <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 – Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry’s most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don’t have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS Software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 9	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p><u>Duvvury '626 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 9	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Duvvury '626, 12:18-30.</p> <p>"FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number "10.0.0.0." Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number "10.0.0.0." Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is "00-e0-1e-01-02-03," then the generated CMIP address will be "10.01.02.03." At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other." Duvvury '626, 16:11-31.</p>

No.	'904 Patent Claim 9	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 9	The Reference
		<pre> graph TD START((START)) -- 1800 --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END((END)) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 -- 1810 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 -- 1820 --> Q2{CONFLICT?} Q2 -- NO --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 -- 1850 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 -- 1860 --> Q4{CONFLICT?} Q4 -- NO --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 -- 1885 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 -- 1890 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> S1[SIGNAL AN ERROR CONDITION] S1 -- 1900 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 9	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 9	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 9	The Reference
		<p>The diagram illustrates a memory array structure. At the top, a horizontal bus is labeled 100. Below it, a series of memory blocks are labeled 102-A through 102-H. Each memory block has a rectangular body with a row of small squares along its bottom edge, representing memory cells. The blocks are arranged vertically, with 102-A at the top and 102-H at the bottom. A curved arrow labeled 110 points from the right side towards the top bus 100.</p>

FIG. 9

Slater '796, FIG. 9.

No.	'904 Patent Claim 9	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

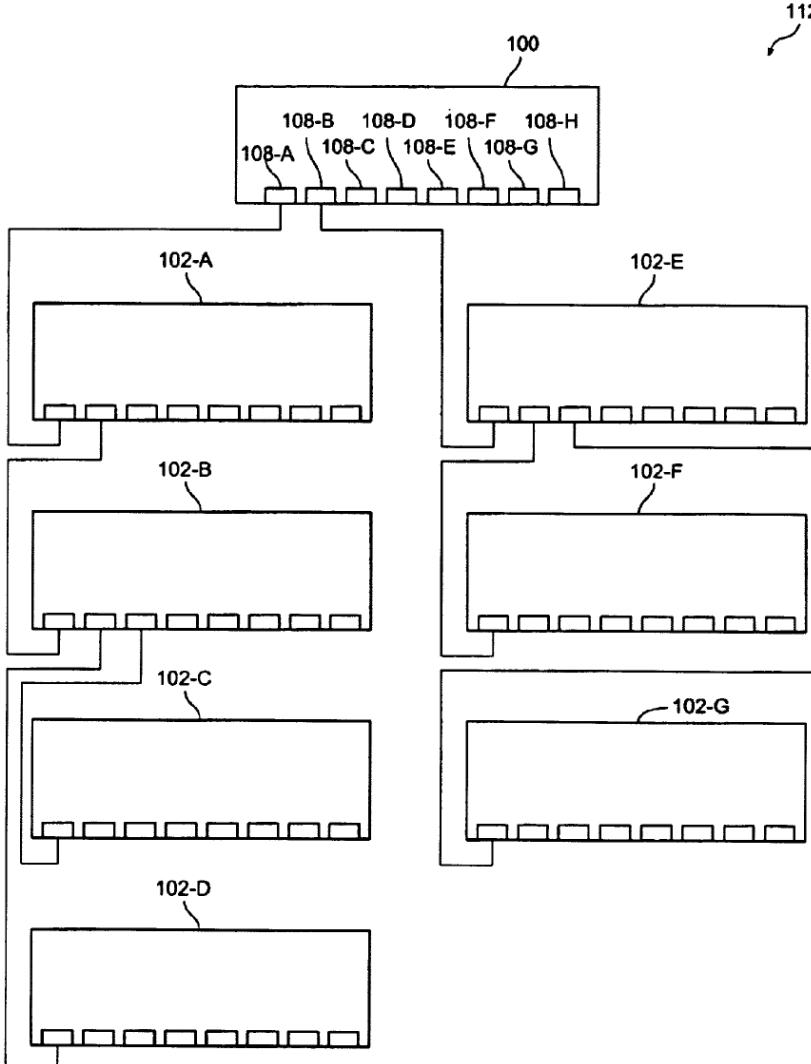
No.	'904 Patent Claim 9	The Reference
		 <p>The diagram illustrates a multi-layered printed circuit board (PCB) structure. At the top, a rectangular layer labeled 100 contains eight pads labeled 108-A through 108-H. Below this is a layer labeled 102-A, which has a series of vias along its bottom edge. A second layer labeled 102-B is positioned below 102-A, also featuring a row of vias at its base. Layer 102-C is situated beneath 102-B, with its own row of vias. Layer 102-D is located below 102-C. To the right of the main structure, two additional layers are shown: 102-E and 102-F, both with rows of vias. A curved arrow labeled 112 points from the right side towards the top layer 100.</p>

FIG. 10

Slater '796, FIG. 10.

No.	'904 Patent Claim 9	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 9	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
9[b]	a plurality of slave units,	<p>The Reference discloses a plurality of slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> at 1[b].</p> <p>Cisco already patented "master" and slave" switch technology and commercialized it before Orkit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater '796

No.	'904 Patent Claim 9	The Reference
		<ul style="list-style-type: none"> • Slater '433 • Duvvury '626 • Duuvury '820 <p>Cisco Catalyst Press Release discloses:</p> <p>“May 24, 1999 – Cisco Systems, Inc. today announced the new Catalyst 3500 Series XL, the industry’s most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to</p>

No.	'904 Patent Claim 9	The Reference
		<p>network managers that now don't have to manage each individual switch as an independent entity.” Cisco Catalyst Press Release, 2.</p> <p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. Software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 9	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p><u>Duvvury '626 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 9	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Duvvury '626, 12:18-30.</p> <p>"FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number "10.0.0.0." Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number "10.0.0.0." Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is "00-e0-1e-01-02-03," then the generated CMIP address will be "10.01.02.03." At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other." Duvvury '626, 16:11-31.</p>

No.	'904 Patent Claim 9	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 9	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 9	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p> <p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented</p>

No.	'904 Patent Claim 9	The Reference
		<p>in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater '796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater '796, 11:1-10.</p>

No.	'904 Patent Claim 9	The Reference
		<p>FIG. 9</p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 9	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 9	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
9[c]	each slave unit comprising one or more ports to respective subscriber lines; and	<p>The Reference discloses each slave unit comprising one or more ports to respective subscriber lines.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra at 1[c].</i></p>

No.	'904 Patent Claim 9	The Reference
9[d]	<p>a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween,</p>	<p>The Reference discloses a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling and Sugawara.</p> <p><i>See supra</i> at 1[d].</p> <p>Cisco continues to make innovative contributions to the area of redundant stacked switch technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <p>Below are examples of such references.</p> <p><u>Sugawara discloses:</u></p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p>

No.	'904 Patent Claim 9	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link</p>

No.	'904 Patent Claim 9	The Reference
		<p>communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>
9[e]	<p>each daisy chain comprising at least a first slave unit connected by one of the physical interface lines to the first master unit and a last slave unit connected by another of the physical interface lines to the second master unit, wherein each of the first and second master units comprises:</p>	<p>The Reference discloses each daisy chain comprising at least a first slave unit connected by one of the physical interface lines to the first master unit and a last slave unit connected by another of the physical interface lines to the second master unit, wherein each of the first and second master units comprises.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> at 1[e].</p> <p>Below are examples of such references.</p>

No.	'904 Patent Claim 9	The Reference
		<p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p> <p>FIG. 2</p> <p>Master 1</p> <p>Master 2</p> <p>SUPERVISORY AND CONTROL UNIT</p> <p>MPU 1</p> <p>MPU 2</p> <p>SWITCH CIRCUIT</p> <p>MUX A LIU A A LIU a MUX a</p> <p>MUX B LIU B B LIU b MUX b</p> <p>MUX C LIU C C LIU c MUX c</p> <p>MUX D LIU D D LIU d MUX d</p> <p>LINE INTERFACE CIRCUIT</p> <p>LINE INTERFACE CIRCUIT</p> <p>LINE INTERFACE CIRCUIT</p> <p>LINE INTERFACE CIRCUIT</p> <p>SW 1</p> <p>MULTIPLEXING/DEMULITPLEXING CIRCUIT</p> <p>SW 2</p> <p>MULTIPLEXING/DEMULITPLEXING CIRCUIT</p> <p>Plurality of daisy chains</p> <p>FIG. 2 (annotation added)</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1</p>

No.	'904 Patent Claim 9	The Reference
		<p>not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>
9[f]	a switch, configured to route data packets between the respective physical interface and the one or more daisy chains, and	<p>The Reference discloses a switch, configured to route data packets between the respective physical interface and the one or more daisy chains.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 9	The Reference
9[g]	<p>a pre-switch, which in the event of a fault at a location in one of the daisy chains, re-routes at least a portion of the data packets exchanged with one or more of the slaves in the daisy chain in which the fault has occurred through another one of the daisy chains.</p>	<p>The Reference discloses a pre-switch, which in the event of a fault at a location in one of the daisy chains, re-routes at least a portion of the data packets exchanged with one or more of the slaves in the daisy chain in which the fault has occurred through another one of the daisy chains.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco commercialized and patented technology relating to monitoring, detecting, and resolving faults without requiring a network reconfiguration <i>before</i> Orkit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Daruwalla • Nederveen • Slater '421 • Petersen <p>Below are examples of such references.</p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p>

No.	'904 Patent Claim 9	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotation added)</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.)</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link</p>

No.	'904 Patent Claim 9	The Reference
		<p>communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p> <p><u>Daruwalla discloses:</u></p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. The present invention provides a protection protocol which simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol will appropriately remove a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, would remove protection for a low priority failure, such as a signal degrade, on another link.” Daruwalla, Abstract.</p>

No.	'904 Patent Claim 9	The Reference
		<p style="text-align: center;"><u>100</u></p> <p>Daruwalla, FIG. 1.</p>

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		<p style="text-align: center;"><u>200</u></p>

Daruwalla, FIG. 2.

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		<p>Daruwalla, FIG. 11.</p> <p>"The present invention relates to computer networks. In particular, the present invention relates to a system and method for providing a protection protocol for fault recovery for a two line bi-directional ring network." Daruwalla, 1:8-11.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“FIG. 1 shows an example of a two line bi-directional ring network. The ring network 100 is shown to include nodes 102 a-102 g. Each node is typically a computer with embedded processors and at least one network connection. Each node 102 a-102 g is shown to be bidirectionally coupled to two neighboring nodes 102 a-102 g via an inner connection ring 110 a-110 g and an outer connection ring 108 a-108 g. For instance, node 102 a is bidirectionally coupled to nodes 102 b and 102 g. The example of FIG. 1 also shows a problem 104 in the connection between node 102 b and node 102 c. When a problem is detected (such as a bi-directional line cut), the connection between nodes 102 b and 102 d wraps back upon itself, as shown by wraps 106 a and 106 b. In this manner, the connection problem 104 can be avoided.” Daruwalla, 1:17-30.</p> <p>“In a conventional SONET network, each message sent by a sending node to a receiving node typically needs the identification and location of the receiving node to arrive at the proper destination. Accordingly, manual configuration is typically needed in each node to store the identity and location of each other node in the ring network in order to provide for communication between the nodes in the network.” Daruwalla, 1:31-44.</p> <p>“In summary, for the protection mechanism to operate, each node needs to know the current ring map (current ring topology). What is needed is a system and method for providing fault recovery for two line bi-directional ring network that minimizes the need to keep track of other nodes in the ring network. Preferably, the system would not require reconfiguration of an internal map of the network when a new node is added to, or existing nodes are removed from the network. The present invention addresses such a need.” Daruwalla, 2:23-31.</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. According to the embodiment, when the problem is identified, the node sends a message identifying the problem to a second neighbor which is located at least one node away from the problem. The second neighbor then forwards the message to a third neighbor, unless the second neighbor is dealing with a situation that is higher in a hierarchy of situations than the problem described</p>

No.	'904 Patent Claim 9	The Reference
		<p>in the message by the original node. In general, if the second neighbor's situation has a higher priority than the situation described by the original node, then the message is ignored and not forwarded. If, however, the message sent by the original node describes a situation with a higher priority than the situation being dealt with by the second neighbor, then, in general, the second neighbor's situation is ignored, at least for the moment, and the original node's message is forwarded to the next neighbor. In general, a higher priority request preempts a lower priority request within the ring. Exceptions are noted as rules of the protection protocol." Daruwalla, 2:35-58.</p> <p>"The present invention provides a protection protocol that simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol automatically appropriately removes a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, removes protection for a low priority failure, such as a signal degrade, on another link." Daruwalla, 2:59-3:3.</p> <p>"A method according to an embodiment of the present invention for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and initiating a fault recovery procedure when the second node receives the first message." Daruwalla, 3:4-12.</p> <p>"In another aspect of an embodiment of the present invention, a method for adding a new node to a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node prior to an addition of the new node; initiating a fault recovery procedure when the second node receives the first message; receiving a second</p>

No.	'904 Patent Claim 9	The Reference
		<p>message from the new node; and entering an idle state when the second message is received.” Daruwalla, 3:13-24.</p> <p>“In yet another aspect of an embodiment of the present invention, a system for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The system comprises means for detecting a situation by a first node, wherein the first node is one of the plurality of nodes; means for sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and means for initiating a fault recovery procedure when the second node receives the first message.” Daruwalla, 3:25-35.</p> <p>“FIG. 2 is block diagram of a ring network utilizing a protection protocol according to an embodiment of the present invention.” Daruwalla, 3:40-42.</p> <p>“FIGS. 4-6 are flow diagrams illustrating various rules within the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:46-48.</p> <p>“FIGS. 8-12 are flow diagrams and a system diagram illustrating further rules of the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:52-54.</p> <p>“FIG. 2 is a block diagram showing a ring network system utilizing a method of fault recovery according to an embodiment of the present invention. The ring network 200 is shown to include nodes 202 a-202 g. The nodes 202 a-202 g are shown to be coupled via an inner ring 210 in which the data flows in one direction, such as a clockwise direction. Additionally, the nodes 202 a-202 g are also shown to be coupled by an outer ring 212 in which data can flow in the opposite direction to the inner ring 210, such as in a counter-clockwise direction. The ring network 200 is shown to have a situation 204 a that requires protection, such as a ring wrap 206.” Daruwalla, 5:35-45.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“FIG. 4 is a flow diagram of an example of a method according to an embodiment of the present invention implied by Rules 1-22. An APS packet is received via step 400. It is determined whether the APS packet has been sent along a long path via step 402. If the packet was not sent via a long path, then the APS packet is not forwarded via step 406. Accordingly, if the APS packet was sent via the short path, then the packet is not forwarded via step 406. If, however, the packet was sent through the long path via step 402, then the APS packet may be forwarded via step 404. Note that for this example of Rule (1), it is assumed that the long path message does not have to pass through a wrapped connection in order to be forwarded. Otherwise, if the long path message needs to pass through a wrapped connection in order to be forwarded, then the message will simply not be forwarded.” Daruwalla, 6:21-36.</p> <p>“FIG. 6 is a flow diagram illustrating Rules 4 and 5. A node detects a problem between the node and a first neighbor via step 600. The node performs a wrap away from the side on which the problem exists via step 602. A short path message is then sent to the first neighbor informing it of the problem via step 604. Additionally, a long path message is also sent to a second neighbor informing the second neighbor of the problem via step 604. The first neighbor then performs a wrap away from the side of the problem via step 606. The first neighbor also sends an IDLE message, indicating a wrapped status, on a short path to the node that detected the problem via step 608. This message is sent across the failed span. Note that IDLE messages do not get wrapped and are sent across failed spans if possible. Additionally, the first neighbor also sends a message on a long path toward the side without the problem via step 608.” Daruwalla, 6:64-11.</p> <p>“An example of the method described in FIG. 6 can be seen in FIG. 2. Node 202 b has detected a problem 204 a and performs a wrap 206 on the side on which the problem exists. Node 202 b also sends a short path message to the neighbor on the other side of the problem 204 a, which is node 202 c. Node 202 b also sends a long path message to its other neighbor node 202 a informing it of the problem. Node 202 c performs a wrap 206 on the side of the problem and sends an IDLE message on a short path to node 202 b. Node 202 c also sent a message on a long path toward the side without the problem to its neighbor 202 d.” Daruwalla, 7:12-21.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“FIG. 7 lists the hierarchy of priorities of Rule (8). For ease of reference, the hierarchy is separated into Class I-III. Class I is the highest priority, while Class III is the lowest priority. An example of a highest priority message in Class I is lockout. Lockout is an order stating that the ring network is not to wrap under any circumstances.” Daruwalla, 7:22-26.</p> <p>“Examples of the next priority listed in Class II are forced switch and signal fail. Forced switch indicates that the ring network is configured to wrap at the point of the forced switch. Signal fail is a situation where either two nodes cannot communicate with each other, or one node cannot hear the other node. An example of a signal fail is a physical break in the communication lines between two nodes.” Daruwalla, 7:27-33.</p> <p>“Note that members of Class II can co-exist (Rule 9). For example, multiple forced switches and signal fails can co-exist. Additionally, members of Class I can co-exist (Rule 10). For example, multiple lockouts in a single ring network can co-exist. However, situations in Class III cannot co-exist with other situations (Rule 11). For example, a signal degrade cannot co-exist with a wait-to-restore.” Daruwalla, 7:52-58.</p> <p>“When there are multiple requests of the same priority within Class III, the first request to complete a long path signaling will take priority (Rule 13). For example, if there are two signal degrades located on the same ring network, then the first signal degrade which completes the long path signaling will take priority over the other signal degrade. By not allowing members of Class III to co-exist with one another, partitioning of the ring network is avoided.” Daruwalla, 7:59-65.</p> <p>“In case of two equal requests within Class III on both inner and outer rings of the ring network, the tie is broken by choosing a request on one of the rings, such as the outer ring request (Rule 14). For example, if a signal degrade occurs both on the inner and outer rings, then a request on a predetermined ring, such as the outer ring, will take priority over the other requests.” Daruwalla, 7:66-8:5.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“FIG. 8 is a flow diagram illustrating Rules (9), (10), (11), (13), and (15). Note that the flow diagram described in FIG. 8 is merely an example of one way in which the rules of the method according to the embodiment of the present invention can be executed. For example, the determination of whether the long path message is a Class I request via step 802 or a Class II request via step 810 can be in reverse order.” Daruwalla, 8:6-11.</p> <p>“A wrapped node receives a long path message via step 800. It is then determined if the long path message is a Class I request via step 802. The classes used in FIG. 8 are meant to correspond with the example of classes defined in FIG. 7. If the long path message is a Class I request, then it is determined if a local situation also has a Class I request via step 804. A local situation includes scenarios such as when a node detects a situation or problem, or when a node is made aware of a problem or situation via a short path message from its neighbor. If a local situation is not a Class I request via step 804, then any existing local wraps are unwrapped and the long path message is forwarded via step 806. If, however, a local situation is a Class I request via step 804, then the connections are already unwrapped or was never wrapped, and the long path message is forwarded via step 808.” Daruwalla, 8:12-26.</p> <p>“FIG. 12 is a flow diagram illustrating rules (20) and (21) of the method according to the embodiment of the present invention. A wrapped node determines that a problem has been cleared via step 1200. It then enters a wait-to-restore state via step 1202. It is then determined if its neighbor is the same neighbor as previously noted via step 1204. The node can save the source of a short path message at the time of wrap initiation to note the identity of its neighbor. If the current neighbor is not the same as the previous neighbor via step 1204, then an IDLE state is entered via step 1206. If, however, the current neighbor is the same as the previous neighbor via step 1204, then it is determined whether a pre-determined wait-to-restore time has expired via step 1208. Once the pre-determined wait-to-restore time has expired, then the node enters an IDLE state via step 1206.” Daruwalla, 12:60-13:6.</p> <p>“A method and system for fault recovery for a two line bi-directional network has been disclosed. Software written according to the present invention may be stored in some form of computer-readable medium, such as memory or CD-ROM, or transmitted over a network, and executed by a processor.” Daruwalla, 13:7-19.</p>

No.	'904 Patent Claim 9	The Reference
		<p>Nederveen discloses:</p> <p>“A technique for use in gathering network activity-related information from cascaded network switches is provided. Using this technique, the information can be gathered without substantially reducing performance of the cascaded switches. In one embodiment, a single remote monitoring probe is connected via respective connections to each of the switches so as to receive the information from the switches. In another embodiment, only one of the switches is connected to the probe, and the other switches transmit their respective portions of the information to the switch connected to probe. The switch connected to the probe provides these portions of the information, as well as, any of its respective activity-related information to the probe. In this latter embodiment, the switches may be connected by dedicated connections and switch ports that are used solely for communicating the activity-related information.” Nederveen, Abstract.</p>

No.	'904 Patent Claim 9	The Reference
		<p>The diagram illustrates a network architecture. On the left, three segments (A, B, and C) are shown, each containing a stack of components: BP (~500), BL (~600), and P (~310). Segment A also contains component R (~306). Segments B and C also contain component C (~312). Segments B and C also contain component P (~310). Segments B and C also contain component BP (~502) and BL (~600B).</p> <p>Segment A has a connection labeled "TO SEGMENT (228)" at the bottom.</p> <p>Segment B has a connection labeled "TO SEGMENT (224)" at the top and "TO SEGMENT (232)" at the bottom.</p> <p>Segment C has a connection labeled "TO SEGMENT (226)" at the top and "TO SEGMENT (240)" at the bottom.</p> <p>Segment A has a connection labeled "TO ROUTER (250)" at the top, which is connected to component R (~306).</p> <p>Segments B and C have connections labeled "300A", "300B", and "300C" respectively, which are connected to a dashed line labeled "700".</p> <p>Segment C has a connection labeled "222" at the bottom, which is connected to a dashed line labeled "704".</p> <p>Segment B has a connection labeled "702" at the bottom, which is connected to a dashed line labeled "700".</p> <p>Segment C has a connection labeled "704" at the bottom, which is connected to a dashed line labeled "700".</p> <p>Segment A has a connection labeled "226" at the top, which is connected to a dashed line labeled "700".</p> <p>Segment B has a connection labeled "224" at the top, which is connected to a dashed line labeled "700".</p> <p>Segment C has a connection labeled "228" at the top, which is connected to a dashed line labeled "700".</p> <p>A dashed line labeled "700" connects the three segments to a "MUX" block. The "MUX" block is connected to an "RMON PROBE" block, which then connects to a "MANAGEMENT PROCESS (408)".</p>

FIG. 3

Nederveen, FIG. 3.

No.	'904 Patent Claim 9	The Reference
		<p>The diagram illustrates a stacked switch monitoring architecture. Three switches, labeled A, B, and C, are connected in a vertical stack. Each switch contains a base processor (BP), a buffer layer (BL), and a transceiver (Tx/Rx) module. - Switch A (bottom) has a BP (~504), BL (~600B), and Tx/Rx modules. It connects to the segment via line 222 and to the stack via line 224. - Switch B (middle) has a BP (~502), BL (~600A), and Tx/Rx modules. It connects to the segment via line 224 and to the stack via line 226. - Switch C (top) has a BP (~500), BL (~600A), and Tx/Rx modules. It connects to the segment via line 226 and to the stack via line 228. - The stack connection between switches B and C is labeled line 300B. The connection between switches A and B is labeled line 300A. The connection between switches C and A is labeled line 300C. - The stack also connects to a 'TO ROUTER' port (250) via line 230 and to an 'RMON PROBE' (402) via line 900. - The 'RMON PROBE' (402) also connects to a 'MANAGEMENT PROCESS' (408).</p> <p style="text-align: center;">FIG. 5</p> <p>Nederveen, FIG. 5.</p> <p>“Thus, it would be desirable to provide a stacked switch monitoring technique that permits efficient offloading of raw data processing from the stacked switches, requires only a minimal number of specialized network entities to gather and process such raw data, and does not result in substantial degradation of stacked switch performance.” Nederveen, 4:38-43.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“Accordingly, the present invention provides a technique for remote monitoring of a switch network that overcomes the aforesaid and other disadvantages and drawbacks of the prior art. More specifically, in one aspect of the present invention, a technique is provided for gathering information that may be useful in network management (e.g., switch port activity-related information), from switches in the network that are in a stacked configuration. The information is gathered from the stacked switches by a single network entity (e.g., an SNMP remote monitoring probe) in such a way that it does not substantially degrade the performance of the switches. This is accomplished, in one embodiment of the technique of the present invention, by connecting the switches via respective connections to a multiplexer that selectively connects the switches, according to an arbitration scheme, to the single network entity. The entity gathers respective portions of the information from switches when it is connected to the switches by the multiplexer. The information gathered by the entity may be provided to another network entity (e.g., an SNMP management node) in order to permit the other entity to use that information in managing the network.” Nederveen, 4:46-67.</p> <p>“In another embodiment of the technique of the present invention, only one of the switches is connected to the single information gathering entity. The switches that are not connected to the entity transmit, via respective dedicated ports and connections (i.e., ports and connections that are used solely for network information gathering activities), their respective portions of the information to the switch that is connected to the entity. The switch that is connected to the entity transmits, via a respective dedicated port and connection, the information received from the other switches, as well as, its own information to the entity.” Nederveen, 5:1-11.</p> <p>“FIG. 3 is a schematic, functional block diagram illustrating in greater detail the construction of the stacked switch network shown in FIG. 2.” Nederveen, 5:26-28.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network configured to employ another embodiment of the present invention.” Nederveen, 5:32-34.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“FIGS. 2-5 illustrate features of a computer network 200 wherein embodiments of the present invention may be advantageously practiced. Network 200 comprises a stacked switch network 300 which interconnects a plurality of network segments 228, 232, 240, and 251. Each segment 228, 232, 240 comprises one or more local area networks having computer endstations (not shown). Segment 251 is a network router segment that comprises network router 250. Each segment 228, 232, 240 is coupled via a respective communications link 222, 224, 226 to a respective port 302 (i.e., port A), 304 (i.e., port B), 312 (i.e., port C) of the switch network 300. Likewise, the router 250 of router segment 251 is coupled via a respective trunk line 230 to router port 306 (i.e., port R).” Nederveen, 5:46-59.</p> <p>“Stacked switch network 300 comprises a plurality of data network switches 300A, 300B, 300C (e.g., Catalyst 3900™ series switches of the type commercially available from the Assignee of the subject application) coupled together via conventional stack link bus connection logic 600A, 600B. More specifically, logic 600A couples a stack link bus port and associated logic 500 in switch 300A to a stack link bus link port and associated logic 502 in switch 300B. Similarly, logic 600B couples another stack link bus port and associated logic 501 in switch 300B to a stack link bus port and associated logic 504 in switch 300C. It should be understood that although, as is shown in FIG. 3, switches 300A and 300B, and switches 300B and 300C, may be coupled serially together by separate respective logic elements 600A, 600B, each of the switches 300A, 300B, 300C may be coupled together via a single respective stack link bus port in the switch to a single stack link bus connection logic block (not shown, e.g., of the type that is commercially available under the tradename Catalyst Matrix™ from the Assignee of the subject application). Further alternatively, depending upon the particular design and functionality of the ports 500, 501, 502, and 504, and the control and forwarding logic (whose operation will be described more fully below) in the switches 300A, 300B, 300C, the circuitry in logic 600A, 600B may instead be comprised in the ports 500, 501, 502, and 504 and/or control and forwarding logic, and therefore, in this alternative configuration, the logic 600A, 600B in the network 300 may be replaced by simple connection means (e.g., cable connectors).” Nederveen, 6:29-57.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“Each switch 300A, 300B, 300C includes a respective internal bus (e.g., element 800 in switch 300C) that is coupled via at least one stack link bus port and associated interface logic (e.g., 504 in switch 300C) to external stack link bus connection logic (e.g., element 600B in switch 300C). Each switch 300A, 300B, 300C also includes respective programmable control and forwarding logic (e.g., element 802 in switch 300C) comprising processing, memory, and other circuitry for storing and learning configuration information (e.g., source and destination MAC addresses of messages received by the switch, switch bridging table, switch segments' spanning tree and virtual local area network information, etc.), and for providing appropriate commands to other elements (e.g., the switch ports) to cause data messages received by the switch to be forwarded to appropriate network segments coupled to the switch based upon this configuration information. In each switch, the switch's port logic circuitry (e.g., port A logic 302 and port P logic 310C in switch 300C) and control and forwarding logic are coupled to each other via that switch's respective internal bus. The stack link bus port and associated logic in each switch 300A, 300B, 300C may comprise a Catalyst™ stack port line interface card (commercially available from the Assignee of the subject application) inserted into a bus expansion slot (not shown) in the switch. Although not shown in the Figures for purposes of clarity of illustration, each switch 300A, 300B, 300C in network 300 typically will include tens or hundreds of ports coupled to network segments.” Nederveen, 6:58-7:19.</p> <p>“The control and forwarding logic and stack link bus port and associated logic in each switch, and the logic 600A, 600B, are configured to together implement conventional techniques for permitting the switches 300A, 300B, 300C to function together as a single logical/virtual switch. More specifically, when configured in the stacked arrangement 300, after the switches 300A, 300B, 300C and logic 600A, 600B are initially activated, they execute initial power-on self-diagnostics, and thereafter, enter a “stack discovery” mode of operation.” Nederveen, 7:20-29.</p> <p>“In the stack discovery mode of operation, the control and forwarding logic in each switch 300A, 300B, 300C first “senses” that its switch is coupled to logic 600A and/or 600B, and then determines the particular configuration of the stacked switch network 300, using suitable conventional autosensing/autoconfiguration techniques. The control and 161forwarding logic in the switches 300A, 300B, 300C then assigns to the switches respective unique</p>

No.	'904 Patent Claim 9	The Reference
		<p>identification numbers (e.g., based upon unique identification numbers of respective ports of the logic 600A, 600B to which the switches are coupled).” Nederveen, 7:30-40.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network 300’ configured to employ another embodiment of the present invention. It should be understood that unless specifically stated to the contrary, the structure and operation of the network 300’ are substantially the same as the structure and operation of network 300. In network 300’, each of the dedicated ports 310A, 310B, 310C comprises a respective transmit portion and receive portion, referenced in FIG. 5 as RX and TX, respectively.” Nederveen, 11:20-29.</p> <p><u>Slater '421 discloses:</u></p> <p>“A method and apparatus for discovering paths to other network devices includes a protocol and network management application that can be executed on network devices. The Ethernet protocol is used to detect paths to other network devices, knowing only the Ethernet address of the destination. A discovery protocol is extended to add hop probe and hop probe reply Type-Length-Value fields in a variable-length list. The hop probe fields contain a hop count, a destination Ethernet address, and a source Ethernet address. When a hop probe is received by a network device, the hop count field is decremented by one and the hop probe is forwarded. Packets received with a hop count of one are not forwarded and a hop probe reply is sent back to the Ethernet source address of the hop probe. The hop probe reply fields contain a destination Ethernet address and a source Ethernet address.” Slater '421, Abstract.</p>

No.	'904 Patent Claim 9	The Reference
		<p>FIG. 6</p> <p>Slater '421, FIG. 6.</p> <p>"Partly as a result of the increased complexity of networks, network administrators must often troubleshoot problems with their network. Two classes of network problems often faced by network administrators are "reachability" problems and performance slowdowns. Reachability problems occur when one or more network devices cannot be accessed through a network, and can be caused by hardware or software failures, cabling problems, or any of several other types of difficult-to-diagnose problems that can occur in a network." Slater '421, 7:11-20.</p> <p>"Some of the world's largest networks today rely on the TCP/IP suite of networking protocols. With a relatively small kit of basic tools, network administrators can learn much about an internetwork. "Ping" and "traceroute" commands, "show" commands, and "debug" commands (all of which are typically available via the basic management interface on a network device) form the core of the network administrator's internetwork toolkit. Ping and</p>

No.	'904 Patent Claim 9	The Reference
		<p>traceroute commands can be useful tools in determining where failures are occurring, but they are cumbersome to use, and require knowledge of the IP address or host name of the destination network device. The show commands provide information about interface conditions, protocol status, neighbor reachability, router configuration and status, level of traffic, errors and drops, and other network data. Finally, debug commands provide clues about the status of a network device and other network devices directly or indirectly connected to it. Because debug commands can create performance slowdowns, they must be used with great care, and using the wrong debug command at the wrong time can exacerbate problems in already poorly performing networks.” Slater ’421, 7:55-8:8.</p> <p>“Embodiments of the present invention as illustrated herein use the Cisco™ Discovery Protocol (“CDP”) to automatically detect paths to specified network devices in Ethernet LANs. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task.” Slater ’421, 9:10-15.</p> <p>“CDP is a media-independent device discovery protocol which can be used by a network administrator to view information about other network devices directly attached to a particular network device. In addition, network management applications can retrieve the device type and SNMP-agent address of neighboring network devices. This enables applications to send SNMP queries to neighboring devices. CDP thus allows network management applications to discover devices that are neighbors of already known devices, such as neighbors running lower-layer, transparent protocols.” Slater ’421, 9:16-26.</p> <p>“It is to be understood that the present invention is not limited to devices that are compatible with CDP. CDP runs on all media that support the Subnetwork Access Protocol (“SNAP”), including LAN and Frame Relay. CDP runs over the data link layer only. Each network device sends periodic messages to a multicast address and listens to the periodic messages sent by others in order to learn about neighboring devices and determine when their interfaces to the media go up or down. Each device also advertises at least one address at which it can receive SNMP messages. CDP messages, or “advertisements,” contain holdtime information, which indicates the period of time a receiving device should hold CDP information from a neighbor before discarding it. With CDP, network management applications can learn the</p>

No.	'904 Patent Claim 9	The Reference
		<p>device type and the SNMP-agent address of neighboring devices. This process enables applications to send SNMP queries to neighboring devices.” Slater ’421, 9:27-43.</p> <p>“It should be noted that—normally, CDP packets according to aspects of the present invention are transmitted at regular intervals (e.g. once every 60 seconds). However, in embodiments of the present invention, when a Hop Probe or Hop Probe Reply needs to be forwarded by a network device, the network device is commanded to send a CDP packet immediately.” Slater ’421, 16:66-17:5.</p> <p>“The present invention is much faster than the previous method that involved logging in to each intermediate network device, entering the “show cdp neighbors” command, and interpreting the output to find the next hop along the path to the destination network device. Also, the present invention allows individual users, such as network administrators, to execute a tool to manually discover paths through a network of Ethernet switches. The present invention can be used by network management software to automatically map the topology of clusters of network devices, such as Ethernet switches. Finally, the present invention is useful in loop detection. Enhancements to Spanning Tree and other bridge-level routing protocols can test proposed changes to switch topology prior to making them.” Slater ’421, 17:6-20.</p> <p>Petersen discloses:</p> <p>“Methods and apparatus for enabling communication between a source network device and one or more destination network devices are disclosed. A system enabling communication between a source network device and one or more destination network devices includes a switch and a ring interconnect. The switch is adapted for connecting to the source network device and the one or more destination network devices. More particularly, the switch is capable of storing data provided by the source network device and retrieving the data for the one or more destination network devices. The ring interconnect is adapted for connecting the source network device and the one or more destination network devices to one another. In addition, the ring interconnect is capable of passing one or more free slot symbols along the ring interconnect. Thus, the ring interconnect is capable of expanding when one or more of the free slot symbols are each replaced by a frame notify message indicating that the data has</p>

No.	'904 Patent Claim 9	The Reference
		<p>been stored by the switch for retrieval by the one or more destination network devices.”</p> <p>Petersen, Abstract.</p> <pre> sequenceDiagram participant SourceSat as Source Sat. participant Switch participant DestSat as Dest. Sat. SourceSat->>Switch: Receive frame and break into cells activate SourceSat Note over SourceSat: 208 Note over SourceSat: -> 202: Source Sat. SourceSat->>Switch: Send cells inc. data and header to switch (inc. retrieval count) activate Switch Note over Switch: 210 Note over Switch: -> 204: Switch Switch->>DestSat: Switch stores cell data in Data Buffer and cell header in Buffer Table (inc. retrieval count) activate DestSat Note over DestSat: 212 Note over DestSat: -> 206: Dest. Sat. DestSat->>Switch: Send Frame Storage message indicating frame stored and location of frame activate Switch Note over Switch: 214 Note over Switch: -> 204: Switch DestSat->>Switch: Send Frame Notify msg. (inc. buffer # in Data Buffer) activate Switch Note over Switch: 216 Note over Switch: -> 204: Switch Switch->>DestSat: Copy and store the Notify msg. (inc. buffer #) in notify queue activate DestSat Note over DestSat: 218 Note over DestSat: -> 206: Dest. Sat. DestSat->>Switch: Modify Frame Notify message to indicate whether able to accept and store the Frame activate Switch Note over Switch: 220 Note over Switch: -> 204: Switch DestSat->>Switch: Notify msg. 1 activate Switch Note over Switch: 222 Note over Switch: -> 204: Switch DestSat->>Switch: Send Frame Retrieval msg. for buffer # at head of notify queue to retrieve frame activate Switch Note over Switch: 224 Note over Switch: -> 204: Switch DestSat->>Switch: Decrement retrieval count activate Switch Note over Switch: 226 Note over Switch: -> 204: Switch DestSat->>Switch: If retrieval count = 0, remove frame from storage upon completion of removal activate Switch Note over Switch: 228 Note over Switch: -> 204: Switch DestSat->>Switch: Send Frame cells to Dest. Sat. activate Switch Note over Switch: 230 Note over Switch: -> 204: Switch DestSat->>Switch: Reconstruct frame for transmission activate Switch Note over Switch: 232 Note over Switch: -> 204: Switch DestSat->>Switch: Send modified Frame Notify message on notify ring activate Switch Note over Switch: 234 Note over Switch: -> 204: Switch DestSat->>Switch: Removes Notify msg. from ring and discards activate Switch Note over Switch: 236 Note over Switch: -> 204: Switch DestSat->>Switch: Send Buffer Table subtract msg. corr. to the modified frame Notify msg. (e.g., subtract # destinations unable to copy frame Notify msg. from retrieval count) activate Switch Note over Switch: 238 Note over Switch: -> 204: Switch </pre>

FIG. 2

Petersen, FIG. 2.

No.	'904 Patent Claim 9	The Reference
		<p>“The present invention relates to a mixed topology data switching system that combines a radial interconnect with a ring interconnect. More particularly, the radial interconnect permits devices to store and retrieve data using a switch, while the ring interconnect permits devices along the ring interconnect to provide notification that data has been stored for retrieval, as well as provide feedback regarding the ability or inability to retrieve such data.” Petersen, 1:34-41.</p> <p>“In controlling the flow of network traffic through a switching system, it is often desirable to provide feedback to the source of the data. For instance, although a transmitting device, hereinafter referred to as a “source device,” may transmit or forward data to a receiving device, hereinafter referred to as a “destination device,” the destination device may be incapable of handling the data. In these circumstances, the source device is often unaware that the data was not accepted by the destination device, complicating switch management. Common solutions to the problem of switch traffic management have included ensuring that all intended destination devices are “ready to receive” prior to transmitting data on a ring or bus interconnect, or insisting that each intended destination device send an explicit acknowledgement back to the source device. Both of these approaches result in reduced efficiency of the interconnect scheme. By way of example, in a ring network, such acknowledgment is typically provided in the data frame being transmitted. As another example, in other interconnect schemes, each such device may send a separate acknowledgment, therefore adding to the traffic on the network. Accordingly, it would be desirable if a traffic management scheme were established which could provide such feedback to the source of the data while minimizing traffic management overhead.” Petersen, 2:8-32.</p> <p>“According to one embodiment, the present invention combines the use of two data transport methods: a point-to-point radial interconnect and a ring interconnect. The radial interconnect connects interface devices to each other through the services of a central switch device to permit the transport of data. Typically, a single interface has a single dedicated radial interconnect to the central switch. These interface devices are further connected to one another via a ring interconnect to convey retrieval notifications regarding forwarding of the data (by source devices) and receipt of the data (by destination devices).” Petersen, 2:36-46.</p>

No.	'904 Patent Claim 9	The Reference
		<p>“Each radial interconnect provides a narrow, high baud-rate connection to convey to the actual data from and to the associated interface without being burdened by the unrelated traffic for the remaining interfaces in the system. This is accomplished through the use of a central switch device, which stores and retrieves data for the various interfaces in the system. As will be apparent from the following description, this architecture provides numerous advantages over a wide parallel bus or ring.” Petersen, 2:47-55.</p> <p>“The ring interconnect may be used to convey a “retrieval notification”(i.e., retrieval message) that may be observed by all potential retrieving interfaces. The retrieval notification notifies specific devices (“destination devices”) or interfaces that one or more frames addressed to them are available from the switch device. Moreover, the ring interconnect permits each destination device to provide feedback to the source device letting the source know whether the destination has accepted the notification provided by the source device and therefore whether the destination can retrieve the data intended for it. The feedback is particularly useful in buffer management applications. In this manner, an efficient and flexible data transport and retrieval notification system that includes a feedback path to the source of the data is provided.” Petersen, 2:56-3:3.</p> <p>“FIG. 2 is a process flow diagram illustrating a method of providing network communication according to an embodiment of the invention.” Petersen, 3:9-11.</p> <p>“FIG. 2 is a process flow diagram illustrating in further detail a method of providing network communication in the above-described system according to an embodiment of the invention. As shown, process steps performed by a source device 202 are illustrated along an associated vertical line, steps performed by a switch 204 are illustrated along another vertical line, and steps performed by a destination device 206 are illustrated along still another vertical line.” Petersen, 4:50-57.</p> <p>“When the frame is stored by the switch 418, the source device preferably receives an acknowledgment that the data has been stored. Thus, to provide this feedback, a frame storage message (i.e., storage reply) is sent from the switch 418 on the channel 416 to the channel interface 414. The frame storage message is then provided to the notify ring interface as</p>

No.	'904 Patent Claim 9	The Reference
		<p>shown at 430 and sent on the notify ring. Once this acknowledgment is received by the interface device 402, the designated destination devices may be notified via notify ring interface 424. As described above, a Frame Notify message may be sent via the notify ring interface 424 to the destination devices. More particularly, the Frame Notify message may identify one or more destination devices for the frame and specify the location of the frame to be retrieved. By way of example, the location of the frame to be retrieved by the destination devices may be designated by a buffer number 430. In addition, the destination devices for the frame may be specified in the Frame Notify message through a notify queue map 426. More particularly, the notify queue map 426 may specify a notify queue associated with a particular destination device. The notify queue may be expressly designated through the use of one or more bits as well as implied through the specification of a priority level for the data. The notify queue map 426 will be described in further detail with reference to FIG. 13. The notify ring interface 424 then creates a Frame Notify message including the notify queue map 426 and the buffer number 430 which is then sent on an outbound interface of the notify ring 432.” Petersen, 7:55-8:16.</p> <p>“As described above, the notify ring may be expanded to accommodate communication between interface devices. The communication between the interface devices and the switch is therefore performed on one or more channels rather than the notify ring. As a result, the flexibility of the notify ring does not effect the speed with which the interface devices may communicate with the switch. Thus, where a single port operates at a faster speed than the channels, multiple channels may be grouped together. In this manner, the speed with which the switch may communicate with the interface devices may be maximized.” Petersen, 20:27-36.</p> <p>“The present invention provides a mixed topology data switching system that combines a point-to-point radial interconnect with a ring interconnect to maximize the speed of network traffic. The radial interconnect provides a narrow, high baud-rate connection to convey the data traffic for just the interface in question, without being burdened by all of the unrelated traffic for the remaining interfaces in the system. At the same time, the ring interconnect permits retrieval notifications to be observed by all potential retrieving interfaces. The ring topology further permits each destination interface to provide feedback to the source</p>

No.	'904 Patent Claim 9	The Reference
		interface, which is valuable for buffer management applications. Moreover, the point-to-point ring topology bus employs a variable latency access method that enables messages to be passed across the bus with low latency when the system is quiet and with increased latency when the system is busy. In addition, since control messaging around the ring interconnect and across the channel interconnects are embedded in the data stream, the number of pins required and manufacturing costs are reduced.” Petersen, 20:38-57.

No.	'904 Patent Claim 10	The Reference
10	Apparatus according to claim 9, wherein the pre-switch re-routes the data packets such that substantially no reconfiguration of the switch is required responsive to the fault.	<p>The Reference discloses apparatus according to claim 9, wherein the pre-switch re-routes the data packets such that substantially no reconfiguration of the switch is required responsive to the fault.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 11	The Reference
11[preamble]	Network access apparatus, comprising:	<p>The Reference discloses network access apparatus, comprising:</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 11	The Reference
11[a]	<p>first and second master units, each comprising a physical interface to a packet-switched network;</p>	<p>The Reference discloses first and second master units, each comprising a physical interface to a packet-switched network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orkit. Some examples of Cisco’s patents (and other documents) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626 • Duuvury ’820 <p>Cisco Catalyst Press Release discloses:</p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don’t have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p> <p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst</p>

No.	'904 Patent Claim 11	The Reference
		<p>3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p> <p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address.</p>

No.	'904 Patent Claim 11	The Reference
		<p>Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury ’626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury ’626, 16:11-31.</p>

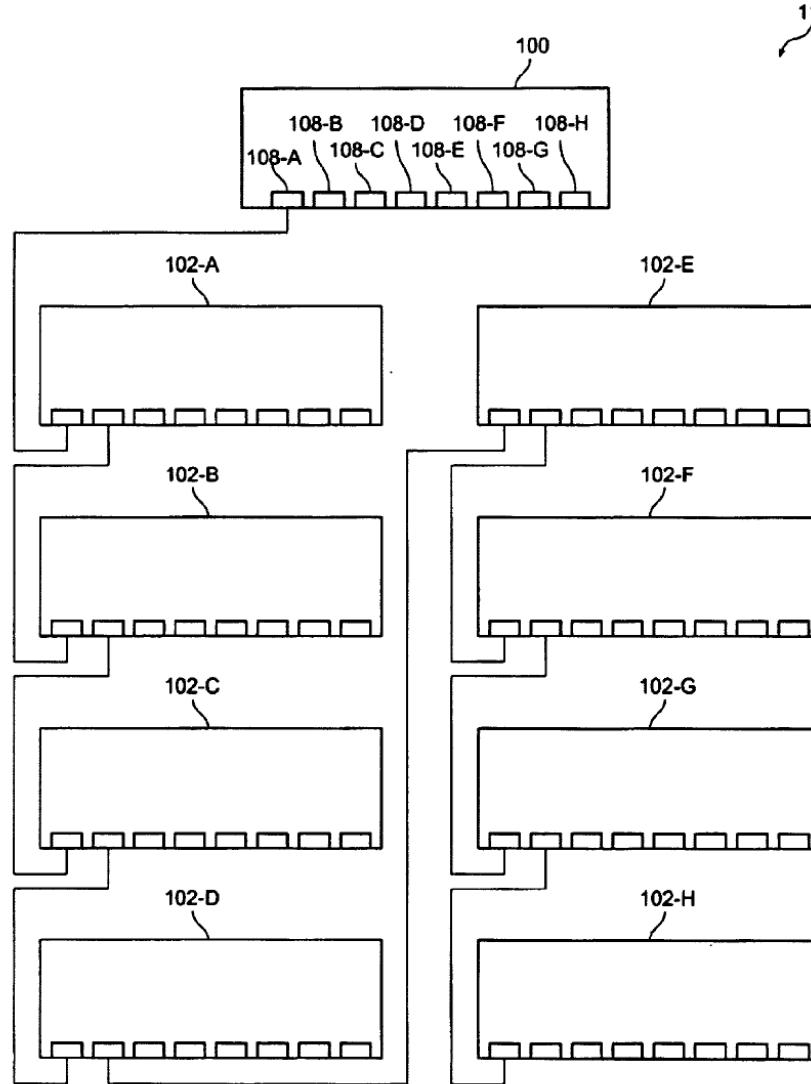
No.	'904 Patent Claim 11	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 11	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 11	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a</p>

No.	'904 Patent Claim 11	The Reference
		<p>graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 11	The Reference
		 <p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 11	The Reference
		<p><i>FIG. 10</i></p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater ’796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater ’796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater ’796, 12:21-34.</p>

No.	'904 Patent Claim 11	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
11[b]	a plurality of slave units,	<p>The Reference discloses a plurality of slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco already patented "master" and slave" switch technology and commercialized it before Orckit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater '796 • Slater '433

No.	'904 Patent Claim 11	The Reference
		<ul style="list-style-type: none"> • Duvvury '626 • Duuvury '820 <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don't have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 11	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury '626, 16:11-31.</p>

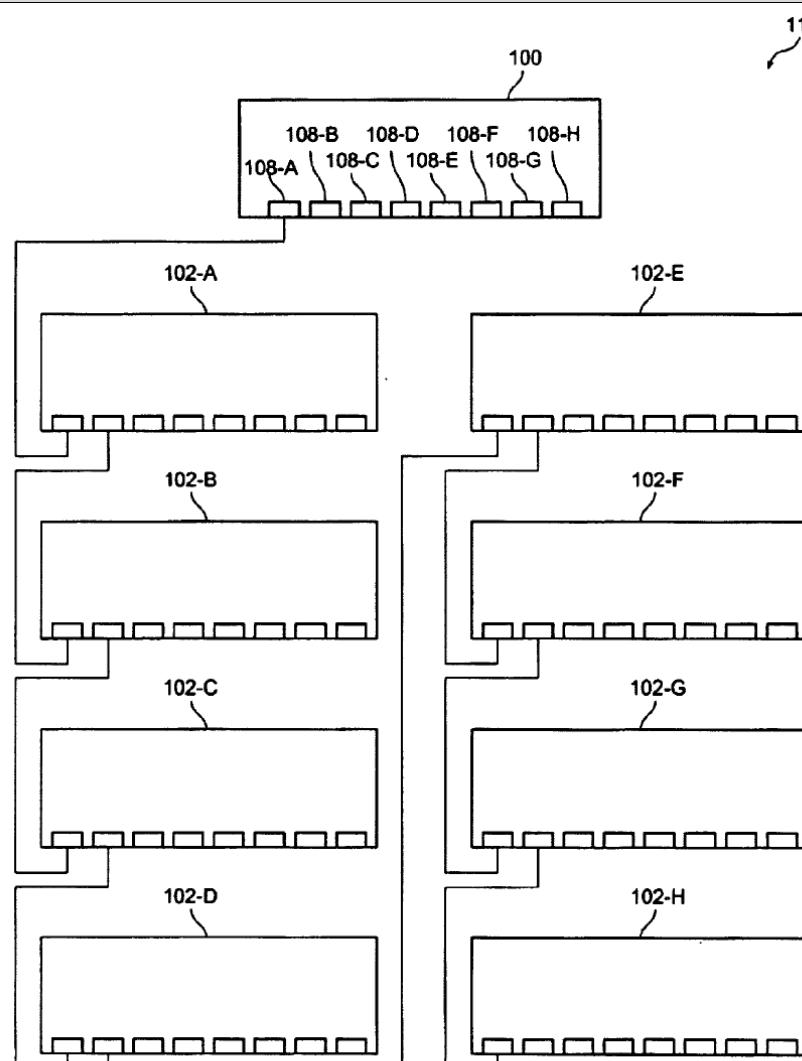
No.	'904 Patent Claim 11	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 11	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 11	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 11	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 11	The Reference
		 <p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 11	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 11	The Reference
		<p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Slater ’796, 12:44-56.</p> <p>“One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections.” Slater ’796, 13:8-14.</p>
11[c]	each slave unit comprising one or more ports to respective subscriber lines, and	<p>The Reference discloses each slave unit comprising one or more ports to respective subscriber lines.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 11	The Reference
11[d]	<p>a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween,</p>	<p>The Reference discloses a plurality of physical interface lines, which link the slave units in one or more daisy chains, in which the slave units are mutually connected in series by the physical interface lines therebetween.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling and Sugawara.</p> <p><i>See supra</i> at 1[d].</p> <p>Cisco continues to make innovative contributions to the area of redundant stacked switch technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <p>Below are examples of such references.</p> <p><u>Sugawara discloses:</u></p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p>

No.	'904 Patent Claim 11	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right; color: red;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.")</p>

No.	'904 Patent Claim 11	The Reference
		<p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>
11[e]	each daisy chain comprising at least a first slave unit connected by one of the physical interface lines to the first master unit and a last slave unit connected by another of the physical interface lines to the second master unit,	<p>The Reference discloses each daisy chain comprising at least a first slave unit connected by one of the physical interface lines to the first master unit and a last slave unit connected by another of the physical interface lines to the second master unit.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Below are examples of such references.</p>

No.	'904 Patent Claim 11	The Reference
		<p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p> <p>FIG. 2</p> <p>Plurality of daisy chains</p> <p>FIG. 2 (annotation added)</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1</p>

No.	'904 Patent Claim 11	The Reference
		<p>not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>

No.	'904 Patent Claim 11	The Reference
11[f]	<p>wherein each of the slave units comprises a switch fabric comprising one or more switches, which convey data packets to respective ports on the switch to which the packets are addressed; and</p>	<p>The Reference discloses wherein each of the slave units comprises a switch fabric comprising one or more switches, which convey data packets to respective ports on the switch to which the packets are addressed.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>
11[g]	<p>a pre-switch, which receives the data packets from one of the physical interface lines connected to the slave unit and passes those of the data packets that are addressed to any of the ports on the slave unit to the switch fabric, while passing packets not addressed to any of the ports on the slave unit for output through another of the physical interface lines.</p>	<p>The Reference discloses a pre-switch, which receives the data packets from one of the physical interface lines connected to the slave unit and passes those of the data packets that are addressed to any of the ports on the slave unit to the switch fabric, while passing packets not addressed to any of the ports on the slave unit for output through another of the physical interface lines.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 12	The Reference
12	<p>Apparatus according to claim 11, wherein each of the slave units is coupled to receive packets transferred thereto from the first and second master units over first and second ones of the physical interface lines, respectively, and wherein the pre-switch passes the packets received through the first and second physical interface line and addressed to any of the ports on the slave unit to respective first and second addresses in the switch fabric.</p>	<p>The Reference discloses apparatus according to claim 11, wherein each of the slave units is coupled to receive packets transferred thereto from the first and second master units over first and second ones of the physical interface lines, respectively, and wherein the pre-switch passes the packets received through the first and second physical interface line and addressed to any of the ports on the slave unit to respective first and second addresses in the switch fabric.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See also supra at 11[f].</i></p>

No.	'904 Patent Claim 13	The Reference
13	Apparatus according to claim 12, wherein in response to a reversal of a direction of data flow in the daisy chain, the first and second addresses are swapped in the pre-switch, so that substantially no reconfiguration of the switch fabric is required.	<p>The Reference discloses apparatus according to claim 12, wherein in response to a reversal of a direction of data flow in the daisy chain, the first and second addresses are swapped in the pre-switch, so that substantially no reconfiguration of the switch fabric is required.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 14	The Reference
14[preamble]	In a network access multiplexing system,	<p>The Reference discloses in a network access multiplexing system.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p>
14[a]	in which a master unit is connected by a physical interface to a packet switched network,	<p>The Reference discloses in which a master unit is connected by a physical interface to a packet switched network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL</p>

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		<p>Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orcket. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626 • Duuvury ’820 <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p>

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		<p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don’t have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p> <p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p>

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		<p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p>

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		<p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p>

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		<p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p> <p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then,</p>

No.	'904 Patent Claim 14	The Reference
		<p>at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury '626, 16:11-31.</p> <pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, “modulo 256,” such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899.” Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 14	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 14	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p> <p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented</p>

No.	'904 Patent Claim 14	The Reference
		<p>in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p>

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		<p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater '796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater '796, 11:1-10.</p>

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		<p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

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		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 14	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

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		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
14[b]	slave unit configured to be coupled to the master unit in a daisy chain of such slave units, the slave unit comprising:	<p>The Reference discloses slave unit configured to be coupled to the master unit in a daisy chain of such slave units, the slave unit comprising.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p> <p>Cisco already patented "master" and "slave" switch technology and commercialized it before Orkit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater '796 • Slater '433

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		<ul style="list-style-type: none"> • Duvvury '626 • Duuvury '820 <p>Below are examples of such references.</p> <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to</p>

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		<p>network managers that now don't have to manage each individual switch as an independent entity.” Cisco Catalyst Press Release, 2.</p> <p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

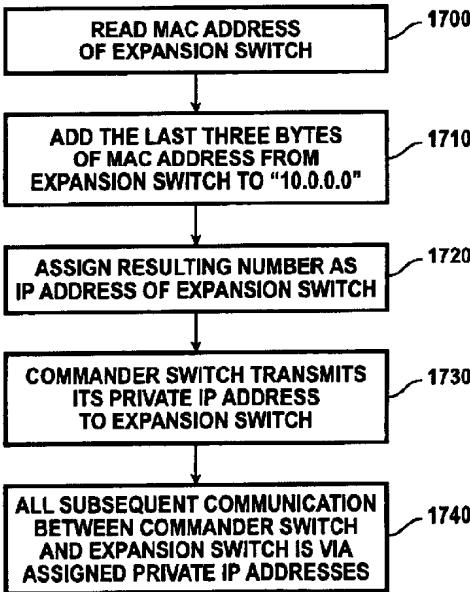
No.	'904 Patent Claim 14	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

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		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p>

No.	'904 Patent Claim 14	The Reference
		<p style="text-align: center;">FIG. 2</p> <p>The diagram illustrates a supervisory and control unit (labeled 'SUPERVISING AND CONTROL UNIT') connected to two master units, Master 1 and Master 2. Master 1 is connected to MPU 1, and Master 2 is connected to MPU 2. Four daisy chains (A, B, C, D) are shown, each consisting of a multiplexing/demultiplexing circuit (MPU), a line interface circuit (LIU), and a switch circuit (SW). The connections are as follows:</p> <ul style="list-style-type: none"> Chain A: MPU 1 - LIU A - SW 1 - MUX a - LIU a - SW 2 - MPU 2 Chain B: MPU 1 - LIU B - SW 1 - MUX b - LIU b - SW 2 - MPU 2 Chain C: MPU 1 - LIU C - SW 1 - MUX c - LIU c - SW 2 - MPU 2 Chain D: MPU 1 - LIU D - SW 1 - MUX d - LIU d - SW 2 - MPU 2 <p>A red line highlights the 'Plurality of daisy chains'.</p> <p style="text-align: center;">FIG. 2 (annotation added)</p> <p>Sugawara discloses:</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.").</p>

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		<p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and</p>

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		<p>correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury ’626, Abstract.</p> <p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury ’626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury ’626, 16:11-31.</p>

No.	'904 Patent Claim 14	The Reference
		 <pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 14	The Reference
		<pre> graph TD START([1800 START]) --> Q1{1805 IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> Q2{1840 IS 3RD BYTE COUNTER < 255?} Q1 -- YES --> I1[1810 INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[1820 ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q3{1830 CONFLICT?} Q3 -- NO --> Q2 Q3 -- YES --> Q4{1880 IS 4TH BYTE COUNTER < 255?} Q2 -- NO --> I2[1850 INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[1860 ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q5{1870 CONFLICT?} Q5 -- NO --> Q4 Q5 -- YES --> Q6{1880 IS 4TH BYTE COUNTER < 255?} Q4 -- NO --> I3[1885 INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[1890 ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q7{1895 CONFLICT?} Q7 -- NO --> END([1899 END]) Q7 -- YES --> Q8{1900 SIGNAL AN ERROR CONDITION} </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 14	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 14	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 14	The Reference
		<p>FIG. 9 Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 14	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 14	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
14[c]	a plurality of ports, for coupling the slave unit to respective subscriber lines;	<p>The Reference discloses a plurality of ports, for coupling the slave unit to respective subscriber lines.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p>
14[d]	first and second physical interfaces, coupled to exchange packets with preceding and succeeding units, respectively, along the daisy chain;	<p>The Reference discloses first and second physical interfaces, coupled to exchange packets with preceding and succeeding units, respectively, along the daisy chain.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

No.	'904 Patent Claim 14	The Reference
		<p>skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p> <p>Cisco continues to make innovative contributions to the area of redundant stacked switch technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <p>Below are examples of such references.</p> <p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p>

No.	'904 Patent Claim 14	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link</p>

No.	'904 Patent Claim 14	The Reference
		<p>communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>
14[e]	a pre-switch, coupled to receive packets from the first physical interface and responsive to address data carried by the packets, to sort the packets such that packets addressed to the slave unit are retained, and packets addressed to the succeeding units are passed to the second physical interface, and	<p>The Reference discloses a pre-switch, coupled to receive packets from the first physical interface and responsive to address data carried by the packets, to sort the packets such that packets addressed to the slave unit are retained, and packets addressed to the succeeding units are passed to the second physical interface.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p> <p>Cisco commercialized and patented technology relating to monitoring, detecting, and resolving faults without requiring a network reconfiguration <i>before</i> Orckit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p>

No.	'904 Patent Claim 14	The Reference
		<ul style="list-style-type: none"> • Daruwalla • Nederveen • Slater '421 • Petersen <p>Daruwalla discloses:</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. The present invention provides a protection protocol which simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol will appropriately remove a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, would remove protection for a low priority failure, such as a signal degrade, on another link.” Daruwalla, Abstract.</p>

No.	'904 Patent Claim 14	The Reference
		<p style="text-align: center;"><u>100</u></p> <p>Daruwalla, FIG. 1.</p>

No.	'904 Patent Claim 14	The Reference
		<p>The diagram illustrates a communication system architecture. At the center is a component labeled 210. Surrounding it are seven rectangular boxes labeled A, B, C, D, E, F, and G, each connected to 210 via curved arrows. Box A is connected to 210 by two curved arrows, one from above and one from below. Box B is connected to 210 by two curved arrows, one from the left and one from the right. Box C is connected to 210 by two curved arrows, one from the top-left and one from the top-right. Box D is connected to 210 by two curved arrows, one from the right and one from the bottom-right. Box E is connected to 210 by two curved arrows, one from the bottom-right and one from the bottom-left. Box F is connected to 210 by two curved arrows, one from the bottom-left and one from the left. Box G is connected to 210 by two curved arrows, one from the left and one from the bottom-left. Each box A through G is also connected to various other components via straight arrows:</p> <ul style="list-style-type: none"> Box A: Connected to 202a (top-left), 202b (left), and 204b (bottom). Box B: Connected to 202b (top-left), 202a (left), and 204a (top-left). Box C: Connected to 202b (top-right), 204a (top-left), and 206 (top). Box D: Connected to 202d (right), 206 (right), and 202e (right). Box E: Connected to 202e (right), 202f (right), and 202g (bottom). Box F: Connected to 202f (right), 202g (bottom), and 204b (bottom). Box G: Connected to 202g (bottom), 204b (bottom), and 204a (top-right). <p>Additional components shown include 200 at the bottom, 202a-g, 204a-b, 206, 212, and 202d. Arrows indicate signal flow between these components, often labeled with triplets such as (A, idle, S), (B, SF, S), (C, SF, L), etc.</p>

Daruwalla, FIG. 2.

No.	'904 Patent Claim 14	The Reference
		<p>Daruwalla, FIG. 11.</p> <p>"The present invention relates to computer networks. In particular, the present invention relates to a system and method for providing a protection protocol for fault recovery for a two line bi-directional ring network." Daruwalla, 1:8-11.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“FIG. 1 shows an example of a two line bi-directional ring network. The ring network 100 is shown to include nodes 102 a-102 g. Each node is typically a computer with embedded processors and at least one network connection. Each node 102 a-102 g is shown to be bidirectionally coupled to two neighboring nodes 102 a-102 g via an inner connection ring 110 a-110 g and an outer connection ring 108 a-108 g. For instance, node 102 a is bidirectionally coupled to nodes 102 b and 102 g. The example of FIG. 1 also shows a problem 104 in the connection between node 102 b and node 102 c. When a problem is detected (such as a bi-directional line cut), the connection between nodes 102 b and 102 d wraps back upon itself, as shown by wraps 106 a and 106 b. In this manner, the connection problem 104 can be avoided.” Daruwalla, 1:17-30.</p> <p>“In a conventional SONET network, each message sent by a sending node to a receiving node typically needs the identification and location of the receiving node to arrive at the proper destination. Accordingly, manual configuration is typically needed in each node to store the identity and location of each other node in the ring network in order to provide for communication between the nodes in the network.” Daruwalla, 1:31-44.</p> <p>“In summary, for the protection mechanism to operate, each node needs to know the current ring map (current ring topology). What is needed is a system and method for providing fault recovery for two line bi-directional ring network that minimizes the need to keep track of other nodes in the ring network. Preferably, the system would not require reconfiguration of an internal map of the network when a new node is added to, or existing nodes are removed from the network. The present invention addresses such a need.” Daruwalla, 2:23-31.</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. According to the embodiment, when the problem is identified, the node sends a message identifying the problem to a second neighbor which is located at least one node away from the problem. The second neighbor then forwards the message to a third neighbor, unless the second neighbor is dealing with a situation that is higher in a hierarchy of situations than the problem described</p>

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		<p>in the message by the original node. In general, if the second neighbor's situation has a higher priority than the situation described by the original node, then the message is ignored and not forwarded. If, however, the message sent by the original node describes a situation with a higher priority than the situation being dealt with by the second neighbor, then, in general, the second neighbor's situation is ignored, at least for the moment, and the original node's message is forwarded to the next neighbor. In general, a higher priority request preempts a lower priority request within the ring. Exceptions are noted as rules of the protection protocol." Daruwalla, 2:35-58.</p> <p>"The present invention provides a protection protocol that simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol automatically appropriately removes a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, removes protection for a low priority failure, such as a signal degrade, on another link." Daruwalla, 2:59-3:3.</p> <p>"A method according to an embodiment of the present invention for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and initiating a fault recovery procedure when the second node receives the first message." Daruwalla, 3:4-12.</p> <p>"In another aspect of an embodiment of the present invention, a method for adding a new node to a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node prior to an addition of the new node; initiating a fault recovery procedure when the second node receives the first message; receiving a second</p>

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		<p>message from the new node; and entering an idle state when the second message is received.” Daruwalla, 3:13-24.</p> <p>“In yet another aspect of an embodiment of the present invention, a system for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The system comprises means for detecting a situation by a first node, wherein the first node is one of the plurality of nodes; means for sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and means for initiating a fault recovery procedure when the second node receives the first message.” Daruwalla, 3:25-35.</p> <p>“FIG. 2 is block diagram of a ring network utilizing a protection protocol according to an embodiment of the present invention.” Daruwalla, 3:40-42.</p> <p>“FIGS. 4-6 are flow diagrams illustrating various rules within the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:46-48.</p> <p>“FIGS. 8-12 are flow diagrams and a system diagram illustrating further rules of the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:52-54.</p> <p>“FIG. 2 is a block diagram showing a ring network system utilizing a method of fault recovery according to an embodiment of the present invention. The ring network 200 is shown to include nodes 202 a-202 g. The nodes 202 a-202 g are shown to be coupled via an inner ring 210 in which the data flows in one direction, such as a clockwise direction. Additionally, the nodes 202 a-202 g are also shown to be coupled by an outer ring 212 in which data can flow in the opposite direction to the inner ring 210, such as in a counter-clockwise direction. The ring network 200 is shown to have a situation 204 a that requires protection, such as a ring wrap 206.” Daruwalla, 5:35-45.</p>

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		<p>“FIG. 4 is a flow diagram of an example of a method according to an embodiment of the present invention implied by Rules 1-22. An APS packet is received via step 400. It is determined whether the APS packet has been sent along a long path via step 402. If the packet was not sent via a long path, then the APS packet is not forwarded via step 406. Accordingly, if the APS packet was sent via the short path, then the packet is not forwarded via step 406. If, however, the packet was sent through the long path via step 402, then the APS packet may be forwarded via step 404. Note that for this example of Rule (1), it is assumed that the long path message does not have to pass through a wrapped connection in order to be forwarded. Otherwise, if the long path message needs to pass through a wrapped connection in order to be forwarded, then the message will simply not be forwarded.” Daruwalla, 6:21-36.</p> <p>“FIG. 6 is a flow diagram illustrating Rules 4 and 5. A node detects a problem between the node and a first neighbor via step 600. The node performs a wrap away from the side on which the problem exists via step 602. A short path message is then sent to the first neighbor informing it of the problem via step 604. Additionally, a long path message is also sent to a second neighbor informing the second neighbor of the problem via step 604. The first neighbor then performs a wrap away from the side of the problem via step 606. The first neighbor also sends an IDLE message, indicating a wrapped status, on a short path to the node that detected the problem via step 608. This message is sent across the failed span. Note that IDLE messages do not get wrapped and are sent across failed spans if possible. Additionally, the first neighbor also sends a message on a long path toward the side without the problem via step 608.” Daruwalla, 6:64-11.</p> <p>“An example of the method described in FIG. 6 can be seen in FIG. 2. Node 202 b has detected a problem 204 a and performs a wrap 206 on the side on which the problem exists. Node 202 b also sends a short path message to the neighbor on the other side of the problem 204 a, which is node 202 c. Node 202 b also sends a long path message to its other neighbor node 202 a informing it of the problem. Node 202 c performs a wrap 206 on the side of the problem and sends an IDLE message on a short path to node 202 b. Node 202 c also sent a message on a long path toward the side without the problem to its neighbor 202 d.” Daruwalla, 7:12-21.</p>

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		<p>“FIG. 7 lists the hierarchy of priorities of Rule (8). For ease of reference, the hierarchy is separated into Class I-III. Class I is the highest priority, while Class III is the lowest priority. An example of a highest priority message in Class I is lockout. Lockout is an order stating that the ring network is not to wrap under any circumstances.” Daruwalla, 7:22-26.</p> <p>“Examples of the next priority listed in Class II are forced switch and signal fail. Forced switch indicates that the ring network is configured to wrap at the point of the forced switch. Signal fail is a situation where either two nodes cannot communicate with each other, or one node cannot hear the other node. An example of a signal fail is a physical break in the communication lines between two nodes.” Daruwalla, 7:27-33.</p> <p>“Note that members of Class II can co-exist (Rule 9). For example, multiple forced switches and signal fails can co-exist. Additionally, members of Class I can co-exist (Rule 10). For example, multiple lockouts in a single ring network can co-exist. However, situations in Class III cannot co-exist with other situations (Rule 11). For example, a signal degrade cannot co-exist with a wait-to-restore.” Daruwalla, 7:52-58.</p> <p>“When there are multiple requests of the same priority within Class III, the first request to complete a long path signaling will take priority (Rule 13). For example, if there are two signal degrades located on the same ring network, then the first signal degrade which completes the long path signaling will take priority over the other signal degrade. By not allowing members of Class III to co-exist with one another, partitioning of the ring network is avoided.” Daruwalla, 7:59-65.</p> <p>“In case of two equal requests within Class III on both inner and outer rings of the ring network, the tie is broken by choosing a request on one of the rings, such as the outer ring request (Rule 14). For example, if a signal degrade occurs both on the inner and outer rings, then a request on a predetermined ring, such as the outer ring, will take priority over the other requests.” Daruwalla, 7:66-8:5.</p>

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		<p>“FIG. 8 is a flow diagram illustrating Rules (9), (10), (11), (13), and (15). Note that the flow diagram described in FIG. 8 is merely an example of one way in which the rules of the method according to the embodiment of the present invention can be executed. For example, the determination of whether the long path message is a Class I request via step 802 or a Class II request via step 810 can be in reverse order.” Daruwalla, 8:6-11.</p> <p>“A wrapped node receives a long path message via step 800. It is then determined if the long path message is a Class I request via step 802. The classes used in FIG. 8 are meant to correspond with the example of classes defined in FIG. 7. If the long path message is a Class I request, then it is determined if a local situation also has a Class I request via step 804. A local situation includes scenarios such as when a node detects a situation or problem, or when a node is made aware of a problem or situation via a short path message from its neighbor. If a local situation is not a Class I request via step 804, then any existing local wraps are unwrapped and the long path message is forwarded via step 806. If, however, a local situation is a Class I request via step 804, then the connections are already unwrapped or was never wrapped, and the long path message is forwarded via step 808.” Daruwalla, 8:12-26.</p> <p>“FIG. 12 is a flow diagram illustrating rules (20) and (21) of the method according to the embodiment of the present invention. A wrapped node determines that a problem has been cleared via step 1200. It then enters a wait-to-restore state via step 1202. It is then determined if its neighbor is the same neighbor as previously noted via step 1204. The node can save the source of a short path message at the time of wrap initiation to note the identity of its neighbor. If the current neighbor is not the same as the previous neighbor via step 1204, then an IDLE state is entered via step 1206. If, however, the current neighbor is the same as the previous neighbor via step 1204, then it is determined whether a pre-determined wait-to-restore time has expired via step 1208. Once the pre-determined wait-to-restore time has expired, then the node enters an IDLE state via step 1206.” Daruwalla, 12:60-13:6.</p> <p>“A method and system for fault recovery for a two line bi-directional network has been disclosed. Software written according to the present invention may be stored in some form of computer-readable medium, such as memory or CD-ROM, or transmitted over a network, and executed by a processor.” Daruwalla, 13:7-19.</p>

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		<p>Nederveen discloses:</p> <p>“A technique for use in gathering network activity-related information from cascaded network switches is provided. Using this technique, the information can be gathered without substantially reducing performance of the cascaded switches. In one embodiment, a single remote monitoring probe is connected via respective connections to each of the switches so as to receive the information from the switches. In another embodiment, only one of the switches is connected to the probe, and the other switches transmit their respective portions of the information to the switch connected to probe. The switch connected to the probe provides these portions of the information, as well as, any of its respective activity-related information to the probe. In this latter embodiment, the switches may be connected by dedicated connections and switch ports that are used solely for communicating the activity-related information.” Nederveen, Abstract.</p>

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		<p style="text-align: center;">FIG. 3</p> <p>Nederveen, FIG. 3.</p>

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		<p>The diagram illustrates a stacked switch monitoring architecture. Three switches, labeled A, B, and C, are connected in a vertical stack. Each switch contains a Base Processor (BP), a Buffer Layer (BL), and a MAC layer consisting of Tx and Rx components. - Switch A (bottom) has a BP labeled ~504, a BL labeled ~600B, and a MAC labeled 302. - Switch B (middle) has a BP labeled ~501, a BL labeled ~600B, and a MAC labeled 310B. - Switch C (top) has a BP labeled ~500, a BL labeled ~600A, and a MAC labeled 306. - A dashed line labeled 300A connects the MAC of Switch C to the BL of Switch B. Another dashed line labeled 300B connects the MAC of Switch B to the BL of Switch A. - A dashed line labeled 300C connects the MAC of Switch A to the BL of Switch C. - Arrows labeled 222, 224, 226, and 228 indicate connections from the MAC layers of Switches A, B, and C to their respective BLs. - Arrows labeled TO SEGMENT (220), TO SEGMENT (222), TO SEGMENT (224), and TO SEGMENT (226) point from the MAC layers to the outside. - An arrow labeled TO ROUTER (250) points from the MAC layer of Switch C to a Router (230). - A line labeled 900 connects the BL of Switch C to a management process. - A separate block labeled RMON PROBE (402) receives data from the management process and sends it to a TO MANAGEMENT PROCESS (408).</p> <p style="text-align: center;">FIG. 5</p> <p>Nederveen, FIG. 5.</p> <p>“Thus, it would be desirable to provide a stacked switch monitoring technique that permits efficient offloading of raw data processing from the stacked switches, requires only a minimal number of specialized network entities to gather and process such raw data, and does not result in substantial degradation of stacked switch performance.” Nederveen, 4:38-43.</p>

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		<p>“Accordingly, the present invention provides a technique for remote monitoring of a switch network that overcomes the aforesaid and other disadvantages and drawbacks of the prior art. More specifically, in one aspect of the present invention, a technique is provided for gathering information that may be useful in network management (e.g., switch port activity-related information), from switches in the network that are in a stacked configuration. The information is gathered from the stacked switches by a single network entity (e.g., an SNMP remote monitoring probe) in such a way that it does not substantially degrade the performance of the switches. This is accomplished, in one embodiment of the technique of the present invention, by connecting the switches via respective connections to a multiplexer that selectively connects the switches, according to an arbitration scheme, to the single network entity. The entity gathers respective portions of the information from switches when it is connected to the switches by the multiplexer. The information gathered by the entity may be provided to another network entity (e.g., an SNMP management node) in order to permit the other entity to use that information in managing the network.” Nederveen, 4:46-67.</p> <p>“In another embodiment of the technique of the present invention, only one of the switches is connected to the single information gathering entity. The switches that are not connected to the entity transmit, via respective dedicated ports and connections (i.e., ports and connections that are used solely for network information gathering activities), their respective portions of the information to the switch that is connected to the entity. The switch that is connected to the entity transmits, via a respective dedicated port and connection, the information received from the other switches, as well as, its own information to the entity.” Nederveen, 5:1-11.</p> <p>“FIG. 3 is a schematic, functional block diagram illustrating in greater detail the construction of the stacked switch network shown in FIG. 2.” Nederveen, 5:26-28.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network configured to employ another embodiment of the present invention.” Nederveen, 5:32-34.</p>

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		<p>“FIGS. 2-5 illustrate features of a computer network 200 wherein embodiments of the present invention may be advantageously practiced. Network 200 comprises a stacked switch network 300 which interconnects a plurality of network segments 228, 232, 240, and 251. Each segment 228, 232, 240 comprises one or more local area networks having computer endstations (not shown). Segment 251 is a network router segment that comprises network router 250. Each segment 228, 232, 240 is coupled via a respective communications link 222, 224, 226 to a respective port 302 (i.e., port A), 304 (i.e., port B), 312 (i.e., port C) of the switch network 300. Likewise, the router 250 of router segment 251 is coupled via a respective trunk line 230 to router port 306 (i.e., port R).” Nederveen, 5:46-59.</p> <p>“Stacked switch network 300 comprises a plurality of data network switches 300A, 300B, 300C (e.g., Catalyst 3900™ series switches of the type commercially available from the Assignee of the subject application) coupled together via conventional stack link bus connection logic 600A, 600B. More specifically, logic 600A couples a stack link bus port and associated logic 500 in switch 300A to a stack link bus link port and associated logic 502 in switch 300B. Similarly, logic 600B couples another stack link bus port and associated logic 501 in switch 300B to a stack link bus port and associated logic 504 in switch 300C. It should be understood that although, as is shown in FIG. 3, switches 300A and 300B, and switches 300B and 300C, may be coupled serially together by separate respective logic elements 600A, 600B, each of the switches 300A, 300B, 300C may be coupled together via a single respective stack link bus port in the switch to a single stack link bus connection logic block (not shown, e.g., of the type that is commercially available under the tradename Catalyst Matrix™ from the Assignee of the subject application). Further alternatively, depending upon the particular design and functionality of the ports 500, 501, 502, and 504, and the control and forwarding logic (whose operation will be described more fully below) in the switches 300A, 300B, 300C, the circuitry in logic 600A, 600B may instead be comprised in the ports 500, 501, 502, and 504 and/or control and forwarding logic, and therefore, in this alternative configuration, the logic 600A, 600B in the network 300 may be replaced by simple connection means (e.g., cable connectors).” Nederveen, 6:29-57.</p>

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		<p>“Each switch 300A, 300B, 300C includes a respective internal bus (e.g., element 800 in switch 300C) that is coupled via at least one stack link bus port and associated interface logic (e.g., 504 in switch 300C) to external stack link bus connection logic (e.g., element 600B in switch 300C). Each switch 300A, 300B, 300C also includes respective programmable control and forwarding logic (e.g., element 802 in switch 300C) comprising processing, memory, and other circuitry for storing and learning configuration information (e.g., source and destination MAC addresses of messages received by the switch, switch bridging table, switch segments' spanning tree and virtual local area network information, etc.), and for providing appropriate commands to other elements (e.g., the switch ports) to cause data messages received by the switch to be forwarded to appropriate network segments coupled to the switch based upon this configuration information. In each switch, the switch's port logic circuitry (e.g., port A logic 302 and port P logic 310C in switch 300C) and control and forwarding logic are coupled to each other via that switch's respective internal bus. The stack link bus port and associated logic in each switch 300A, 300B, 300C may comprise a Catalyst™ stack port line interface card (commercially available from the Assignee of the subject application) inserted into a bus expansion slot (not shown) in the switch. Although not shown in the Figures for purposes of clarity of illustration, each switch 300A, 300B, 300C in network 300 typically will include tens or hundreds of ports coupled to network segments.” Nederveen, 6:58-7:19.</p> <p>“The control and forwarding logic and stack link bus port and associated logic in each switch, and the logic 600A, 600B, are configured to together implement conventional techniques for permitting the switches 300A, 300B, 300C to function together as a single logical/virtual switch. More specifically, when configured in the stacked arrangement 300, after the switches 300A, 300B, 300C and logic 600A, 600B are initially activated, they execute initial power-on self-diagnostics, and thereafter, enter a “stack discovery” mode of operation.” Nederveen, 7:20-29.</p> <p>“In the stack discovery mode of operation, the control and forwarding logic in each switch 300A, 300B, 300C first “senses” that its switch is coupled to logic 600A and/or 600B, and then determines the particular configuration of the stacked switch network 300, using suitable conventional autosensing/autoconfiguration techniques. The control and 261forwarding logic in the switches 300A, 300B, 300C then assigns to the switches respective unique</p>

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		<p>identification numbers (e.g., based upon unique identification numbers of respective ports of the logic 600A, 600B to which the switches are coupled).” Nederveen, 7:30-40.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network 300' configured to employ another embodiment of the present invention. It should be understood that unless specifically stated to the contrary, the structure and operation of the network 300' are substantially the same as the structure and operation of network 300. In network 300', each of the dedicated ports 310A, 310B, 310C comprises a respective transmit portion and receive portion, referenced in FIG. 5 as RX and TX, respectively.” Nederveen, 11:20-29.</p> <p><u>Slater '421 discloses:</u></p> <p>“A method and apparatus for discovering paths to other network devices includes a protocol and network management application that can be executed on network devices. The Ethernet protocol is used to detect paths to other network devices, knowing only the Ethernet address of the destination. A discovery protocol is extended to add hop probe and hop probe reply Type-Length-Value fields in a variable-length list. The hop probe fields contain a hop count, a destination Ethernet address, and a source Ethernet address. When a hop probe is received by a network device, the hop count field is decremented by one and the hop probe is forwarded. Packets received with a hop count of one are not forwarded and a hop probe reply is sent back to the Ethernet source address of the hop probe. The hop probe reply fields contain a destination Ethernet address and a source Ethernet address.” Slater '421, Abstract.</p>

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		<p style="text-align: center;">FIG. 6</p> <p>Slater '421, FIG. 6.</p> <p>"Partly as a result of the increased complexity of networks, network administrators must often troubleshoot problems with their network. Two classes of network problems often faced by network administrators are "reachability" problems and performance slowdowns. Reachability problems occur when one or more network devices cannot be accessed through a network, and can be caused by hardware or software failures, cabling problems, or any of several other types of difficult-to-diagnose problems that can occur in a network." Slater '421, 7:11-20.</p> <p>"Some of the world's largest networks today rely on the TCP/IP suite of networking protocols. With a relatively small kit of basic tools, network administrators can learn much about an internetwork. "Ping" and "traceroute" commands, "show" commands, and "debug" commands (all of which are typically available via the basic management interface on a network device) form the core of the network administrator's internetwork toolkit. Ping and</p>

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		<p>traceroute commands can be useful tools in determining where failures are occurring, but they are cumbersome to use, and require knowledge of the IP address or host name of the destination network device. The show commands provide information about interface conditions, protocol status, neighbor reachability, router configuration and status, level of traffic, errors and drops, and other network data. Finally, debug commands provide clues about the status of a network device and other network devices directly or indirectly connected to it. Because debug commands can create performance slowdowns, they must be used with great care, and using the wrong debug command at the wrong time can exacerbate problems in already poorly performing networks.” Slater ’421, 7:55-8:8.</p> <p>“Embodiments of the present invention as illustrated herein use the Cisco™ Discovery Protocol (“CDP”) to automatically detect paths to specified network devices in Ethernet LANs. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task.” Slater ’421, 9:10-15.</p> <p>“CDP is a media-independent device discovery protocol which can be used by a network administrator to view information about other network devices directly attached to a particular network device. In addition, network management applications can retrieve the device type and SNMP-agent address of neighboring network devices. This enables applications to send SNMP queries to neighboring devices. CDP thus allows network management applications to discover devices that are neighbors of already known devices, such as neighbors running lower-layer, transparent protocols.” Slater ’421, 9:16-26.</p> <p>“It is to be understood that the present invention is not limited to devices that are compatible with CDP. CDP runs on all media that support the Subnetwork Access Protocol (“SNAP”), including LAN and Frame Relay. CDP runs over the data link layer only. Each network device sends periodic messages to a multicast address and listens to the periodic messages sent by others in order to learn about neighboring devices and determine when their interfaces to the media go up or down. Each device also advertises at least one address at which it can receive SNMP messages. CDP messages, or “advertisements,” contain holdtime information, which indicates the period of time a receiving device should hold CDP information from a neighbor before discarding it. With CDP, network management applications can learn the device type</p>

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		<p>and the SNMP-agent address of neighboring devices. This process enables applications to send SNMP queries to neighboring devices.” Slater ’421, 9:27-43.</p> <p>“It should be noted that—normally, CDP packets according to aspects of the present invention are transmitted at regular intervals (e.g. once every 60 seconds). However, in embodiments of the present invention, when a Hop Probe or Hop Probe Reply needs to be forwarded by a network device, the network device is commanded to send a CDP packet immediately.” Slater ’421, 16:66-17:5.</p> <p>“The present invention is much faster than the previous method that involved logging in to each intermediate network device, entering the “show cdp neighbors” command, and interpreting the output to find the next hop along the path to the destination network device. Also, the present invention allows individual users, such as network administrators, to execute a tool to manually discover paths through a network of Ethernet switches. The present invention can be used by network management software to automatically map the topology of clusters of network devices, such as Ethernet switches. Finally, the present invention is useful in loop detection. Enhancements to Spanning Tree and other bridge-level routing protocols can test proposed changes to switch topology prior to making them.” Slater ’421, 17:6-20.</p> <p>Petersen discloses:</p> <p>“Methods and apparatus for enabling communication between a source network device and one or more destination network devices are disclosed. A system enabling communication between a source network device and one or more destination network devices includes a switch and a ring interconnect. The switch is adapted for connecting to the source network device and the one or more destination network devices. More particularly, the switch is capable of storing data provided by the source network device and retrieving the data for the one or more destination network devices. The ring interconnect is adapted for connecting the source network device and the one or more destination network devices to one another. In addition, the ring interconnect is capable of passing one or more free slot symbols along the ring interconnect. Thus, the ring interconnect is capable of expanding when one or more of the free slot symbols are each replaced by a frame notify message indicating that the data has</p>

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		<p>been stored by the switch for retrieval by the one or more destination network devices.”</p> <p>Petersen, Abstract.</p> <pre> sequenceDiagram participant SourceSat as Source Sat. participant Switch participant DestSat as Dest. Sat. SourceSat->>Switch: Receive frame and break into cells activate SourceSat Note over SourceSat: 208 Note over SourceSat: -> 202: Source Sat. deactivate SourceSat Switch->>SourceSat: Send cells inc. data and header to switch (inc. retrieval count) activate SourceSat Note over SourceSat: 210 Note over SourceSat: -> 204: Switch deactivate SourceSat Switch->>DestSat: Switch stores cell data in Data Buffer and cell header in Buffer Table (inc. retrieval count) activate DestSat Note over DestSat: 212 Note over DestSat: -> 206: Dest. Sat. deactivate DestSat DestSat->>Switch: Send Frame Storage message indicating frame stored and location of frame activate Switch Note over Switch: 214 Note over Switch: -> 218: Copy and store the Notify msg. (inc. buffer #) in notify queue Note over Switch: 216: Send Frame Notify msg. (inc. buffer # in Data Buffer) on notify ring Note over Switch: 220: Modify Frame Notify message to indicate whether able to accept and store the Frame Notify msg. Note over Switch: 222: Send Frame Retrieval msg. for buffer # at head of notify queue to retrieve frame Note over Switch: 224: - Decrement retrieval count Note over Switch: 226: - If retrieval count = 0, remove frame from storage upon completion of removal Note over Switch: 228: Send Frame cells to Dest. Sat. Note over Switch: 230: Reconstruct frame for transmission Note over Switch: 232: Send modified Frame Notify message on notify ring Note over Switch: 234: Removes Notify msg. from ring and discards Note over Switch: 236: Send Buffer Table subtract msg. corr. to the modified frame Notify msg. (e.g., subtract # destinations unable to copy frame Notify msg. from retrieval count) </pre> <p>FIG. 2</p> <p>Petersen, FIG. 2.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“The present invention relates to a mixed topology data switching system that combines a radial interconnect with a ring interconnect. More particularly, the radial interconnect permits devices to store and retrieve data using a switch, while the ring interconnect permits devices along the ring interconnect to provide notification that data has been stored for retrieval, as well as provide feedback regarding the ability or inability to retrieve such data.” Petersen, 1:34-41.</p> <p>“In controlling the flow of network traffic through a switching system, it is often desirable to provide feedback to the source of the data. For instance, although a transmitting device, hereinafter referred to as a “source device,” may transmit or forward data to a receiving device, hereinafter referred to as a “destination device,” the destination device may be incapable of handling the data. In these circumstances, the source device is often unaware that the data was not accepted by the destination device, complicating switch management. Common solutions to the problem of switch traffic management have included ensuring that all intended destination devices are “ready to receive” prior to transmitting data on a ring or bus interconnect, or insisting that each intended destination device send an explicit acknowledgement back to the source device. Both of these approaches result in reduced efficiency of the interconnect scheme. By way of example, in a ring network, such acknowledgment is typically provided in the data frame being transmitted. As another example, in other interconnect schemes, each such device may send a separate acknowledgment, therefore adding to the traffic on the network. Accordingly, it would be desirable if a traffic management scheme were established which could provide such feedback to the source of the data while minimizing traffic management overhead.” Petersen, 2:8-32.</p> <p>“According to one embodiment, the present invention combines the use of two data transport methods: a point-to-point radial interconnect and a ring interconnect. The radial interconnect connects interface devices to each other through the services of a central switch device to permit the transport of data. Typically, a single interface has a single dedicated radial interconnect to the central switch. These interface devices are further connected to one another via a ring interconnect to convey retrieval notifications regarding forwarding of the data (by source devices) and receipt of the data (by destination devices).” Petersen, 2:36-46.</p>

No.	'904 Patent Claim 14	The Reference
		<p>“Each radial interconnect provides a narrow, high baud-rate connection to convey to the actual data from and to the associated interface without being burdened by the unrelated traffic for the remaining interfaces in the system. This is accomplished through the use of a central switch device, which stores and retrieves data for the various interfaces in the system. As will be apparent from the following description, this architecture provides numerous advantages over a wide parallel bus or ring.” Petersen, 2:47-55.</p> <p>“The ring interconnect may be used to convey a “retrieval notification”(i.e., retrieval message) that may be observed by all potential retrieving interfaces. The retrieval notification notifies specific devices (“destination devices”) or interfaces that one or more frames addressed to them are available from the switch device. Moreover, the ring interconnect permits each destination device to provide feedback to the source device letting the source know whether the destination has accepted the notification provided by the source device and therefore whether the destination can retrieve the data intended for it. The feedback is particularly useful in buffer management applications. In this manner, an efficient and flexible data transport and retrieval notification system that includes a feedback path to the source of the data is provided.” Petersen, 2:56-3:3.</p> <p>“FIG. 2 is a process flow diagram illustrating a method of providing network communication according to an embodiment of the invention.” Petersen, 3:9-11.</p> <p>“FIG. 2 is a process flow diagram illustrating in further detail a method of providing network communication in the above-described system according to an embodiment of the invention. As shown, process steps performed by a source device 202 are illustrated along an associated vertical line, steps performed by a switch 204 are illustrated along another vertical line, and steps performed by a destination device 206 are illustrated along still another vertical line.” Petersen, 4:50-57.</p> <p>“When the frame is stored by the switch 418, the source device preferably receives an acknowledgment that the data has been stored. Thus, to provide this feedback, a frame storage message (i.e., storage reply) is sent from the switch 418 on the channel 416 to the channel interface 414. The frame storage message is then provided to the notify ring interface as</p>

No.	'904 Patent Claim 14	The Reference
		<p>shown at 430 and sent on the notify ring. Once this acknowledgment is received by the interface device 402, the designated destination devices may be notified via notify ring interface 424. As described above, a Frame Notify message may be sent via the notify ring interface 424 to the destination devices. More particularly, the Frame Notify message may identify one or more destination devices for the frame and specify the location of the frame to be retrieved. By way of example, the location of the frame to be retrieved by the destination devices may be designated by a buffer number 430. In addition, the destination devices for the frame may be specified in the Frame Notify message through a notify queue map 426. More particularly, the notify queue map 426 may specify a notify queue associated with a particular destination device. The notify queue may be expressly designated through the use of one or more bits as well as implied through the specification of a priority level for the data. The notify queue map 426 will be described in further detail with reference to FIG. 13. The notify ring interface 424 then creates a Frame Notify message including the notify queue map 426 and the buffer number 430 which is then sent on an outbound interface of the notify ring 432.” Petersen, 7:55-8:16.</p> <p>“As described above, the notify ring may be expanded to accommodate communication between interface devices. The communication between the interface devices and the switch is therefore performed on one or more channels rather than the notify ring. As a result, the flexibility of the notify ring does not effect the speed with which the interface devices may communicate with the switch. Thus, where a single port operates at a faster speed than the channels, multiple channels may be grouped together. In this manner, the speed with which the switch may communicate with the interface devices may be maximized.” Petersen, 20:27-36.</p> <p>“The present invention provides a mixed topology data switching system that combines a point-to-point radial interconnect with a ring interconnect to maximize the speed of network traffic. The radial interconnect provides a narrow, high baud-rate connection to convey the data traffic for just the interface in question, without being burdened by all of the unrelated traffic for the remaining interfaces in the system. At the same time, the ring interconnect permits retrieval notifications to be observed by all potential retrieving interfaces. The ring topology further permits each destination interface to provide feedback to the source interface,</p>

No.	'904 Patent Claim 14	The Reference
		which is valuable for buffer management applications. Moreover, the point-to-point ring topology bus employs a variable latency access method that enables messages to be passed across the bus with low latency when the system is quiet and with increased latency when the system is busy. In addition, since control messaging around the ring interconnect and across the channel interconnects are embedded in the data stream, the number of pins required and manufacturing costs are reduced.” Petersen, 20:38-57.
14[f]	a fabric of one or more switches, which convey the retained packets to the ports, responsive to the address data.	<p>The Reference discloses a fabric of one or more switches, which convey the retained packets to the ports, responsive to the address data.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p>

No.	'904 Patent Claim 15	The Reference
15	The slave unit according to claim 14, wherein the pre-switch is further coupled to receive packets transferred thereto from the second physical interface and to sort the packets in like manner to the packets received through the first physical interface.	<p>The Reference discloses the slave unit according to claim 14, wherein the pre-switch is further coupled to receive packets transferred thereto from the second physical interface and to sort the packets in like manner to the packets received through the first physical interface.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling, and NattKemper.</p>

No.	'904 Patent Claim 16	The Reference
16[a]	The slave unit according to claim 15, wherein the retained packets that were received from the first and second physical interfaces and are passed by the pre-switch to the switch fabric are identified by respective first and second port numbers,	<p>The Reference discloses the slave unit according to claim 15, wherein the retained packets that were received from the first and second physical interfaces and are passed by the pre-switch to the switch fabric are identified by respective first and second port numbers.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>
16[b]	and wherein in response to a reversal of direction of data flow in the daisy chain, the first and second port numbers are swapped in the pre-switch, so that substantially no reconfiguration of the switch fabric is required in response to the reversal.	<p>The Reference discloses wherein in response to a reversal of direction of data flow in the daisy chain, the first and second port numbers are swapped in the pre-switch, so that substantially no reconfiguration of the switch fabric is required in response to the reversal.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 17	The Reference
17	<p>The slave unit according to claim 14, wherein when one of the packets received by the pre-switch comprises a multicast packet addressed to one or more of the ports on the slave unit, the pre-switch sorts the multicast packet such that one copy of the packet is retained and another copy of the packet is passed to the second physical interface.</p>	<p>The Reference discloses the slave unit according to claim 14, wherein when one of the packets received by the pre-switch comprises a multicast packet addressed to one or more of the ports on the slave unit, the pre-switch sorts the multicast packet such that one copy of the packet is retained and another copy of the packet is passed to the second physical interface.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 18	The Reference
18	<p>The slave unit according to claim 14, wherein in the event of a fault in the switch fabric, the pre-switch continues to pass the packets addressed to the succeeding units on to the succeeding units without significant interruption.</p>	<p>The Reference discloses the slave unit according to claim 14, wherein in the event of a fault in the switch fabric, the pre-switch continues to pass the packets addressed to the succeeding units on to the succeeding units without significant interruption.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Below are examples of such references.</p>

No.	'904 Patent Claim 18	The Reference
		<p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 ("FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.").</p> <p style="text-align: center;">FIG. 2</p> <p style="text-align: center;">FIG. 2 (annotation added)</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure</p>

No.	'904 Patent Claim 18	The Reference
		<p>of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>

No.	'904 Patent Claim 19	The Reference
19[preamble]	A method for providing access to a network, comprising:	<p>The Reference discloses a method for providing access to a network, comprising:</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>
19[a]	coupling first and second master units to interface with the network,	<p>The Reference discloses coupling first and second master units to interface with the network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra at 1[a].</i></p> <p>Cisco already patented “master” and slave” switch technology and commercialized it before Orkit. Some examples of Cisco’s patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater ’796 • Slater ’433 • Duvvury ’626 • Duuvury ’820

No.	'904 Patent Claim 19	The Reference
		<p>Cisco Catalyst Press Release discloses:</p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst. 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to network managers that now don't have to manage each individual switch as an independent entity.’” Cisco Catalyst Press Release, 2.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 19	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury '626, 16:11-31.</p>

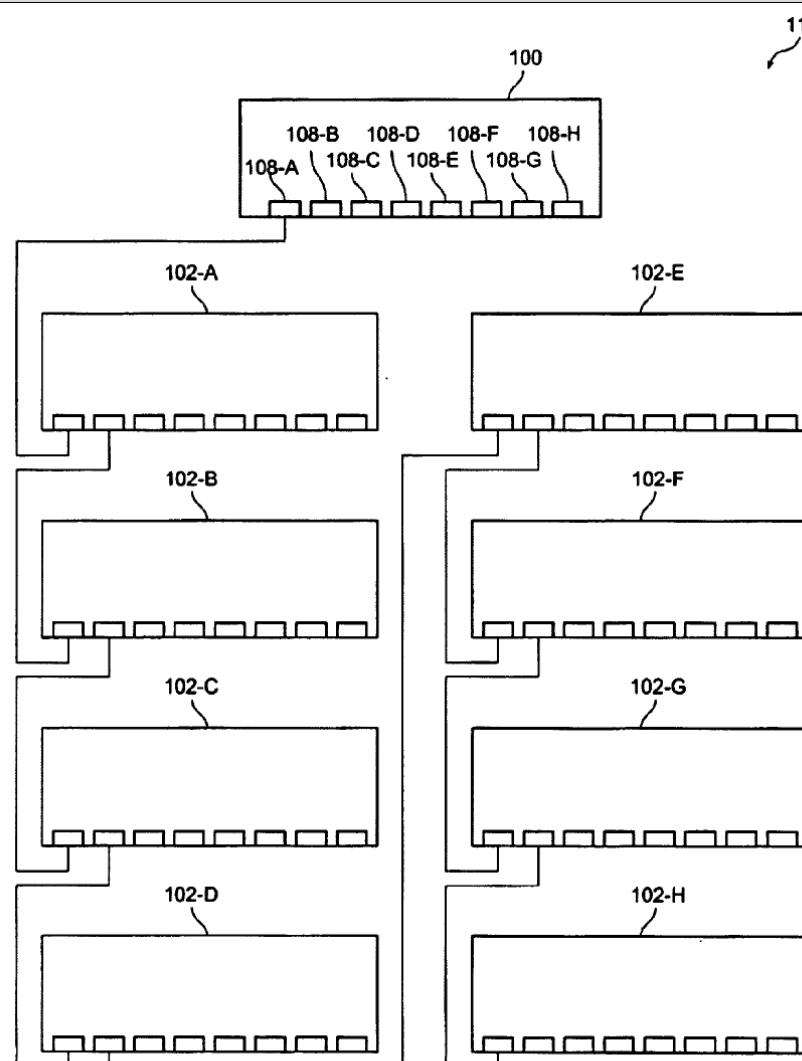
No.	'904 Patent Claim 19	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17 Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 19	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 19	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 19	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 19	The Reference
		 <p><i>FIG. 9</i></p> <p>Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 19	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 19	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
19[b]	linking a plurality of slave units,	<p>The Reference discloses linking a plurality of slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, Dowling and Sugawara.</p> <p><i>See supra</i> at 1[b], 1[d].</p> <p>Cisco already patented "master" and slave" switch technology and commercialized it before Orkit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Cisco Catalyst Press Release • Slater '796

No.	'904 Patent Claim 19	The Reference
		<ul style="list-style-type: none"> • Slater '433 • Duvvury '626 • Duuvury '820 <p><u>Cisco Catalyst Press Release discloses:</u></p> <p>“May 24, 1999 -- Cisco Systems, Inc. today announced the new Catalyst 3500 Series XL, the industry's most scalable line of stackable 10/100 and Gigabit Ethernet desktop switches that delivers premium performance, manageability, flexibility and unparalleled investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“The new family of stackable switches, consisting of the Catalyst 3512 XL, Catalyst 3524 XL and Catalyst 3508G XL switches, use Cisco Switch Clustering technology to take traditional stacking to the next level by allowing network managers to manage geographically dispersed switches through a single IP address, using a standard Web browser.” Cisco Catalyst Press Release, 2.</p> <p>“Cisco delivers next-generation stacking through a new scalable stacking architecture consisting of a new hardware platform, the Gigabit-enabled Catalyst 3500 Series XL; a unique flexible stacking transceiver, the GigaStack Gigabit Interface Connector (GBIC); and Cisco Switch Clustering technology that enables single IP management of geographically dispersed switches.” Cisco Catalyst Press Release, 2.</p> <p>“The Cisco stacking architecture is fully backwards compatible with all Catalyst 2900 Series XL and Catalyst 1900 Standard and Enterprise Edition switches, giving customers unparalleled flexibility and investment protection.” Cisco Catalyst Press Release, 2.</p> <p>“‘The new Catalyst 3500 XL switches with the unique Cisco Switch Clustering technology and enhanced Cisco Visual Switch Manager makes managing these switches easy and hassle-free,’ said Juan Garcia, system network administrator at Acer America. ‘You can now manage an entire group of Catalyst 3500 XL, 2900 XL and 1900 switches from a single IP address regardless of their location, using one Web interface. This is a very powerful message to</p>

No.	'904 Patent Claim 19	The Reference
		<p>network managers that now don't have to manage each individual switch as an independent entity.” Cisco Catalyst Press Release, 2.</p> <p>“With the introduction of the Catalyst 3500 Series XL and Cisco Switch Clustering technology, Cisco introduces next generation stacking. The Catalyst 3500 Series XL switches feature a 10 Gbps switching fabric that delivers wire-speed performance to each 10/100 port. The new stackable switches feature Cisco IOS. software and Cisco Visual Switch Manager (CVSM) software, an easy-to-use, Web-based management interface. All Catalyst 3500 Series XL switches are available in Standard and Enterprise Editions. Enterprise Edition switches offer advanced software features such as, complete 802.1Q and ISL VLAN support, TACACS+ security, and fault tolerance through Uplink Fast.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 Series XL consists of three switch models.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3512 XL: a single rack unit (RU) stackable 10/100 and Gigabit Ethernet switch with 12 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of industry-standard GBICs, including the Cisco GigaStack GBIC, and 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3512 XL is a high-performance, non-blocking switch, ideal for aggregating a small group of Catalyst 2900 XL or Catalyst 1900 switches in a clustered configuration. In a standalone configuration, the Catalyst 3512 XL offers low port density at a low entry price.” Cisco Catalyst Press Release, 3.</p> <p>“Catalyst 3524 XL: a single RU stackable 10/100 and Gigabit Ethernet switch with 24 10BaseT/100BaseTX ports and two GBIC-based Gigabit Ethernet ports that accommodate a range of GBICs, including the Cisco GigaStack GBIC, 1000BaseSX and 1000BaseLX/LH GBICs. The Catalyst 3524 XL is ideal for delivering dedicated 10 or 100 Mbps bandwidth to individual users and servers in a stack or cluster configuration. Built-in dual GBIC-based Gigabit Ethernet ports provide users with a flexible and scalable solution for Gigabit Ethernet uplinks or GigaStack stacking.” Cisco Catalyst Press Release, 3.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Catalyst 3508G XL: a single RU stackable Gigabit Ethernet switch with 8 GBIC-based Gigabit Ethernet ports. The Catalyst 3508G XL is ideal for aggregating a group of 10/100 switches and Gigabit Ethernet servers using Cisco GigaStack GBICs or standard 1000BaseX GBICs.” Cisco Catalyst Press Release, 3.</p> <p>“The Catalyst 3500 XL and modular Catalyst 2900 XL switches can be stacked using the low-cost Cisco GigaStack GBIC. The two-port GigaStack GBIC offers a range of highly flexible stacking and price/performance connectivity options. It delivers a 1 Gbps stack bus in a daisy-chained configuration or up to 2 Gbps full-duplex connectivity in a dedicated, switch-to-switch configuration. GBIC-based Gigabit Ethernet aggregation via the Catalyst 3508 XL delivers up to 5 Gbps aggregated forwarding bandwidth to connected switches in a switch ‘cluster.’” Cisco Catalyst Press Release, 3-4.</p> <p>“Cisco Switch Clustering software enables up to 16 interconnected Catalyst 3500 XL, 2900 XL and 1900 switches, regardless of geographic proximity, to form a managed single-IP address network. These switches can be interconnected using a broad range of connectivity options, delivering different levels of performance to meet customer needs. Clustering connectivity options include Ethernet, Fast Ethernet, Fast EtherChannel, low-cost Cisco GigaStack GBIC, Gigabit Ethernet and Gigabit EtherChannel technologies.” Cisco Catalyst Press Release, 4.</p> <p>“Because the technology is not limited by proprietary stacking modules and stacking cables, Cisco Switch Clustering software expands the traditional stacking domain beyond a single wiring closet and allows users to mix and match interconnections to meet specific management, performance and cost requirements.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering software for the Catalyst 3500 XL, 2900 XL and 1900 switches, enables the management of a ‘cluster’ of switches through a single IP address. The clusters can be grouped regardless of interconnection media or physical proximity. In a Cisco switch cluster, one Catalyst 3500 XL or 2900 XL switch is designated as the “command” switch and all other switches in the cluster are designated as “member” switches. The command switch</p>

No.	'904 Patent Claim 19	The Reference
		<p>serves as the single IP management point and disburses all management action dictated by the network manager.” Cisco Catalyst Press Release, 4.</p> <p>“Cisco Switch Clustering command software is pre-installed on all Catalyst 3500 XL switches and is available as an upgrade for Catalyst 2900 XL and 1900 switches. Cisco Switch Clustering technology supports Command Line Interface (CLI) in addition to Cisco Visual Switch Manager.” Cisco Catalyst Press Release, 4.</p> <p>“The Catalyst 3500 Series XL features the Cisco Web-based management tool, Cisco Visual Switch Manager (CVSM 2.0), which allows network managers to view and manage a switch cluster from anywhere on the network through a standard Web browser such as Microsoft Internet Explorer or Netscape Navigator. CVSM 2.0 is launched from the switch itself and delivers simple network and device-level management, including VLAN set-up, port configuration, network cluster views and port monitoring. CVSM is an integral part of the Cisco scalable stacking architecture, allowing users to easily configure and manage switch stacks and clusters, and administer software upgrades across multiple switches.” Cisco Catalyst Press Release, 4-5.</p> <p>Duvvury '626 discloses:</p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more member devices. Each network device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have a public IP address. The cluster commander automatically assigns private IP addresses to the other devices in the cluster. Network devices in the cluster constantly monitor network traffic on all their ports to detect conflicts between the automatically assigned IP addresses and the IP addresses of network devices outside of the cluster. When a conflict is detected, the cluster commander assigns a different private IP address to the cluster network device that caused the conflict. The process of detecting and correcting IP address conflicts continues continuously to enable the cluster network devices to react automatically to network configuration changes.” Duvvury '626, Abstract.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch.” Duvvury '626, 12:18-30.</p> <p>“FIG. 17 is a flow chart illustrating an automatic IP address generation algorithm according to one embodiment of the present invention. When a member switch first joins a cluster, the commander switch generates a CMP address for the member switch by adding last three bytes of the member switch's MAC address to the number “10.0.0.0.” Thus, as shown in FIG. 17, at step 1700 the commander switch reads the MAC address of a member switch from an Ethernet frame received from the member switch. Next, at step 1710, the commander switch adds the last three bytes of the member switch's MAC address to the number “10.0.0.0.” Then, at step 1720, the commander switch assigns the resulting number to be the CMP IP address of the member switch. For example, if the MAC address of the member switch is “00-e0-1e-01-02-03,” then the generated CMIP address will be “10.01.02.03.” At step 1730, the commander switch communicates its own CMP address to the member switch. Finally, at step 1740, once a member switch has been assigned a CMP address, the commander switch and the member switch use CMP addresses to communicate with each other.” Duvvury '626, 16:11-31.</p>

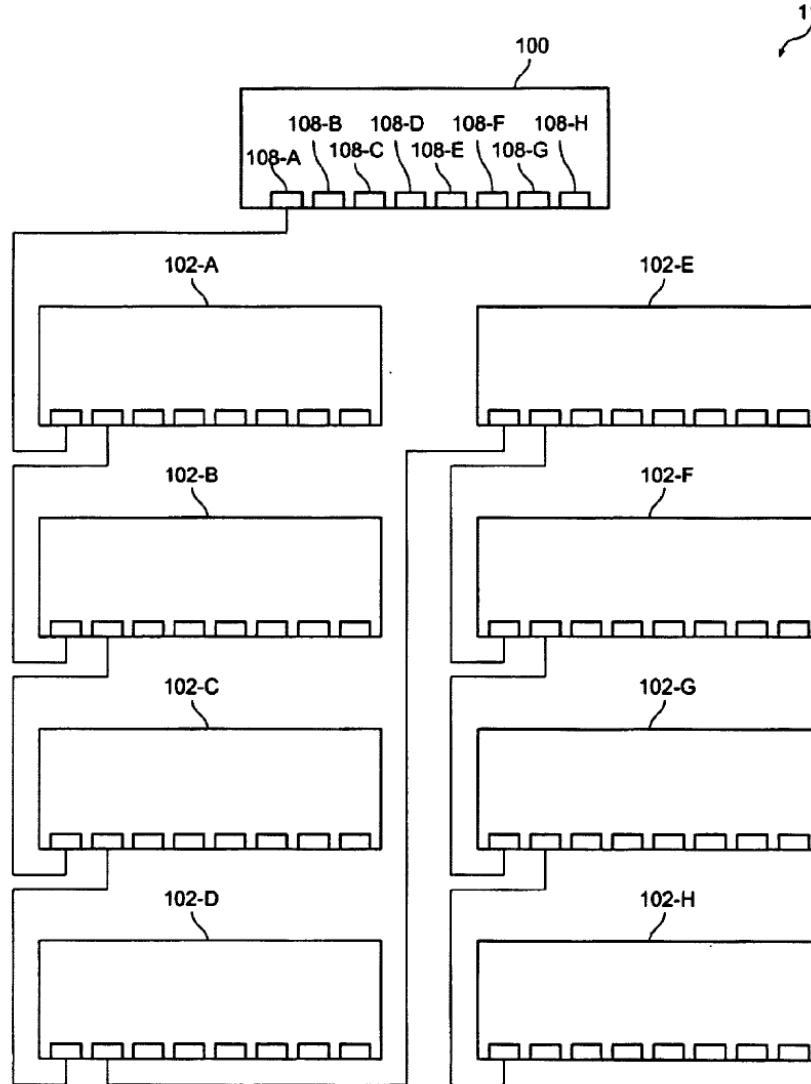
No.	'904 Patent Claim 19	The Reference
		<pre> graph TD A[READ MAC ADDRESS OF EXPANSION SWITCH] --> B[ADD THE LAST THREE BYTES OF MAC ADDRESS FROM EXPANSION SWITCH TO "10.0.0.0"] B --> C[ASSIGN RESULTING NUMBER AS IP ADDRESS OF EXPANSION SWITCH] C --> D[COMMANDER SWITCH TRANSMITS ITS PRIVATE IP ADDRESS TO EXPANSION SWITCH] D --> E[ALL SUBSEQUENT COMMUNICATION BETWEEN COMMANDER SWITCH AND EXPANSION SWITCH IS VIA ASSIGNED PRIVATE IP ADDRESSES] </pre> <p>FIG. 17</p> <p>Duvvury '626, FIG. 17.</p> <p>"FIG. 18 is a flow chart illustrating an automatic IP address conflict correction algorithm according to one embodiment of the present invention. In this embodiment, after detecting the conflict, the commander switch generates a new CMP address according to the algorithm shown in FIG. 18. First, at step 1800, three counters are initialized to zero, each representing the number of address correction attempts for the second byte, third byte, and fourth byte of the IP address, respectively. Next, at step 1805, the value of the second byte counter is compared to the highest possible value (255). If the value is less than 255, then at step 1810, the second byte of the IP address is incremented by one, "modulo 256," such that the number wraps back to zero if the present number is 255 and the second byte counter is less than 255. At step 1820, a new CMP address corresponding to the result is assigned to the switch that caused the conflict. At step 1830, if a conflict is still detected, the algorithm loops back to step 1805. Otherwise, the algorithm terminates at step 1899." Duvvury '626, 17:5-16.</p>

No.	'904 Patent Claim 19	The Reference
		<pre> graph TD START([START]) --> Q1{IS 2ND BYTE COUNTER < 255?} Q1 -- NO --> END([END]) Q1 -- YES --> I1[INCREMENT 2ND BYTE BY ONE (MODULO 256)] I1 --> A1[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A1 --> Q2{CONFLICT?} Q2 -- NO --> END Q2 -- YES --> Q3{IS 3RD BYTE COUNTER < 255?} Q3 -- NO --> END Q3 -- YES --> I2[INCREMENT 3RD BYTE BY ONE (MODULO 256)] I2 --> A2[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A2 --> Q4{CONFLICT?} Q4 -- NO --> END Q4 -- YES --> Q5{IS 4TH BYTE COUNTER < 255?} Q5 -- NO --> END Q5 -- YES --> I3[INCREMENT 4TH BYTE BY ONE (MODULO 256)] I3 --> A3[ASSIGN RESULT AS NEW PRIVATE IP ADDRESS] A3 --> Q6{CONFLICT?} Q6 -- NO --> END Q6 -- YES --> Q7{SIGNAL AN ERROR CONDITION} Q7 --> END </pre> <p>FIG. 18</p> <p>Duvvury '626, FIG. 18.</p>

No.	'904 Patent Claim 19	The Reference
		<p><u>Slater '796 discloses:</u></p> <p>“A group of network devices, such as Ethernet switches, are logically configured as a single cluster, with one commander device and one or more expansion devices. Each device in the cluster contains an embedded HTML server that facilitates configuration and management of the network device via a management station running a Web browser. Each device in the cluster is identified by a unique Universal Resource Locator (“URL”). However, only the cluster commander is required to have an IP address. The cluster commander redirects and translates configuration and management requests from the Web browser on the management station so that requests are processed by the appropriate device in the cluster. The exchange of information between the Web browser on the management station and the devices in a cluster is accomplished via redirection of HTTP GET and POST methods. This provides a consistent, device-independent interface between the device and the Web browser on the management station.” Slater '796, Abstract.</p> <p>“Network devices, such as LAN switches, may be configured and managed using either out-of-band or in-band techniques. Out-of-band configuration and management are typically performed by connecting to the console port on the network device and using the management console locally from a terminal or remotely through a modem. Alternatively, network devices may be configured and managed “in-band,” either by connecting via Telnet to the network device and using a management console, or by communicating with the network device's in-band management interface using the industry standard Simple Network Management Protocol (“SNMP”). This can be accomplished by using an SNMP-compatible network management application and the network device's Management Interface Base (“MIB”) files. Normally, however, in order to perform in-band administrative tasks of a network device, such as configuration and management, the network device must first be assigned an IP address. Additionally, in order to use in-band configuration and management capabilities, the SNMP management platform of the network device must be configured to understand and be able to access the objects contained in the network device's MIB.” Slater '796, 8:55-9:10.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Embodiments of the present invention use a subset of the Transmission Control Protocol/Internet Protocol (“TCP/IP”) suite as the underlying mechanism to transport the SNMP configuration and management data. Without limitation, the protocols implemented in embodiments of the present invention include the Internet Protocol (“IP”), the Internet Control Message Protocol (“ICMP”), the User Datagram Protocol (“UDP”), the Trivial File Transfer Protocol (“TFTP”), the Bootstrap Protocol (“BOOTP”), and the Address Resolution Protocol (“ARP”).” Slater ’796, 9:11-20.</p> <p>“The MIB variables of network devices according to embodiments of the present invention are accessible through SNMP. SNMP is an application-layer protocol designed to facilitate the exchange of management information between network devices. SNMP is used to monitor IP gateways and their networks, and defines a set of variables that the gateway must keep and specifies that all operations on the gateway are a side effect of fetching or storing to data variables. SNMP consists of three parts: a Structure of Management Information (“SMI”), a Management Information Base (“MIB”) and the protocol itself. The SMI and MIB define and store the set of managed entities, while SNMP itself conveys information to and from the SMI and the MIB.” Slater ’796, 9:21-34.</p> <p>“Instead of defining a large set of commands, SNMP places all operations in a get-request, get-next-request, and set-request format. For example, an SNMP manager can get a value from an SNMP agent or store a value into that SNMP agent. The SNMP manager can be part of a network management system (“NMS”), and the SNMP agent can reside on a networking device such as a LAN switch. The switch MIB files may be compiled with network management software, which then permits the SNMP agent to respond to MIB-related queries being sent by the NMS.” Slater ’796, 9:35-44.</p> <p>“An example of an NMS is the CiscoWorks™ network management software, available from Cisco Systems, Inc. of San Jose, Calif. CiscoWorks™ uses the switch MIB variables to set device variables and to poll devices on the network for specific information. Among other tasks, the CiscoWorks™ software permits the results of a poll to be displayed as a graph and analyzed in order to troubleshoot internetworking problems, increase network performance, verify the configuration of devices, and monitor traffic loads. Other products known to those</p>

No.	'904 Patent Claim 19	The Reference
		<p>of ordinary skill in the art, available from several other vendors, provide similar functionality.” Slater ’796, 9:45-57.</p> <p>“A cluster is a group of connected switches that are managed as a single entity. The switches can be in the same location, or they can be distributed across a network. According to embodiments of the present invention, all communication with cluster switches is through a single IP address assigned to the commander switch. Clusters may be configured in a variety of topologies. As an example, FIG. 8 illustrates a switch cluster 106 configured in a “star,” or “radial stack,” topology. In this configuration, each of the eight expansion switches 102-A-102-H in cluster 106 is directly connected to one of the ports 108A-108-H of commander switch 100.” Slater ’796, 10:55-67.</p> <p>“A second example of a cluster configuration, known as a “daisy chain” configuration, is shown in FIG. 9. In cluster 110, only expansion switch 102-A is directly connected to the commander switch 100. Expansion switches 102-B-102-G are each connected to an “upstream” switch (one that is fewer “hops” away from commander switch 100) and to a “downstream” switch (one that is more “hops” away from commander switch 100). Finally, the last switch in the chain (expansion switch 102-H) is only connected to its upstream “neighbor” 102-G.” Slater ’796, 11:1-10.</p>

No.	'904 Patent Claim 19	The Reference
		 <p><i>FIG. 9</i> Slater '796, FIG. 9.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“As a third example, FIG. 10 illustrates a “hybrid” cluster configuration with one commander switch 100 and seven expansion switches 102-A-102-G. In cluster 112, expansion switches 102-A and 102-E are in a star configuration with respect to commander switch 100. Expansion switch 102-B is in a daisy chain configuration with respect to expansion switch 102-A, while expansion switches 102-C and 102-D are in a star configuration with respect to expansion switch 102-B. Finally, expansion switches 102-F and 102-G are in a star configuration with respect to expansion switch 102-E. Thus, hybrid cluster 112 as shown in FIG. 10 consists of a combination of star and daisy chain configurations.” Slater '796, 11:11-22.</p>

No.	'904 Patent Claim 19	The Reference
		<p>FIG. 10</p> <p>Slater '796, FIG. 10.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“The commander switch is the single point of access used to configure and monitor all the switches in a cluster. According to embodiments of the present invention, expansion switches are managed through a commander switch. The commander switch is used to manage the cluster, and is managed directly by the network management station. Expansion switches operate under the control of the commander. While they are a part of a cluster, expansion switches are not managed directly. Rather, requests intended for an expansion switch are first sent to the commander, then forwarded to the appropriate expansion switch in the cluster.” Slater '796, 11:26-36.</p> <p>“When switches are first installed, they are cabled together according to the network configuration desired for a particular application, and an IP address is assigned to the commander switch. In addition, the commander switch must be enabled as the commander switch of the cluster. Once the commander switch has been enabled, it can use information known about the network topology to identify other network devices in the network that may be added to the cluster. According to one embodiment of the present invention, the commander switch uses the Cisco™ Discovery Protocol (“CDP”) to automatically identify candidate network devices. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task. Alternatively, discovery of candidate network devices may be performed manually by inspecting the network topology and the network devices attached to the network.” Slater '796, 11:37-54.</p> <p>“The method of creating a cluster of Ethernet switches depends on each particular network configuration. If the switches are arranged in a star topology, as in FIG. 8, with the commander switch at the center, all of the expansion switches may be added to the cluster at once. On the other hand, if the switches are connected in a daisy-chain topology, as in FIG. 9, the candidate switch that is connected to the commander switch is added first, and then each subsequent switch in the chain is added as it is discovered by CDP. If switches are daisy-chained off a star topology, as in the exemplary hybrid configuration shown in FIG. 10, all the switches that are directly connected to the commander switch may be added first, and then the daisy-chained switches may be added one at a time.” Slater '796, 12:21-34.</p>

No.	'904 Patent Claim 19	The Reference
		<p>"If the commander switch of a cluster fails, member switches continue forwarding but cannot be managed through the commander switch. Member switches retain the ability to be managed through normal standalone means, such as the console-port CLI, and they can be managed through SNMP, HTML, and Telnet after they have been assigned an IP address. Recovery from a failed command switch can be accomplished by replacing the failed unit with a cluster member or another switch. To have a cluster member ready to replace the commander switch, the network administrator must assign an IP address to another cluster member, and know the command-switch enable password for that switch." Slater '796, 12:44-56.</p> <p>"One advantage of the present invention is that a network administrator need set only one IP address, one password, and one system SNMP configuration in order to manage an entire cluster of switches. A cluster can be formed from switches located in several different buildings on a campus, and may be linked by fiber optic, Fast Ethernet, or Gigabit Ethernet connections." Slater '796, 13:8-14.</p>
19[c]	each slave unit comprising one or more ports to respective subscriber lines, in a daisy chain between the first and second master units,	<p>The Reference discloses each slave unit comprising one or more ports to respective subscriber lines, in a daisy chain between the first and second master units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra at 1[c].</i></p>

No.	'904 Patent Claim 19	The Reference
19[d]	<p>conveying initial downstream data packets, received from the network by one of the master units, along the daisy chain in a first direction, so as to deliver the packets to the ports of the slave units, and</p>	<p>The Reference discloses conveying initial downstream data packets, received from the network by one of the master units, along the daisy chain in a first direction, so as to deliver the packets to the ports of the slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco continues to make innovative contributions to the area of redundant stacked switch technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <p>Cisco commercialized and patented technology relating to monitoring, detecting, and resolving faults without requiring a network reconfiguration <i>before</i> Orckit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Daruwalla • Nederveen • Slater '421 • Petersen <p><u>Daruwalla discloses:</u></p> <p>"The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. The present invention provides a protection protocol which simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring,</p>

No.	'904 Patent Claim 19	The Reference
		<p>identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol will appropriately remove a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, would remove protection for a low priority failure, such as a signal degrade, on another link.” Daruwalla, Abstract.</p> <p style="text-align: center;"><u>100</u></p> <p>Daruwalla, FIG. 1.</p>

No.	'904 Patent Claim 19	The Reference
		<p>The diagram illustrates a state transition system with seven nodes labeled A through G. The nodes are arranged in a roughly circular pattern. Transitions between nodes are indicated by arrows, each labeled with a code such as (A, idle, S), (B, SF, L), or (C, SF, L). The labels include:</p> <ul style="list-style-type: none"> Node A: (B, SF, L) from G, (B, SF, L) to F, (A, idle, S) to B. Node B: (C, idle, S) from C, (B, SF, S) to C, (B, SF, L) to A, (B, SF, L) to D. Node C: (C, SF, L) to B, (C, SF, L) to D. Node D: (C, SF, L) from C, (C, SF, L) to E. Node E: (C, SF, L) from D, (202e) to F. Node F: (202f) to G, (202g) to A. Node G: (204b) to F, (204a) to B. <p>Other components shown include a central vertical structure labeled 210, a curved structure labeled 212, and a dashed box labeled 206 containing node C.</p>

No.	'904 Patent Claim 19	The Reference
		<p>Daruwalla, FIG. 11.</p> <p>"The present invention relates to computer networks. In particular, the present invention relates to a system and method for providing a protection protocol for fault recovery for a two line bi-directional ring network." Daruwalla, 1:8-11.</p> <p>"FIG. 1 shows an example of a two line bi-directional ring network. The ring network 100 is shown to include nodes 102 a-102 g. Each node is typically a computer with embedded processors and at least one network connection. Each node 102 a-102 g is shown to be bidirectionally coupled to two neighboring nodes 102 a-102 g via an inner connection ring</p>

No.	'904 Patent Claim 19	The Reference
		<p>110 a-110 g and an outer connection ring 108 a-108 g. For instance, node 102 a is bidirectionally coupled to nodes 102 b and 102 g. The example of FIG. 1 also shows a problem 104 in the connection between node 102 b and node 102 c. When a problem is detected (such as a bi-directional line cut), the connection between nodes 102 b and 102 d wraps back upon itself, as shown by wraps 106 a and 106 b. In this manner, the connection problem 104 can be avoided.” Daruwalla, 1:17-30.</p> <p>“In a conventional SONET network, each message sent by a sending node to a receiving node typically needs the identification and location of the receiving node to arrive at the proper destination. Accordingly, manual configuration is typically needed in each node to store the identity and location of each other node in the ring network in order to provide for communication between the nodes in the network.” Daruwalla, 1:31-44.</p> <p>“In summary, for the protection mechanism to operate, each node needs to know the current ring map (current ring topology). What is needed is a system and method for providing fault recovery for two line bi-directional ring network that minimizes the need to keep track of other nodes in the ring network. Preferably, the system would not require reconfiguration of an internal map of the network when a new node is added to, or existing nodes are removed from the network. The present invention addresses such a need.” Daruwalla, 2:23-31.</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. According to the embodiment, when the problem is identified, the node sends a message identifying the problem to a second neighbor which is located at least one node away from the problem. The second neighbor then forwards the message to a third neighbor, unless the second neighbor is dealing with a situation that is higher in a hierarchy of situations than the problem described in the message by the original node. In general, if the second neighbor's situation has a higher priority than the situation described by the original node, then the message is ignored and not forwarded. If, however, the message sent by the original node describes a situation with a higher priority than the situation being dealt with by the second neighbor, then, in general,</p>

No.	'904 Patent Claim 19	The Reference
		<p>the second neighbor's situation is ignored, at least for the moment, and the original node's message is forwarded to the next neighbor. In general, a higher priority request preempts a lower priority request within the ring. Exceptions are noted as rules of the protection protocol." Daruwalla, 2:35-58.</p> <p>"The present invention provides a protection protocol that simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol automatically appropriately removes a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, removes protection for a low priority failure, such as a signal degrade, on another link." Daruwalla, 2:59-3:3.</p> <p>"A method according to an embodiment of the present invention for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and initiating a fault recovery procedure when the second node receives the first message." Daruwalla, 3:4-12.</p> <p>"In another aspect of an embodiment of the present invention, a method for adding a new node to a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node prior to an addition of the new node; initiating a fault recovery procedure when the second node receives the first message; receiving a second message from the new node; and entering an idle state when the second message is received." Daruwalla, 3:13-24.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“In yet another aspect of an embodiment of the present invention, a system for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The system comprises means for detecting a situation by a first node, wherein the first node is one of the plurality of nodes; means for sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and means for initiating a fault recovery procedure when the second node receives the first message.” Daruwalla, 3:25-35.</p> <p>“FIG. 2 is block diagram of a ring network utilizing a protection protocol according to an embodiment of the present invention.” Daruwalla, 3:40-42.</p> <p>“FIGS. 4-6 are flow diagrams illustrating various rules within the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:46-48.</p> <p>“FIGS. 8-12 are flow diagrams and a system diagram illustrating further rules of the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:52-54.</p> <p>“FIG. 2 is a block diagram showing a ring network system utilizing a method of fault recovery according to an embodiment of the present invention. The ring network 200 is shown to include nodes 202 a-202 g. The nodes 202 a-202 g are shown to be coupled via an inner ring 210 in which the data flows in one direction, such as a clockwise direction. Additionally, the nodes 202 a-202 g are also shown to be coupled by an outer ring 212 in which data can flow in the opposite direction to the inner ring 210, such as in a counter-clockwise direction. The ring network 200 is shown to have a situation 204 a that requires protection, such as a ring wrap 206.” Daruwalla, 5:35-45.</p> <p>“FIG. 4 is a flow diagram of an example of a method according to an embodiment of the present invention implied by Rules 1-22. An APS packet is received via step 400. It is determined whether the APS packet has been sent along a long path via step 402. If the packet was not sent via a long path, then the APS packet is not forwarded via step 406. Accordingly, if the APS packet was sent via the short path, then the packet is not forwarded via step 406. If, however, the packet was sent through the long path via step 402, then the APS packet may</p>

No.	'904 Patent Claim 19	The Reference
		<p>be forwarded via step 404. Note that for this example of Rule (1), it is assumed that the long path message does not have to pass through a wrapped connection in order to be forwarded. Otherwise, if the long path message needs to pass through a wrapped connection in order to be forwarded, then the message will simply not be forwarded.” Daruwalla, 6:21-36.</p> <p>“FIG. 6 is a flow diagram illustrating Rules 4 and 5. A node detects a problem between the node and a first neighbor via step 600. The node performs a wrap away from the side on which the problem exists via step 602. A short path message is then sent to the first neighbor informing it of the problem via step 604. Additionally, a long path message is also sent to a second neighbor informing the second neighbor of the problem via step 604. The first neighbor then performs a wrap away from the side of the problem via step 606. The first neighbor also sends an IDLE message, indicating a wrapped status, on a short path to the node that detected the problem via step 608. This message is sent across the failed span. Note that IDLE messages do not get wrapped and are sent across failed spans if possible. Additionally, the first neighbor also sends a message on a long path toward the side without the problem via step 608.” Daruwalla, 6:64-11.</p> <p>“An example of the method described in FIG. 6 can be seen in FIG. 2. Node 202 b has detected a problem 204 a and performs a wrap 206 on the side on which the problem exists. Node 202 b also sends a short path message to the neighbor on the other side of the problem 204 a, which is node 202 c. Node 202 b also sends a long path message to its other neighbor node 202 a informing it of the problem. Node 202 c performs a wrap 206 on the side of the problem and sends an IDLE message on a short path to node 202 b. Node 202 c also sent a message on a long path toward the side without the problem to its neighbor 202 d.” Daruwalla, 7:12-21.</p> <p>“FIG. 7 lists the hierarchy of priorities of Rule (8). For ease of reference, the hierarchy is separated into Class I-III. Class I is the highest priority, while Class III is the lowest priority. An example of a highest priority message in Class I is lockout. Lockout is an order stating that the ring network is not to wrap under any circumstances.” Daruwalla, 7:22-26.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Examples of the next priority listed in Class II are forced switch and signal fail. Forced switch indicates that the ring network is configured to wrap at the point of the forced switch. Signal fail is a situation where either two nodes cannot communicate with each other, or one node cannot hear the other node. An example of a signal fail is a physical break in the communication lines between two nodes.” Daruwalla, 7:27-33.</p> <p>“Note that members of Class II can co-exist (Rule 9). For example, multiple forced switches and signal fails can co-exist. Additionally, members of Class I can co-exist (Rule 10). For example, multiple lockouts in a single ring network can co-exist. However, situations in Class III cannot co-exist with other situations (Rule 11). For example, a signal degrade cannot co-exist with a wait-to-restore.” Daruwalla, 7:52-58.</p> <p>“When there are multiple requests of the same priority within Class III, the first request to complete a long path signaling will take priority (Rule 13). For example, if there are two signal degrades located on the same ring network, then the first signal degrade which completes the long path signaling will take priority over the other signal degrade. By not allowing members of Class III to co-exist with one another, partitioning of the ring network is avoided.” Daruwalla, 7:59-65.</p> <p>“In case of two equal requests within Class III on both inner and outer rings of the ring network, the tie is broken by choosing a request on one of the rings, such as the outer ring request (Rule 14). For example, if a signal degrade occurs both on the inner and outer rings, then a request on a predetermined ring, such as the outer ring, will take priority over the other requests.” Daruwalla, 7:66-8:5.</p> <p>“FIG. 8 is a flow diagram illustrating Rules (9), (10), (11), (13), and (15). Note that the flow diagram described in FIG. 8 is merely an example of one way in which the rules of the method according to the embodiment of the present invention can be executed. For example, the determination of whether the long path message is a Class I request via step 802 or a Class II request via step 810 can be in reverse order.” Daruwalla, 8:6-11.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“A wrapped node receives a long path message via step 800. It is then determined if the long path message is a Class I request via step 802. The classes used in FIG. 8 are meant to correspond with the example of classes defined in FIG. 7. If the long path message is a Class I request, then it is determined if a local situation also has a Class I request via step 804. A local situation includes scenarios such as when a node detects a situation or problem, or when a node is made aware of a problem or situation via a short path message from its neighbor. If a local situation is not a Class I request via step 804, then any existing local wraps are unwrapped and the long path message is forwarded via step 806. If, however, a local situation is a Class I request via step 804, then the connections are already unwrapped or was never wrapped, and the long path message is forwarded via step 808.” Daruwalla, 8:12-26.</p> <p>“FIG. 12 is a flow diagram illustrating rules (20) and (21) of the method according to the embodiment of the present invention. A wrapped node determines that a problem has been cleared via step 1200. It then enters a wait-to-restore state via step 1202. It is then determined if its neighbor is the same neighbor as previously noted via step 1204. The node can save the source of a short path message at the time of wrap initiation to note the identity of its neighbor. If the current neighbor is not the same as the previous neighbor via step 1204, then an IDLE state is entered via step 1206. If, however, the current neighbor is the same as the previous neighbor via step 1204, then it is determined whether a pre-determined wait-to-restore time has expired via step 1208. Once the pre-determined wait-to-restore time has expired, then the node enters an IDLE state via step 1206.” Daruwalla, 12:60-13:6.</p> <p>“A method and system for fault recovery for a two line bi-directional network has been disclosed. Software written according to the present invention may be stored in some form of computer-readable medium, such as memory or CD-ROM, or transmitted over a network, and executed by a processor.” Daruwalla, 13:7-19.</p>

No.	'904 Patent Claim 19	The Reference
		<p>Nederveen discloses:</p> <p>“A technique for use in gathering network activity-related information from cascaded network switches is provided. Using this technique, the information can be gathered without substantially reducing performance of the cascaded switches. In one embodiment, a single remote monitoring probe is connected via respective connections to each of the switches so as to receive the information from the switches. In another embodiment, only one of the switches is connected to the probe, and the other switches transmit their respective portions of the information to the switch connected to probe. The switch connected to the probe provides these portions of the information, as well as, any of its respective activity-related information to the probe. In this latter embodiment, the switches may be connected by dedicated connections and switch ports that are used solely for communicating the activity-related information.” Nederveen, Abstract.</p>

No.	'904 Patent Claim 19	The Reference
		<p style="text-align: center;">FIG. 3</p> <p>Nederveen, FIG. 3.</p>

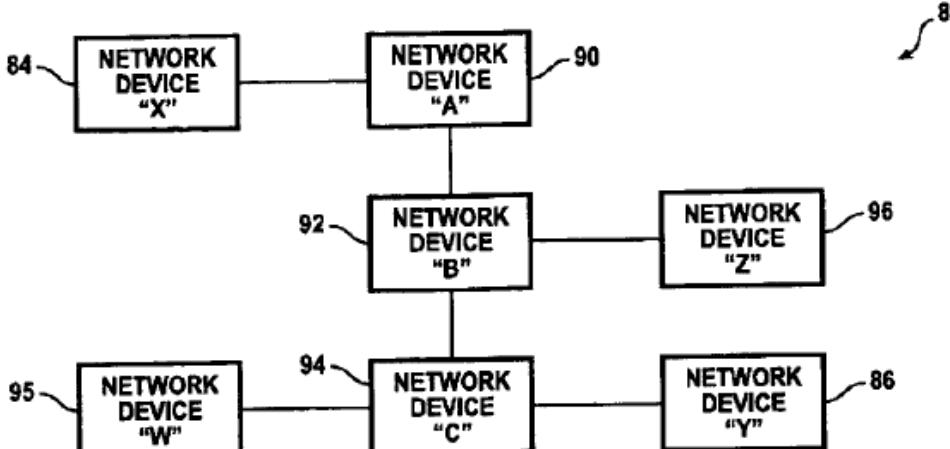
No.	'904 Patent Claim 19	The Reference
		<p style="text-align: center;">FIG. 5</p> <p>Nederveen, FIG. 5.</p> <p>“Thus, it would be desirable to provide a stacked switch monitoring technique that permits efficient offloading of raw data processing from the stacked switches, requires only a minimal number of specialized network entities to gather and process such raw data, and does not result in substantial degradation of stacked switch performance.” Nederveen, 4:38-43.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Accordingly, the present invention provides a technique for remote monitoring of a switch network that overcomes the aforesaid and other disadvantages and drawbacks of the prior art. More specifically, in one aspect of the present invention, a technique is provided for gathering information that may be useful in network management (e.g., switch port activity-related information), from switches in the network that are in a stacked configuration. The information is gathered from the stacked switches by a single network entity (e.g., an SNMP remote monitoring probe) in such a way that it does not substantially degrade the performance of the switches. This is accomplished, in one embodiment of the technique of the present invention, by connecting the switches via respective connections to a multiplexer that selectively connects the switches, according to an arbitration scheme, to the single network entity. The entity gathers respective portions of the information from switches when it is connected to the switches by the multiplexer. The information gathered by the entity may be provided to another network entity (e.g., an SNMP management node) in order to permit the other entity to use that information in managing the network.” Nederveen, 4:46-67.</p> <p>“In another embodiment of the technique of the present invention, only one of the switches is connected to the single information gathering entity. The switches that are not connected to the entity transmit, via respective dedicated ports and connections (i.e., ports and connections that are used solely for network information gathering activities), their respective portions of the information to the switch that is connected to the entity. The switch that is connected to the entity transmits, via a respective dedicated port and connection, the information received from the other switches, as well as, its own information to the entity.” Nederveen, 5:1-11.</p> <p>“FIG. 3 is a schematic, functional block diagram illustrating in greater detail the construction of the stacked switch network shown in FIG. 2.” Nederveen, 5:26-28.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network configured to employ another embodiment of the present invention.” Nederveen, 5:32-34.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“FIGS. 2-5 illustrate features of a computer network 200 wherein embodiments of the present invention may be advantageously practiced. Network 200 comprises a stacked switch network 300 which interconnects a plurality of network segments 228, 232, 240, and 251. Each segment 228, 232, 240 comprises one or more local area networks having computer endstations (not shown). Segment 251 is a network router segment that comprises network router 250. Each segment 228, 232, 240 is coupled via a respective communications link 222, 224, 226 to a respective port 302 (i.e., port A), 304 (i.e., port B), 312 (i.e., port C) of the switch network 300. Likewise, the router 250 of router segment 251 is coupled via a respective trunk line 230 to router port 306 (i.e., port R).” Nederveen, 5:46-59.</p> <p>“Stacked switch network 300 comprises a plurality of data network switches 300A, 300B, 300C (e.g., Catalyst 3900™ series switches of the type commercially available from the Assignee of the subject application) coupled together via conventional stack link bus connection logic 600A, 600B. More specifically, logic 600A couples a stack link bus port and associated logic 500 in switch 300A to a stack link bus link port and associated logic 502 in switch 300B. Similarly, logic 600B couples another stack link bus port and associated logic 501 in switch 300B to a stack link bus port and associated logic 504 in switch 300C. It should be understood that although, as is shown in FIG. 3, switches 300A and 300B, and switches 300B and 300C, may be coupled serially together by separate respective logic elements 600A, 600B, each of the switches 300A, 300B, 300C may be coupled together via a single respective stack link bus port in the switch to a single stack link bus connection logic block (not shown, e.g., of the type that is commercially available under the tradename Catalyst Matrix™ from the Assignee of the subject application). Further alternatively, depending upon the particular design and functionality of the ports 500, 501, 502, and 504, and the control and forwarding logic (whose operation will be described more fully below) in the switches 300A, 300B, 300C, the circuitry in logic 600A, 600B may instead be comprised in the ports 500, 501, 502, and 504 and/or control and forwarding logic, and therefore, in this alternative configuration, the logic 600A, 600B in the network 300 may be replaced by simple connection means (e.g., cable connectors).” Nederveen, 6:29-57.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Each switch 300A, 300B, 300C includes a respective internal bus (e.g., element 800 in switch 300C) that is coupled via at least one stack link bus port and associated interface logic (e.g., 504 in switch 300C) to external stack link bus connection logic (e.g., element 600B in switch 300C). Each switch 300A, 300B, 300C also includes respective programmable control and forwarding logic (e.g., element 802 in switch 300C) comprising processing, memory, and other circuitry for storing and learning configuration information (e.g., source and destination MAC addresses of messages received by the switch, switch bridging table, switch segments' spanning tree and virtual local area network information, etc.), and for providing appropriate commands to other elements (e.g., the switch ports) to cause data messages received by the switch to be forwarded to appropriate network segments coupled to the switch based upon this configuration information. In each switch, the switch's port logic circuitry (e.g., port A logic 302 and port P logic 310C in switch 300C) and control and forwarding logic are coupled to each other via that switch's respective internal bus. The stack link bus port and associated logic in each switch 300A, 300B, 300C may comprise a Catalyst™ stack port line interface card (commercially available from the Assignee of the subject application) inserted into a bus expansion slot (not shown) in the switch. Although not shown in the Figures for purposes of clarity of illustration, each switch 300A, 300B, 300C in network 300 typically will include tens or hundreds of ports coupled to network segments.” Nederveen, 6:58-7:19.</p> <p>“The control and forwarding logic and stack link bus port and associated logic in each switch, and the logic 600A, 600B, are configured to together implement conventional techniques for permitting the switches 300A, 300B, 300C to function together as a single logical/virtual switch. More specifically, when configured in the stacked arrangement 300, after the switches 300A, 300B, 300C and logic 600A, 600B are initially activated, they execute initial power-on self-diagnostics, and thereafter, enter a “stack discovery” mode of operation.” Nederveen, 7:20-29.</p> <p>“In the stack discovery mode of operation, the control and forwarding logic in each switch 300A, 300B, 300C first “senses” that its switch is coupled to logic 600A and/or 600B, and then determines the particular configuration of the stacked switch network 300, using suitable conventional autosensing/autoconfiguration techniques. The control and 321orwarding logic in the switches 300A, 300B, 300C then assigns to the switches respective unique</p>

No.	'904 Patent Claim 19	The Reference
		<p>identification numbers (e.g., based upon unique identification numbers of respective ports of the logic 600A, 600B to which the switches are coupled).” Nederveen, 7:30-40.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network 300' configured to employ another embodiment of the present invention. It should be understood that unless specifically stated to the contrary, the structure and operation of the network 300' are substantially the same as the structure and operation of network 300. In network 300', each of the dedicated ports 310A, 310B, 310C comprises a respective transmit portion and receive portion, referenced in FIG. 5 as RX and TX, respectively.” Nederveen, 11:20-29.</p> <p><u>Slater '421 discloses:</u></p> <p>“A method and apparatus for discovering paths to other network devices includes a protocol and network management application that can be executed on network devices. The Ethernet protocol is used to detect paths to other network devices, knowing only the Ethernet address of the destination. A discovery protocol is extended to add hop probe and hop probe reply Type-Length-Value fields in a variable-length list. The hop probe fields contain a hop count, a destination Ethernet address, and a source Ethernet address. When a hop probe is received by a network device, the hop count field is decremented by one and the hop probe is forwarded. Packets received with a hop count of one are not forwarded and a hop probe reply is sent back to the Ethernet source address of the hop probe. The hop probe reply fields contain a destination Ethernet address and a source Ethernet address.” Slater '421, Abstract.</p>

No.	'904 Patent Claim 19	The Reference
		 <p>FIG. 6</p> <p>Slater '421, FIG. 6.</p> <p>"Partly as a result of the increased complexity of networks, network administrators must often troubleshoot problems with their network. Two classes of network problems often faced by network administrators are "reachability" problems and performance slowdowns. Reachability problems occur when one or more network devices cannot be accessed through a network, and can be caused by hardware or software failures, cabling problems, or any of several other types of difficult-to-diagnose problems that can occur in a network." Slater '421, 7:11-20.</p> <p>"Some of the world's largest networks today rely on the TCP/IP suite of networking protocols. With a relatively small kit of basic tools, network administrators can learn much about an internetwork. "Ping" and "traceroute" commands, "show" commands, and "debug" commands (all of which are typically available via the basic management interface on a network device) form the core of the network administrator's internetwork toolkit. Ping and</p>

No.	'904 Patent Claim 19	The Reference
		<p>traceroute commands can be useful tools in determining where failures are occurring, but they are cumbersome to use, and require knowledge of the IP address or host name of the destination network device. The show commands provide information about interface conditions, protocol status, neighbor reachability, router configuration and status, level of traffic, errors and drops, and other network data. Finally, debug commands provide clues about the status of a network device and other network devices directly or indirectly connected to it. Because debug commands can create performance slowdowns, they must be used with great care, and using the wrong debug command at the wrong time can exacerbate problems in already poorly performing networks.” Slater '421, 7:55-8:8.</p> <p>“Embodiments of the present invention as illustrated herein use the Cisco™ Discovery Protocol (“CDP”) to automatically detect paths to specified network devices in Ethernet LANs. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task.” Slater '421, 9:10-15.</p> <p>“CDP is a media-independent device discovery protocol which can be used by a network administrator to view information about other network devices directly attached to a particular network device. In addition, network management applications can retrieve the device type and SNMP-agent address of neighboring network devices. This enables applications to send SNMP queries to neighboring devices. CDP thus allows network management applications to discover devices that are neighbors of already known devices, such as neighbors running lower-layer, transparent protocols.” Slater '421, 9:16-26.</p> <p>“It is to be understood that the present invention is not limited to devices that are compatible with CDP. CDP runs on all media that support the Subnetwork Access Protocol (“SNAP”), including LAN and Frame Relay. CDP runs over the data link layer only. Each network device sends periodic messages to a multicast address and listens to the periodic messages sent by others in order to learn about neighboring devices and determine when their interfaces to the media go up or down. Each device also advertises at least one address at which it can receive SNMP messages. CDP messages, or “advertisements,” contain holdtime information, which indicates the period of time a receiving device should hold CDP information from a neighbor before discarding it. With CDP, network management applications can learn the device type</p>

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		<p>and the SNMP-agent address of neighboring devices. This process enables applications to send SNMP queries to neighboring devices.” Slater ’421, 9:27-43.</p> <p>“It should be noted that—normally, CDP packets according to aspects of the present invention are transmitted at regular intervals (e.g. once every 60 seconds). However, in embodiments of the present invention, when a Hop Probe or Hop Probe Reply needs to be forwarded by a network device, the network device is commanded to send a CDP packet immediately.” Slater ’421, 16:66-17:5.</p> <p>“The present invention is much faster than the previous method that involved logging in to each intermediate network device, entering the “show cdp neighbors” command, and interpreting the output to find the next hop along the path to the destination network device. Also, the present invention allows individual users, such as network administrators, to execute a tool to manually discover paths through a network of Ethernet switches. The present invention can be used by network management software to automatically map the topology of clusters of network devices, such as Ethernet switches. Finally, the present invention is useful in loop detection. Enhancements to Spanning Tree and other bridge-level routing protocols can test proposed changes to switch topology prior to making them.” Slater ’421, 17:6-20.</p> <p>Petersen discloses:</p> <p>“Methods and apparatus for enabling communication between a source network device and one or more destination network devices are disclosed. A system enabling communication between a source network device and one or more destination network devices includes a switch and a ring interconnect. The switch is adapted for connecting to the source network device and the one or more destination network devices. More particularly, the switch is capable of storing data provided by the source network device and retrieving the data for the one or more destination network devices. The ring interconnect is adapted for connecting the source network device and the one or more destination network devices to one another. In addition, the ring interconnect is capable of passing one or more free slot symbols along the ring interconnect. Thus, the ring interconnect is capable of expanding when one or more of the free slot symbols are each replaced by a frame notify message indicating that the data has</p>

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		<p>been stored by the switch for retrieval by the one or more destination network devices.”</p> <p>Petersen, Abstract.</p> <pre> sequenceDiagram participant SourceSat as Source Sat. participant Switch participant DestSat as Dest. Sat. SourceSat->>Switch: Receive frame and break into cells activate SourceSat Note over SourceSat: 208 Note over SourceSat: ->202: Receive frame and break into cells deactivate SourceSat Switch->>SourceSat: Send cells inc. data and header to switch (inc. retrieval count) activate Switch Note over Switch: 210 Note over Switch: ->204: Send cells inc. data and header to switch (inc. retrieval count) deactivate Switch Note over SourceSat: 210 Note over SourceSat: ->204: Send cells inc. data and header to switch (inc. retrieval count) activate SourceSat Note over SourceSat: 212 Note over SourceSat: ->206: Switch stores cell data in Data Buffer and cell header in Buffer Table (inc. retrieval count) deactivate SourceSat Note over Switch: 212 Note over Switch: ->206: Switch stores cell data in Data Buffer and cell header in Buffer Table (inc. retrieval count) deactivate Switch Note over DestSat: 214 Note over DestSat: ->206: Dest. Sat. activate DestSat Note over DestSat: 216 Note over DestSat: ->204: Send Frame Storage message indicating frame stored and location of frame deactivate DestSat Note over Switch: 216 Note over Switch: ->204: Send Frame Storage message indicating frame stored and location of frame deactivate Switch Note over SourceSat: 218 Note over SourceSat: ->206: Copy and store the Notify msg. (inc. buffer #) in notify queue deactivate SourceSat Note over Switch: 218 Note over Switch: ->206: Copy and store the Notify msg. (inc. buffer #) in notify queue deactivate Switch Note over DestSat: 220 Note over DestSat: ->206: Modify Frame Notify message to indicate whether able to accept and store the Frame Notify msg. deactivate DestSat Note over Switch: 220 Note over Switch: ->206: Modify Frame Notify message to indicate whether able to accept and store the Frame Notify msg. deactivate Switch Note over DestSat: 222 Note over DestSat: ->206: Send Frame Retrieval msg. for buffer # at head of notify queue to retrieve frame deactivate DestSat Note over Switch: 222 Note over Switch: ->206: Send Frame Retrieval msg. for buffer # at head of notify queue to retrieve frame deactivate Switch Note over DestSat: 228 Note over DestSat: ->206: Reconstruct frame for transmission deactivate DestSat Note over Switch: 228 Note over Switch: ->206: Reconstruct frame for transmission deactivate Switch Note over DestSat: 230 Note over DestSat: ->206: Send modified Frame Notify message on notify ring deactivate DestSat Note over Switch: 230 Note over Switch: ->206: Send modified Frame Notify message on notify ring deactivate Switch Note over DestSat: 232 Note over DestSat: ->206: Send Buffer Table subtract msg. corr. to the modified frame Notify msg. (e.g., subtract # destinations unable to copy frame Notify msg. from retrieval count) deactivate DestSat Note over Switch: 232 Note over Switch: ->206: Send Buffer Table subtract msg. corr. to the modified frame Notify msg. (e.g., subtract # destinations unable to copy frame Notify msg. from retrieval count) deactivate Switch Note over DestSat: 234 Note over DestSat: ->206: Removes Notify msg. from ring and discards deactivate DestSat Note over Switch: 234 Note over Switch: ->206: Removes Notify msg. from ring and discards deactivate Switch </pre>

FIG. 2

Petersen, FIG. 2.

No.	'904 Patent Claim 19	The Reference
		<p>“The present invention relates to a mixed topology data switching system that combines a radial interconnect with a ring interconnect. More particularly, the radial interconnect permits devices to store and retrieve data using a switch, while the ring interconnect permits devices along the ring interconnect to provide notification that data has been stored for retrieval, as well as provide feedback regarding the ability or inability to retrieve such data.” Petersen, 1:34-41.</p> <p>“In controlling the flow of network traffic through a switching system, it is often desirable to provide feedback to the source of the data. For instance, although a transmitting device, hereinafter referred to as a “source device,” may transmit or forward data to a receiving device, hereinafter referred to as a “destination device,” the destination device may be incapable of handling the data. In these circumstances, the source device is often unaware that the data was not accepted by the destination device, complicating switch management. Common solutions to the problem of switch traffic management have included ensuring that all intended destination devices are “ready to receive” prior to transmitting data on a ring or bus interconnect, or insisting that each intended destination device send an explicit acknowledgement back to the source device. Both of these approaches result in reduced efficiency of the interconnect scheme. By way of example, in a ring network, such acknowledgment is typically provided in the data frame being transmitted. As another example, in other interconnect schemes, each such device may send a separate acknowledgment, therefore adding to the traffic on the network. Accordingly, it would be desirable if a traffic management scheme were established which could provide such feedback to the source of the data while minimizing traffic management overhead.” Petersen, 2:8-32.</p> <p>“According to one embodiment, the present invention combines the use of two data transport methods: a point-to-point radial interconnect and a ring interconnect. The radial interconnect connects interface devices to each other through the services of a central switch device to permit the transport of data. Typically, a single interface has a single dedicated radial interconnect to the central switch. These interface devices are further connected to one another via a ring interconnect to convey retrieval notifications regarding forwarding of the data (by source devices) and receipt of the data (by destination devices).” Petersen, 2:36-46.</p>

No.	'904 Patent Claim 19	The Reference
		<p>“Each radial interconnect provides a narrow, high baud-rate connection to convey to the actual data from and to the associated interface without being burdened by the unrelated traffic for the remaining interfaces in the system. This is accomplished through the use of a central switch device, which stores and retrieves data for the various interfaces in the system. As will be apparent from the following description, this architecture provides numerous advantages over a wide parallel bus or ring.” Petersen, 2:47-55.</p> <p>“The ring interconnect may be used to convey a “retrieval notification”(i.e., retrieval message) that may be observed by all potential retrieving interfaces. The retrieval notification notifies specific devices (“destination devices”) or interfaces that one or more frames addressed to them are available from the switch device. Moreover, the ring interconnect permits each destination device to provide feedback to the source device letting the source know whether the destination has accepted the notification provided by the source device and therefore whether the destination can retrieve the data intended for it. The feedback is particularly useful in buffer management applications. In this manner, an efficient and flexible data transport and retrieval notification system that includes a feedback path to the source of the data is provided.” Petersen, 2:56-3:3.</p> <p>“FIG. 2 is a process flow diagram illustrating a method of providing network communication according to an embodiment of the invention.” Petersen, 3:9-11.</p> <p>“FIG. 2 is a process flow diagram illustrating in further detail a method of providing network communication in the above-described system according to an embodiment of the invention. As shown, process steps performed by a source device 202 are illustrated along an associated vertical line, steps performed by a switch 204 are illustrated along another vertical line, and steps performed by a destination device 206 are illustrated along still another vertical line.” Petersen, 4:50-57.</p> <p>“When the frame is stored by the switch 418, the source device preferably receives an acknowledgment that the data has been stored. Thus, to provide this feedback, a frame storage message (i.e., storage reply) is sent from the switch 418 on the channel 416 to the channel interface 414. The frame storage message is then provided to the notify ring interface as</p>

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		<p>shown at 430 and sent on the notify ring. Once this acknowledgment is received by the interface device 402, the designated destination devices may be notified via notify ring interface 424. As described above, a Frame Notify message may be sent via the notify ring interface 424 to the destination devices. More particularly, the Frame Notify message may identify one or more destination devices for the frame and specify the location of the frame to be retrieved. By way of example, the location of the frame to be retrieved by the destination devices may be designated by a buffer number 430. In addition, the destination devices for the frame may be specified in the Frame Notify message through a notify queue map 426. More particularly, the notify queue map 426 may specify a notify queue associated with a particular destination device. The notify queue may be expressly designated through the use of one or more bits as well as implied through the specification of a priority level for the data. The notify queue map 426 will be described in further detail with reference to FIG. 13. The notify ring interface 424 then creates a Frame Notify message including the notify queue map 426 and the buffer number 430 which is then sent on an outbound interface of the notify ring 432.” Petersen, 7:55-8:16.</p> <p>“As described above, the notify ring may be expanded to accommodate communication between interface devices. The communication between the interface devices and the switch is therefore performed on one or more channels rather than the notify ring. As a result, the flexibility of the notify ring does not effect the speed with which the interface devices may communicate with the switch. Thus, where a single port operates at a faster speed than the channels, multiple channels may be grouped together. In this manner, the speed with which the switch may communicate with the interface devices may be maximized.” Petersen, 20:27-36.</p> <p>“The present invention provides a mixed topology data switching system that combines a point-to-point radial interconnect with a ring interconnect to maximize the speed of network traffic. The radial interconnect provides a narrow, high baud-rate connection to convey the data traffic for just the interface in question, without being burdened by all of the unrelated traffic for the remaining interfaces in the system. At the same time, the ring interconnect permits retrieval notifications to be observed by all potential retrieving interfaces. The ring topology further permits each destination interface to provide feedback to the source interface,</p>

No.	'904 Patent Claim 19	The Reference
		which is valuable for buffer management applications. Moreover, the point-to-point ring topology bus employs a variable latency access method that enables messages to be passed across the bus with low latency when the system is quiet and with increased latency when the system is busy. In addition, since control messaging around the ring interconnect and across the channel interconnects are embedded in the data stream, the number of pins required and manufacturing costs are reduced.” Petersen, 20:38-57.
19[e]	in the event of a fault in the daisy chain, conveying further downstream data packets, received from the network by one of the master units, along the daisy chain in a second direction, opposite to the first direction, so as to deliver the further packets to the ports of at least some of the slave units.	<p>The Reference discloses in the event of a fault in the daisy chain, conveying further downstream data packets, received from the network by one of the master units, along the daisy chain in a second direction, opposite to the first direction, so as to deliver the further packets to the ports of at least some of the slave units.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Below are examples of such references.</p> <p>Sugawara discloses:</p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p>

No.	'904 Patent Claim 19	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right; color: red;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotation added)</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.")</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link</p>

No.	'904 Patent Claim 19	The Reference
		<p>communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>

No.	'904 Patent Claim 20	The Reference
20	<p>A method according to claim 19, wherein the initial and further downstream packets are received from the network by the first master unit, and wherein conveying the further downstream packets in the second direction comprises conveying the further downstream packets from the first master unit to the second master unit, and then conveying the further downstream packets from the second master unit to the daisy chain.</p>	<p>The Reference discloses a method according to claim 19, wherein the initial and further downstream packets are received from the network by the first master unit, and wherein conveying the further downstream packets in the second direction comprises conveying the further downstream packets from the first master unit to the second master unit, and then conveying the further downstream packets from the second master unit to the daisy chain.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 21	The Reference
21	<p>A method according to claim 20, wherein conveying the further downstream packets from the first master unit to the second master unit comprises linking further slave units in an additional daisy chain between the first and second master units, and conveying the further downstream packets from the first master unit to the second master unit over the additional daisy chain.</p>	<p>The Reference discloses a method according to claim 20, wherein conveying the further downstream packets from the first master unit to the second master unit comprises linking further slave units in an additional daisy chain between the first and second master units, and conveying the further downstream packets from the first master unit to the second master unit over the additional daisy chain.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Below are examples of such references.</p> <p><u>Sugawara discloses:</u></p> <p>Sugawara, 3:6-14 (“FIG. 2 is a schematic block diagram illustrating the principle of the present invention. As illustrated, according to the present invention, switch circuits SW1 and SW2 are provided which selectively connect supervisory and control units MPU1 and MPU2 to corresponding ones of multiplexing/demultiplexing circuits MUXA to MUXD and MUXa to MUXd. A backup line P is not provided.”).</p>

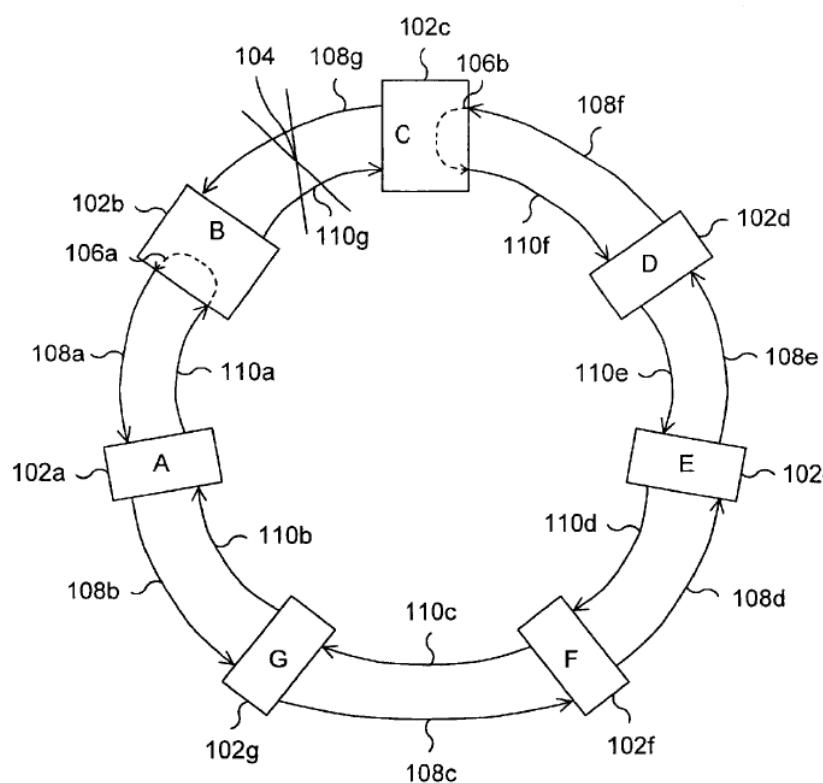
No.	'904 Patent Claim 21	The Reference
		<p style="text-align: center;">FIG. 2</p> <p style="text-align: right;">Plurality of daisy chains</p> <p style="text-align: center;">FIG. 2 (annotated).</p> <p>Sugawara, 3:24-50 ("When a failure of the line A is detected, the data link communication is switched from the line A to another, for example, the line B. This switching is made as follows. That is, a line failure is usually detected as a failure in reception. When detecting the line failure in terms of error rate by way of example, the line interface circuit LIUa on the receiving side informs the supervisory and control unit MPU2 via MUXa of the detection of the line failure. Upon being informed of the line failure, MPU2 informs MPU1 of the failure of the line A via another line, for example, another line of B directed from MPU2 to MPU1 not shown. Responsive to this, the supervisory and control unit MPU1 switches switch circuit SW1 to connect MPU1 to another line, for example, the line B. Thereby, the data link communication becomes effected over the line B.</p> <p>If a failure should also occur in the line B, the line switching will be made likewise. The line B is thus switched to another line, for example, the line C. In FIG. 1, if the line A is faulty, it is switched to the backup line P and, if the backup line P is also faulty, the data link</p>

No.	'904 Patent Claim 21	The Reference
		<p>communication will be interrupted. According to the present invention, as long as there are normal lines, data link communications can be continued. No backup is needed. However, if a failure occurs in a line, the transmission of a main signal over the line is interrupted.”).</p> <p>Sugawara, Abstract (“A line interface circuit on the receiving side detects communication failure in terms of an error rate and informs the supervisory and control unit on the receiving side of the occurrence of the failure. The supervisory and control unit on the receiving side in turn switches the line data communications from the receiving side to the transmitting side to another line unused for data communications to inform the supervisory and control unit on the transmitting side of the occurrence of the failure. The supervisory and control unit on the transmitting side switches the line for data communications from the transmitting side to the receiving side to the other line. The switching circuit on the receiving side comprises buffers each inserted in a line and a buffer connected to the common outputs of the buffers so that the supervisory and control unit on the receiving side automatically receives a transmit signal transmitted over a line switched on the transmitting side.”)</p>

No.	'904 Patent Claim 22	The Reference
22[a]	A method according to claim 19, and comprising conveying initial upstream data packets, received by the slave units from the subscriber lines, along the daisy chain in the second direction so as to transmit the upstream data packets via the first master unit over the network,	<p>The Reference discloses a method according to claim 19, and comprising conveying initial upstream data packets, received by the slave units from the subscriber lines, along the daisy chain in the second direction so as to transmit the upstream data packets via the first master unit over the network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 22	The Reference
22[b]	and in the event of the fault, conveying further upstream data packets received by one or more of the slave units along the daisy chain in the first direction via the second master unit.	<p>The Reference discloses and in the event of the fault, conveying further upstream data packets received by one or more of the slave units along the daisy chain in the first direction via the second master unit.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>
23	A method according to claim 22, and comprising multicasting the upstream data packets from the first master unit to the network and to the second master unit, which transmits the multicast upstream data packets over the network.	<p>The Reference discloses a method according to claim 22, and comprising multicasting the upstream data packets from the first master unit to the network and to the second master unit, which transmits the multicast upstream data packets over the network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p>

No.	'904 Patent Claim 24	The Reference
24	<p>A method according to claim 19, wherein conveying the initial downstream data packets along the daisy chain comprises pre-switching the packets at each of the slave units, so that packets not addressed to any of the ports on the slave unit are passed to the next slave unit in the daisy chain, while packets that are addressed to one or more of the ports on the slave unit are passed to a switch fabric that directs the packets to the ports to which they are addressed.</p>	<p>The Reference discloses a method according to claim 19, wherein conveying the initial downstream data packets along the daisy chain comprises pre-switching the packets at each of the slave units, so that packets not addressed to any of the ports on the slave unit are passed to the next slave unit in the daisy chain, while packets that are addressed to one or more of the ports on the slave unit are passed to a switch fabric that directs the packets to the ports to which they are addressed.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p>Cisco commercialized and patented technology relating to monitoring, detecting, and resolving faults without requiring a network reconfiguration <i>before</i> Orckit. Some examples of Cisco's patents (and other disclosures) for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Daruwalla • Nederveen • Slater '421 • Petersen <p>Daruwalla discloses:</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. The present invention provides a protection protocol which simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally,</p>

No.	'904 Patent Claim 24	The Reference
		<p>independently operating ring networks can be merged and the protection protocol will appropriately remove a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, would remove protection for a low priority failure, such as a signal degrade, on another link.” Daruwalla, Abstract.</p>  <p>The diagram illustrates a network topology with seven nodes labeled A through G. Node A is at the bottom left, node B is above it, node C is at the top center, node D is to the right of C, node E is below D, node F is below E, and node G is to the left of F. There are several curved arrows representing connections between nodes:</p> <ul style="list-style-type: none"> From A to B: 102a, 108a, 110a From B to C: 102b, 108g, 110g From C to D: 102c, 106b, 108f, 110f From D to E: 102d, 108e, 110e From E to F: 102e, 108d, 110d From F to G: 102f, 108c, 110c From G to A: 102g, 108b, 110b <p>Some connections are shown with dashed lines, indicating different components or states. For example, the connection from B to C is shown with a dashed line labeled 106a, while the main connection is labeled 102b and 108g. The connection from C to D is shown with a dashed line labeled 106b, while the main connection is labeled 102c and 108f.</p>

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Daruwalla, FIG. 1.

No.	'904 Patent Claim 24	The Reference
		<p>The diagram illustrates a network node architecture, labeled <u>200</u>, consisting of seven nodes (A, B, C, D, E, F, G) arranged around a central circular component <u>210</u>. Each node is connected to the central component via specific interfaces:</p> <ul style="list-style-type: none"> Node A is connected to <u>210</u> via interface <u>202a</u>. Node B is connected to <u>210</u> via interface <u>202b</u>. Node C is connected to <u>210</u> via interface <u>202b</u>. Node D is connected to <u>210</u> via interface <u>202d</u>. Node E is connected to <u>210</u> via interface <u>202e</u>. Node F is connected to <u>210</u> via interface <u>202f</u>. Node G is connected to <u>210</u> via interface <u>202g</u>. <p>Internal states and transitions between nodes are indicated by arrows:</p> <ul style="list-style-type: none"> From C to A: (C,idle,S) From C to B: (B,SF,S) From C to D: (C,SF,L) From B to A: (B,SF,L) From B to C: (A, idle, S) From B to D: (C,SF,L) From A to B: (B,SF,L) From A to G: (B,SF,L) From D to E: (C,SF,L) From D to F: (C,SF,L) From E to F: (C,SF,L) From F to G: (C,SF,L) From G to F: (C,SF,L)

Daruwalla, FIG. 2.

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		<p>Daruwalla, FIG. 11.</p> <p>"The present invention relates to computer networks. In particular, the present invention relates to a system and method for providing a protection protocol for fault recovery for a two line bi-directional ring network." Daruwalla, 1:8-11.</p> <p>"FIG. 1 shows an example of a two line bi-directional ring network. The ring network 100 is shown to include nodes 102 a-102 g. Each node is typically a computer with embedded processors and at least one network connection. Each node 102 a-102 g is shown to be bidirectionally coupled to two neighboring nodes 102 a-102 g via an inner connection ring 110 a-110 g and an outer connection ring 108 a-108 g. For instance, node 102 a is bidirectionally coupled to nodes 102 b and 102 g. The example of FIG. 1 also shows a problem</p>

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		<p>104 in the connection between node 102 b and node 102 c. When a problem is detected (such as a bi-directional line cut), the connection between nodes 102 b and 102 d wraps back upon itself, as shown by wraps 106 a and 106 b. In this manner, the connection problem 104 can be avoided.” Daruwalla, 1:17-30.</p> <p>“In a conventional SONET network, each message sent by a sending node to a receiving node typically needs the identification and location of the receiving node to arrive at the proper destination. Accordingly, manual configuration is typically needed in each node to store the identity and location of each other node in the ring network in order to provide for communication between the nodes in the network.” Daruwalla, 1:31-44.</p> <p>“In summary, for the protection mechanism to operate, each node needs to know the current ring map (current ring topology). What is needed is a system and method for providing fault recovery for two line bi-directional ring network that minimizes the need to keep track of other nodes in the ring network. Preferably, the system would not require reconfiguration of an internal map of the network when a new node is added to, or existing nodes are removed from the network. The present invention addresses such a need.” Daruwalla, 2:23-31.</p> <p>“The present invention provides a protection protocol for fault recovery, such as a ring wrap, for a network, such as a two line bi-directional ring network. An embodiment of the present invention works in conjunction with a ring topology network in which a node in the network can identify a problem with a connection between the node and a first neighbor. According to the embodiment, when the problem is identified, the node sends a message identifying the problem to a second neighbor which is located at least one node away from the problem. The second neighbor then forwards the message to a third neighbor, unless the second neighbor is dealing with a situation that is higher in a hierarchy of situations than the problem described in the message by the original node. In general, if the second neighbor's situation has a higher priority than the situation described by the original node, then the message is ignored and not forwarded. If, however, the message sent by the original node describes a situation with a higher priority than the situation being dealt with by the second neighbor, then, in general, the second neighbor's situation is ignored, at least for the moment, and the original node's message is forwarded to the next neighbor. In general, a higher priority request preempts a</p>

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		<p>lower priority request within the ring. Exceptions are noted as rules of the protection protocol.” Daruwalla, 2:35-58.</p> <p>“The present invention provides a protection protocol that simplifies the coordination required by the nodes in a ring network. The nodes do not need to maintain a topology map of the ring, identifying and locating each node on the ring, for effective protection. Additionally, independently operating ring networks can be merged and the protection protocol automatically appropriately removes a protection, such as a ring wrap, to allow the formation of a single ring. It also provides for multiple levels of protection priority so that protection for a high priority failure, such as a physical break in a connection, removes protection for a low priority failure, such as a signal degrade, on another link.” Daruwalla, 2:59-3:3.</p> <p>“A method according to an embodiment of the present invention for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and initiating a fault recovery procedure when the second node receives the first message.” Daruwalla, 3:4-12.</p> <p>“In another aspect of an embodiment of the present invention, a method for adding a new node to a ring computer network, the ring network including a plurality of nodes, is presented. The method comprises detecting a situation by a first node, wherein the first node is one of the plurality of nodes; sending a first message via a short path to a second node, wherein the first node is adjacent to the second node prior to an addition of the new node; initiating a fault recovery procedure when the second node receives the first message; receiving a second message from the new node; and entering an idle state when the second message is received.” Daruwalla, 3:13-24.</p>

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		<p>“In yet another aspect of an embodiment of the present invention, a system for fault recovery for a ring computer network, the ring network including a plurality of nodes, is presented. The system comprises means for detecting a situation by a first node, wherein the first node is one of the plurality of nodes; means for sending a first message via a short path to a second node, wherein the first node is adjacent to the second node; and means for initiating a fault recovery procedure when the second node receives the first message.” Daruwalla, 3:25-35.</p> <p>“FIG. 2 is block diagram of a ring network utilizing a protection protocol according to an embodiment of the present invention.” Daruwalla, 3:40-42.</p> <p>“FIGS. 4-6 are flow diagrams illustrating various rules within the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:46-48.</p> <p>“FIGS. 8-12 are flow diagrams and a system diagram illustrating further rules of the protection protocol according to an embodiment of the present invention.” Daruwalla, 3:52-54.</p> <p>“FIG. 2 is a block diagram showing a ring network system utilizing a method of fault recovery according to an embodiment of the present invention. The ring network 200 is shown to include nodes 202 a-202 g. The nodes 202 a-202 g are shown to be coupled via an inner ring 210 in which the data flows in one direction, such as a clockwise direction. Additionally, the nodes 202 a-202 g are also shown to be coupled by an outer ring 212 in which data can flow in the opposite direction to the inner ring 210, such as in a counter-clockwise direction. The ring network 200 is shown to have a situation 204 a that requires protection, such as a ring wrap 206.” Daruwalla, 5:35-45.</p> <p>“FIG. 4 is a flow diagram of an example of a method according to an embodiment of the present invention implied by Rules 1-22. An APS packet is received via step 400. It is determined whether the APS packet has been sent along a long path via step 402. If the packet was not sent via a long path, then the APS packet is not forwarded via step 406. Accordingly, if the APS packet was sent via the short path, then the packet is not forwarded via step 406. If, however, the packet was sent through the long path via step 402, then the APS packet may</p>

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		<p>be forwarded via step 404. Note that for this example of Rule (1), it is assumed that the long path message does not have to pass through a wrapped connection in order to be forwarded. Otherwise, if the long path message needs to pass through a wrapped connection in order to be forwarded, then the message will simply not be forwarded.” Daruwalla, 6:21-36.</p> <p>“FIG. 6 is a flow diagram illustrating Rules 4 and 5. A node detects a problem between the node and a first neighbor via step 600. The node performs a wrap away from the side on which the problem exists via step 602. A short path message is then sent to the first neighbor informing it of the problem via step 604. Additionally, a long path message is also sent to a second neighbor informing the second neighbor of the problem via step 604. The first neighbor then performs a wrap away from the side of the problem via step 606. The first neighbor also sends an IDLE message, indicating a wrapped status, on a short path to the node that detected the problem via step 608. This message is sent across the failed span. Note that IDLE messages do not get wrapped and are sent across failed spans if possible. Additionally, the first neighbor also sends a message on a long path toward the side without the problem via step 608.” Daruwalla, 6:64-11.</p> <p>“An example of the method described in FIG. 6 can be seen in FIG. 2. Node 202 b has detected a problem 204 a and performs a wrap 206 on the side on which the problem exists. Node 202 b also sends a short path message to the neighbor on the other side of the problem 204 a, which is node 202 c. Node 202 b also sends a long path message to its other neighbor node 202 a informing it of the problem. Node 202 c performs a wrap 206 on the side of the problem and sends an IDLE message on a short path to node 202 b. Node 202 c also sent a message on a long path toward the side without the problem to its neighbor 202 d.” Daruwalla, 7:12-21.</p> <p>“FIG. 7 lists the hierarchy of priorities of Rule (8). For ease of reference, the hierarchy is separated into Class I-III. Class I is the highest priority, while Class III is the lowest priority. An example of a highest priority message in Class I is lockout. Lockout is an order stating that the ring network is not to wrap under any circumstances.” Daruwalla, 7:22-26.</p>

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		<p>“Examples of the next priority listed in Class II are forced switch and signal fail. Forced switch indicates that the ring network is configured to wrap at the point of the forced switch. Signal fail is a situation where either two nodes cannot communicate with each other, or one node cannot hear the other node. An example of a signal fail is a physical break in the communication lines between two nodes.” Daruwalla, 7:27-33.</p> <p>“Note that members of Class II can co-exist (Rule 9). For example, multiple forced switches and signal fails can co-exist. Additionally, members of Class I can co-exist (Rule 10). For example, multiple lockouts in a single ring network can co-exist. However, situations in Class III cannot co-exist with other situations (Rule 11). For example, a signal degrade cannot co-exist with a wait-to-restore.” Daruwalla, 7:52-58.</p> <p>“When there are multiple requests of the same priority within Class III, the first request to complete a long path signaling will take priority (Rule 13). For example, if there are two signal degrades located on the same ring network, then the first signal degrade which completes the long path signaling will take priority over the other signal degrade. By not allowing members of Class III to co-exist with one another, partitioning of the ring network is avoided.” Daruwalla, 7:59-65.</p> <p>“In case of two equal requests within Class III on both inner and outer rings of the ring network, the tie is broken by choosing a request on one of the rings, such as the outer ring request (Rule 14). For example, if a signal degrade occurs both on the inner and outer rings, then a request on a predetermined ring, such as the outer ring, will take priority over the other requests.” Daruwalla, 7:66-8:5.</p> <p>“FIG. 8 is a flow diagram illustrating Rules (9), (10), (11), (13), and (15). Note that the flow diagram described in FIG. 8 is merely an example of one way in which the rules of the method according to the embodiment of the present invention can be executed. For example, the determination of whether the long path message is a Class I request via step 802 or a Class II request via step 810 can be in reverse order.” Daruwalla, 8:6-11.</p>

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		<p>“A wrapped node receives a long path message via step 800. It is then determined if the long path message is a Class I request via step 802. The classes used in FIG. 8 are meant to correspond with the example of classes defined in FIG. 7. If the long path message is a Class I request, then it is determined if a local situation also has a Class I request via step 804. A local situation includes scenarios such as when a node detects a situation or problem, or when a node is made aware of a problem or situation via a short path message from its neighbor. If a local situation is not a Class I request via step 804, then any existing local wraps are unwrapped and the long path message is forwarded via step 806. If, however, a local situation is a Class I request via step 804, then the connections are already unwrapped or was never wrapped, and the long path message is forwarded via step 808.” Daruwalla, 8:12-26.</p> <p>“FIG. 12 is a flow diagram illustrating rules (20) and (21) of the method according to the embodiment of the present invention. A wrapped node determines that a problem has been cleared via step 1200. It then enters a wait-to-restore state via step 1202. It is then determined if its neighbor is the same neighbor as previously noted via step 1204. The node can save the source of a short path message at the time of wrap initiation to note the identity of its neighbor. If the current neighbor is not the same as the previous neighbor via step 1204, then an IDLE state is entered via step 1206. If, however, the current neighbor is the same as the previous neighbor via step 1204, then it is determined whether a pre-determined wait-to-restore time has expired via step 1208. Once the pre-determined wait-to-restore time has expired, then the node enters an IDLE state via step 1206.” Daruwalla, 12:60-13:6.</p> <p>“A method and system for fault recovery for a two line bi-directional network has been disclosed. Software written according to the present invention may be stored in some form of computer-readable medium, such as memory or CD-ROM, or transmitted over a network, and executed by a processor.” Daruwalla, 13:7-19.</p>

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		<p>Nederveen discloses:</p> <p>“A technique for use in gathering network activity-related information from cascaded network switches is provided. Using this technique, the information can be gathered without substantially reducing performance of the cascaded switches. In one embodiment, a single remote monitoring probe is connected via respective connections to each of the switches so as to receive the information from the switches. In another embodiment, only one of the switches is connected to the probe, and the other switches transmit their respective portions of the information to the switch connected to probe. The switch connected to the probe provides these portions of the information, as well as, any of its respective activity-related information to the probe. In this latter embodiment, the switches may be connected by dedicated connections and switch ports that are used solely for communicating the activity-related information.” Nederveen, Abstract.</p>

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		<p style="text-align: center;">FIG. 3</p> <p>Nederveen, FIG. 3.</p>

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		<p>The diagram illustrates a stacked switch monitoring architecture. Three switches, labeled A, B, and C, are connected in a vertical stack. Each switch contains a Base Processor (BP), a Buffer Layer (BL), and a MAC layer with Tx and Rx components. - Switch A (bottom) has a BP labeled ~504, a BL labeled ~600B, and a MAC layer with Tx and Rx. It receives a signal from the segment labeled 222 and transmits a signal to the segment labeled 228. - Switch B (middle) has a BP labeled ~502, a BL labeled ~600A, and a MAC layer with Tx and Rx. It receives a signal from the segment labeled 224 and transmits a signal to the segment labeled 226. - Switch C (top) has a BP labeled ~500, a BL labeled ~600A, and a MAC layer with Tx and Rx. It receives a signal from the segment labeled 226 and transmits a signal to the segment labeled 230. A dashed line labeled 300A connects the MAC layers of switches B and C. A dashed line labeled 300B connects the MAC layers of switches A and B. A dashed line labeled 300C connects the MAC layers of switches A and C. An external connection labeled 900 connects the MAC layer of switch C to a Router (250). A signal labeled 230 connects the MAC layer of switch C to an RMON PROBE (402). The RMON PROBE (402) also receives signals from the MAC layers of switches B and A. Finally, the RMON PROBE (402) connects to a MANAGEMENT PROCESS (408).</p> <p style="text-align: center;">FIG. 5</p> <p>Nederveen, FIG. 5.</p> <p>“Thus, it would be desirable to provide a stacked switch monitoring technique that permits efficient offloading of raw data processing from the stacked switches, requires only a minimal number of specialized network entities to gather and process such raw data, and does not result in substantial degradation of stacked switch performance.” Nederveen, 4:38-43.</p>

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		<p>“Accordingly, the present invention provides a technique for remote monitoring of a switch network that overcomes the aforesaid and other disadvantages and drawbacks of the prior art. More specifically, in one aspect of the present invention, a technique is provided for gathering information that may be useful in network management (e.g., switch port activity-related information), from switches in the network that are in a stacked configuration. The information is gathered from the stacked switches by a single network entity (e.g., an SNMP remote monitoring probe) in such a way that it does not substantially degrade the performance of the switches. This is accomplished, in one embodiment of the technique of the present invention, by connecting the switches via respective connections to a multiplexer that selectively connects the switches, according to an arbitration scheme, to the single network entity. The entity gathers respective portions of the information from switches when it is connected to the switches by the multiplexer. The information gathered by the entity may be provided to another network entity (e.g., an SNMP management node) in order to permit the other entity to use that information in managing the network.” Nederveen, 4:46-67.</p> <p>“In another embodiment of the technique of the present invention, only one of the switches is connected to the single information gathering entity. The switches that are not connected to the entity transmit, via respective dedicated ports and connections (i.e., ports and connections that are used solely for network information gathering activities), their respective portions of the information to the switch that is connected to the entity. The switch that is connected to the entity transmits, via a respective dedicated port and connection, the information received from the other switches, as well as, its own information to the entity.” Nederveen, 5:1-11.</p> <p>“FIG. 3 is a schematic, functional block diagram illustrating in greater detail the construction of the stacked switch network shown in FIG. 2.” Nederveen, 5:26-28.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network configured to employ another embodiment of the present invention.” Nederveen, 5:32-34.</p>

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		<p>“FIGS. 2-5 illustrate features of a computer network 200 wherein embodiments of the present invention may be advantageously practiced. Network 200 comprises a stacked switch network 300 which interconnects a plurality of network segments 228, 232, 240, and 251. Each segment 228, 232, 240 comprises one or more local area networks having computer endstations (not shown). Segment 251 is a network router segment that comprises network router 250. Each segment 228, 232, 240 is coupled via a respective communications link 222, 224, 226 to a respective port 302 (i.e., port A), 304 (i.e., port B), 312 (i.e., port C) of the switch network 300. Likewise, the router 250 of router segment 251 is coupled via a respective trunk line 230 to router port 306 (i.e., port R).” Nederveen, 5:46-59.</p> <p>“Stacked switch network 300 comprises a plurality of data network switches 300A, 300B, 300C (e.g., Catalyst 3900™ series switches of the type commercially available from the Assignee of the subject application) coupled together via conventional stack link bus connection logic 600A, 600B. More specifically, logic 600A couples a stack link bus port and associated logic 500 in switch 300A to a stack link bus link port and associated logic 502 in switch 300B. Similarly, logic 600B couples another stack link bus port and associated logic 501 in switch 300B to a stack link bus port and associated logic 504 in switch 300C. It should be understood that although, as is shown in FIG. 3, switches 300A and 300B, and switches 300B and 300C, may be coupled serially together by separate respective logic elements 600A, 600B, each of the switches 300A, 300B, 300C may be coupled together via a single respective stack link bus port in the switch to a single stack link bus connection logic block (not shown, e.g., of the type that is commercially available under the tradename Catalyst Matrix™ from the Assignee of the subject application). Further alternatively, depending upon the particular design and functionality of the ports 500, 501, 502, and 504, and the control and forwarding logic (whose operation will be described more fully below) in the switches 300A, 300B, 300C, the circuitry in logic 600A, 600B may instead be comprised in the ports 500, 501, 502, and 504 and/or control and forwarding logic, and therefore, in this alternative configuration, the logic 600A, 600B in the network 300 may be replaced by simple connection means (e.g., cable connectors).” Nederveen, 6:29-57.</p>

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		<p>“Each switch 300A, 300B, 300C includes a respective internal bus (e.g., element 800 in switch 300C) that is coupled via at least one stack link bus port and associated interface logic (e.g., 504 in switch 300C) to external stack link bus connection logic (e.g., element 600B in switch 300C). Each switch 300A, 300B, 300C also includes respective programmable control and forwarding logic (e.g., element 802 in switch 300C) comprising processing, memory, and other circuitry for storing and learning configuration information (e.g., source and destination MAC addresses of messages received by the switch, switch bridging table, switch segments' spanning tree and virtual local area network information, etc.), and for providing appropriate commands to other elements (e.g., the switch ports) to cause data messages received by the switch to be forwarded to appropriate network segments coupled to the switch based upon this configuration information. In each switch, the switch's port logic circuitry (e.g., port A logic 302 and port P logic 310C in switch 300C) and control and forwarding logic are coupled to each other via that switch's respective internal bus. The stack link bus port and associated logic in each switch 300A, 300B, 300C may comprise a Catalyst™ stack port line interface card (commercially available from the Assignee of the subject application) inserted into a bus expansion slot (not shown) in the switch. Although not shown in the Figures for purposes of clarity of illustration, each switch 300A, 300B, 300C in network 300 typically will include tens or hundreds of ports coupled to network segments.” Nederveen, 6:58-7:19.</p> <p>“The control and forwarding logic and stack link bus port and associated logic in each switch, and the logic 600A, 600B, are configured to together implement conventional techniques for permitting the switches 300A, 300B, 300C to function together as a single logical/virtual switch. More specifically, when configured in the stacked arrangement 300, after the switches 300A, 300B, 300C and logic 600A, 600B are initially activated, they execute initial power-on self-diagnostics, and thereafter, enter a “stack discovery” mode of operation.” Nederveen, 7:20-29.</p> <p>“In the stack discovery mode of operation, the control and forwarding logic in each switch 300A, 300B, 300C first “senses” that its switch is coupled to logic 600A and/or 600B, and then determines the particular configuration of the stacked switch network 300, using suitable conventional autosensing/autoconfiguration techniques. The control and 353forwarding logic in the switches 300A, 300B, 300C then assigns to the switches respective unique</p>

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		<p>identification numbers (e.g., based upon unique identification numbers of respective ports of the logic 600A, 600B to which the switches are coupled).” Nederveen, 7:30-40.</p> <p>“FIG. 5 is schematic, functional block diagram illustrating construction of a stacked switch network 300' configured to employ another embodiment of the present invention. It should be understood that unless specifically stated to the contrary, the structure and operation of the network 300' are substantially the same as the structure and operation of network 300. In network 300', each of the dedicated ports 310A, 310B, 310C comprises a respective transmit portion and receive portion, referenced in FIG. 5 as RX and TX, respectively.” Nederveen, 11:20-29.</p> <p><u>Slater '421 discloses:</u></p> <p>“A method and apparatus for discovering paths to other network devices includes a protocol and network management application that can be executed on network devices. The Ethernet protocol is used to detect paths to other network devices, knowing only the Ethernet address of the destination. A discovery protocol is extended to add hop probe and hop probe reply Type-Length-Value fields in a variable-length list. The hop probe fields contain a hop count, a destination Ethernet address, and a source Ethernet address. When a hop probe is received by a network device, the hop count field is decremented by one and the hop probe is forwarded. Packets received with a hop count of one are not forwarded and a hop probe reply is sent back to the Ethernet source address of the hop probe. The hop probe reply fields contain a destination Ethernet address and a source Ethernet address.” Slater '421, Abstract.</p>

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		<p>FIG. 6</p> <p>Slater '421, FIG. 6.</p> <p>"Partly as a result of the increased complexity of networks, network administrators must often troubleshoot problems with their network. Two classes of network problems often faced by network administrators are "reachability" problems and performance slowdowns. Reachability problems occur when one or more network devices cannot be accessed through a network, and can be caused by hardware or software failures, cabling problems, or any of several other types of difficult-to-diagnose problems that can occur in a network." Slater '421, 7:11-20.</p> <p>"Some of the world's largest networks today rely on the TCP/IP suite of networking protocols. With a relatively small kit of basic tools, network administrators can learn much about an internetwork. "Ping" and "traceroute" commands, "show" commands, and "debug" commands (all of which are typically available via the basic management interface on a network device) form the core of the network administrator's internetwork toolkit. Ping and</p>

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		<p>traceroute commands can be useful tools in determining where failures are occurring, but they are cumbersome to use, and require knowledge of the IP address or host name of the destination network device. The show commands provide information about interface conditions, protocol status, neighbor reachability, router configuration and status, level of traffic, errors and drops, and other network data. Finally, debug commands provide clues about the status of a network device and other network devices directly or indirectly connected to it. Because debug commands can create performance slowdowns, they must be used with great care, and using the wrong debug command at the wrong time can exacerbate problems in already poorly performing networks.” Slater ’421, 7:55-8:8.</p> <p>“Embodiments of the present invention as illustrated herein use the Cisco™ Discovery Protocol (“CDP”) to automatically detect paths to specified network devices in Ethernet LANs. However, other similar products known to those of ordinary skill in the art are available from other vendors to accomplish the same task.” Slater ’421, 9:10-15.</p> <p>“CDP is a media-independent device discovery protocol which can be used by a network administrator to view information about other network devices directly attached to a particular network device. In addition, network management applications can retrieve the device type and SNMP-agent address of neighboring network devices. This enables applications to send SNMP queries to neighboring devices. CDP thus allows network management applications to discover devices that are neighbors of already known devices, such as neighbors running lower-layer, transparent protocols.” Slater ’421, 9:16-26.</p> <p>“It is to be understood that the present invention is not limited to devices that are compatible with CDP. CDP runs on all media that support the Subnetwork Access Protocol (“SNAP”), including LAN and Frame Relay. CDP runs over the data link layer only. Each network device sends periodic messages to a multicast address and listens to the periodic messages sent by others in order to learn about neighboring devices and determine when their interfaces to the media go up or down. Each device also advertises at least one address at which it can receive SNMP messages. CDP messages, or “advertisements,” contain holdtime information, which indicates the period of time a receiving device should hold CDP information from a neighbor before discarding it. With CDP, network management applications can learn the device type</p>

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		<p>and the SNMP-agent address of neighboring devices. This process enables applications to send SNMP queries to neighboring devices.” Slater '421, 9:27-43.</p> <p>“It should be noted that-normally, CDP packets according to aspects of the present invention are transmitted at regular intervals (e.g. once every 60 seconds). However, in embodiments of the present invention, when a Hop Probe or Hop Probe Reply needs to be forwarded by a network device, the network device is commanded to send a CDP packet immediately.” Slater '421, 16:66-17:5.</p> <p>“The present invention is much faster than the previous method that involved logging in to each intermediate network device, entering the “show cdp neighbors” command, and interpreting the output to find the next hop along the path to the destination network device. Also, the present invention allows individual users, such as network administrators, to execute a tool to manually discover paths through a network of Ethernet switches. The present invention can be used by network management software to automatically map the topology of clusters of network devices, such as Ethernet switches. Finally, the present invention is useful in loop detection. Enhancements to Spanning Tree and other bridge-level routing protocols can test proposed changes to switch topology prior to making them.” Slater '421, 17:6-20.</p> <p>Petersen discloses:</p> <p>“Methods and apparatus for enabling communication between a source network device and one or more destination network devices are disclosed. A system enabling communication between a source network device and one or more destination network devices includes a switch and a ring interconnect. The switch is adapted for connecting to the source network device and the one or more destination network devices. More particularly, the switch is capable of storing data provided by the source network device and retrieving the data for the one or more destination network devices. The ring interconnect is adapted for connecting the source network device and the one or more destination network devices to one another. In addition, the ring interconnect is capable of passing one or more free slot symbols along the ring interconnect. Thus, the ring interconnect is capable of expanding when one or more of the free slot symbols are each replaced by a frame notify message indicating that the data has</p>

No.	'904 Patent Claim 24	The Reference
		<p>been stored by the switch for retrieval by the one or more destination network devices."</p> <p>Petersen, Abstract.</p> <pre> sequenceDiagram participant SourceSat as Source Sat. participant Switch participant DestSat as Dest. Sat. SourceSat->>Switch: Receive frame and break into cells activate SourceSat Note over SourceSat: 208 Note over SourceSat: Receive frame and break into cells 208 Note over SourceSat: Note over SourceSat: 202 Note over SourceSat: Source Sat. Note over SourceSat: Note over SourceSat: 208 Note over SourceSat: Send cells inc. data and header to switch (inc. retrieval count) 210 Note over SourceSat: Note over SourceSat: 210 Note over SourceSat: Send Frame Storage message indicating frame stored and location of frame 214 Note over SourceSat: Note over SourceSat: 214 Note over SourceSat: Send Frame Notify msg. (inc. buffer # in Data Buffer) on notify ring 216 Note over SourceSat: Note over SourceSat: 216 Note over SourceSat: 224 Note over SourceSat: Decrement retrieval count Note over SourceSat: 226 Note over SourceSat: If retrieval count = 0, remove frame from storage upon completion of removal Note over SourceSat: Note over SourceSat: 234 Note over SourceSat: Removes Notify msg. from ring and discards Note over SourceSat: Note over SourceSat: 204 Note over SourceSat: Switch Note over SourceSat: Note over SourceSat: 206 Note over SourceSat: Dest. Sat. Note over SourceSat: Note over SourceSat: 206 Note over SourceSat: Switch stores cell data in Data Buffer and cell header in Buffer Table (inc. retrieval count) 212 Note over SourceSat: Note over SourceSat: 212 Note over SourceSat: Copy and store the Notify msg. (inc. buffer #) in notify queue 218 Note over SourceSat: Note over SourceSat: 218 Note over SourceSat: Modify Frame Notify message to indicate whether able to accept and store the Frame Notify msg. 220 Note over SourceSat: Note over SourceSat: 1 Note over SourceSat: 2 Note over SourceSat: 220 Note over SourceSat: 222 Note over SourceSat: Send Frame Retrieval msg. for buffer # at head of notify queue to retrieve frame 222 Note over SourceSat: Note over SourceSat: 222 Note over SourceSat: Send Frame cells to Dest. Sat. 228 Note over SourceSat: Note over SourceSat: 228 Note over SourceSat: Reconstruct frame for transmission 230 Note over SourceSat: Note over SourceSat: 230 Note over SourceSat: 232 Note over SourceSat: Send modified Frame Notify message on notify ring 232 Note over SourceSat: Note over SourceSat: 232 Note over SourceSat: 236 Note over SourceSat: Send Buffer Table subtract msg. corr. to the modified frame Notify msg. (e.g., subtract # destinations unable to copy frame Notify msg. from retrieval count) 236 Note over SourceSat: Note over SourceSat: 236 </pre>

FIG. 2

Petersen, FIG. 2.

No.	'904 Patent Claim 24	The Reference
		<p>“The present invention relates to a mixed topology data switching system that combines a radial interconnect with a ring interconnect. More particularly, the radial interconnect permits devices to store and retrieve data using a switch, while the ring interconnect permits devices along the ring interconnect to provide notification that data has been stored for retrieval, as well as provide feedback regarding the ability or inability to retrieve such data.” Petersen, 1:34-41.</p> <p>“In controlling the flow of network traffic through a switching system, it is often desirable to provide feedback to the source of the data. For instance, although a transmitting device, hereinafter referred to as a “source device,” may transmit or forward data to a receiving device, hereinafter referred to as a “destination device,” the destination device may be incapable of handling the data. In these circumstances, the source device is often unaware that the data was not accepted by the destination device, complicating switch management. Common solutions to the problem of switch traffic management have included ensuring that all intended destination devices are “ready to receive” prior to transmitting data on a ring or bus interconnect, or insisting that each intended destination device send an explicit acknowledgement back to the source device. Both of these approaches result in reduced efficiency of the interconnect scheme. By way of example, in a ring network, such acknowledgment is typically provided in the data frame being transmitted. As another example, in other interconnect schemes, each such device may send a separate acknowledgment, therefore adding to the traffic on the network. Accordingly, it would be desirable if a traffic management scheme were established which could provide such feedback to the source of the data while minimizing traffic management overhead.” Petersen, 2:8-32.</p> <p>“According to one embodiment, the present invention combines the use of two data transport methods: a point-to-point radial interconnect and a ring interconnect. The radial interconnect connects interface devices to each other through the services of a central switch device to permit the transport of data. Typically, a single interface has a single dedicated radial interconnect to the central switch. These interface devices are further connected to one another via a ring interconnect to convey retrieval notifications regarding forwarding of the data (by source devices) and receipt of the data (by destination devices).” Petersen, 2:36-46.</p>

No.	'904 Patent Claim 24	The Reference
		<p>“Each radial interconnect provides a narrow, high baud-rate connection to convey to the actual data from and to the associated interface without being burdened by the unrelated traffic for the remaining interfaces in the system. This is accomplished through the use of a central switch device, which stores and retrieves data for the various interfaces in the system. As will be apparent from the following description, this architecture provides numerous advantages over a wide parallel bus or ring.” Petersen, 2:47-55.</p> <p>“The ring interconnect may be used to convey a “retrieval notification”(i.e., retrieval message) that may be observed by all potential retrieving interfaces. The retrieval notification notifies specific devices (“destination devices”) or interfaces that one or more frames addressed to them are available from the switch device. Moreover, the ring interconnect permits each destination device to provide feedback to the source device letting the source know whether the destination has accepted the notification provided by the source device and therefore whether the destination can retrieve the data intended for it. The feedback is particularly useful in buffer management applications. In this manner, an efficient and flexible data transport and retrieval notification system that includes a feedback path to the source of the data is provided.” Petersen, 2:56-3:3.</p> <p>“FIG. 2 is a process flow diagram illustrating a method of providing network communication according to an embodiment of the invention.” Petersen, 3:9-11.</p> <p>“FIG. 2 is a process flow diagram illustrating in further detail a method of providing network communication in the above-described system according to an embodiment of the invention. As shown, process steps performed by a source device 202 are illustrated along an associated vertical line, steps performed by a switch 204 are illustrated along another vertical line, and steps performed by a destination device 206 are illustrated along still another vertical line.” Petersen, 4:50-57.</p> <p>“When the frame is stored by the switch 418, the source device preferably receives an acknowledgment that the data has been stored. Thus, to provide this feedback, a frame storage message (i.e., storage reply) is sent from the switch 418 on the channel 416 to the channel interface 414. The frame storage message is then provided to the notify ring interface as</p>

No.	'904 Patent Claim 24	The Reference
		<p>shown at 430 and sent on the notify ring. Once this acknowledgment is received by the interface device 402, the designated destination devices may be notified via notify ring interface 424. As described above, a Frame Notify message may be sent via the notify ring interface 424 to the destination devices. More particularly, the Frame Notify message may identify one or more destination devices for the frame and specify the location of the frame to be retrieved. By way of example, the location of the frame to be retrieved by the destination devices may be designated by a buffer number 430. In addition, the destination devices for the frame may be specified in the Frame Notify message through a notify queue map 426. More particularly, the notify queue map 426 may specify a notify queue associated with a particular destination device. The notify queue may be expressly designated through the use of one or more bits as well as implied through the specification of a priority level for the data. The notify queue map 426 will be described in further detail with reference to FIG. 13. The notify ring interface 424 then creates a Frame Notify message including the notify queue map 426 and the buffer number 430 which is then sent on an outbound interface of the notify ring 432.” Petersen, 7:55-8:16.</p> <p>“As described above, the notify ring may be expanded to accommodate communication between interface devices. The communication between the interface devices and the switch is therefore performed on one or more channels rather than the notify ring. As a result, the flexibility of the notify ring does not effect the speed with which the interface devices may communicate with the switch. Thus, where a single port operates at a faster speed than the channels, multiple channels may be grouped together. In this manner, the speed with which the switch may communicate with the interface devices may be maximized.” Petersen, 20:27-36.</p> <p>“The present invention provides a mixed topology data switching system that combines a point-to-point radial interconnect with a ring interconnect to maximize the speed of network traffic. The radial interconnect provides a narrow, high baud-rate connection to convey the data traffic for just the interface in question, without being burdened by all of the unrelated traffic for the remaining interfaces in the system. At the same time, the ring interconnect permits retrieval notifications to be observed by all potential retrieving interfaces. The ring topology further permits each destination interface to provide feedback to the source interface,</p>

No.	'904 Patent Claim 24	The Reference
		which is valuable for buffer management applications. Moreover, the point-to-point ring topology bus employs a variable latency access method that enables messages to be passed across the bus with low latency when the system is quiet and with increased latency when the system is busy. In addition, since control messaging around the ring interconnect and across the channel interconnects are embedded in the data stream, the number of pins required and manufacturing costs are reduced.” Petersen, 20:38-57.

No.	'904 Patent Claim 25	The Reference
25	A method according to claim 19, wherein the network comprises an asynchronous transfer mode (ATM) network.	<p>The Reference discloses a method according to claim 19, wherein the network comprises an asynchronous transfer mode (ATM) network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> Claim 2.</p>

No.	'904 Patent Claim 26	The Reference
26	<p>A method according to claim 19, wherein the network comprises an Internet protocol (IP) network.</p>	<p>The Reference discloses a method according to claim 19, wherein the network comprises an Internet protocol (IP) network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or in view of one or more of the following references: the Catalyst XL Switches, the BayStack 450 Switches, TRENDnet Stackable Hubs, Czerwiec, Quoc, Vink, and Dowling.</p> <p><i>See supra</i> Claim 3.</p>

EXHIBIT E-2

Defendant's First Amended Invalidity Contentions
Orckit Corporation v. Cisco Systems, Inc., 2:22-cv-00276-JRG-RSP

Chart for U.S. Patent 8,830,821 (“the ’821 Patent”) **U35 U.S.C. § 103**

In this chart, “The Reference” refers to each of the following references and/or systems:

- Cisco IOS System (“Cisco IOS system”)
- Juniper Networks’ Junos OS Release 11.1 (“Juniper IOS System”)
- MPLS-TP IETF Standards (“IETF MPLS-TP System”)
- U.S. Patent Publication No. 2004/0205239 to Doshi (“Doshi ’239”)
- U.S. Patent No. US 2005/0083928 A1 to Sivabalan, *et al.* (“Sivabalan ’928”)
- U.S. Patent No. US 2006/0250948 A1 to Zamfir, *et al.* (“Zamfir ’948”)

The following additional references are identified individually:

- Kurose, James. F. *et al.*, COMPUTER NETWORKING: A TOP-DOWN APPROACH FEATURING THE INTERNET, i-ii, 280-282 (1st ed. 2001) (“Kurose”)
- U.S. Patent No. 9,014,049 to Filsfils *et al.* (“Filsfils”)
- U.S. Patent No. 8,553,533 to Taylor *et al.* (“Taylor”)
- U.S. Patent No. 8,072,879 to Vasseur *et al.* (“Vasseur ’879”)
- U.S. Patent No. 8,885,460 to Vasseur *et al.* (“Vasseur ’460”)
- U.S. Patent Application Publication No. 2011/0242988 to Rustogi *et al.* (“Rustogi”)

As shown in the chart below, all Asserted Claims of the ’821 patent are invalid under 35 U.S.C. § 103 because The Reference renders those claims obvious either alone, or in combination with the knowledge of a person having ordinary skill in the art, and in further combination with the references specifically identified below and in the following claim chart and/or one or more references identified in Defendant’s Preliminary Invalidity Contentions.

Motivations to combine include at least the similarity in subject matter between the references to the extent they concern methods of selecting paths in a network and/or determining the cost of potential paths in a network. Insofar as the references cite other patents or publications, or suggest additional changes, one of ordinary skill in the art would look beyond a single reference to other references in the field.

These invalidity contentions are based on Defendant's present understanding of the Asserted Claims, and Orckit's apparent construction of the claims in its November 3, 2022 Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1, and Orckit's January 19, 2023 First Amended Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1 (Orckit's "Infringement Disclosures"), which is deficient at least insofar as it fails to cite any documents or identify accused structures, acts, or materials in the Accused Products with particularity. Defendant does not agree with Orckit's application of the claims, or that the claims satisfy the requirements of 35 U.S.C. § 112. Defendant's contentions herein are not, and should in no way be seen as, admissions or adoptions as to any particular claim scope or construction, or as any admission that any particular element is met by any accused product in any particular way. Defendant objects to any attempt to imply claim construction from this chart. Defendant's prior art invalidity contentions are made in a variety of alternatives and do not represent Defendant's agreement or view as to the meaning, definiteness, written description support for, or enablement of any claim contained therein.

The following contentions are subject to revision and amendment pursuant to Federal Rule of Civil Procedure 26(e), the Local Rules, and the Orders of record in this matter subject to further investigation and discovery regarding the prior art and the Court's construction of the claims at issue.

No.	'821 Patent Claim 1	The Reference
1[preamble]	An entity selection method performed by a network device, comprising the steps of:	<p>The Reference discloses an entity selection method performed by a network device.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>
1[a]	providing a plurality of multiprotocol label switching (MPLS) transport entities between a first endpoint and a second endpoint;	<p>The Reference discloses providing a plurality of multiprotocol label switching (MPLS) transport entities between a first endpoint and a second endpoint.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Cisco created and developed the MPLS and MPLS-TE standards and patented technology based on those standards <i>before</i> Orckit utilized such technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Filsfils • Taylor • Vasseur '879¹ • Rustogi

¹ Vasseur '879 is the parent to the CON patent application that became Vasseur '460. Since Vasseur '879 and share substantially identical specifications, the citations to Vasseur '879 for each of the claim limitations herein also apply to Vasseur '460.

No.	'821 Patent Claim 1	The Reference
		<p>Filsfils discloses:</p> <p>“In one embodiment, forwarding information bases (FIBs) are selectively populated in a packet switch. A packet switching device determines, based on one or more protocol signaling messages, a subset, which is less than all, on which FIBs a lookup operation may be performed for identifying forwarding information for a received particular packet. The packet switching device populates each of these FIBs, but not all of the FIBs of the packet switching device, with forwarding information corresponding to the particular forwarding value. Thus, FIB resources are consumed for only those FIBs which could actually be used, and not all of the FIBs, for forwarding packets in the data plane of the packet switching device, whether these packets are received on a primary or backup path.” Filsfils, Abstract.</p> <p style="text-align: center;">FIGURE 1</p> <p>Filsfils, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<p>APPARATUS (E.G., PACKET SWITCHING DEVICE APPLIANCE, CORE ROUTER, LABEL SWITCH ROUTER) 200</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>ROUTE PROCESSOR CONFIGURED TO CORRELATE PRIMARY AND BACKUP PATHS AND POPULATE FIBS ACCORDINGLY</p> <p>COMMUNICATION MECHANISM(S) (E.G., BUS, SWITCHING FABRIC, MATRIX)</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>NETWORK(S)</p> <p>NETWORK(S)</p> <p>FIGURE 2</p> <p>Filsfils, FIG. 2 (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<pre> graph TD START([START]) --> 402[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL] 402 --> 404[DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)] 404 --> 406[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 406 --> END([END]) </pre> <p>The flowchart illustrates a process for establishing network paths. It begins with a 'START' node, followed by a step to exchange protocol signaling messages to establish a primary path for a particular tunnel, including receiving a particular label from a downstream label switch router (LSR). This is followed by a decision point to determine if a backup path needs to be created to protect a portion of the tunnel or a link it may traverse. Finally, protocol signaling messages are exchanged to establish a backup path for a portion of the tunnel, providing information to the downstream LSR to correlate primary and backup paths and program forwarding information bases.</p>

FIGURE 4

Filsfils, FIG. 4 (annotated).

No.	'821 Patent Claim 1	The Reference
		<pre> graph TD START([START]) --> S1[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, SENDING A PARTICULAR LABEL FROM TO THE NEIGHBOR UPSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THIS LSR OVER THE PARTICULAR TUNNEL.] S1 --> S2[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF THE PARTICULAR TUNNEL, INCLUDING RECEIVING INFORMATION THAT IT CAN USE TO CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] S2 --> S3[CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL, AND ONLY POPULATE THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, THE LSR KNOWS WHAT ON WHAT INTERFACE(S) PACKETS FROM THE BACKUP PATH COULD BE RECEIVED. THIS CORRELATION MAY INCLUDE USING DATA CONCERNING BUNDLED INTERFACES, AND EVEN RECURSIVE CORRELATION OF BACKUP TUNNELS USED TO BACKUP OTHER BACKUP TUNNELS, AS WELL AS LOAD BALANCING AND OTHER TECHNIQUES TO DETERMINE WHERE BACKUP PATH PACKETS COULD BE RECEIVED, AND THE SUBSET OF FORWARDING INFORMATION BASES IN THE DATA PLANE THAT COULD BE USED TO FORWARD PACKETS OVER THE PARTICULAR TUNNEL, WHETHER VIA A PRIMARY OR BACKUP PATH.] S3 --> END([END]) </pre> <p>FIGURE 5</p> <p>Filsfils, FIG. 5.</p>

No.	'821 Patent Claim 1	The Reference
		<p>“The communications industry is rapidly changing to adjust to emerging technologies and ever increasing customer demand. This customer demand for new applications and increased performance of existing applications is driving communications network and system providers to employ networks and systems having greater speed and capacity (e.g., greater bandwidth). In trying to achieve these goals, a common approach taken by many communications providers is to use packet switching technology, including switching packets on labels especially in the core network using Multiprotocol Label Switching (MPLS).” Filsfils, 1:12-22.</p> <p>“Tunnels, such as MPLS-TE (Traffic Engineering) and MPLS-TP (Transport Profile), are paths established through a network in order to transport packets efficiently through a label switched network. Fast Re-Route (FRR) is a technology that allows backup paths to be established in the network, which can be used in case of a problem with a primary path (original primary path or currently used backup path) of the tunnel. RFC 4090, entitled “Fast Reroute Extensions to RSVP-TE for LSP Tunnels,” provides an extension of the protocol signaling to establish backup label switched path (LSP) tunnels for local repair of LSP tunnels.” Filsfils, 1:23-33.</p> <p>“Expressly turning to the figures, FIG. 1 illustrates a network 100 operating according to one embodiment. Shown are four apparatus 101-104 (e.g., packet switching devices such as a label switch router, network device, and/or appliance). For explanation purposes, each of apparatus 101-104 will be referenced as a label switch router (LSR).” Filsfils, 5:41-46.</p> <p>“As shown, a particular tunnel is established, using a signaling protocol and exchanging of protocol signaling messages. Note, LSR 101 may, or may not, be an endpoint of the particular tunnel (e.g., LSR 101 may be an intermediate LSR on the path of the particular tunnel). The primary path of the particular tunnel includes spans from LSR 101 via link 111 to LSR 103 and via link 112 to LSR 104. Note, LSR 104 may be an intermediate LSR on the path of the particular tunnel, or an endpoint of the particular tunnel. Further, for this example embodiment, LSR 103 signals LSR 101 to use label T1 at the top of the label stack in the header of a packet sent to it on the particular tunnel.” Filsfils, 5:47-58.</p> <p>“A second tunnel from LSR 101 via link 121 to LSR 102 and via link 122 to LSR 103 is similarly configured using a signaling protocol and exchanging of protocol signaling</p>

No.	'821 Patent Claim 1	The Reference
		<p>messages. For example purposes, LSR 102 signals LSR 101 to use label (T2) at the top of the label stack in the header of a packet sent to it on the second tunnel. In one embodiment, LSR 101 creates the second tunnel in response to determining, or being instructed to, create a backup path to protect link 111 and/or protect all or certain tunnels traversing link 111.” Filsfils, 5:59-67.</p> <p>“As shown in FIG. 1, link 111 (primary path of the particular tunnel and/or all or certain tunnels traversing link 111) is protected by LSR 101 using the second tunnel (backup path). When sending packets over the particular tunnel over link 111, LSR 101 includes label T1 at the top of the label stack of these packets. If link 111 cannot be used for communicating packets of the particular tunnel, LSR 101 sends packets over the backup path for the particular tunnel by sending packets to LSR 102, with these packets having a label stack including: label T2 followed by label T1. Thus, LSR 102 will receive these packets, pop the top label (T2) from the label stack of each of these packets, identify based on label T2 to send these packets to LSR 103. After popping the top label, the label at the top of the label stack of these packets is T1, which is the same label LSR 103 expects to receive for the particular tunnel. Therefore, these packets received with label T1 at the top of their label stack, are forwarded (after popping label T1 from their label stack) by LSR 103 over the particular tunnel to LSR 104.” Filsfils, 6:6-24.</p> <p>“One embodiment acquires such additional information by extending Resource Reservation Protocol (RSVP) to provide information which allows a packet switch to correlate primary and backup paths. Thus, a packet switch can use this additional information in determining which of its forwarding information bases (FIBs) could possibly be used in forwarding packets (e.g., in the data plane of the packet switch).” Filsfils, 6:51-57.</p> <p>“In providing this additional information to LSR 103, one embodiment communicates an extended RSVP message (including a new or modified RSVP object) or other message to LSR 103 on the second tunnel. This messages designates one or more primary tunnels (e.g., label T1 in our example) and/or a link (e.g., link 111). As LSR 103 knows what interface that it received this message, LSR 103 knows that it must populate forwarding information for these primary tunnels, either specified (e.g., by a label such as T1), or all labels corresponding to</p>

No.	'821 Patent Claim 1	The Reference
		<p>tunnels which could be received over link 111. In one embodiment, the extended RSVP or other message communicated to LSR 103 also includes an identification of the backup tunnel (e.g., T2) over which the RSVP or other message is being received, as the identification the tunnel over which a packet is received is often not communicated in a packet (e.g., in the case of Penultimate Hop Popping).” Filsfils, 7:19-34.</p> <p>“As shown in FIG. 1, one embodiment includes apparatus 103, which populates less than all of its FIBs with forwarding information for a tunnel (although all FIBs may be populated for certain tunnels). One embodiment includes apparatus 101 and/or 102 which communicates, via a signaling protocol (e.g., an extension of RSVP, or using another protocol), information which allows apparatus 103 to determine the relationship between primary and backup paths, such that apparatus 104 can correlate this primary and backup path information (possibly also correlating backup path of backup path information, and/or bundled interfaces and/or bundled links) to identify a minimum subset of the FIBs that could possibly be used in forwarding packets of particular primary paths (e.g., tunnels).” Filsfils, 7:49-62</p> <p>“Turning to FIG. 2, illustrates an apparatus 200 (e.g., packet switching devices such as a label switch router, network device, and/or appliance) operating in one embodiment. As shown, apparatus 200 includes line cards 201, 202 communicatively coupled via communication mechanism(s) 203 (e.g., bus, switching fabric, and/or matrix). Additionally, route processor 204 is configured to correlate primary and backup paths of tunnels, and to populate minimum subsets of FIBs with forwarding information for labels. Again, a minimum subset of FIBs for a particular path or label of the particular path is the set of FIBs that are determined to possibly be used in forwarding packets of a primary path, whether the label is received in a packet over the primary path or over a backup path, and possibly considering backup paths of a backup path and/or the possibly effect of bundled interfaces and/or bundled links.” Filsfils, 7:63-8:11.</p> <p>“As shown in FIG. 2, apparatus 200 is communicatively coupled via primary and backup paths 211, 212 to networks 210 and 213 (which could be the same network). As illustrated, each of line cards 201, 202 includes one or more FIBs. By correlating on which line card(s) 201, 202 and even within line cards 201, 202 that have multiple FIBs, primary and backup path(s) of tunnels, the number of FIB entries populated in apparatus 200 can typically be reduced,</p>

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		<p>possibly significantly saving memory/storage resources and resources used to populate the FIBs.” Filsfils, 8:12-21.</p> <p>“FIG. 4 illustrates a process performed in one embodiment. Processing begins with process block 400. In process block 402, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes receiving a particular label for a downstream LSR to use when sending packets to the downstream LSR over the particular tunnel.” Filsfils, 8:61-67.</p> <p>“In process block 404, a determination is made to create a backup path from the node (e.g. the node performing these operations). This backup path may be used to protect one or more particular tunnels, and/or may be used to protect a link which is used to carry packet traffic of one or more tunnels.” Filsfils, 9:1-5.</p> <p>“In process block 406, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including providing information to the downstream LSR so that the downstream LSR can correlate primary and backup path(s) of the particular tunnel and substantially only program the FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:6-23.</p> <p>“FIG. 5 illustrates a process performed in one embodiment. Processing begins with process block 500. In process block 502, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes sending a particular label for an upstream LSR to use when sending packets over the particular tunnel to this apparatus (e.g., an LSR performing these operations).” Filsfils, 9:26-32.</p>

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		<p>“In process block 504, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including receiving information that the LSR can use to correlate primary and backup path(s) of the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:33-47.</p> <p>“In process block 506, the primary and backup path(s) of the particular tunnel are correlated to identify the set of FIBs that could possibly be used in forwarding packets of the particular tunnel. Substantially only those FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel (either through a primary or backup path) are populated with the forwarding information (e.g., an entry corresponding to the label it advertised to use for the particular tunnel) for the particular tunnel. For example, the LSR knows what on what interface(s) packets from the backup path could be received. This correlation may include using data concerning bundled interfaces, and even recursive correlation of backup tunnels used to backup other backup tunnels, as well as load balancing and other techniques to determine where backup path packets could be received, and the subset of forwarding information bases in the data plane that could be used to forward packets over the tunnel, whether via a primary or backup path.” Filsfils, 9:48-65.</p> <p>Taylor discloses: “Grouping pseudowires based on hardware interfaces and configured control paths enables improved pseudowire failover performance. Signaling status changes (e.g., from standby to active status) is facilitated by using group IDs for the pseudowire groups, thereby enabling improved failover performance when there is disruption in the network.” Taylor, Abstract.</p>

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		<p>T-PE1 Interface e1/0.111 xconnect 10.2.2.2 1 encap mpls backup peer 10.3.3.3 101 interface e1/0.222 xconnect 10.3.3.3 2 encap mpls backup peer 10.2.2.2 102</p> <p>S-PE2 12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls neighbor 10.4.4.4 102 encap mpls 12 vfi two point-to-point neighbor 10.1.1.1 102 encap mpls neighbor 10.4.4.4 102 encap mpls</p> <p>T-PE2 10.2.2.2</p> <p>T-PE4 10.4.4.4 e4/0</p> <p>S-PE3 12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls neighbor 10.4.4.4 101 encap mpls 12 vfi two point-to-point neighbor 10.1.1.1 2 encap mpls neighbor 10.4.4.4 2 encap mpls</p> <p>T-PE4 interface e4/0.111 xconnect 10.2.2.2 1 encap mpls backup peer 10.3.3.3 101 interface e4/0.222 xconnect 10.3.3.3 2 encap mpls backup peer 10.2.2.2 102</p>

FIG. 4

Taylor, FIG. 4 (annotated).

No.	'821 Patent Claim 1	The Reference
		<p>The diagram illustrates a network topology with six nodes:</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1): Located at the bottom left. It has two interfaces: e0/0 (solid lines) and e1/0 (dashed lines). e0/0 connects to S-PE2 (12.1.1.1) via link 1 and S-PE3 (13.1.1.1) via link 2. e1/0 connects to T-PE4 (14.1.1.1) via link 3 and T-PE5 (15.1.1.1) via link 4. S-PE2 (12.1.1.1): Located at the top center. It has two interfaces: e0/0 (solid lines) and e1/0 (dashed lines). e0/0 connects to T-PE1 via link 1 and T-PE4 via link 5. e1/0 connects to T-PE4 via link 6 and T-PE5 via link 7. S-PE3 (13.1.1.1): Located at the bottom center. It has two interfaces: e0/0 (solid lines) and e1/0 (dashed lines). e0/0 connects to T-PE1 via link 3 and T-PE5 via link 5. e1/0 connects to T-PE5 via link 6 and T-PE4 via link 8. T-PE4 (14.1.1.1): Located at the top right. It has two interfaces: e0/0 (solid lines) and e1/0 (dashed lines). e0/0 connects to S-PE2 via link 5 and S-PE4 via link 9. e1/0 connects to S-PE3 via link 6 and S-PE4 via link 10. S-PE4: Located at the bottom right. It has two interfaces: e0/0 (solid lines) and e1/0 (dashed lines). e0/0 connects to T-PE4 via link 9 and T-PE5 via link 11. e1/0 connects to S-PE3 via link 10 and T-PE5 via link 12. T-PE5 (15.1.1.1): Located at the bottom right. It has two interfaces: e0/0 (solid lines) and e1/0 (dashed lines). e0/0 connects to S-PE5 via link 13 and T-PE4 via link 14. e1/0 connects to S-PE4 via link 11 and S-PE5 via link 15. <p>Annotations provide specific configuration details for each node's interfaces and virtual links:</p> <ul style="list-style-type: none"> T-PE1 annotations (504): <ul style="list-style-type: none"> e0/0: xconnect 12.1.1.1 encaps mpls, backup peer 13.1.1.15 e0/0.100: xconnect 12.1.1.1 encaps mpls e0/0.200: xconnect 12.1.1.1 encaps mpls e0/0.300: xconnect 12.1.1.1 encaps mpls e0/0.400: xconnect 12.1.1.1 encaps mpls S-PE2 annotations (514): <ul style="list-style-type: none"> e0/0: vfi abc point-to-point, neighbor 11.1.1.1 encaps mpls, neighbor 14.1.1.1 encaps mpls e0/0: vfi def point-to-point, neighbor 11.1.1.2 encaps mpls, neighbor 14.1.1.2 encaps mpls e0/0: vfi ghi point-to-point, neighbor 11.1.1.3 encaps mpls, neighbor 14.1.1.3 encaps mpls e0/0: vfi jkl point-to-point, neighbor 11.1.1.4 encaps mpls, neighbor 14.1.1.4 encaps mpls T-PE4 annotations (506): <ul style="list-style-type: none"> e0/0.100: xconnect 12.1.1.1 encaps mpls e0/0.200: xconnect 12.1.1.1 encaps mpls e1/0.100: xconnect 12.1.1.13 encaps mpls e1/0.200: xconnect 12.1.1.14 encaps mpls S-PE3 annotations (516): <ul style="list-style-type: none"> e0/0: vfi abc point-to-point, neighbor 11.1.1.5 encaps mpls, neighbor 15.1.1.5 encaps mpls e0/0: vfi def point-to-point, neighbor 11.1.1.6 encaps mpls, neighbor 15.1.1.6 encaps mpls e0/0: vfi ghi point-to-point, neighbor 11.1.1.7 encaps mpls, neighbor 15.1.1.7 encaps mpls e0/0: vfi jkl point-to-point, neighbor 11.1.1.8 encaps mpls, neighbor 15.1.1.8 encaps mpls T-PE5 annotations (518): <ul style="list-style-type: none"> e0/0.100: xconnect 13.1.1.15 encaps mpls e0/0.200: xconnect 13.1.1.16 encaps mpls e1/0.100: xconnect 13.1.1.17 encaps mpls e1/0.200: xconnect 13.1.1.18 encaps mpls

FIG. 5

Taylor, FIG. 5 (annotated).

No.	'821 Patent Claim 1	The Reference
		<p>TRAFFIC FLOW IS TOWER ← CORE</p> <p>TOWER T-PE</p> <p>S-PE1</p> <p>CORE T-PE1</p> <p>mLACP</p> <p>RNC/BSC</p> <p>CORE T-PE2</p> <p>ACTIVE PW/ACTIVE AC</p> <p>STANDBY PW/ACTIVE AC</p> <p>STANDBY* PW/STANDBY AC</p> <p>STANDBY PW/STANDBY AC</p> <p>* GOES ACTIVE WHEN mLACP FAILS OVER</p> <p>FIG. 8</p>

Taylor, FIG. 8 (annotated).

No.	'821 Patent Claim 1	The Reference
		<pre> sequenceDiagram participant TOWER as TOWER T-PE participant S1 as S-PE1 participant S2 as S-PE2 participant CORE as CORE T-PE TOWER->>S1: S-PE1 FFD PWs MARKED DOWN activate S1 S1-->>S2: S-PE1 FAILURE deactivate S1 S2-->>CORE: HOT STANDBY → ACTIVE activate CORE CORE-->>S2: HOT STANDBY → ACTIVE deactivate CORE S2-->>CORE: S-PE1 GROUPS MARKED DOWN deactivate S2 </pre> <p><i>Annotations:</i></p> <ul style="list-style-type: none"> S-PE1 FFD PWs MARKED DOWN S-PE1 FAILURE (boxed) HOT STANDBY → ACTIVE (yellow boxes) S-PE1 GROUPS MARKED DOWN

FIG. 9

Taylor, FIG. 9 (annotated).

No.	'821 Patent Claim 1	The Reference
		<pre> graph TD 1002((1002)) --> 1004[1004 SPECIFY PHYSICAL INTERFACE AT FIRST PE] 1004 --> 1006[1006 SPECIFY FIRST CONTROL PATH FROM FIRST PE TO SECOND PE] 1006 --> 1008[1008 SPECIFY SECOND CONTROL PATH FROM FIRST PE TO THIRD PE] 1008 --> 1010[1010 SPECIFY REDUNDANT COMBINATIONS OF PWs] 1010 --> 1012[1012 GROUP PWs INTO A PRIMARY PW GROUP AND A BACKUP PW GROUP] 1012 --> 1014[1014 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO PRIMARY/BACKUP PW GROUPS] 1014 --> 1016[1016 DETECT FAILURE ON FIRST CONTROL PATH] 1016 --> 1018[1018 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO BACKUP/PRIMARY PW GROUPS] </pre> <p>FIG. 10</p> <p>Taylor, FIG. 10 (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<p>“The present disclosure relates generally to communication networks and more particularly to pseudowire configurations in communication networks.” Taylor, 1:8-10.</p> <p>“Virtual Private Network (VPN) services provide secure network connections between different locations. A company, for example, can use a VPN to provide secure connections between geographically dispersed sites that need to access the corporate network so that each customer edge (CE) end point or node can communicate directly and independently with all other CE nodes. Different types of VPNs have been classified by the network layer used to establish the connection between the customer and provider network. For example, Virtual Private LAN Service (VPLS) is an architecture that delivers a multipoint Layer 2 VPN (L2VPN) service that in all respects emulates an Ethernet Local Area Network (LAN) across a wide metropolitan geographic area. All services in a VPLS appear to be on the same LAN, regardless of location. In other words, with VPLS, customers can communicate as if they were connected via a private Ethernet segment, i.e., multipoint Ethernet LAN services.” Taylor, 1:12-28.</p> <p>“In this context, each CE device at a customer site is connected to the service provider network at a provider edge PE) device by an Attachment Circuit (AC) that provides the customer connection to a service provider network, that is, the connection between a CE node and its associated PE node. Within the provider network, each PE device includes a Virtual Switch Instance (VSI) that emulates an Ethernet bridge (i.e., switch) function in terms of Media Access Control (MAC) address learning and forwarding in order to facilitate the provisioning of a multipoint L2VPN. A pseudowire (PW) is a virtual connection between two PE devices that connect two attachment circuits. In the context of the VPLS service, a pseudowire can be thought of as a point-to-point virtual link for each offered service between a pair of VSIs. Therefore, if each VSI can be thought of as a virtual Ethernet switch for a given customer service instance, then each pseudowire can be thought of as a virtual link connecting these virtual switches to each other over a Packet Switched Network (PSN) for that service instance.” Taylor, 1:29-47.</p>

No.	'821 Patent Claim 1	The Reference
		<p>“Since the failure of pseudowires obviously degrades network performance, some effort has been directed towards adding system redundancies including redundant pseudowires. However, the presence of redundant pseudowires alone is insufficient to improve overall failover performance, that is, the ability to switch over automatically to a redundant or backup system. Thus, there is a need for improved methods for managing pseudowires to facilitate pseudowire switching and enable improved failover performance.” Taylor, 1:48-56.</p> <p>“FIG. 4 shows details for PW connectivity in an exemplary network for an example embodiment.” Taylor, 1:66-67.</p> <p>“FIG. 5 shows details for PW grouping for an example embodiment.” Taylor, 2:1-2.</p> <p>“FIG. 8 shows an example network including redundant PW connections for an example embodiment.” Taylor, 2:9-10.</p> <p>“FIG. 9 shows an example sequence diagram for a failure mode related to the embodiment shown in FIG. 8.” Taylor, 2:11-12.</p> <p>“FIG. 10 shows a flowchart that illustrates a method of providing improved PW grouping according to an example embodiment.” Taylor, 2:13-15.</p> <p>“According to one embodiment, a method of providing improved pseudowire performance includes specifying a physical interface at a first PE node in a network, a first control path from the first PE node to a second PE node in the network, and a second control path from the first PE node to a third PE node in the network. With these specifications, the method then includes specifying redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node, and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Then these pseudowires can be grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. Group identifiers for the primary PW group and the backup PW group can then be used to assign an</p>

No.	'821 Patent Claim 1	The Reference
		<p>active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths. The method may then include detecting a failure on the first control path, and in response to the detected failure, using the group identifiers to assign the active status to the backup pseudowires and the standby status to the primary pseudowires.” Taylor, 2:27-52.</p> <p>“Pseudowires are used in pseudowire emulation edge-to-edge to provide a Layer 2 Virtual Private Network (L2VPN) connection. When large numbers (e.g., 4,000-6,000) of pseudowires are aggregated together on a single router, failure performance tends to be linear or O(n) where n is the number of pseudowires. While O(n) performance may be acceptable for small numbers of pseudowires, the effect on network outages can be increasingly undesirable as the number of pseudowires increases.” Taylor, 2:54-62.</p> <p>“For example, a cell-site router will typically start an approximately 2-minute procedure if contact with its controller, which is reached via a pseudowire, is lost for more than some threshold amount (e.g., between approximately 0.75 and 1.75 seconds in some cases). This can be a major impediment to the scalability of pseudowire deployments. These issues have become increasingly relevant as providers of Multiservice Broadband Networks (MBNs) are rapidly replacing or augmenting their traditional Synchronous Optical Networking (SONET) equipment with cheaper Ethernet equipment in the evolution towards a 4G (i.e., 4th generation) network.” Taylor, 2:63-3:7.</p> <p>“One aspect of a solution to the problem of pseudowire failure is the deployment of redundant pseudowires. For example, redundant pseudowires have been used in the context of Multiprotocol Label Switching (MPLS) networks, which use a Label Distribution Protocol (LDP) to manage labels for forwarding traffic between routers. In this context, general requirements for redundancy schemes have been developed so that duplicate pseudowires are available when a given pseudowire fails (e.g., by using active/standby status indicators). In addition, more specific implementations for redundant pseudowires have also been developed.” Taylor, 3:8-18.</p>

No.	'821 Patent Claim 1	The Reference
		<p>“FIG. 1 shows a reference network model 102 with applications to example embodiments disclosed herein. The reference network model 102 includes an aggregation network 104 of PE nodes and a distribution network 106 of PE nodes between a radio network controller (RNC) (or base station controller (BSC)) 108 on the core side of the model 102 and a radio tower 111 on the tower side of the model 102. Switching provider edge nodes S-PE1 and S-PE2 connect the two networks 104, 106. On the core side, two core terminating provider edges T-PE1 and T-PE2 connect to the RNC/BSC 108 through attachment circuits 110, 112. On the tower side, one tower terminating provider edge T-PE connects to the radio tower 111 through an attachment circuit 114.” Taylor, 3:19-31.</p> <p>“Additionally as noted in FIG. 1, peer-PE monitoring is carried out within each network 104, 106. That is, there is peer-PE monitoring between provider edges that share a segment, for example, by multi-hop bidirectional forwarding detection (BFD). Alternatively, peer monitoring can be accomplished by other means (e.g., MPLS-TP (Transport Protocol) keep-alives). This peer-PE monitoring is used to provide the mechanism for fast failure detection. Once a failure is detected, the network can react by “rerouting” the failed pseudowires to pre-provisioned backup paths and thus provide a minimal disruption in service to the end-user. This rerouting can be accomplished by LDP signaling between provider edges.” Taylor, 3:32-44.</p> <p>“The reference network model 102 may be considered as part of a larger hub-and-spoke model as shown in FIG. 2. A hub-and-spoke distribution model 202 includes a core network 204, distribution networks 206, and aggregation networks 208. Network elements including distribution nodes, aggregation nodes, and towers are also shown with nominal count values (e.g., 30 distribution nodes between the core network 204 and a distribution network 206). In this model 202, tower T-PEs are the spokes while core-PEs constitute the hub. Dozens to hundreds of tower T-PEs connect to a few S-PEs; these S-PEs are quite similar to ASBRs as they act as forwarders between the two distinct MPLS domains, providing isolation and, in the case of mobility, aggregation services. Typically, several aggregation networks 208 are connected to a single distribution network 206, eventually connecting the tower with the core router that connects the tower's ACs to the RNC/BSC. There are typically several distribution</p>

No.	'821 Patent Claim 1	The Reference
		<p>networks in a Radio Access Network (RAN) connected to the service provider's core Internet Protocol (IP) network.” Taylor, 4:11-30.</p> <p>“With reference to FIG. 1, FIG. 3 shows a variety of failure modes encountered in the reference network model 102. Failure 302 of communications between tower T-PE and the S-PE can be detected via peer monitoring when both the S-PE and the T-PE are still active/alive. For example, this failure may be due to a loss of connectivity when the BFD session goes down. Failure 304 of S-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability (e.g., if the BFD-session hello timers cannot be serviced for the prescribed period of time). Failure 306 of communications between S-PE1 and core T-PE1 can be detected via peer monitoring when both S-PE1 and core T-PE1 are still active/alive. Failure 308 at core T-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability.” Taylor, 4:46-60.</p> <p>“Pseudowire connectivity is further illustrated in FIG. 4 where the illustrated network includes four nodes: T-PE1 (10.1.1.1), S-PE2 (10.2.2.2), S-PE3 (10.3.3.3), and T-PEA (10.4.4.4). For the terminating nodes T-PE1 and T-PE4, specifications for VLANs (virtual Local Area Networks) connections (i.e., pseudowires) are shown using the Internet Operating System Command Line Interface (IOS CLI). The specification 402 for T-PE1 defines two VLANs as primary/backup combinations of virtual circuits for the network. The first three lines of the specification 402 define “VLAN 111” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 111” in the first line. The second line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE2 (10.12.2) with a virtual circuit Identification (VCID) set as VCID=1, and the third line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=101. The next three lines of the specification 402 define “VLAN 222” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 222” in the fourth line. The fifth line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=2, and the sixth line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE2 (10.2.2.2) with VCID=102. These virtual circuits, VCID=1, VCID=2, VCID=101 and VCID=102 are shown in the figure between T-PE1 and the S-PEs with a solid line for the primary circuits VCID=1</p>

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		<p>and VCID=2 and a dashed line for the backup circuits VCID=101 and VCID=102.” Taylor, 4:61-5:23.</p> <p>“With respect to T-PE1 in FIG. 4, although “VLAN 111” and “VLAN 222” share the same hardware port, they do not share the same “control path disposition.” That is, “VLAN 111” is primary to S-PE2 (VCID=1) and standby to S-PE3 (VCID=101), while “VLAN 222” has an opposite configuration since it is primary to S-PE3 (VCID=2) and standby to S-PE2 (VCID=102). As discussed below, certain embodiments group pseudowires according to “control path disposition” (e.g., xconnect configuration as well as the hardware interface in order to improve failover performance. That is, to deal with both hardware port failures and switching path failures, the grouping criteria also considers the cross connects. In this case, on T-PE1 as well as T-PE4, there would exist two groups: one for active to S-PE2 and standby to S-PE3 and another for active to S-PE3 and standby to S-PE2 (i.e., the inverse configuration).” Taylor, 5:61-6:9.</p> <p>“First, local connectivity is characterized by local group identifications (Group-IDs), which depend on whether the allocation is done at a T-PE or S-PE. FIG. 5 shows an embodiment that illustrates an allocation of local group IDs in a network including terminating nodes T-PE1 (11.1.1.1), T-PE4 (14.1.1.1), and T-PE5 (15.1.1.1) and switching nodes S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1). The specification 502 for T-PE1 determines corresponding local group IDs 504 based on the hardware interface and the control path. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, and the Group-ID=2 corresponds to VCID=5, VCID=6, VCID=7, and VCID=8.” Taylor, 6:10-21.</p> <p>“Local group IDs are maintained in a database so that pseudowire redundancy is also maintained. First, in a case without pseudowire redundancy, all the xconnect configurations from the same physical interface to the same peer are assigned the same local group ID. So, for example, in Ethernet cases all xconnect configurations under sub-interfaces of the same physical interface to the same peer will be assigned the same local group ID (e.g., e0/0 and e0/1 are sub-interfaces of e0). FIG. 6 shows a database representation for T-PE4 from FIG. 5. From the root node 602 for T-PE4, there is a first interface node 604 for e0/1 and a second interface node 606 for e1/0. The first interface node 604 is configured towards a single peer</p>

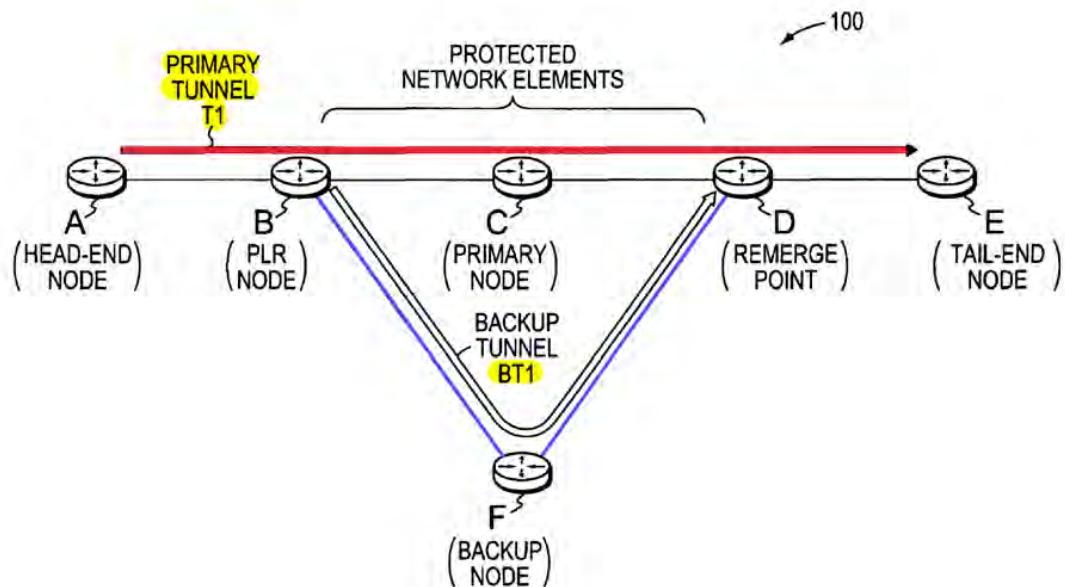
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		<p>node (12.1.1.1) 608 and is thus assigned a single local group ID (Group-ID=200) 610. Similarly, the second interface node 606 is configured towards a single peer node (12.1.1.1) 612 and is thus assigned a single local group ID (Group-ID=201) 614. In this case from the assignment of local group IDs 508 in FIG. 5, Group-ID=200 corresponds to VCID=1 and VCID=2, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). Both pseudowires (VCID=1 and VCID=2) are assigned the same local group ID (Group-ID=200) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly, from the assignment of local group IDs 508 in FIG. 5, T-PE4 has VCID=3 and VCID=4 under the physical interface e1/0 going to the same peer S-PE2 (12.1.1.1), and the local Group ID (Group-ID=201) is assigned to these VCs.” Taylor, 6:48-7:8.</p> <p>“For the pseudowire redundancy case, a separate redundancy-group database is maintained by the xconnect application. This redundancy-group database contains the peer IDs in the group and the local group IDs advertised to them. This is needed to maintain a 1:1 mapping between the primary pseudowires and their corresponding backup pseudowires. FIG. 7 shows a database representation for T-PE1 from FIG. 5. From the root node 702 for T-PE1, there is an interface node 704 for e0/0 and a redundancy group node 706 that shows connections for configurations to a first peer node (12.1.1.1) 708, which is assigned a local group ID (Group-ID=1) 710, and a second peer node (13.1.1.1) 712, which is assigned a local group ID (Group-ID=2) 714. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). These pseudowires are assigned the same group ID (Group-ID=1) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly from the assignment of local group IDs 504 in FIG. 5, T-PE1 has VCID=5, VCID=6, VCID=7, and VCID=8 under the physical interface e0/0 going to another peer S-PE3 (13.1.1.1), and the local group ID (Group-ID=2) is assigned to these VCs. In this case these local group IDs are organized as a redundancy group 706.” Taylor, 7:9-35.</p> <p>“Pseudowire grouping allows multiple pseudowires to be aggregated together when signaling either label withdrawals or status changes between segment end-point provider edges. This signaling can be carried out through LDP grouping TLV (Type Length Value). For example,</p>

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		<p>when a PE node fails (e.g., failure 304 of S-PE1), aggregating the inter-segment PE signaling using the grouping TLV can provide significant scaling advantages. This allows all pseudowires sharing a physical port and PW configuration (e.g., xconnect configuration) to be signaled en masse between segment-adjacent provider edges.” Taylor, 7:36-46.</p> <p>“FIG. 8 shows an example based on FIG. 1 where VLAN ACs are shown as grouped by both port/HW-interface and pseudowire-class. The grouping criterion allows all “similar” pseudowires to be signaled together: All the grouped pseudowires share the same port and next-hop provider edge. Additionally, the figure contains many pseudowires, each grouped into a shaded tube. For example, the tube labeled “VLANs 100-549” contains 450 pseudowires grouped together. This figure depicts an incoming Ethernet comprised of 900 VLANs being segmented in two with 450 VLANs (100-549) active to S-PE1 while the other half of the VLANs (550-999) being active to S-PE2. This might be considered a type of manual load balancing. Furthermore, the aggregation network is only showing a single tower and the VLANs associated with it; other VLAN destinations are not shown in the figure.” Taylor, 7:47-62.</p> <p>“‘VLANs 110-112’ are active along a first pseudowire path 802 from Core T-PE1 to S-PE1 and a second pseudowire path 804 from S-PE1 to Tower T-PE. When a failure occurs at S-PE1 (e.g., as the switching node failure 304 shown in FIG. 3), then the standby pseudowires become active for ‘VLANs 110-112’ along a first pseudowire path 806 from Core T-PE1 to S-PE2 and a second pseudowire path 808 from S-PE2 to Tower T-PE.” Taylor, 7:63-8:3.</p> <p>“The standby pseudowires in FIG. 8 can be configured as HSPWs, a configuration that enables ACs to quickly failover to pre-provisioned pseudowires that are in active state but set to not-forwarding. Then when a failure occurs, switching over to these pre-provisioned HSPWs occurs quickly by switching from not-forwarding status to forwarding status.” Taylor, 8:4-9.</p> <p>“FIG. 9 shows a UML (Unified Modeling Language) sequence diagram of signaling events related to the failover procedure illustrated in FIG. 8 for a switching node failure 304. The Tower T-PE and the Core T-PE independently detect a failure at S-PE1 (e.g., BFD forwarding with LDP signaling), and then mark down the status of the currently active pseudowires routed</p>

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		<p>through S-PE1 and mark up the status of the standby pseudowires routed through S-PW2. Other failure modes shown in FIG. 3 can be handled similarly.” Taylor, 8:10-18.</p> <p>“In general, it is desirable for MPLS-based Ethernet networks to react quickly to failures, so proactive detection mechanisms are employed in order to pick up system failures quickly. All proactive monitoring is typically done between PE peers on a single MPLS network. These provider edges on the edges of the MPLS network act similarly to an Autonomous System Boundary Router (ASBR). As a result, related embodiments detect control path failures, which may not be the same as pseudowire data path failures. That is, the data packets and control packets may take different paths between provider edges in a MPLS network although typically these paths are coincident. Thus, when the control and data paths are not coincident, if the control path fails, then all pseudowires utilizing the control path are marked as failed. As a corollary, if the data path fails and the control path remains healthy, then failure will not be detected from monitoring the control path.” Taylor, 8:19-35.</p> <p>“A failure of a monitored provider edge initiates a switchover of all active pseudowires using the failing provider edge to their configured HSPWs (if they exist). Grouping can greatly reduce the number of messages needed between provider edges (Inter-PE Aggregation) and within a single provider edge (Intra-PE Aggregation). Furthermore, the MPLS network itself may be internally resilient deploying technologies such as, but not limited to, MPLS-TE (MPLS Traffic Engineering) and ERR (Fast Reroute). The paths across the MPLS network may recover quickly and might not trip the fault-monitoring systems.” Taylor, 8:36-46.</p> <p>“With reference to the above discussion, FIG. 10 shows a method 1002 of providing improved PW grouping according to an example embodiment. In a first operation 1004 of the method 1002, a physical interface is specified at a first PE node in a network. In a second operation 1006, a first control path is specified from the first PE node to a second PE node in the network. In a third operation 1008, a second control path is specified from the first PE node to a third PE node in the network. These control paths related to a common physical interface can be used to characterize redundant pairs of pseudowires.” Taylor, 8:48-58.</p>

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		<p>“In a fourth operation 1010, redundant combinations of pseudowires are specified, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Although a redundant combination may relate a single backup pseudowire to a given primary pseudowire, in some cases multiple backup pseudowires will be related to a given primary pseudowire for increased redundancy. In a fifth operation 1012, these pseudowires are grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. In a sixth operation 1014, group identifiers for the primary PW group and the backup PW group are used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 8:59-9:11.</p> <p>“In an optional seventh operation 1016, a failure may be detected on the first control path, and in an optional eighth operation 1018, in response to the detected failure, the group identifiers may be used to assign the active status to the backup pseudowires and the standby status to the primary pseudowires. For example, the failure on the first control path may be detected by using BED packet streams between PE nodes of the network. Then the detected failure can be signaled to PE nodes in the network by sending LDP status updates between PE nodes in the network. Then, after receiving the failure detection signals, the group identifiers can be used again to assign the active status to the backup pseudowires and the standby status to the primary pseudowires by sending LDP status updates between PE nodes in the network.” Taylor, 9:12-26.</p> <p>“Typically the network in is an MPLS network and the PE nodes are routers that provide network services to connected CE nodes of a customer network. In general, each control path is an Internet Protocol (IP) routing path between PE nodes in the network and each data path is a label switched path (LSP) between PE nodes in the network.” Taylor, 9:27-32.</p>

No.	'821 Patent Claim 1	The Reference
		<p>“FIG. 11 shows a schematic representation of an apparatus 1102, in accordance with an example embodiment. For example, the apparatus 1102 may be used to implement the method 1002 of providing improved pseudowire grouping as described above with reference to FIG. 10. The apparatus 1102 is shown to include a processing system 1104 that may be implemented on a server, client, or other processing device that includes an operating system 1106 for executing software instructions.” Taylor, 10:2-10.</p> <p>“In accordance with an example embodiment, the apparatus 1102 includes a PW management module 1108 that includes a first specification module 1110, a second specification module 1112, third specification module 1114, a fourth specification module 1116, a grouping module 1118, and an assignment module 1120. The first specification module 1110 operates to specify a physical interface at a first PE node in a network. The second specification module 1112 operates to specify a first control path from the first PE node to a second PE node in the network. The third specification module 1114 operates to specify a second control path from the first PE node to a third PE node in the network. The fourth specification module 1116 operates to specify redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node.” Taylor, 10:11-29.</p> <p>“The grouping module 1118 operates to group the pseudowires into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. The assignment module 1120 operates to use group identifiers for the PW groups to assign an active status to the primary pseudowires and a standby status to the backup pseudowires. The active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 10:30-38.</p>

No.	'821 Patent Claim 1	The Reference
		<p>Vasseur '879 discloses:</p> <p>“A technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, Abstract.</p>  <p style="text-align: center;">FIG. 1</p> <p>Vasseur '879, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<p>SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)</p> <pre> graph LR SM[SIGNALING MESSAGE 300] --- CH[COMMON HEADER 310] CH --- SA[SOURCE ADDRESS 312] CH --- DA[DESTINATION ADDRESS 314] SM --- SP[SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320] SP --- LAO[LSP-ATTRIBUTE OBJECT 330] SP --- EO[EXTENSION OBJECT(S) 400] LAO --- EO style SM fill:none,stroke:none style CH fill:none,stroke:none style SA fill:none,stroke:none style DA fill:none,stroke:none style SP fill:none,stroke:none style LAO fill:none,stroke:none style EO fill:none,stroke:none </pre> <p>COMMON HEADER 310</p> <p>SOURCE ADDRESS 312</p> <p>DESTINATION ADDRESS 314</p> <p>SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320</p> <p>LSP-ATTRIBUTE OBJECT 330</p> <p>EXTENSION OBJECT(S) 400</p>

FIG. 3

Vasseur '879, FIG. 3 (annotated).

No.	'821 Patent Claim 1	The Reference
		<p>FIG. 5</p> <p>Vasseur '879, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<p>FIG. 6</p> <p>Vasseur '879, FIG. 6 (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<pre> graph TD START([START]) --> 705[HEAD-END NODE ESTABLISHES PRIMARY TUNNEL] 705 --> 710[HEAD-END NODE REQUESTS PROTECTION OF PRIMARY TUNNEL] 710 --> 715[POINTS OF LOCAL REPAIR (PLRs) CREATE BACKUP TUNNELS TO PROTECT PRIMARY TUNNEL] 715 --> 720[HEAD-END NODE REQUESTS BACKUP TUNNEL PATH QUALITY NOTIFICATION] 720 --> 725[PLRs OF PRIMARY TUNNEL COLLECT PATH QUALITY INFORMATION (e.g., METRICS/STATISTICS) FOR PRIMARY AND BACKUP TUNNELS (e.g., FROM PRIMARY AND/OR BACKUP NODES)] 725 --> 730[PLRs MAY SEND PATH QUALITY NOTIFICATION TO HEAD-END NODE] 730 --> 735[PLRs MONITOR PROTECTED SEGMENT OF PRIMARY TUNNEL FOR FAILURE] 735 --> 740{DETECT FAILURE ?} 740 -- N --> 705 740 -- Y --> A((A)) </pre> <p>FIG. 7A Vasseur '879, FIG. 7A (annotated).</p>

No.	'821 Patent Claim 1	The Reference
		<pre> graph TD A(()) --> B["DETECTING PLR DIVERTS PRIMARY TUNNEL TRAFFIC TO BACKUP TUNNEL AND SENDS ERROR MESSAGE TO HEAD-END NODE"] B --> C["PLR CONTINUES TO COLLECT PATH QUALITY INFORMATION FOR BACKUP TUNNEL"] C --> D{PLR SEND NOTIFICATION TO HEAD-END NODE ?} D -- N --> E{TIMER EXPIRED ?} E -- Y --> F["HEAD-END NODE DETERMINES WHETHER TO REESTABLISH PRIMARY TUNNEL BASED ON BACKUP TUNNEL PATH QUALITY NOTIFICATION"] F --> G{ACCEPTABLE PATH QUALITY ?} G -- Y --> H["HEAD-END NODE ATTEMPTS TO REESTABLISH PRIMARY TUNNEL"] H --> I([END]) G -- N --> I D -- Y --> F </pre> <p>FIG. 7B</p> <p>Vasseur '879, FIG. 7B.</p>

No.	'821 Patent Claim 1	The Reference
		<p>“The present invention relates to computer networks and more particularly to dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 1:10-14.</p> <p>“Since management of interconnected computer networks can prove burdensome, smaller groups of computer networks may be maintained as routing domains or autonomous systems. The networks within an autonomous system (AS) are typically coupled together by conventional “intradomain” routers configured to execute intradomain routing protocols, and are generally subject to a common authority. To improve routing scalability, a service provider (e.g., an ISP) may divide an AS into multiple “areas.” It may be desirable, however, to increase the number of nodes capable of exchanging data; in this case, interdomain routers executing interdomain routing protocols are used to interconnect nodes of the various ASes. Moreover, it may be desirable to interconnect various ASes that operate under different administrative domains. As used herein, an AS or an area is generally referred to as a “domain,” and a router that interconnects different domains together is generally referred to as a ‘border router.’” Vasseur '879, 1:40-56.</p> <p>“An example of an interdomain routing protocol is the Border Gateway Protocol version 4 (BGP), which performs routing between domains (ASes) by exchanging routing and reachability information among neighboring interdomain routers of the systems. An adjacency is a relationship formed between selected neighboring (peer) routers for the purpose of exchanging routing information messages and abstracting the network topology. The routing information exchanged by BGP peer routers typically includes destination address prefixes, i.e., the portions of destination addresses used by the routing protocol to render routing (“next hop”) decisions. Examples of such destination addresses include IP version 4 (IPv4) and version 6 (IPv6) addresses. BGP generally operates over a reliable transport protocol, such as TCP, to establish a TCP connection/session. The BGP protocol is well known and generally described in Request for Comments (RFC) 1771, entitled A Border Gateway Protocol 4 (BGP-4), published March 1995.” Vasseur '879, 1:57-2:7.</p>

No.	'821 Patent Claim 1	The Reference
		<p>“Examples of an intradomain routing protocol, or an interior gateway protocol (IGP), are the Open Shortest Path First (OSPF) routing protocol and the Intermediate-System-to-Intermediate-System (IS-IS) routing protocol. The OSPF and IS-IS protocols are based on link-state technology and, therefore, are commonly referred to as link-state routing protocols. Link-state protocols define the manner with which routing information and network-topology information are exchanged and processed in a domain. This information is generally directed to an intradomain router's local state (e.g., the router's usable interfaces and reachable neighbors or adjacencies). The OSPF protocol is described in RFC 2328, entitled OSPF Version 2, dated April 1998 and the IS-IS protocol used in the context of IP is described in RFC 1195, entitled Use of OSI IS-IS for routing in TCP/IP and Dual Environments, dated December 1990, both of which are hereby incorporated by reference.” Vasseur '879, 2:8-24.</p> <p>“An intermediate network node often stores its routing information in a routing table maintained and managed by a routing information base (RIB). The routing table is a searchable data structure in which network addresses are mapped to their associated routing information. However, those skilled in the art will understand that the routing table need not be organized as a table, and alternatively may be another type of searchable data structure. Although the intermediate network node's routing table may be configured with a predetermined set of routing information, the node also may dynamically acquire (“learn”) network routing information as it sends and receives data packets. When a packet is received at the intermediate network node, the packet's destination address (e.g., stored in a header of the packet) may be used to identify a routing table entry containing routing information associated with the received packet. Among other things, the packet's routing information indicates the packet's next-hop address.” Vasseur '879, 2:25-41.</p> <p>“Multi-Protocol Label Switching (MPLS) Traffic Engineering has been developed to meet data networking requirements such as guaranteed available bandwidth or fast restoration. MPLS Traffic Engineering exploits modern label switching techniques to build guaranteed bandwidth end-to-end tunnels through an IP/MPLS network of label switched routers (LSRs). These tunnels are a type of label switched path (LSP) and thus are generally referred to as MPLS Traffic Engineering (TE) LSPs. Examples of MPLS TE can be found in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels dated December 2001, RFC 3784</p>

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		<p>entitled Intermediate-System-to-Intermediate-System (IS-IS) Extensions for Traffic Engineering (TE) dated June 2004, and RFC 3630, entitled Traffic Engineering (TE) Extensions to OSPF Version 2 dated September 2003, the contents of all of which are hereby incorporated by reference in their entirety.” Vasseur '879, 2:58-3:6.</p> <p>“Establishment of an MPLS TE-LSP from a head-end LSR to a tail-end LSR involves computation of a path through a network of LSRs. Optimally, the computed path is the “shortest” path, as measured in some metric, that satisfies all relevant LSP Traffic Engineering constraints such as e.g., required bandwidth, “affinities” (administrative constraints to avoid or include certain links), etc. Path computation can either be performed by the head-end LSR or by some other entity operating as a path computation element (PCE) not co-located on the head-end LSR. The head-end LSR (or a PCE) exploits its knowledge of network topology and resources available on each link to perform the path computation according to the LSP Traffic Engineering constraints. Various path computation methodologies are available including CSPF (constrained shortest path first). MPLS TE-LSPs can be configured within a single domain, e.g., area, level, or AS, or may also span multiple domains, e.g., areas, levels, or ASes.” Vasseur '879, 3:7-24.</p> <p>“The PCE is an entity having the capability to compute paths between any nodes of which the PCE is aware in an AS or area. PCEs are especially useful in that they are more cognizant of network traffic and path selection within their AS or area, and thus may be used for more optimal path computation. A head-end LSR may further operate as a path computation client (PCC) configured to send a path computation request to the PCE, and receive a response with the computed path, potentially taking into consideration other path computation requests from other PCCs. It is important to note that when one PCE sends a request to another PCE, it acts as a PCC.” Vasseur '879, 3:25-36.</p> <p>“Some applications may incorporate unidirectional data flows configured to transfer time-sensitive traffic from a source (sender) in a computer network to a destination (receiver) in the network in accordance with a certain “quality of service” (QoS). Here, network resources may be reserved for the unidirectional flow to ensure that the QoS associated with the data flow is maintained. The Resource ReSerVation Protocol (RSVP) is a network-control protocol that</p>

No.	'821 Patent Claim 1	The Reference
		<p>enables applications to reserve resources in order to obtain special QoS for their data flows. RSVP works in conjunction with routing protocols to, e.g., reserve resources for a data flow in a computer network in order to establish a level of QoS required by the data flow. RSVP is defined in R. Braden, et al., Resource ReSerVation Protocol (RSVP), RFC 2205, the contents of which are hereby incorporated by reference in its entirety. In the case of traffic engineering applications, RSVP signaling (with Traffic Engineering extensions) is used to establish a TE-LSP and to convey various TE-LSP attributes to routers, such as border routers, along the TE-LSP obeying the set of required constraints whose path may have been computed by various means.” Vasseur ’879, 3:37-57.</p> <p>“Generally, a tunnel is a logical structure that encapsulates a packet (a header and data) of one protocol inside a data field of another protocol packet with a new header. In this manner, the encapsulated data may be transmitted through networks that it would otherwise not be capable of traversing. More importantly, a tunnel creates a transparent virtual network link between two network nodes that is generally unaffected by physical network links or devices (i.e., the physical network links or devices merely forward the encapsulated packet based on the new header). While one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur ’879, 3:58-4:3.</p> <p>“Occasionally, a network element (e.g., a node or link) will fail, causing redirection of the traffic that originally traversed the failed network element to other network elements that bypass the failure. Generally, notice of this failure is relayed to the nodes in the network through an advertisement of the new network topology, e.g., an IGP or BGP Advertisement, and routing tables are updated to avoid the failure accordingly. Reconfiguring a network in response to a network element failure using, e.g., pure IP rerouting, can be time consuming. Many recovery techniques, however, are available to provide fast recovery and/or network configuration in the event of a network element failure, including, inter alia, “Fast Reroute”, e.g., MPLS TE Fast Reroute. An example of MPLS TE Fast Reroute is described in Pan, et al., Fast Reroute Extensions to RSVP-TE for LSP Tunnels, RFC 4090, May 2005, which is hereby incorporated by reference as though fully set forth herein.” Vasseur ’879, 4:4-21.</p>

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		<p>“Fast Reroute (or FRR) has been widely deployed to protect against network element failures, where “backup tunnels” are created to bypass one or more protected network elements (e.g., links, shared risk link groups (SRLGs), and nodes). When the network element fails, traffic is quickly diverted (“Fast Rerouted”) over a backup tunnel to bypass the failed element, or more particularly, in the case of MPLS, a set of primary TE-LSPs (tunnels) is quickly diverted. Specifically, the point of local repair (PLR) node configured to reroute the traffic inserts (“pushes”) a new label for the backup tunnel, and the traffic is diverted accordingly. Once the failed element is bypassed, the backup tunnel label is removed (“popped”), and the traffic is routed along the original path according to the next label (e.g., that of the original TE-LSP). Notably, the backup tunnel, in addition to bypassing the failed element along a protected primary TE-LSP, also intersects the primary TE-LSP, i.e., it begins and ends at nodes along the protected primary TE-LSP.” Vasseur '879, 4:22-39.</p> <p>“To offer maximum protection, e.g., guaranteed bandwidth, during Fast Reroute, backup tunnels may reserve a configurable amount of bandwidth to ensure that Fast Rerouted traffic from the primary tunnel has a reserved path to follow. For example, the bandwidth reserved for the primary tunnel may also be reserved on the backup tunnel. While this approach provides maximum protection, it also requires a non-negligible amount of network resources (e.g., capacity/bandwidth) and may increase operational complexity.” Vasseur '879, 4:40-48.</p> <p>“Certain techniques are available to efficiently minimize the amount of resources required by the establishment and maintenance of the backup tunnels for Fast Reroute. One such technique is to create zero-bandwidth (“0-BW”) backup tunnels (i.e., tunnels that reserve no bandwidth) to protect non-0-BW primary tunnels. This “best effort” approach does not guarantee that the path followed by the backup tunnel will have enough bandwidth to support the diverted primary tunnel at the time of failure without QoS degradation, however in many situations the path quality of the backup tunnel is sufficient. For instance, if the network is not overly congested, or the backup tunnel follows a non-congested path, there may be enough available bandwidth along the backup tunnel to support the newly rerouted traffic. Also, because primary tunnels often reserve bandwidth in response to “peak” traffic utilization, the amount of traffic over the primary tunnel at the time of failure may be far less than the reserved bandwidth (e.g., at “off-peak” times). Notably, those skilled in the art will understand that in</p>

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		<p>the absence of the above exceptions, a 0-BW backup tunnel may have unacceptable bandwidth (e.g., affecting path quality) to support the diverted traffic.” Vasseur '879, 4:49-5:2.</p> <p>“Currently, head-end nodes (LSRs) may be configured to systematically reroute the primary tunnels affected by the network element failure (e.g., diverted primary tunnels), especially in the case with 0-BW backup tunnels, such as, e.g., by reestablishing a new primary tunnel that follows a path excluding the failed network element. In particular, 0-BW backup tunnels represent a best effort attempt to allow the head-end node to more gracefully reestablish the primary tunnel in response to a failure, since the backup tunnels may not be able to support the diverted traffic without QoS degradation. The systematic reestablishing may potentially result in the reestablishment of a large number of primary tunnels (e.g., up to 3000 for a single network element failure in today's networks). Notably, reestablishing diverted primary tunnels may be undesirable for the network, such as by creating traffic churn, jitter, control plane overloads, etc., as will be understood by those skilled in the art. However, as noted above, there are situations where the backup tunnel may provide acceptable bandwidth, at least, for example, for a period of time (e.g., possibly short) until the failed network element is restored. In these situations, then, it may have been unnecessary to reestablish the diverted primary tunnels. There remains a need, therefore, for a technique that dynamically determines whether to reestablish a diverted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 5:3-28.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, 5:32-47.</p>

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		<p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as Resource ReSerVation Protocol (RSVP) Traffic Engineering (TE) signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or type/length/value (TLV) encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object of the RSVP messages to convey the path quality notification.” Vasseur '879, 5:48-58.</p> <p>“In accordance with one aspect of the present invention, the head-end node requests the establishment of the primary tunnel (e.g., a TE-Label Switched Path, TE-LSP), along with a request for Fast Reroute protection of one or more network elements (e.g., with zero-bandwidth, 0-BW backup tunnels) at a PLR node. In addition, the head-end node may include a request for backup tunnel path quality notification, such as, e.g., through the use of the novel Feedback sub-object. The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and sends an error message (e.g., an RSVP PathErr) to the head end node indicating the “Fast Rerouting” of the primary tunnel.” Vasseur '879, 5:59-6:6.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, the PLR node may collect metrics/statistics (e.g., packet drops, path cost, jitter, etc.) of the primary and/or backup tunnels. Once the primary tunnel is Fast Rerouted, the PLR node continues to collect metrics of the backup tunnel, and may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. Notably, the PLR node may be configured to send path quality notifications to the head-end node once, continually, periodically, in response to configurable changes in path quality, etc. Also, as in the case where multiple primary tunnels are Fast Rerouted, the path quality notification may include an indication of which Fast Rerouted primary tunnels in particular have been effected by the changed path quality.” Vasseur '879, 6:7-23.</p>

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		<p>“In accordance with yet another aspect of the present invention, upon receiving the error message (PathErr), the head-end node may wait for the path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. Notably, in the event the head-end node does not receive a path quality notification for the backup tunnel (e.g., within a configurable time limit), the head-end node may attempt to reestablish the new primary tunnel. Moreover, where the head-end node has multiple primary tunnels being Fast Rerouted, a configurable subset of the primary tunnels may be reestablished, e.g., to reduce congestion of the backup tunnels, and/or to limit the number of reestablished primary tunnels within a given period of time.” Vasseur '879, 6:24-43.</p> <p>“Advantageously, the novel technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. By providing the head-end node of the primary tunnel with path quality feedback of the backup tunnel, the novel technique avoids reestablishing a potentially large number of tunnels over one or more alternate paths after a failure (and Fast Reroute) if the backup tunnels have acceptable path quality. In particular, the backup tunnels, e.g., 0-BW backup tunnels, may not be congested or subsequently burdened by the Fast Rerouted traffic of the primary tunnel. Also, the failed network element (thus the primary tunnel) may be quickly restored; therefore by not reestablishing the primary tunnel, network jitter, churn, etc., may be avoided. Further, the dynamic nature of the novel technique alleviates the need for cumbersome manual configuration.” Vasseur '879, 6:44-59.</p> <p>“FIG. 3 is schematic block diagram of an exemplary signaling (RSVP) message that may be advantageously used with the present invention.” Vasseur '879, 7:6-8.</p> <p>“FIG. 5 is a schematic block diagram of the computer network in FIG. 1 showing Fast Reroute protection of a primary tunnel using a backup tunnel in accordance with the present invention.” Vasseur '879, 7:12-15.</p>

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		<p>“FIG. 6 is a schematic block diagram of the computer network in FIG. 5 showing an unacceptable backup tunnel path quality and resultant reestablishing of the primary tunnel in accordance with the present invention.” Vasseur '879, 7:16-19.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention.” Vasseur '879, 7:20-24.</p> <p>“FIG. 1 is a schematic block diagram of an exemplary computer network 100 comprising a plurality of nodes A-F, such as routers or other network devices, interconnected as shown. The nodes may be a part of one or more autonomous systems, routing domains, or other networks or subnetworks. For instance, routers A and E may be provider edge (PE) devices of a provider network, (e.g., a service provider network) that are interconnected to one or more customer networks through customer edge (CE) devices (not shown, while the remaining nodes B-D and F may be core provider (P) devices, as will be understood by those skilled in the art. Those skilled in the art will also understand that the nodes A-F may be any nodes within any arrangement of computer networks, and that the view shown herein is merely an example. For example, the nodes may be configured as connections to/from one or more virtual private networks (VPNs), as will be understood by those skilled in the art. These examples are merely representative. Those skilled in the art will understand that any number of routers, nodes, links, etc. may be used in the computer network 100 and connected in a variety of ways, and that the view shown herein is for simplicity.” Vasseur '879, 7:29-49.</p> <p>“Data packets may be exchanged among the computer network 100 using predefined network communication protocols such as the Transmission Control Protocol/Internet Protocol (TCP/IP), User Datagram Protocol (UDP), Asynchronous Transfer Mode (ATM) protocol, Frame Relay protocol, Internet Packet Exchange (IPX) protocol, etc. Routing information may be distributed among the routers of the computer network using predetermined Interior Gateway Protocols (IGPs), such as conventional distance-vector protocols or, illustratively, link-state protocols, through the use of IGP Advertisements.” Vasseur '879, 7:50-60.</p>

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		<p>“FIG. 2 is a schematic block diagram of an exemplary router 200 that may be advantageously used with the present invention, e.g., as an edge router or a core router. The router comprises a plurality of network interfaces 210, a processor 220, and a memory 240 interconnected by a system bus 250. The network interfaces 210 contain the mechanical, electrical and signaling circuitry for communicating data over physical links coupled to the network 100. The network interfaces may be configured to transmit and/or receive data using a variety of different communication protocols, including, inter alia, TCP/IP, UDP, ATM, synchronous optical networks (SONET), wireless protocols, Frame Relay, Ethernet, Fiber Distributed Data Interface (FDDI), etc.” Vasseur '879, 7:61-8:6.</p> <p>“The memory 240 comprises a plurality of storage locations that are addressable by the processor 220 and the network interfaces 210 for storing software programs and data structures associated with the present invention. The processor 220 may comprise necessary elements or logic adapted to execute the software programs and manipulate the data structures. A router operating system 242 (e.g., the Internetworking Operating System, or IOS™, of Cisco Systems, Inc.), portions of which is typically resident in memory 240 and executed by the processor, functionally organizes the router by, inter alia, invoking network operations in support of software processes and/or services executing on the router. These software processes and/or services may comprise routing services 247, Traffic Engineering (TE) services 244, and RSVP services 249. It will be apparent to those skilled in the art that other processor and memory means, including various computer-readable media, may be used to store and execute program instructions pertaining to the inventive technique described herein.” Vasseur '879, 8:7-26.</p> <p>“Routing services 247 contain computer executable instructions executed by processor 220 to perform functions provided by one or more routing protocols, such as IGP (e.g., OSPF and IS-IS), IP, BGP, etc. These functions may be configured to manage a forwarding information database (not shown) containing, e.g., data used to make forwarding decisions. Routing services 247 may also perform functions related to virtual routing protocols, such as maintaining VRF instances (not shown) as will be understood by those skilled in the art.” Vasseur '879, 8:27-36.</p>

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		<p>“RSVP services 249 contain computer executable instructions for implementing RSVP and processing RSVP messages in accordance with the present invention. RSVP is described in RFC 2205, entitled Resource ReSerVation Protocol (RSVP), and in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels, both as incorporated above.” Vasseur '879, 8:37-42.</p> <p>“TE services 244 contain computer executable instructions for operating TE functions in accordance with the present invention. Examples of Traffic Engineering are described in RFC 3209, RFC 3784, and RFC 3630 as incorporated above, and in RFC 3473, entitled, Generalized Multi-Protocol Label Switching (GMPLS) Signaling Resource ReSerVation Protocol-Traffic Engineering (RSVP-TE) Extensions dated January 2003, which is hereby incorporated by reference in its entirety. A TE database (TED, not shown) may be illustratively resident in memory 240 and used to store TE information provided by the routing protocols, such as IGP, BGP, and/or RSVP (with TE extensions, e.g., as described herein), including, inter alia, path quality information as described herein. The TED may be illustratively maintained and managed by TE services 244.” Vasseur '879, 8:43-57.</p> <p>“Changes in the network topology may be communicated among routers 200 using a link-state protocol, such as the conventional OSPF and IS-IS protocols. Suppose, for example, that a communication link fails or a cost value associated with a network node changes. Once the change in the network's state is detected by one of the routers, that router may flood an IGP Advertisement communicating the change to the other routers in the network. In this manner, each of the routers eventually “converges” to an identical view of the network topology.” Vasseur '879, 8:58-67.</p> <p>“In one embodiment, the routers described herein are IP routers that implement Multi-Protocol Label Switching (MPLS) and operate as label switched routers (LSRs). In one simple MPLS scenario, at an ingress to a network, a label is assigned to each incoming packet based on its forwarding equivalence class before forwarding the packet to a next-hop router. At each router, a forwarding selection and a new substitute label are determined by using the label found in the incoming packet as a reference to a label forwarding table that includes this information. At the network egress, a forwarding decision is made based on the incoming label</p>

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		<p>but optionally no label is included when the packet is sent on to the next hop. In some network configurations, one hop prior to the network egress, a penultimate hop popping (PHP) operation may be performed. Particularly, because the hop prior to the network egress (the penultimate hop) is attached to the network egress, the label is no longer needed to assure that the traffic follows a particular path to the network egress. As such, the PHP-enabled device “pops” the labels from the traffic before forwarding the traffic to the network egress, e.g., using conventional or native (IP) routing, thereby alleviating the task of removing the labels at the network egress.” Vasseur '879, 9:1-23.</p> <p>“The paths taken by packets that traverse the network in this manner are referred to as label switched paths (LSPs) or Traffic Engineering (TE)-LSPs. An example TE-LSP is shown as the thick line and arrow (T1) between a head-end node (router A) and a tailend node (router E) in FIG. 1. Establishment of a TE-LSP requires computation of a path, signaling along the path, and modification of forwarding tables along the path. MPLS TE establishes LSPs that have guaranteed bandwidth under certain conditions. Illustratively, the TE-LSPs may be signaled through the use of the RSVP protocol (with Traffic Engineering extensions), and in particular, RSVP TE signaling messages. Notably, when incorporating the use of PCEs (described below), the path computation request (and response) between PCC and PCE can be exchanged in accordance with a protocol specified in Vasseur, et al., Path Computation Element (PCE) Communication Protocol (PCEP)—Version 1—<draft-vasseur-pce-pcep-02.txt>, Internet Draft, September 2005, the contents of which are hereby incorporated by reference in its entirety. It should be understood that the use of RSVP or PCEP serves only as an example, and that other communication protocols may be used in accordance with the present invention.” Vasseur '879, 9:24-45.</p> <p>“In accordance with RSVP, to request a data flow (TE-LSP) between a sender and a receiver, the sender may send an RSVP path request (Path) message downstream to the receiver along a path (e.g., a unicast route) to identify the sender and indicate e.g., bandwidth needed to accommodate the data flow, along with other attributes of the TE-LSP. The Path message may contain various information about the data flow including, e.g., traffic characteristics of the data flow. Also in accordance with the RSVP, a receiver establishes the TE-LSP between the sender and receiver by responding to the sender's Path message with a reservation request</p>

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		<p>(Resv) message. The reservation request message travels upstream hop-by-hop along the flow from the receiver to the sender. The reservation request message contains information that is used by intermediate nodes along the flow to reserve resources for the data flow between the sender and the receiver, to confirm the attributes of the TE-LSP, and provide a TE-LSP label. If an intermediate node in the path between the sender and receiver acquires a Path message or Resv message for a new or established reservation (TE-LSP) and encounters an error (e.g., insufficient resources, failed network element, etc.), the intermediate node generates and forwards a path or reservation error (PathErr or ResvErr, hereinafter Error) message to the sender or receiver, respectively.” Vasseur '879, 9:46-10:2.</p> <p>“FIG. 3 is a schematic block diagram of portions of a signaling message 300 (e.g., RSVP message, such as Path, Resv or Error) that may be advantageously used with the present invention. Message 300 contains, inter alia, a common header 310 and one or more signaling protocol specific objects 320, such as an LSP-ATTRIBUTE object 330. The common header 310 may comprise a source address 312 and destination address 314, denoting the origination and requested termination of the message 300. Protocol specific objects 320 contain objects necessary for each type of message 300 (e.g., Path, Resv, Error, etc.). For instance, a Path message may have a sender template object, Tspec object, Previous-hop object, etc. The LSP-ATTRIBUTE object 330, for instance, may be used to signal attributes and/or information regarding an LSP (tunnel). To communicate this information, LSP-ATTRIBUTE object 330 (as well as specific objects 320) may include various type/length/value (TLV) encoding formats and/or flags, as will be understood by those skilled in the art. An example of an LSP-ATTRIBUTE object is further described in Farrel, et al., Encoding of Attributes for Multiprotocol Label Switching (MPLS) Label Switched Path (LSP) Establishment Using RSVP-TE <draft-ietf-mpls-rsvp-te-attributes-05.txt>, Internet Draft, May 2005, which is hereby incorporated by reference as though fully set forth herein. A Resv message, on the other hand, may have specific objects 320 for a label object, session object, filter spec object, etc., in addition to the LSP-ATTRIBUTE object 330. Error messages 300 (e.g., PathErr or ResvErr), may also have specific objects 320, such as for defining the type of error, etc.” Vasseur '879, 10:3-31.</p>

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		<p>“It should be noted that in accordance with RSVP signaling, the state of the TE-LSP is refreshed on a timed interval, e.g., every thirty seconds, in which RSVP Path and Resv messages are exchanged. This timed interval is configurable by a system administrator. Moreover, various methods understood by those skilled in the art may be utilized to protect against route record objects (RROs) contained in signaling messages for a TE-LSP in the event security/privacy is desired. Such RRO filtering prevents a head-end node of the TE-LSP from learning of the nodes along the TE-LSP, i.e., nodes within the provider network.” Vasseur '879, 10:4-42.</p> <p>“Although the illustrative embodiment described herein is directed to MPLS, it should also be noted that the present invention may advantageously apply to Generalized MPLS (GMPLS), which pertains not only to packet and cell-based networks, but also to Time Division Multiplexed (TDM) and optical networks. GMPLS is well known and described in RFC 3945, entitled Generalized Multi-Protocol Label Switching (GMPLS) Architecture, dated October 2004, and RFC 3946, entitled Generalized Multi-Protocol Label Switching (GMPLS) Extensions for Synchronous Optical Network (SONET) and Synchronous Digital Hierarchy (SDH) Control, dated October 2004, the contents of both of which are hereby incorporated by reference in their entirety.” Vasseur '879, 10:43-55.</p> <p>“To obviate delays associated with updating routing tables when attempting to avoid a failed network element (i.e., during convergence), some networks have employed MPLS TE Fast Reroute (FRR). MPLS Fast Reroute is a technique that may be used to quickly divert (“Fast Reroute”) traffic around failed network elements in a TE-LSP. MPLS Fast Reroute is further described, for example, by Fast Reroute Extensions to RSVP-TE for LSP Tunnels, as incorporated by reference above. According to the technique, one or more network elements (e.g. links or nodes) in a network are protected by backup tunnels following an alternate path. If a failure occurs on a protected link or node, TE-LSPs (and consequently the traffic that they carry) are locally diverted onto an appropriate alternate path (e.g., a “backup tunnel”) by the node immediately upstream from the failure. The backup tunnel acts as a Fast Reroute path for the primary TE-LSP and obviates delays associated with other measures, such as tearing down the primary TE-LSP after having gracefully diverted the TE-LSPs affected by the failure, should an alternate path around the failed network element exist. In the event of a</p>

No.	'821 Patent Claim 1	The Reference
		<p>failure of a protected element the head-end node of the backup tunnel (or a “point of local repair,” PLR node) may quickly begin diverting traffic over the backup tunnel with minimal disruption to traffic flow. Those skilled in the art will understand that MPLS Fast Reroute is one example of link or node failure protection, and that other known correction mechanisms may be used in accordance with the present invention. As mentioned above, however, the head-end node of the Fast Rerouted primary tunnel may attempt to reestablish the primary tunnel in response to learning of the protected element failure, particularly in the case where the backup tunnel is a zero-bandwidth (0-BW) tunnel. The attempt to reestablish the primary tunnel has conventionally been a systematic response to Fast Rerouting (diverting) of the primary tunnel, regardless of the path quality of the backup tunnel.” Vasseur '879, 10:56-11:23.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, 11:24-39.</p> <p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as RSVP TE signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or TLV encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object 330 of the RSVP messages 300 to convey the path quality notification.” Vasseur '879, 11:40-49.</p>

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		<p>“FIG. 4 is a schematic block diagram illustrating the format of an extension object (TLV) 400, such as a Fast Reroute Feedback object, that may be advantageously used with the present invention. The extension object (or sub-object) 400 is illustratively embodied as a TLV contained in an LSP-ATTRIBUTE object 330 of an RSVP message 300 and is extended to carry backup (and/or primary) tunnel path quality information. To that end, the “Feedback” object 400 is organized to include a Type field 405 containing a predetermined type value signifying the specific content of the object 400. The Length field 410 is a variable length value. The TLV encoded format may also comprise one or more non-ordered sub-TLVs 450 carried within the TLV “payload” (e.g. Value field 415), each having a Type field 455, Length field 460, and Value field 465. The fields of the TLV 400 and sub-TLV(s) 450 are used in a variety of manners, including as described herein, according to the present invention.” Vasseur '879, 11:59-12:8.</p> <p>“In accordance with one aspect of the present invention, the head-end node (e.g., router A) requests the establishment of the primary tunnel (e.g., T1), such as a TE-LSP. Notably, the head-end node may be a head-end node for multiple primary tunnels, as will be understood by those skilled in the art. Along with the primary tunnel establishment, the head-end node may also request Fast Reroute protection of one or more network elements (e.g., all intermediate network elements) at a PLR node (e.g., router B as shown). Note that each intermediate node along the primary tunnel may act as a PLR node, and that router B is shown merely for simplicity. Illustratively, the Fast Reroute protection may be embodied as one or more zero-bandwidth (0-BW) backup tunnels at the PLR node (e.g., BT1). Those skilled in the art will also understand that the PLR node may protect more than one primary tunnel originating at more than one corresponding head-end node (not shown).” Vasseur '879, 12:9-25.</p> <p>“The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. For example, various connectivity verification protocols, such as, e.g., Bidirectional Forwarding Detection (BFD), IGP “Hello” packets, BGP KEEPALIVE messages, etc., may be used to detect a failure of a network element, as will be understood by those skilled in the art. Furthermore, other lower-layer failure detection mechanisms (e.g. optical or SONET/SDH alarms) may be used to detect a network element failure. Once failure is detected, the PLR node diverts the</p>

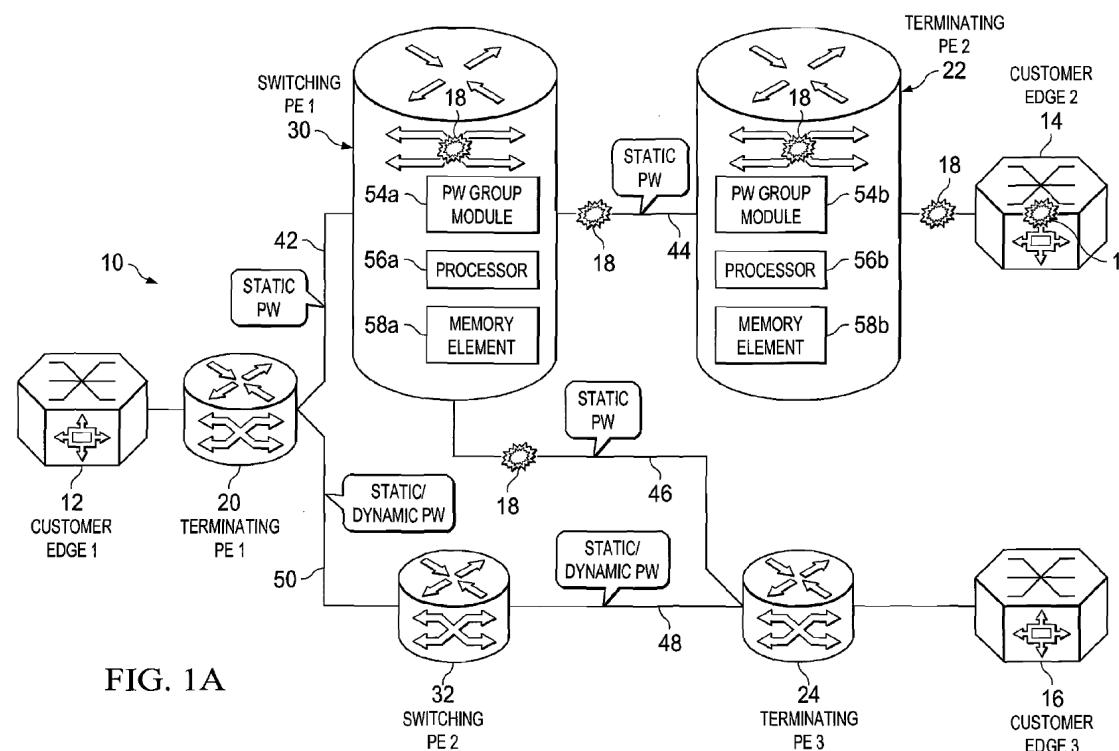
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		<p>traffic onto the backup tunnel, and may send an error message (e.g., an RSVP PathErr 300, such as a conventional “tunnel locally repaired” message) to the head end node indicating the “Fast Rerouting” of the primary tunnel. FIG. 5 is a schematic block diagram of the computer network 100 in FIG. 1 showing Fast Reroute protection of the primary tunnel T1 (e.g., in response to a protected network element, router C, failure, indicated with an overlaid “X”) using a backup tunnel BT1 in accordance with the present invention. Traffic originally received at the PLR node (router B) over the primary tunnel is now diverted over the backup tunnel to a remerge point (router D) of the primary tunnel, as will be understood by those skilled in the art.” Vasseur '879, 12:42-65.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, each PLR node may collect metrics/statistics of the primary and/or backup tunnels. For instance, example metrics may comprise, inter alia, packet drops, path cost, jitter, delay, bandwidth, etc. The PLR node may collect the metrics through traffic monitoring, probes, independent calculations, and/or through cooperation with protected nodes of the primary tunnel (primary nodes) and nodes of the backup tunnel (backup nodes), e.g., transmitting path quality notifications. Once the primary tunnel is Fast Rerouted (i.e., diverted after failure of a protected network element), the PLR node continues to collect metrics of the backup tunnel. (Alternatively, metrics of the backup tunnel may be collected only after Fast Reroute, and not prior to Fast Reroute.)” Vasseur '879, 12:66-13:12.</p> <p>“For example, a path cost increase from the primary tunnel to the backup tunnel may be computed (and transmitted, below) by the PLR node prior to the failure (or during the failure while the primary TE-LSP is diverted onto the backup tunnel) using its own routing tables. The path cost increase may be calculated as a difference between the entire length (head-end node to tail-end node) of the primary and backup tunnels, or just the difference between the protected segment of the primary tunnel and the backup tunnel (PLR node to remerge point). Also, a jitter increase of the primary and backup tunnels, which may be generally described as a difference between inter-arrival of packets, may be monitored using various known techniques, such as, e.g., sending probe packets (probes) from the PLR node to the remerge point. For instance, probes may determine that packets arrive at the remerge point along the primary tunnel from the PLR node consistently, e.g., every 10 milliseconds (ms) (e.g., an</p>

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		<p>average value). After Fast Reroute, however, probes may determine that packets do not arrive at the remerge point along the backup tunnel from the PLR node consistently, e.g., one may arrive in 10 ms, another in 100 ms, another in 50 ms, etc. The non-constant rate of received packets (jitter) may be undesirable, e.g., in particular for voice over IP (VoIP) traffic, as will be understood by those skilled in the art.” Vasseur '879, 13:13-36.</p> <p>“As a further example, packet dropping may be monitored for the primary and backup tunnels prior to and after Fast Reroute (respectively). For instance, based on the tunnel label of the dropped packet, primary nodes and/or backup nodes may be able to distinguish which tunnel corresponds to the dropped packets. Each of the primary and/or backup nodes collect packet drop data, and periodically inform the PLR node of the number of dropped packets (e.g., through a corresponding Feedback object 400). In the case of a backup node, the PLR node receiving the notification may interpret the association of the backup tunnel label and the primary tunnel label to reference an appropriate primary tunnel. Those skilled in the art will understand that the above path quality metrics are merely examples, and that any other metrics/statistics useful for determining path quality of the backup tunnel may be used in accordance with the present invention (e.g., delay, bandwidth, etc.). Further, the path quality information may be measured and compared in a variety of manners, such as, e.g., as a difference between primary and backup tunnels before and after Fast Reroute, or simply the difference between the backup tunnel before and after Fast Reroute, etc.” Vasseur '879, 13:37-58.</p> <p>“Also after the primary tunnel is Fast Rerouted, the PLR node may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. For instance, the novel Feedback object 400 may include one or more sub-TLVs 450 corresponding to metrics/statistics, as described above. Notably, the path quality information pertaining to a particular metric/statistic may be transmitted as total values for interpretation by the head-end node (e.g., to determine the difference), or as PLR-node-computed differences (e.g., between the primary and backup tunnels, or before and after Fast Reroute). For instance, if the delay of the primary tunnel (along the protected segment) prior to Fast Reroute were 2 ms, and after Fast Reroute the delay of the backup tunnel were 5 ms, the notification may be configured to</p>

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		<p>include both values 2 ms and 5 ms, or instead simply the difference, i.e., an increase of 3 ms.” Vasseur '879, 13:59-14:8.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message 300 (PathErr), the head-end node may wait for at least one path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. Also, any number of metrics may be used in the determination, e.g., as configured by a system administrator. For example, using the metrics described above, a head-end node may be configured to reestablish the primary tunnel in response to i) a certain number of packet drops, ii) a percent increase in packet drops, iii) a number of packet drops and a percent increase in path cost, iv) a percent increase in path cost and a percent increase in jitter, etc. Those skilled in the art will understand that these are merely examples of possible path quality comparisons and reestablishment determinations, and that any comparisons to any metrics at any configurable changes may be used in accordance with the present invention.” Vasseur '879, 14:41-59.</p> <p>“If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. FIG. 6 is a schematic block diagram of the computer network 100 in FIG. 5 showing an unacceptable backup tunnel path quality (dotted line and arrow) and resultant reestablishment of the primary tunnel in accordance with the present invention. Those skilled in the art will understand that the reestablished primary tunnel may traverse one or more primary nodes (not shown), and that it may be computed specifically to avoid the failed network element and any network elements of the unacceptable backup tunnel. Those skilled in the art will also understand that the attempt to reestablish the primary tunnel may not be able to find an acceptable path, in which case the head-end node may continue to use the unacceptable backup tunnel or other unacceptable rerouted path.” Vasseur '879, 14:60-15:9.</p>

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		<p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention. The procedure 700 starts at step 705, and continues to step 710, where a head-end node (e.g., router A) establishes a primary tunnel (e.g., T1) to a destination tail-end node (e.g., router E). During or after establishment, the head-end node requests protection of the primary tunnel at step 715, and in response, PLR nodes along the primary tunnel (e.g., router B) create backup tunnels (e.g., BT1) to protect the primary tunnel in step 720. (Those skilled in the art will understand that backup tunnels around the protected network elements may already exist at the PLR node, and that “creating backup tunnels” in step 720 implies an association with pre-existing backup tunnels.) As mentioned above, these backup tunnels may illustratively be embodied as 0-BW backup tunnels. In accordance with the present invention, the head-end node may additionally request backup tunnel path quality notification from the PLR nodes in step 725, such as, e.g., through the use of empty corresponding Feedback objects in RSVP (Path) messages 300, as described above.” Vasseur '879, 15:37-58.</p> <p>“The procedure 700 continues to FIG. 7B (step “A”), where in step 750 the PLR node detecting the failure diverts (“Fast Reroutes”) the primary tunnel traffic to the backup tunnel and sends an error message (e.g., an RSVP (Error) message 300) to the head end node, e.g., a “tunnel locally repaired” message. The detecting PLR node continues to collect path quality information for the backup tunnel in step 755 and at step 760 determines whether to send the path quality notification to the head-end node. For example, as mentioned above, the PLR node may be configured to continually send notifications, or periodically, or in response to a configurable change in path quality, etc. Also as mentioned above, the PLR node may be configured to send either the actual path quality information or the change (difference) in path quality. (As further mentioned above, metrics of the backup tunnel may alternatively be collected only after Fast Reroute, and not prior to Fast Reroute.).” Vasseur '879, 16:4-20.</p> <p>“If the PLR node decides to send the notification in step 760, then the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification in step 765, e.g., based on one or more configurable thresholds, percentages, etc., as described above. If the backup tunnel is currently maintaining an acceptable quality for the</p>

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		<p>traffic flow in step 770, the head-end node may continue to utilize the backup tunnel, and the PLR node continues to collect path quality information in step 755 to detect any change in quality. Otherwise, if the backup tunnel quality is not acceptable in step 770, the head-end node may attempt to reestablish the primary tunnel in step 775. Notably, as mentioned above, if the PLR node has not sent any notification (step 760) within a configurable period of time in step 780, e.g., due to a backup tunnel failure, over-congestion, etc., then the head-end node may also attempt to reestablish the primary tunnel in step 775 accordingly. Moreover, as described above, in the event more than one primary tunnel is Fast Rerouted for the head-end node, various techniques to reestablish one or more of the primary tunnels may be used (e.g., as many tunnels as necessary, a configurable subset of tunnels, all tunnels, the congested tunnels, etc.). The procedure 700 ends in step 785.” Vasseur '879, 16:21-43.</p> <p>“While there has been shown and described an illustrative embodiment that dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network, it is to be understood that various other adaptations and modifications may be made within the spirit and scope of the present invention. For example, the invention has been shown and described herein using “Fast Reroute,” e.g., MPLS TE Fast Reroute (FRR). However, the invention in its broader sense is not so limited, and may, in fact, be used with other network element protection and failure correction mechanisms as will be understood by those skilled in the art. Moreover, while the above description describes performing the technique at the head-end node and PLR node, the invention may also be advantageously used with PCEs. In addition, while one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 16:63-17:13.</p> <p>Rustogi discloses:</p> <p>“An example method includes identifying a fault condition in a network, and evaluating pseudowires affected by the fault condition in order to make a determination as to whether an aggregate failure occurred in the network for a group of pseudowires. The method also includes communicating a group message indicating that the group of pseudowires is associated with the fault condition. The group message includes a group identification (ID),</p>

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		<p>which identifies the group of pseudowires, and the group message includes a pseudowire group label identifying an in-band aggregate channel. More specifically, the pseudowire group label can be applicable to static pseudowires. In more detailed embodiments, the group ID identifies the group of pseudowires that are associated with an attachment circuit, a label switched path, or a port. Internal mappings can be maintained such that a plurality of pseudowires is mapped to individual interfaces of network elements in the network.” Rustogi, Abstract.</p>  <p>FIG. 1A</p> <p>Rustogi, FIG. 1A.</p>

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		<pre> graph TD START([START]) --> 100[100 ~ A GIVEN NETWORK ELEMENT CAN IDENTIFY A FAULT CONDITION IT RECEIVES] 100 --> 102[102 ~ THE NETWORK ELEMENT EVALUATES PSEUDOWIRES IN ORDER TO DETERMINE WHETHER A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED] 102 --> 104[104 ~ IF ONLY A FEW PSEUDOWIRES ARE AFFECTED BY THE FAULT CONDITION, THE GROUPING PROTOCOL MAY OPTIONALY NOT BE USED, WHERE A MORE ROUTINE MESSAGING PROTOCOL COULD BE EMPLOYED] 104 --> 106[106 ~ IF A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED, THE GROUPING PROTOCOL IS EMPLOYED TO MINIMIZE THE MESSAGES THAT ARE SENT, RECEIVED, AND PROCESSED IN THE NETWORK] 106 --> 108[108 ~ IN THE CASE OF AN AGGREGATE FAILURE, AN AGGREGATE CHANNEL CAN BE USED TO OFFER APPROPRIATE GROUP MESSAGING. THE INDIVIDUAL MESSAGES THAT CONVEY GROUP IDENTIFICATIONS (IDS) CAN QUICKLY SIGNIFY THE FAULT CONDITION TO NETWORK PEERS] 108 --> END([END]) </pre> <p>FIG. 1B</p> <p>Rustogi, FIG. 1B.</p>

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		<p>FIG. 2</p> <p>Rustogi, FIG. 2.</p>

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		<p>FIG. 3</p> <p>Rustogi, FIG. 3.</p>

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		<p>The diagram illustrates a network topology involving three PE (Provider Edge) nodes and two switching nodes:</p> <ul style="list-style-type: none"> TERMINATING PE 1 (represented by circle 62) has two interfaces: INTERFACE A and INTERFACE B. SWITCHING PE 1 (represented by circle 68) has two interfaces: INTERFACE E and INTERFACE F. It receives traffic from PE1 via interface B and sends traffic to PE2 via interface E. SWITCHING PE 2 (represented by circle 70) has two interfaces: INTERFACE G and INTERFACE H. It receives traffic from PE1 via interface A and sends traffic to PE3 via interface G. TERMINATING PE 2 (represented by circle 64) has two interfaces: INTERFACE C and INTERFACE D. It receives traffic from PE2 via interface H and sends traffic to PE3 via interface C. TERMINATING PE 3 (represented by circle 66) has two interfaces: INTERFACE I and INTERFACE J. It receives traffic from PE2 via interface G and sends traffic to PE1 via interface I. <p>Annotations indicate various group identifications and pseudowire labels:</p> <ul style="list-style-type: none"> PSEUDOWIRE GROUP LABEL F: Associated with the connection between PE1 and PE2. PSEUDOWIRE GROUP LABEL E: Associated with the connection between PE1 and PE2. GROUP ID E → F: Label for the connection between PE1 and PE2. GROUP IDENTIFICATION E: Label for the connection between PE1 and PE2. GROUP IDENTIFICATION F: Label for the connection between PE1 and PE2. GROUP IDENTIFICATION I: Label for the connection between PE2 and PE3. GROUP IDENTIFICATION J: Label for the connection between PE2 and PE3. GROUP IDENTIFICATION G: Label for the connection between PE2 and PE3. GROUP IDENTIFICATION H: Label for the connection between PE2 and PE3. INTERFACE C FAILS: An annotation indicating a failure condition for interface C. INTERFACE C: The interface between PE2 and PE3. INTERFACE D: The interface between PE2 and PE3. INTERFACE A: The interface between PE1 and PE2. INTERFACE B: The interface between PE1 and PE2. INTERFACE I: The interface between PE2 and PE3. INTERFACE J: The interface between PE2 and PE3. INTERFACE G: The interface between PE2 and PE3. INTERFACE H: The interface between PE2 and PE3. <p style="text-align: center;">FIG. 4</p> <p style="text-align: right;">76</p>

Rustogi, FIG. 4.

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		<p>FIG. 5</p> <p>80</p> <p>Rustogi, FIG. 5.</p> <p>The diagram illustrates a network topology with three PE nodes (PE1, PE2, PE3) connected via switching nodes (68, 70) and terminating nodes (62, 64). The connections are labeled with pseudowire group labels (e.g., G, H, I, J, L, M) and group identifications (e.g., GROUP ID I → G, GROUP ID J → H, etc.). The connections are color-coded by group identification. The terminating nodes (62, 64, 66) are connected to their respective PE nodes via interfaces A, C, and D. Interface D fails at node PE3.</p>

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		<p style="text-align: center;">74</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>AC PORT</th><th>LSP</th><th>PW GROUP</th><th>PW GROUP LABEL IDENTIFICATION</th></tr> </thead> <tbody> <tr> <td>INTERFACE A</td><td>LSP 1</td><td>A</td><td>A</td></tr> <tr> <td>INTERFACE B</td><td>LSP 1</td><td>B</td><td>B</td></tr> <tr> <td>REMOTE INTERFACE C ON TERMINATING PE 2</td><td>LSP 2</td><td>F</td><td>F</td></tr> <tr> <td>REMOTE INTERFACE D ON TERMINATING PE 3</td><td>LSP 2</td><td>G, H</td><td>G, H</td></tr> </tbody> </table> <p style="text-align: center;">FIG. 6</p> <p>Rustogi, FIG. 6.</p> <p>“The field of communications has become increasingly important in today's society. In particular, the ability to quickly and to effectively provision connections presents a significant challenge to component manufacturers, system designers, and network operators. Multiprotocol Label Switching (MPLS) is a mechanism in telecommunications networks that carries data from one network node to the next. Layer 2 services (such as Frame Relay, Asynchronous Transfer Mode, and Ethernet) can be emulated over an MPLS backbone by encapsulating the Layer 2 Protocol Data Units (PDUs) and transmitting them over pseudowires. Protocols exist for establishing and maintaining the pseudowires. Certain issues have arisen in pseudowire scenarios, where faults are detected in the network.” Rustogi, ¶ [0002].</p> <p>“FIG. 1A is a simplified block diagram of a communication system for providing pseudowire group labels in a network environment in accordance with one embodiment of the present disclosure.” Rustogi, ¶ [0004].</p>	AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION	INTERFACE A	LSP 1	A	A	INTERFACE B	LSP 1	B	B	REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F	REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H
AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION																			
INTERFACE A	LSP 1	A	A																			
INTERFACE B	LSP 1	B	B																			
REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F																			
REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H																			

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		<p>“FIG. 1B is a simplified flowchart depicting one possible, generic operational flow associated with the communication system.” Rustogi, ¶ [0005].</p> <p>“FIG. 2 is a simplified block diagram of an example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0006].</p> <p>“FIG. 3 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0007].</p> <p>“FIG. 4 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0008].</p> <p>“FIG. 5 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0009].</p> <p>“FIG. 6 is a simplified table of an example set of pseudowire group provisioning parameters in accordance with one embodiment.” Rustogi, ¶ [0010].</p> <p>“FIG. 1A is a simplified block diagram of a communication system 10 for providing pseudowire group labels in accordance with one example of the present disclosure. FIG. 1A includes a customer edge 1 (CE1) 12, a CE2 14, and a CE3 16, where a number of faults 18 are shown as propagating in the network. Typically, when an error or a failure occurs in the network (e.g., an interface failure, a pulled cable, a switch failure, hardware/software failures generally, etc.), messages are sent to various network devices in order to inform them of these fault conditions. Faults 18 of FIG. 1A are indicative of such messages, where the underlying fault condition (being signaled by the messages) can occur virtually anywhere in a network (e.g., in a customer edge, in provider equipment, etc.). FIG. 1A also includes a terminating provider equipment 1 (TPE1) 20, a TPE2 22, a TPE3 24, a switching provider edge 1 (SPE1) 30, and a SPE2 32. In one particular example implementation, the TPEs and SPEs of FIG. 1A are switches that are configured to exchange data in a network environment.” Rustogi, ¶ [0012].</p>

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		<p>“SPE1 30 may include a pseudowire (PW) group module 54 a, a processor 56 a, and a memory element 58 a. In a similar fashion, TPE2 22 may include a pseudowire group module 54 b, a processor 56 b, and a memory element 58 b. FIG. 1A also includes a number of static pseudowires 42, 44, and 46. A set of static/dynamic pseudowires 48, 50 is also provided. Note that the group labeling protocol discussed herein can be executed between individual SPEs, TPEs, or between any combinations of these elements.” Rustogi, ¶ [0013].</p> <p>“In one particular arrangement, communication system 10 is provided in conjunction with a Layer-2 virtual private networks (L2VPN)/operation, administration, and maintenance (OAM) L2VPN/OAM framework. The OAM framework is intended to provide OAM layering across L2VPN services, pseudowires, and packet switched network (PSN) tunnels. Communication system 10 may include any suitable networking protocol or arrangement that provides a communicative platform for communication system 10. Thus, communication system 10 may include a configuration capable of transmission control protocol/internet protocol (TCP/IP) communications for the transmission and/or reception of packets in a network. Communication system 10 may also operate in conjunction with a user datagram protocol/IP (UDP/IP) or any other suitable protocol where appropriate and based on particular needs.” Rustogi, ¶ [0014].</p> <p>“Failure detection and failure notification for static pseudowires is inadequate, where sluggish signaling can result in poor scalability for failover performance. Typically, static pseudowires are manually configured at respective endpoints, where control channels are absent for providing group level signaling messages. Aggregate channels are significant tools for providing suitable scalability in the network, but no such aggregate channel exists for static pseudowires. For dynamic pseudowires, such an aggregate channel may be present in the form of a label distribution protocol (LDP) directed session. However, no such protocol exists for static pseudowire configurations such that an in-band aggregate channel would be available for static pseudowires.” Rustogi, ¶ [0016].</p>

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		<p>“Communication system 10 can address the aforementioned issues (and others) by offering a pseudowire group label that can represent an aggregate channel for groups of static pseudowires. The aggregate channel of communication system 10 can allow for improved scalability of failover performance. In accordance with one potential configuration of communication system 10, a pseudowire group label is representative of a group of static pseudowires transported over a label switched path (LSP). The pseudowire group label can identify the aggregate channel, which captures the hierarchy relevant to OAM mechanisms. Additionally, the groups represented by the group identification (ID) can be mutually exclusive, where a pseudowire is part of only one group. In other embodiments, a pseudowire can be part of multiple groups, or be configured in any other suitable manner based on particular network arrangements.” Rustogi, ¶ [0017].</p> <p>“During operations, and with brief reference to FIG. 1B, a given network element can identify a fault condition it receives (at step 100) and, subsequently, evaluate pseudowires in order to determine whether a sufficient number of pseudowires have been affected. This is reflected by step 102. If only a few pseudowires are affected by the fault condition, the grouping protocol outlined herein may have only nominal value, where there could be an option to simply communicate the fault condition in a more routine manner, as outlined in step 104. However, if a sufficient number of pseudowires have been affected, the grouping protocol outlined herein can be employed to minimize the messages that are sent, received, and processed in the network. This is reflected as step 106. Note that the determination (as to whether a sufficient number of pseudowires have been impacted by the fault condition) can involve accessing internal tables such that a quick mapping can occur to determine if an aggregate failure has occurred. As used herein, the term ‘aggregate failure’ simply connotes that a sufficient number of pseudowires have experienced the fault condition such that an aggregate channel can be employed to offer appropriate group messaging. For the aggregate failure condition, the individual messages that convey Group identifications (IDs) can quickly signify the fault condition to network peers, as shown in step 108.” Rustogi, ¶ [0018].</p>

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		<p>“In specific regards to OAM mechanisms, OAM messages typically result from common failures in the network. These fault conditions can be aggregated such that they are signaled as a single message, which could represent a group of failed pseudowires (as opposed to sending individual messages for each failed pseudowire). Hence, a single message could be sent to represent all the relevant OAM messages propagating in communication system 10. The group label that propagates in communication system 10 provides an architecture with a significant level of aggregation for failed pseudowires (i.e., pseudowires being affected by a given fault condition), particularly for OAM messaging. Moreover, the in-band aggregate channel of communication system 10 is based (at least in part) on the evolving trends of OAM mechanisms, which are required to be fast, responsive, and capable of being implemented in hardware or software. Additionally, in-band OAM protocols are a better measure of the path availability.” Rustogi, ¶ [0020].</p> <p>“In operation of one example implementation, a group label can represent the tuple <attachment circuit (AC) port level grouping, LSP>. This could signify that all pseudowires on an AC port (sought for aggregation) traverse a given LSP. Multiple pseudowire groups can exist within an LSP. Similarly, pseudowires on the same AC port (that traverse a different LSP) can use a different pseudowire group label. Alternatively, an administrator may seek to employ a one-to-one mapping between an LSP and a group label. If that were the case, then only one pseudowire group would exist within an LSP. In scenarios where there is no LSP label in the packet (e.g., due to penultimate hop popping), the pseudowire group label can provide the hierarchy that is appropriate.” Rustogi, ¶ [0021].</p> <p>“In one particular example, the group level pseudowire OAM message can be sent with the following label stack: Explicit/Implicit LSP Label+pseudowire group Label+GAL+ACH+pseudowire OAM with grouping TLV (where GAL=Generic Associated Channel Label, ACH=Associated Channel Header, TLV=Type-Length-Value). If there are multiple LSPs, then one group label can be provisioned for each LSP (for each pseudowire group), where per group messages can be sent on each LSP. The group label does not necessarily have a one-to-one mapping to the grouping of pseudowires implied by the Group ID in the grouping TLV. Note also that the group-based aggregate channel is applicable to</p>

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		<p>static pseudowires, as well as for dynamic pseudowires in certain applications.” Rustogi, ¶ [0022].</p> <p>“As discussed herein, the aggregate channel of communication system 10 can be configured in various ways. For example, and with regards to a first option, a separate label may simply be used to identify a pseudowire group within an LSP. The association of an OAM message and a pseudowire group is straightforward. There could potentially be multiple pseudowire group labels per LSP. As a second option, one group label can be used to identify a common pseudowire group channel on the LSP. In this implementation, one pseudowire group label is provided per LSP. The OAM message association to a pseudowire group is not as simple as the first option. As a third option, one pseudowire is simply designated to convey grouping information (e.g., without using a group label). In this case, there is no need for a pseudowire group label. Again, the OAM message association to a pseudowire group is not as simple as the first option.” Rustogi, ¶ [0023].</p> <p>“Any combination of formatting (for the Group ID and the pseudowire group label) can be used in the group message to be communicated in the network. In one example, only one of these elements is communicated when an aggregate fault condition is detected, or these elements can be combined into a single unique identifier. In the most generic example, a group message would at least include the Group ID (identifying the pseudowires affected by the fault) and a pseudowire group label (identifying an aggregate channel for communicating the group message). In this generic sense, a pipe (the Group ID) within a pipe (the pseudowire group label) is being identified, where the group message is identifying both elements during an aggregate fault condition. Operational details of communication system 10 are described below with reference to FIGS. 2-6. Note that before turning to additional example flows and example embodiments of the present disclosure, a brief overview of the infrastructure of communication system 10 is provided.” Rustogi, ¶ [0024].</p> <p>“CE1 12, CE2 14, and CE3 16 represent devices, infrastructure, equipment, clients, or customers seeking to initiate a data session in communication system 10. These elements may comprise a digital subscriber line access multiplexer (DSLAM), a router, a personal computer, a server, a switch, and/or other devices associated with data propagation. Further,</p>

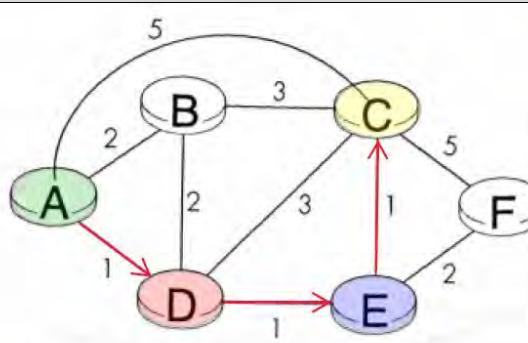
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		<p>these elements may sit behind, or in front of, one or more of these identified devices. The term ‘CE’ may be inclusive of the devices identified above (e.g., a DSLAM, a switch, etc.), as well as devices used to initiate a communication, such as a console, a proprietary endpoint, a telephone, a cellular telephone, a bridge, a computer, a personal digital assistant (PDA), a laptop or an electronic notebook, or any other device, component, element, or object capable of initiating voice, audio, media, or data exchanges within communication system 10. The customer element may also include any device that seeks to initiate a communication on behalf of another entity or element, such as a program, a database, or any other component, device, element, or object capable of initiating a voice, a video, text, or a data exchange within communication system 10. Data, as used herein in this document, refers to any type of video, numeric, voice, media, or script data, or any type of source or object code, or any other suitable information in any appropriate format that may be communicated from one point to another.” Rustogi, ¶ [0025].</p> <p>“SPE1 30, SPE2 32, TPE1 20, TPE2 22, and TPE3 24 are network elements that facilitate communications in two directions in a network environment. In one particular example, each of these network elements is a switch configured to exchange data over static and/or dynamic pseudowire links. Further, the traffic exchanged between these components may be directed over an MPLS transport in certain embodiments. As used herein in this Specification, the term ‘network element’ is meant to encompass switches, routers, bridges, gateways, servers, processors, loadbalancers, firewalls, or any other suitable device, component, element, or object operable to exchange or process information in a network environment. Moreover, these network elements may include any suitable hardware, software, components, modules, interfaces, or objects that facilitate the operations thereof. This may be inclusive of appropriate algorithms and communication protocols that allow for the effective exchange of data or information. Along similar design alternatives, any of the internal modules and components of these network elements may be combined in various possible configurations.” Rustogi, ¶ [0029].</p>

No.	'821 Patent Claim 1	The Reference
		<p>“Turning to FIG. 2, FIG. 2 is a simplified block diagram of an example system 60 for providing an example use case using per-label switched path (LSP) pseudowire group labels. FIG. 2 includes a TPE1 62, a TPE2 64, a TPE3 66, a SPE1 68, and a SPE2 70. Each pseudowire group is identified, where a group identification (ID) for Group A and Group B is depicted at TPE1 62. Similarly, Groups C, D, and E have Group IDs at SPE1 68. TPE2 64 and TPE3 66 can couple to interfaces C and D, respectively.” Rustogi, ¶ [0031].</p> <p>“In this particular example, interfaces A and B have failed. Note that there is a multitude of attachment circuits (e.g., 1000 attachment circuits) that are being transported over these interfaces A and B, where the attachment circuits are being tunneled into a corresponding number of pseudowires. For example, there could be 500 attachment circuits on interface A (implicating 500 pseudowires) and 500 attachment circuits on interface B, where the fault condition for the pseudowires should be signaled. In other flawed systems, an architecture would individually signal this fault condition for each pseudowire (e.g., via signaling between TPE1 62 and SPE1 68). Instead of sending 500 messages, a single message can be sent, where a single label (and Group ID) can be used to identify the pseudowires. In this case, the Group ID A is used to signal the fault condition for 300 pseudowires and for 200 pseudowires (i.e., the top two links connecting TPE1 62 and SPE1 68) using a single message (that includes Group Label A and Group ID A). Thus, the status for Group A is quickly communicated to SPE1 68. Similarly, Group ID B can be used to signal the status of the other 500 pseudowires to appropriately convey the status for Group B. More specifically, the message can include Group Label A and Group ID B. Note that all 1000 pseudowires have effectively been accounted for using these Group IDs A and B.” Rustogi, ¶ [0032].</p> <p>“FIG. 3 is a simplified block diagram of an example system 72 for providing another use case for pseudowire group labels. Note that the grouping mechanism outlined herein is not limited to pseudowires that propagate over LSPs. Certain pseudowires can propagate over an LSP and represent one group, where two ports can be provisioned for two different groups (e.g., Group A and Group B). Hence, FIG. 3 is depicting a use case using pseudowire group labels for <port, LSP>mapping. In a general sense, such a configuration is showing how pseudowire mechanics can be used to offer different group signaling, which may be based on various possible implementations. Thus, there is a group level construct corresponding to the group</p>

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		<p>labels that are created such that any OAM protocol can send the appropriate aggregate messages. In this particular example, the signaling for Group ID A, B, C, and D is similar to that of FIG. 2; however, the grouping mechanism has simply changed.” Rustogi, ¶ [0035].</p> <p>“FIG. 4 is a simplified block diagram of an example system 76 for providing another use case for pseudowire group labels. In this particular example, interface C fails (as shown at TPE2 64). Note that the same logical flow occurs in FIG. 4 in terms of the group signaling, as previously discussed. The group labels in two directions do not have to be the same, where the groupings for the messaging are not necessarily symmetrical. In this particular example, TPE2 64 sends a status for Group E with the corresponding group label (i.e., Group ID E for 300 pseudowires), where that message will have a Group Label E and a Group ID E. Hence, this particular signaling is indicative of 300 pseudowires failing in the network. SPE1 68 can send the status for Group F (where the Group ID F is associated with 300 pseudowires) to TPE1 62, where that message includes a Group Label F and a Group ID F.” Rustogi, ¶ [0036].</p> <p>“FIG. 5 is a simplified block diagram of an example system 80 for providing another use case for pseudowire group labels. In this particular example, interface D fails (as shown at TPE3 66), where all 700 pseudowires fail. In one implementation, TPE3 66 does not have a 700 pseudowire Group ID. Instead, the Group IDs can correspond to 200 and 500 pseudowires, when summed together account for the 700 pseudowires. In this particular example, TPE3 66 sends one message for Group I (representing 200 pseudowires) and another message for Group J (representing 500 pseudowires) to SPE1 68. In response, SPE1 68 sends a message for Group G (representing 200 pseudowires) and another message for Group H (representing 500 pseudowires). Again, the signaling being exchanged between these elements is minimal due to the effective grouping of pseudowires. SPE1 68 also sends a single message for Group I (associated with 200 pseudowires) and Group J (associated with 500 pseudowires) to TPE3 66, which is coupled to interface D. Group ID G is associated with 200 pseudowires, whereas Group ID H is representative of 500 pseudowires.” Rustogi, ¶ [0037].</p>

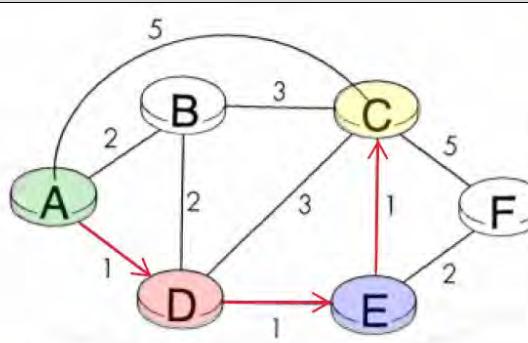
No.	'821 Patent Claim 1	The Reference
		<p>“FIG. 6 is a simplified table 74 illustrating an example set of pseudowire group provisioning parameters for TPE1 62, where these particular provisioning parameters could be relevant to the configuration of FIG. 3. At least in one generic sense, FIG. 2 can reflect one approach for mapping a PW group label to a PW Group ID, while FIGS. 3-5 can reflect a second approach for such mappings, where table 74 is associated with that second approach.” Rustogi, ¶ [0038].</p> <p>“In particular, table 74 illustrates the mapping between SPE1 68 and TPE1 62. The first column represents the attachment circuit port (e.g., interface A, interface B, remote interface C on TPE2 64, and remote interface D on TPE3 66). Additionally, table 74 depicts a number of LSPs, a set of pseudowire grouping labels, and a set of pseudowire Group IDs. Note that the Group IDs are provided inside the pseudowire group labels in this example such that these two columns match in table 74. Additionally, note that table 74 is merely representing some of the possible characteristics in a single direction, where different constructs could be used in the reverse direction. Note that the provisioning as discussed herein can significantly reduce messaging such that these presented concepts offer increased scalability. This is due in part to the nominal processing that occurs in the network, in contrast to the processing required to evaluate a prolific amount of signaling messages associated with particular pseudowires. Additionally, the paradigm discussed herein can afford service providers an adequate amount of downtime after a failure has occurred in the network.” Rustogi, ¶ [0039].</p>
1[b]	determining an overall cost for each entity pair of said plurality of entities:	<p>The Reference discloses determining an overall cost for each entity pair of said plurality of entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p>

No.	'821 Patent Claim 1	The Reference
		<p>Kurose discloses:</p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p> <p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current purposes, we’ll simply take the link costs as a given and won’t worry about how they are determined.” Kurose at 280.</p>

No.	'821 Patent Claim 1	The Reference
		 <p>Figure 4.4 Abstract model of a network</p> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 1	The Reference
		<p>"A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network." Kurose at 281.</p> <p>"In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner." Kurose at 282.</p>
1[c]	selecting an entity pair from said plurality of transport entities based at least in part upon said overall cost; and	<p>The Reference discloses selecting an entity pair from said plurality of transport entities based at least in part upon said overall cost.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[b].</i></p>
1[d]	if an entity pair reselection event occurs, reselecting said entity pair from the group consisting of said entity pair and a replacement entity pair comprising at least one entity distinct from the entities of said entity pair,	<p>The Reference discloses if an entity pair reselection event occurs, reselecting said entity pair from the group consisting of said entity pair and a replacement entity pair comprising at least one entity distinct from the entities of said entity pair.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 1	The Reference
1[e]	<p>wherein said entity pair reselection event is selected from a group consisting of adding an entity to said plurality of transport entities, removing an entity from said plurality of transport entities, an operational status change for one of said plurality of transport entities, and a change in overall cost for one of said plurality of transport entities.</p>	<p>The Reference discloses wherein said entity pair reselection event is selected from a group consisting of adding an entity to said plurality of transport entities, removing an entity from said plurality of transport entities, an operational status change for one of said plurality of transport entities, and a change in overall cost for one of said plurality of transport entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p> <p><u>Kurose discloses:</u></p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p> <p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current purposes, we’ll simply take the link costs as a given and won’t worry about how they are determined.” Kurose at 280.</p>

No.	'821 Patent Claim 1	The Reference
		 <p>Figure 4.4 Abstract model of a network</p> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 1	The Reference
		<p>"A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network." Kurose at 281.</p> <p>"In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner." Kurose at 282.</p>

No.	'821 Patent Claim 2	The Reference
2[preamble]	The method of claim 1, wherein said step of selecting an entity pair further comprises:	<p>The Reference discloses the method of claim 1, wherein said step of selecting an entity pair further comprises:</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[c].</i></p>
2[a]	selecting a working entity from said entity pair, and	<p>The Reference discloses selecting a working entity from said entity pair.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>
2[b]	selecting a protection entity from said entity pair.	<p>The Reference discloses selecting a protection entity from said entity pair.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

No.	'821 Patent Claim 2	The Reference
		skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.

No.	'821 Patent Claim 3	The Reference
3	The method of claim 2, further comprising the step of selecting an active entity from the set consisting of said working entity and said protection entity.	<p>The Reference discloses the method of claim 2, further comprising the step of selecting an active entity from the set consisting of said working entity and said protection entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 4	The Reference
4	The method of claim 2, wherein selecting an entity pair further comprises minimizing an overall cost function.	<p>The Reference discloses the method of claim 2, wherein selecting an entity pair further comprises minimizing an overall cost function.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p> <p>Kurose discloses:</p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p>

No.	'821 Patent Claim 4	The Reference
		<p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current purposes, we’ll simply take the link costs as a given and won’t worry about how they are determined.” Kurose at 280.</p>

No.	'821 Patent Claim 4	The Reference
		<p>Figure 4.4 + Abstract model of a network</p> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 4	The Reference
		<p>“A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network.” Kurose at 281.</p> <p>“In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner.” Kurose at 282.</p>

No.	'821 Patent Claim 5	The Reference
5	The method of claim 4, wherein said overall cost function comprises substantially minimizing a probability of concurrent failure of said protection entity and said working entity.	<p>The Reference discloses the method of claim 4, wherein said overall cost function comprises substantially minimizing a probability of concurrent failure of said protection entity and said working entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 6	The Reference
6	The method of claim 4, wherein said overall cost function comprises a predefined entity cost metric.	<p>The Reference discloses the method of claim 4, wherein said overall cost function comprises a predefined entity cost metric.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p>

No.	'821 Patent Claim 6	The Reference
		<p><u>Kurose discloses:</u></p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p> <p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current purposes, we’ll simply take the link costs as a given and won’t worry about how they are determined.” Kurose at 280.</p>

No.	'821 Patent Claim 6	The Reference
		<p>Figure 4.4 + Abstract model of a network</p> <pre> graph LR A((A)) --- 1 D((D)) A --- 2 B((B)) A --- 2 C((C)) B --- 3 C B --- 2 D C --- 5 F((F)) C --- 3 E((E)) D --- 1 E D --- 2 F E --- 2 F </pre> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 6	The Reference
		<p>“A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network.” Kurose at 281.</p> <p>“In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner.” Kurose at 282.</p>

No.	'821 Patent Claim 7	The Reference
7	<p>The method of claim 6, wherein said predefined entity cost metric is selected from the group consisting of interior gateway protocol (IGP) and traffic engineering (TE).</p>	<p>The Reference discloses the method of claim 6, wherein said predefined entity cost metric is selected from the group consisting of interior gateway protocol (IGP) and traffic engineering (TE).</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Cisco created and developed the MPLS and MPLS-TE standards and patented technology based on those standards <i>before</i> Orckit utilized such technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Filsfils • Taylor • Vasseur '879 • Rustogi <p><u>Filsfils discloses:</u></p> <p>“In one embodiment, forwarding information bases (FIBs) are selectively populated in a packet switch. A packet switching device determines, based on one or more protocol signaling messages, a subset, which is less than all, on which FIBs a lookup operation may be performed for identifying forwarding information for a received particular packet. The packet switching device populates each of these FIBs, but not all of the FIBs of the packet switching device,</p>

No.	'821 Patent Claim 7	The Reference
		<p>with forwarding information corresponding to the particular forwarding value. Thus, FIB resources are consumed for only those FIBs which could actually be used, and not all of the FIBs, for forwarding packets in the data plane of the packet switching device, whether these packets are received on a primary or backup path.” Filsfils, Abstract.</p> <p>FIGURE 1</p> <p>Filsfils, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<p>APPARATUS (E.G., PACKET SWITCHING DEVICE APPLIANCE, CORE ROUTER, LABEL SWITCH ROUTER) 200</p> <p>ROUTE PROCESSOR CONFIGURED TO CORRELATE PRIMARY AND BACKUP PATHS AND POPULATE FIBS ACCORDINGLY</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>COMMUNICATION MECHANISM(S) (E.G., BUS, SWITCHING FABRIC, MATRIX)</p> <p>PRIMARY AND BACKUP PATHS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>201</p> <p>202</p> <p>203</p> <p>204</p> <p>210</p> <p>211</p> <p>212</p> <p>213</p> <p>NETWORK(S)</p> <p>NETWORK(S)</p> <p>FIGURE 2</p> <p>Filsfils, FIG. 2 (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<pre> graph TD START([START]) --> 402[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL] 402 --> 404[DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)] 404 --> 406[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 406 --> END([END]) </pre> <p>The flowchart illustrates a process for establishing paths for a particular tunnel. It begins with a START node, followed by a step to exchange protocol signaling messages to establish the primary path. This is followed by a decision point to determine if a backup path needs to be created. Finally, protocol signaling messages are exchanged to establish a backup path for a portion of the tunnel, and the process concludes at an END node.</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL</p> <p>DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.</p>

FIGURE 4

Filsfils, FIG. 4 (annotated).

No.	'821 Patent Claim 7	The Reference
		<pre> graph TD START([START]) --> 502[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, SENDING A PARTICULAR LABEL FROM THE NEIGHBOR UPSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THIS LSR OVER THE PARTICULAR TUNNEL.] 502 --> 504[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF THE PARTICULAR TUNNEL, INCLUDING RECEIVING INFORMATION THAT IT CAN USE TO CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 504 --> 506[CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL, AND ONLY POPULATE THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, THE LSR KNOWS WHAT ON WHAT INTERFACE(S) PACKETS FROM THE BACKUP PATH COULD BE RECEIVED. THIS CORRELATION MAY INCLUDE USING DATA CONCERNING BUNDLED INTERFACES, AND EVEN RECURSIVE CORRELATION OF BACKUP TUNNELS USED TO BACKUP OTHER BACKUP TUNNELS, AS WELL AS LOAD BALANCING AND OTHER TECHNIQUES TO DETERMINE WHERE BACKUP PATH PACKETS COULD BE RECEIVED, AND THE SUBSET OF FORWARDING INFORMATION BASES IN THE DATA PLANE THAT COULD BE USED TO FORWARD PACKETS OVER THE PARTICULAR TUNNEL, WHETHER VIA A PRIMARY OR BACKUP PATH.] 506 --> END([END]) </pre> <p>FIGURE 5</p> <p>Filsfils, FIG. 5.</p>

No.	'821 Patent Claim 7	The Reference
		<p>“The communications industry is rapidly changing to adjust to emerging technologies and ever increasing customer demand. This customer demand for new applications and increased performance of existing applications is driving communications network and system providers to employ networks and systems having greater speed and capacity (e.g., greater bandwidth). In trying to achieve these goals, a common approach taken by many communications providers is to use packet switching technology, including switching packets on labels especially in the core network using Multiprotocol Label Switching (MPLS).” Filsfils, 1:12-22.</p> <p>“Tunnels, such as MPLS-TE (Traffic Engineering) and MPLS-TP (Transport Profile), are paths established through a network in order to transport packets efficiently through a label switched network. Fast Re-Route (FRR) is a technology that allows backup paths to be established in the network, which can be used in case of a problem with a primary path (original primary path or currently used backup path) of the tunnel. RFC 4090, entitled “Fast Reroute Extensions to RSVP-TE for LSP Tunnels,” provides an extension of the protocol signaling to establish backup label switched path (LSP) tunnels for local repair of LSP tunnels.” Filsfils, 1:23-33.</p> <p>“Expressly turning to the figures, FIG. 1 illustrates a network 100 operating according to one embodiment. Shown are four apparatus 101-104 (e.g., packet switching devices such as a label switch router, network device, and/or appliance). For explanation purposes, each of apparatus 101-104 will be referenced as a label switch router (LSR).” Filsfils, 5:41-46.</p> <p>“As shown, a particular tunnel is established, using a signaling protocol and exchanging of protocol signaling messages. Note, LSR 101 may, or may not, be an endpoint of the particular tunnel (e.g., LSR 101 may be an intermediate LSR on the path of the particular tunnel). The primary path of the particular tunnel includes spans from LSR 101 via link 111 to LSR 103 and via link 112 to LSR 104. Note, LSR 104 may be an intermediate LSR on the path of the particular tunnel, or an endpoint of the particular tunnel. Further, for this example embodiment, LSR 103 signals LSR 101 to use label T1 at the top of the label stack in the header of a packet sent to it on the particular tunnel.” Filsfils, 5:47-58.</p> <p>“A second tunnel from LSR 101 via link 121 to LSR 102 and via link 122 to LSR 103 is similarly configured using a signaling protocol and exchanging of protocol signaling</p>

No.	'821 Patent Claim 7	The Reference
		<p>messages. For example purposes, LSR 102 signals LSR 101 to use label (T2) at the top of the label stack in the header of a packet sent to it on the second tunnel. In one embodiment, LSR 101 creates the second tunnel in response to determining, or being instructed to, create a backup path to protect link 111 and/or protect all or certain tunnels traversing link 111.” Filsfils, 5:59-67.</p> <p>“As shown in FIG. 1, link 111 (primary path of the particular tunnel and/or all or certain tunnels traversing link 111) is protected by LSR 101 using the second tunnel (backup path). When sending packets over the particular tunnel over link 111, LSR 101 includes label T1 at the top of the label stack of these packets. If link 111 cannot be used for communicating packets of the particular tunnel, LSR 101 sends packets over the backup path for the particular tunnel by sending packets to LSR 102, with these packets having a label stack including: label T2 followed by label T1. Thus, LSR 102 will receive these packets, pop the top label (T2) from the label stack of each of these packets, identify based on label T2 to send these packets to LSR 103. After popping the top label, the label at the top of the label stack of these packets is T1, which is the same label LSR 103 expects to receive for the particular tunnel. Therefore, these packets received with label T1 at the top of their label stack, are forwarded (after popping label T1 from their label stack) by LSR 103 over the particular tunnel to LSR 104.” Filsfils, 6:6-24.</p> <p>“One embodiment acquires such additional information by extending Resource Reservation Protocol (RSVP) to provide information which allows a packet switch to correlate primary and backup paths. Thus, a packet switch can use this additional information in determining which of its forwarding information bases (FIBs) could possibly be used in forwarding packets (e.g., in the data plane of the packet switch).” Filsfils, 6:51-57.</p> <p>“In providing this additional information to LSR 103, one embodiment communicates an extended RSVP message (including a new or modified RSVP object) or other message to LSR 103 on the second tunnel. This messages designates one or more primary tunnels (e.g., label T1 in our example) and/or a link (e.g., link 111). As LSR 103 knows what interface that it received this message, LSR 103 knows that it must populate forwarding information for these primary tunnels, either specified (e.g., by a label such as T1), or all labels corresponding to</p>

No.	'821 Patent Claim 7	The Reference
		<p>tunnels which could be received over link 111. In one embodiment, the extended RSVP or other message communicated to LSR 103 also includes an identification of the backup tunnel (e.g., T2) over which the RSVP or other message is being received, as the identification the tunnel over which a packet is received is often not communicated in a packet (e.g., in the case of Penultimate Hop Popping).” Filsfils, 7:19-34.</p> <p>“As shown in FIG. 1, one embodiment includes apparatus 103, which populates less than all of its FIBs with forwarding information for a tunnel (although all FIBs may be populated for certain tunnels). One embodiment includes apparatus 101 and/or 102 which communicates, via a signaling protocol (e.g., an extension of RSVP, or using another protocol), information which allows apparatus 103 to determine the relationship between primary and backup paths, such that apparatus 104 can correlate this primary and backup path information (possibly also correlating backup path of backup path information, and/or bundled interfaces and/or bundled links) to identify a minimum subset of the FIBs that could possibly be used in forwarding packets of particular primary paths (e.g., tunnels).” Filsfils, 7:49-62</p> <p>“Turning to FIG. 2, illustrates an apparatus 200 (e.g., packet switching devices such as a label switch router, network device, and/or appliance) operating in one embodiment. As shown, apparatus 200 includes line cards 201, 202 communicatively coupled via communication mechanism(s) 203 (e.g., bus, switching fabric, and/or matrix). Additionally, route processor 204 is configured to correlate primary and backup paths of tunnels, and to populate minimum subsets of FIBs with forwarding information for labels. Again, a minimum subset of FIBs for a particular path or label of the particular path is the set of FIBs that are determined to possibly be used in forwarding packets of a primary path, whether the label is received in a packet over the primary path or over a backup path, and possibly considering backup paths of a backup path and/or the possibly effect of bundled interfaces and/or bundled links.” Filsfils, 7:63-8:11.</p> <p>“As shown in FIG. 2, apparatus 200 is communicatively coupled via primary and backup paths 211, 212 to networks 210 and 213 (which could be the same network). As illustrated, each of line cards 201, 202 includes one or more FIBs. By correlating on which line card(s) 201, 202 and even within line cards 201, 202 that have multiple FIBs, primary and backup path(s) of tunnels, the number of FIB entries populated in apparatus 200 can typically be reduced,</p>

No.	'821 Patent Claim 7	The Reference
		<p>possibly significantly saving memory/storage resources and resources used to populate the FIBs.” Filsfils, 8:12-21.</p> <p>“FIG. 4 illustrates a process performed in one embodiment. Processing begins with process block 400. In process block 402, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes receiving a particular label for a downstream LSR to use when sending packets to the downstream LSR over the particular tunnel.” Filsfils, 8:61-67.</p> <p>“In process block 404, a determination is made to create a backup path from the node (e.g. the node performing these operations). This backup path may be used to protect one or more particular tunnels, and/or may be used to protect a link which is used to carry packet traffic of one or more tunnels.” Filsfils, 9:1-5.</p> <p>“In process block 406, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including providing information to the downstream LSR so that the downstream LSR can correlate primary and backup path(s) of the particular tunnel and substantially only program the FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:6-23.</p> <p>“FIG. 5 illustrates a process performed in one embodiment. Processing begins with process block 500. In process block 502, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes sending a particular label for an upstream LSR to use when sending packets over the particular tunnel to this apparatus (e.g., an LSR performing these operations).” Filsfils, 9:26-32.</p>

No.	'821 Patent Claim 7	The Reference
		<p>“In process block 504, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including receiving information that the LSR can use to correlate primary and backup path(s) of the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:33-47.</p> <p>“In process block 506, the primary and backup path(s) of the particular tunnel are correlated to identify the set of FIBs that could possibly be used in forwarding packets of the particular tunnel. Substantially only those FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel (either through a primary or backup path) are populated with the forwarding information (e.g., an entry corresponding to the label it advertised to use for the particular tunnel) for the particular tunnel. For example, the LSR knows what on what interface(s) packets from the backup path could be received. This correlation may include using data concerning bundled interfaces, and even recursive correlation of backup tunnels used to backup other backup tunnels, as well as load balancing and other techniques to determine where backup path packets could be received, and the subset of forwarding information bases in the data plane that could be used to forward packets over the tunnel, whether via a primary or backup path.” Filsfils, 9:48-65.</p> <p>Taylor discloses:</p> <p>“Grouping pseudowires based on hardware interfaces and configured control paths enables improved pseudowire failover performance. Signaling status changes (e.g., from standby to active status) is facilitated by using group IDs for the pseudowire groups, thereby enabling improved failover performance when there is disruption in the network.” Taylor, Abstract.</p>

No.	'821 Patent Claim 7	The Reference
		<p>T-PE1</p> <p>Interface e1/0.111 xconnect 10.2.2.2 1 encap mpls backup peer 10.3.3.3 101</p> <p>Interface e1/0.222 xconnect 10.3.3.3 2 encap mpls backup peer 10.2.2.2 102</p> <p>S-PE2 10.2.2.2</p> <p>T-PE2 10.4.4.4</p> <p>12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls neighbor 10.4.4.4 101 encap mpls</p> <p>12 vfi two point-to-point neighbor 10.1.1.1 102 encap mpls neighbor 10.4.4.4 102 encap mpls</p> <p>T-PE1 10.1.1.1</p> <p>e1/0</p> <p>S-PE3 10.3.3.3</p> <p>T-PE4 10.4.4.4</p> <p>e4/0</p> <p>T-PE4</p> <p>interface e4/0.111 xconnect 10.2.2.2 1 encap mpls backup peer 10.3.3.3 101</p> <p>interface e4/0.222 xconnect 10.3.3.3 2 encap mpls backup peer 10.2.2.2 102</p> <p>S-PE4</p>

FIG. 4

Taylor, FIG. 4 (annotated).

No.	'821 Patent Claim 7	The Reference
		<p>Diagram illustrating a network topology with five routers (T-PE1, S-PE2, T-PE4, S-PE3, T-PE5) connected via various interfaces (e0/0, e1/0). Each router has associated configuration boxes containing interface details and neighbor information.</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1) has interfaces e0/0 and e1/0. Configuration box 504 shows: <ul style="list-style-type: none"> T-PE1 → S-PE2: Gid=1 for 1,2,3,4 T-PE1 → S-PE3: Gid=2 for 5,6,7,8 S-PE2 (12.1.1.1) has interfaces e0/0 and e1/0. Configuration box 514 shows: <ul style="list-style-type: none"> I2 vif abc point-to-point neighbor 14.1.1.1 1 encaps mpls neighbor 14.1.1.1 1 encaps mpls I2 vif def point-to-point neighbor 11.1.1.2 2 encaps mpls neighbor 14.1.1.2 2 encaps mpls I2 vif ghi point-to-point neighbor 11.1.1.3 3 encaps mpls neighbor 14.1.1.3 3 encaps mpls I2 vif jkl point-to-point neighbor 11.1.1.4 4 encaps mpls neighbor 14.1.1.4 4 encaps mpls T-PE4 (14.1.1.1) has interfaces e0/0 and e1/0. Configuration box 506 shows: <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.1 encaps mpls interface e0/0.200 xconnect 12.1.1.2 encaps mpls interface e1/0.100 xconnect 12.1.1.3 encaps mpls interface e1/0.200 xconnect 12.1.1.4 encaps mpls S-PE3 (13.1.1.1) has interfaces e0/0 and e1/0. Configuration box 516 shows: <ul style="list-style-type: none"> S-PE2 → T-PE4: Gid=10 for 1,2,3,4 S-PE2 → T-PE1: Gid=20 for 1,2 S-PE2 → T-PE1: Gid=21 for 3,4 S-PE3 → T-PE5: Gid=50 for 5,6,7,8 S-PE3 → T-PE1: Gid=75 for 5,6 S-PE3 → T-PE1: Gid=76 for 7,8 T-PE5 (15.1.1.1) has interfaces e0/0 and e1/0. Configuration box 512 shows: <ul style="list-style-type: none"> T-PE5 → S-PE3: Gid=250 for 5,6 T-PE5 → S-PE3: Gid=251 for 7,8 interface e0/0.100 xconnect 13.1.1.5 encaps mpls interface e0/0.200 xconnect 13.1.1.6 encaps mpls interface e1/0.100 xconnect 13.1.1.7 encaps mpls interface e1/0.200 xconnect 13.1.1.8 encaps mpls

FIG. 5

Taylor, FIG. 5 (annotated).

No.	'821 Patent Claim 7	The Reference				
		<p style="text-align: center;">TRAFFIC FLOW IS TOWER ← CORE</p> <p>TOWER T-PE: Contains two parallel paths labeled 804 and 808, each containing "VLANs 100-112" and "VLANs 110-112".</p> <p>S-PE1: Contains two parallel paths labeled 802 and 806, each containing "VLANs 100-549", "VLANs 550-999", "VLANs 100-549", "VLANs 550-999", and "VLANs 100-549".</p> <p>CORE T-PE1: Contains two parallel paths labeled 802 and 806, each containing "VLANs 100-549", "VLANs 550-999", and "VLANs 100-549".</p> <p>mLACP: A group connecting S-PE1 and S-PE2, containing "VLANs 100-999" and "VLANs 550-999".</p> <p>RNC/BSC: Associated with CORE T-PE2.</p> <p>Legend:</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td>ACTIVE PW/ACTIVE AC</td> <td>STANDBY PW/ACTIVE AC</td> </tr> <tr> <td>STANDBY PW/STANDBY AC</td> <td>STANDBY PW/STANDBY AC</td> </tr> </table> <p style="text-align: center;">* GOES ACTIVE WHEN mLACP FAILS OVER</p> <p style="text-align: right;">FIG. 8</p> <p>Taylor, FIG. 8 (annotated).</p>	ACTIVE PW/ACTIVE AC	STANDBY PW/ACTIVE AC	STANDBY PW/STANDBY AC	STANDBY PW/STANDBY AC
ACTIVE PW/ACTIVE AC	STANDBY PW/ACTIVE AC					
STANDBY PW/STANDBY AC	STANDBY PW/STANDBY AC					

No.	'821 Patent Claim 7	The Reference
		<pre> sequenceDiagram participant TOWER as TOWER T-PE participant S1 as S-PE1 participant S2 as S-PE2 participant CORE as CORE T-PE TOWER->>S1: S-PE1 FFD PWs MARKED DOWN S1->>S2: S-PE1 FAILURE S2-->>CORE: HOT STANDBY → ACTIVE CORE-->>S2: HOT STANDBY → ACTIVE CORE->>S1: S-PE1 GROUPS MARKED DOWN </pre> <p><i>Annotations:</i></p> <ul style="list-style-type: none"> S-PE1 FFD PWs MARKED DOWN S-PE1 FAILURE (boxed in red) HOT STANDBY → ACTIVE (boxed in yellow) S-PE1 GROUPS MARKED DOWN

FIG. 9

Taylor, FIG. 9 (annotated).

No.	'821 Patent Claim 7	The Reference
		<p>1002 ↗</p> <pre> graph TD A[1004 SPECIFY PHYSICAL INTERFACE AT FIRST PE] --> B[1006 SPECIFY FIRST CONTROL PATH FROM FIRST PE TO SECOND PE] B --> C[1008 SPECIFY SECOND CONTROL PATH FROM FIRST PE TO THIRD PE] C --> D[1010 SPECIFY REDUNDANT COMBINATIONS OF PWs] D --> E[1012 GROUP PWs INTO A PRIMARY PW GROUP AND A BACKUP PW GROUP] E --> F[1014 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO PRIMARY/BACKUP PW GROUPS] F --> G[1016 DETECT FAILURE ON FIRST CONTROL PATH] G --> H[1018 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO BACKUP/PRIMARY PW GROUPS] </pre> <p>FIG. 10</p> <p>Taylor, FIG. 10 (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<p>“The present disclosure relates generally to communication networks and more particularly to pseudowire configurations in communication networks.” Taylor, 1:8-10.</p> <p>“Virtual Private Network (VPN) services provide secure network connections between different locations. A company, for example, can use a VPN to provide secure connections between geographically dispersed sites that need to access the corporate network so that each customer edge (CE) end point or node can communicate directly and independently with all other CE nodes. Different types of VPNs have been classified by the network layer used to establish the connection between the customer and provider network. For example, Virtual Private LAN Service (VPLS) is an architecture that delivers a multipoint Layer 2 VPN (L2VPN) service that in all respects emulates an Ethernet Local Area Network (LAN) across a wide metropolitan geographic area. All services in a VPLS appear to be on the same LAN, regardless of location. In other words, with VPLS, customers can communicate as if they were connected via a private Ethernet segment, i.e., multipoint Ethernet LAN services.” Taylor, 1:12-28.</p> <p>“In this context, each CE device at a customer site is connected to the service provider network at a provider edge PE) device by an Attachment Circuit (AC) that provides the customer connection to a service provider network, that is, the connection between a CE node and its associated PE node. Within the provider network, each PE device includes a Virtual Switch Instance (VSI) that emulates an Ethernet bridge (i.e., switch) function in terms of Media Access Control (MAC) address learning and forwarding in order to facilitate the provisioning of a multipoint L2VPN. A pseudowire (PW) is a virtual connection between two PE devices that connect two attachment circuits. In the context of the VPLS service, a pseudowire can be thought of as a point-to-point virtual link for each offered service between a pair of VSIs. Therefore, if each VSI can be thought of as a virtual Ethernet switch for a given customer service instance, then each pseudowire can be thought of as a virtual link connecting these virtual switches to each other over a Packet Switched Network (PSN) for that service instance.” Taylor, 1:29-47.</p>

No.	'821 Patent Claim 7	The Reference
		<p>“Since the failure of pseudowires obviously degrades network performance, some effort has been directed towards adding system redundancies including redundant pseudowires. However, the presence of redundant pseudowires alone is insufficient to improve overall failover performance, that is, the ability to switch over automatically to a redundant or backup system. Thus, there is a need for improved methods for managing pseudowires to facilitate pseudowire switching and enable improved failover performance.” Taylor, 1:48-56.</p> <p>“FIG. 4 shows details for PW connectivity in an exemplary network for an example embodiment.” Taylor, 1:66-67.</p> <p>“FIG. 5 shows details for PW grouping for an example embodiment.” Taylor, 2:1-2.</p> <p>“FIG. 8 shows an example network including redundant PW connections for an example embodiment.” Taylor, 2:9-10.</p> <p>“FIG. 9 shows an example sequence diagram for a failure mode related to the embodiment shown in FIG. 8.” Taylor, 2:11-12.</p> <p>“FIG. 10 shows a flowchart that illustrates a method of providing improved PW grouping according to an example embodiment.” Taylor, 2:13-15.</p> <p>“According to one embodiment, a method of providing improved pseudowire performance includes specifying a physical interface at a first PE node in a network, a first control path from the first PE node to a second PE node in the network, and a second control path from the first PE node to a third PE node in the network. With these specifications, the method then includes specifying redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node, and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Then these pseudowires can be grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. Group identifiers for the primary PW group and the backup PW group can then be used to assign an</p>

No.	'821 Patent Claim 7	The Reference
		<p>active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths. The method may then include detecting a failure on the first control path, and in response to the detected failure, using the group identifiers to assign the active status to the backup pseudowires and the standby status to the primary pseudowires.” Taylor, 2:27-52.</p> <p>“Pseudowires are used in pseudowire emulation edge-to-edge to provide a Layer 2 Virtual Private Network (L2VPN) connection. When large numbers (e.g., 4,000-6,000) of pseudowires are aggregated together on a single router, failure performance tends to be linear or O(n) where n is the number of pseudowires. While O(n) performance may be acceptable for small numbers of pseudowires, the effect on network outages can be increasingly undesirable as the number of pseudowires increases.” Taylor, 2:54-62.</p> <p>“For example, a cell-site router will typically start an approximately 2-minute procedure if contact with its controller, which is reached via a pseudowire, is lost for more than some threshold amount (e.g., between approximately 0.75 and 1.75 seconds in some cases). This can be a major impediment to the scalability of pseudowire deployments. These issues have become increasingly relevant as providers of Multiservice Broadband Networks (MBNs) are rapidly replacing or augmenting their traditional Synchronous Optical Networking (SONET) equipment with cheaper Ethernet equipment in the evolution towards a 4G (i.e., 4th generation) network.” Taylor, 2:63-3:7.</p> <p>“One aspect of a solution to the problem of pseudowire failure is the deployment of redundant pseudowires. For example, redundant pseudowires have been used in the context of Multiprotocol Label Switching (MPLS) networks, which use a Label Distribution Protocol (LDP) to manage labels for forwarding traffic between routers. In this context, general requirements for redundancy schemes have been developed so that duplicate pseudowires are available when a given pseudowire fails (e.g., by using active/standby status indicators). In addition, more specific implementations for redundant pseudowires have also been developed.” Taylor, 3:8-18.</p>

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		<p>“FIG. 1 shows a reference network model 102 with applications to example embodiments disclosed herein. The reference network model 102 includes an aggregation network 104 of PE nodes and a distribution network 106 of PE nodes between a radio network controller (RNC) (or base station controller (BSC)) 108 on the core side of the model 102 and a radio tower 111 on the tower side of the model 102. Switching provider edge nodes S-PE1 and S-PE2 connect the two networks 104, 106. On the core side, two core terminating provider edges T-PE1 and T-PE2 connect to the RNC/BSC 108 through attachment circuits 110, 112. On the tower side, one tower terminating provider edge T-PE connects to the radio tower 111 through an attachment circuit 114.” Taylor, 3:19-31.</p> <p>“Additionally as noted in FIG. 1, peer-PE monitoring is carried out within each network 104, 106. That is, there is peer-PE monitoring between provider edges that share a segment, for example, by multi-hop bidirectional forwarding detection (BFD). Alternatively, peer monitoring can be accomplished by other means (e.g., MPLS-TP (Transport Protocol) keep-alives). This peer-PE monitoring is used to provide the mechanism for fast failure detection. Once a failure is detected, the network can react by “rerouting” the failed pseudowires to pre-provisioned backup paths and thus provide a minimal disruption in service to the end-user. This rerouting can be accomplished by LDP signaling between provider edges.” Taylor, 3:32-44.</p> <p>“The reference network model 102 may be considered as part of a larger hub-and-spoke model as shown in FIG. 2. A hub-and-spoke distribution model 202 includes a core network 204, distribution networks 206, and aggregation networks 208. Network elements including distribution nodes, aggregation nodes, and towers are also shown with nominal count values (e.g., 30 distribution nodes between the core network 204 and a distribution network 206). In this model 202, tower T-PEs are the spokes while core-PEs constitute the hub. Dozens to hundreds of tower T-PEs connect to a few S-PEs; these S-PEs are quite similar to ASBRs as they act as forwarders between the two distinct MPLS domains, providing isolation and, in the case of mobility, aggregation services. Typically, several aggregation networks 208 are connected to a single distribution network 206, eventually connecting the tower with the core router that connects the tower's ACs to the RNC/BSC. There are typically several distribution</p>

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		<p>networks in a Radio Access Network (RAN) connected to the service provider's core Internet Protocol (IP) network.” Taylor, 4:11-30.</p> <p>“With reference to FIG. 1, FIG. 3 shows a variety of failure modes encountered in the reference network model 102. Failure 302 of communications between tower T-PE and the S-PE can be detected via peer monitoring when both the S-PE and the T-PE are still active/alive. For example, this failure may be due to a loss of connectivity when the BFD session goes down. Failure 304 of S-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability (e.g., if the BFD-session hello timers cannot be serviced for the prescribed period of time). Failure 306 of communications between S-PE1 and core T-PE1 can be detected via peer monitoring when both S-PE1 and core T-PE1 are still active/alive. Failure 308 at core T-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability.” Taylor, 4:46-60.</p> <p>“Pseudowire connectivity is further illustrated in FIG. 4 where the illustrated network includes four nodes: T-PE1 (10.1.1.1), S-PE2 (10.2.2.2), S-PE3 (10.3.3.3), and T-PEA (10.4.4.4). For the terminating nodes T-PE1 and T-PE4, specifications for VLANs (virtual Local Area Networks) connections (i.e., pseudowires) are shown using the Internet Operating System Command Line Interface (IOS CLI). The specification 402 for T-PE1 defines two VLANs as primary/backup combinations of virtual circuits for the network. The first three lines of the specification 402 define “VLAN 111” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 111” in the first line. The second line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE2 (10.12.2) with a virtual circuit Identification (VCID) set as VCID=1, and the third line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=101. The next three lines of the specification 402 define “VLAN 222” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 222” in the fourth line. The fifth line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=2, and the sixth line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE2 (10.2.2.2) with VCID=102. These virtual circuits, VCID=1, VCID=2, VCID=101 and VCID=102 are shown in the figure between T-PE1 and the S-PEs with a solid line for the primary circuits VCID=1</p>

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		<p>and VCID=2 and a dashed line for the backup circuits VCID=101 and VCID=102.” Taylor, 4:61-5:23.</p> <p>“With respect to T-PE1 in FIG. 4, although “VLAN 111” and “VLAN 222” share the same hardware port, they do not share the same “control path disposition.” That is, “VLAN 111” is primary to S-PE2 (VCID=1) and standby to S-PE3 (VCID=101), while “VLAN 222” has an opposite configuration since it is primary to S-PE3 (VCID=2) and standby to S-PE2 (VCID=102). As discussed below, certain embodiments group pseudowires according to “control path disposition” (e.g., xconnect configuration as well as the hardware interface in order to improve failover performance. That is, to deal with both hardware port failures and switching path failures, the grouping criteria also considers the cross connects. In this case, on T-PE1 as well as T-PE4, there would exist two groups: one for active to S-PE2 and standby to S-PE3 and another for active to S-PE3 and standby to S-PE2 (i.e., the inverse configuration).” Taylor, 5:61-6:9.</p> <p>“First, local connectivity is characterized by local group identifications (Group-IDs), which depend on whether the allocation is done at a T-PE or S-PE. FIG. 5 shows an embodiment that illustrates an allocation of local group IDs in a network including terminating nodes T-PE1 (11.1.1.1), T-PE4 (14.1.1.1), and T-PE5 (15.1.1.1) and switching nodes S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1). The specification 502 for T-PE1 determines corresponding local group IDs 504 based on the hardware interface and the control path. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, and the Group-ID=2 corresponds to VCID=5, VCID=6, VCID=7, and VCID=8.” Taylor, 6:10-21.</p> <p>“Local group IDs are maintained in a database so that pseudowire redundancy is also maintained. First, in a case without pseudowire redundancy, all the xconnect configurations from the same physical interface to the same peer are assigned the same local group ID. So, for example, in Ethernet cases all xconnect configurations under sub-interfaces of the same physical interface to the same peer will be assigned the same local group ID (e.g., e0/0 and e0/1 are sub-interfaces of e0). FIG. 6 shows a database representation for T-PE4 from FIG. 5. From the root node 602 for T-PE4, there is a first interface node 604 for e0/1 and a second interface node 606 for e1/0. The first interface node 604 is configured towards a single peer</p>

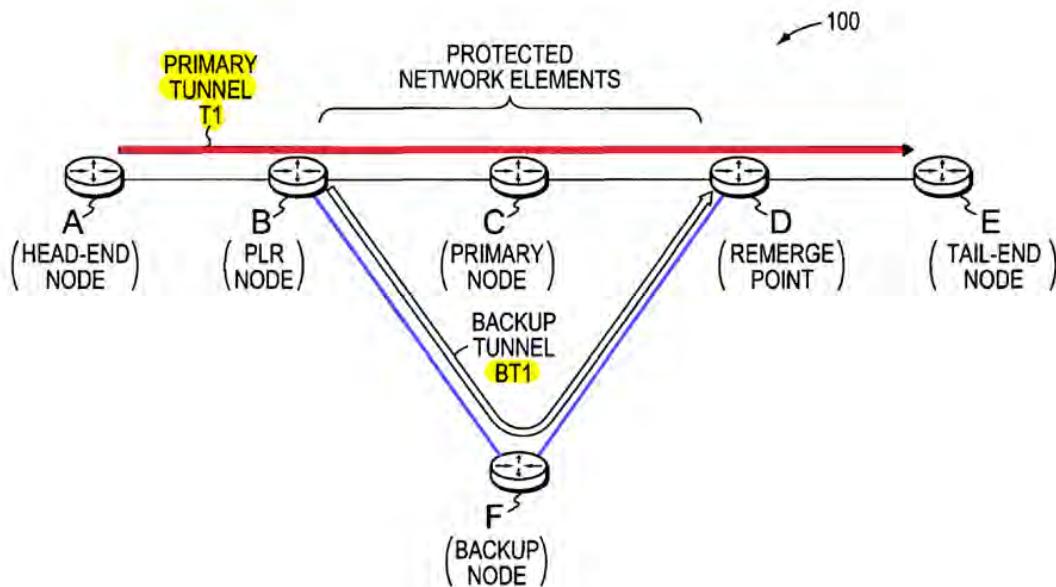
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		<p>node (12.1.1.1) 608 and is thus assigned a single local group ID (Group-ID=200) 610. Similarly, the second interface node 606 is configured towards a single peer node (12.1.1.1) 612 and is thus assigned a single local group ID (Group-ID=201) 614. In this case from the assignment of local group IDs 508 in FIG. 5, Group-ID=200 corresponds to VCID=1 and VCID=2, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). Both pseudowires (VCID=1 and VCID=2) are assigned the same local group ID (Group-ID=200) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly, from the assignment of local group IDs 508 in FIG. 5, T-PE4 has VCID=3 and VCID=4 under the physical interface e1/0 going to the same peer S-PE2 (12.1.1.1), and the local Group ID (Group-ID=201) is assigned to these VCs.” Taylor, 6:48-7:8.</p> <p>“For the pseudowire redundancy case, a separate redundancy-group database is maintained by the xconnect application. This redundancy-group database contains the peer IDs in the group and the local group IDs advertised to them. This is needed to maintain a 1:1 mapping between the primary pseudowires and their corresponding backup pseudowires. FIG. 7 shows a database representation for T-PE1 from FIG. 5. From the root node 702 for T-PE1, there is an interface node 704 for e0/0 and a redundancy group node 706 that shows connections for configurations to a first peer node (12.1.1.1) 708, which is assigned a local group ID (Group-ID=1) 710, and a second peer node (13.1.1.1) 712, which is assigned a local group ID (Group-ID=2) 714. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). These pseudowires are assigned the same group ID (Group-ID=1) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly from the assignment of local group IDs 504 in FIG. 5, T-PE1 has VCID=5, VCID=6, VCID=7, and VCID=8 under the physical interface e0/0 going to another peer S-PE3 (13.1.1.1), and the local group ID (Group-ID=2) is assigned to these VCs. In this case these local group IDs are organized as a redundancy group 706.” Taylor, 7:9-35.</p>

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		<p>“Pseudowire grouping allows multiple pseudowires to be aggregated together when signaling either label withdrawals or status changes between segment end-point provider edges. This signaling can be carried out through LDP grouping TLV (Type Length Value). For example, when a PE node fails (e.g., failure 304 of S-PE1), aggregating the inter-segment PE signaling using the grouping TLV can provide significant scaling advantages. This allows all pseudowires sharing a physical port and PW configuration (e.g., xconnect configuration) to be signaled en masse between segment-adjacent provider edges.” Taylor, 7:36-46.</p> <p>“FIG. 8 shows an example based on FIG. 1 where VLAN ACs are shown as grouped by both port/HW-interface and pseudowire-class. The grouping criterion allows all “similar” pseudowires to be signaled together: All the grouped pseudowires share the same port and next-hop provider edge. Additionally, the figure contains many pseudowires, each grouped into a shaded tube. For example, the tube labeled “VLANs 100-549” contains 450 pseudowires grouped together. This figure depicts an incoming Ethernet comprised of 900 VLANs being segmented in two with 450 VLANs (100-549) active to S-PE1 while the other half of the VLANs (550-999) being active to S-PE2. This might be considered a type of manual load balancing. Furthermore, the aggregation network is only showing a single tower and the VLANs associated with it; other VLAN destinations are not shown in the figure.” Taylor, 7:47-62.</p> <p>“‘VLANs 110-112’ are active along a first pseudowire path 802 from Core T-PE1 to S-PE1 and a second pseudowire path 804 from S-PE1 to Tower T-PE. When a failure occurs at S-PE1 (e.g., as the switching node failure 304 shown in FIG. 3), then the standby pseudowires become active for ‘VLANs 110-112’ along a first pseudowire path 806 from Core T-PE1 to S-PE2 and a second pseudowire path 808 from S-PE2 to Tower T-PE.” Taylor, 7:63-8:3.</p> <p>“The standby pseudowires in FIG. 8 can be configured as HSPWs, a configuration that enables ACs to quickly failover to pre-provisioned pseudowires that are in active state but set to not-forwarding. Then when a failure occurs, switching over to these pre-provisioned HSPWs occurs quickly by switching from not-forwarding status to forwarding status.” Taylor, 8:4-9.</p>

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		<p>“FIG. 9 shows a UML (Unified Modeling Language) sequence diagram of signaling events related to the failover procedure illustrated in FIG. 8 for a switching node failure 304. The Tower T-PE and the Core T-PE independently detect a failure at S-PE1 (e.g., BFD forwarding with LDP signaling), and then mark down the status of the currently active pseudowires routed through S-PE1 and mark up the status of the standby pseudowires routed through S-PW2. Other failure modes shown in FIG. 3 can be handled similarly.” Taylor, 8:10-18.</p> <p>“In general, it is desirable for MPLS-based Ethernet networks to react quickly to failures, so proactive detection mechanisms are employed in order to pick up system failures quickly. All proactive monitoring is typically done between PE peers on a single MPLS network. These provider edges on the edges of the MPLS network act similarly to an Autonomous System Boundary Router (ASBR). As a result, related embodiments detect control path failures, which may not be the same as pseudowire data path failures. That is, the data packets and control packets may take different paths between provider edges in a MPLS network although typically these paths are coincident. Thus, when the control and data paths are not coincident, if the control path fails, then all pseudowires utilizing the control path are marked as failed. As a corollary, if the data path fails and the control path remains healthy, then failure will not be detected from monitoring the control path.” Taylor, 8:19-35.</p> <p>“A failure of a monitored provider edge initiates a switchover of all active pseudowires using the failing provider edge to their configured HSPWs (if they exist). Grouping can greatly reduce the number of messages needed between provider edges (Inter-PE Aggregation) and within a single provider edge (Intra-PE Aggregation). Furthermore, the MPLS network itself may be internally resilient deploying technologies such as, but not limited to, MPLS-TE (MPLS Traffic Engineering) and ERR (Fast Reroute). The paths across the MPLS network may recover quickly and might not trip the fault-monitoring systems.” Taylor, 8:36-46.</p> <p>“With reference to the above discussion, FIG. 10 shows a method 1002 of providing improved PW grouping according to an example embodiment. In a first operation 1004 of the method 1002, a physical interface is specified at a first PE node in a network. In a second operation 1006, a first control path is specified from the first PE node to a second PE node in the network. In a third operation 1008, a second control path is specified from the first PE node to a third</p>

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		<p>PE node in the network. These control paths related to a common physical interface can be used to characterize redundant pairs of pseudowires.” Taylor, 8:48-58.</p> <p>“In a fourth operation 1010, redundant combinations of pseudowires are specified, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Although a redundant combination may relate a single backup pseudowire to a given primary pseudowire, in some cases multiple backup pseudowires will be related to a given primary pseudowire for increased redundancy. In a fifth operation 1012, these pseudowires are grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. In a sixth operation 1014, group identifiers for the primary PW group and the backup PW group are used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 8:59-9:11.</p> <p>“In an optional seventh operation 1016, a failure may be detected on the first control path, and in an optional eighth operation 1018, in response to the detected failure, the group identifiers may be used to assign the active status to the backup pseudowires and the standby status to the primary pseudowires. For example, the failure on the first control path may be detected by using BED packet streams between PE nodes of the network. Then the detected failure can be signaled to PE nodes in the network by sending LDP status updates between PE nodes in the network. Then, after receiving the failure detection signals, the group identifiers can be used again to assign the active status to the backup pseudowires and the standby status to the primary pseudowires by sending LDP status updates between PE nodes in the network.” Taylor, 9:12-26.</p> <p>“Typically the network in is an MPLS network and the PE nodes are routers that provide network services to connected CE nodes of a customer network. In general, each control path is an Internet Protocol (IP) routing path between PE nodes in the network and each data path is a label switched path (LSP) between PE nodes in the network.” Taylor, 9:27-32.</p>

No.	'821 Patent Claim 7	The Reference
		<p>“FIG. 11 shows a schematic representation of an apparatus 1102, in accordance with an example embodiment. For example, the apparatus 1102 may be used to implement the method 1002 of providing improved pseudowire grouping as described above with reference to FIG. 10. The apparatus 1102 is shown to include a processing system 1104 that may be implemented on a server, client, or other processing device that includes an operating system 1106 for executing software instructions.” Taylor, 10:2-10.</p> <p>“In accordance with an example embodiment, the apparatus 1102 includes a PW management module 1108 that includes a first specification module 1110, a second specification module 1112, third specification module 1114, a fourth specification module 1116, a grouping module 1118, and an assignment module 1120. The first specification module 1110 operates to specify a physical interface at a first PE node in a network. The second specification module 1112 operates to specify a first control path from the first PE node to a second PE node in the network. The third specification module 1114 operates to specify a second control path from the first PE node to a third PE node in the network. The fourth specification module 1116 operates to specify redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node.” Taylor, 10:11-29.</p> <p>“The grouping module 1118 operates to group the pseudowires into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. The assignment module 1120 operates to use group identifiers for the PW groups to assign an active status to the primary pseudowires and a standby status to the backup pseudowires. The active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 10:30-38.</p>

No.	'821 Patent Claim 7	The Reference
		<p>Vasseur '879 discloses:</p> <p>"A technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node "Fast Reroutes," i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel." Vasseur '879, Abstract.</p>  <p>FIG. 1</p> <p>Vasseur '879, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<p>SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)</p> <pre> graph TD A["SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)"] B["COMMON HEADER 310 SOURCE ADDRESS 312 DESTINATION ADDRESS 314"] C["SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320"] D["LSP-ATTRIBUTE OBJECT 330 EXTENSION OBJECT(S) 400"] A --- B A --- C A --- D B --- C C --- D style B fill:#fff,stroke:#000,stroke-width:1px style C fill:#fff,stroke:#000,stroke-width:1px style D fill:#fff,stroke:#000,stroke-width:1px style D stroke:#f00,stroke-width:2px </pre>

FIG. 3

Vasseur '879, FIG. 3 (annotated).

No.	'821 Patent Claim 7	The Reference
		<p>FIG. 5</p> <p>Vasseur '879, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<p>FIG. 6</p> <p>Vasseur '879, FIG. 6 (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<pre> graph TD START([START]) --> 705[HEAD-END NODE ESTABLISHES PRIMARY TUNNEL] 705 --> 710[HEAD-END NODE REQUESTS PROTECTION OF PRIMARY TUNNEL] 710 --> 715[POINTS OF LOCAL REPAIR (PLRs) CREATE BACKUP TUNNELS TO PROTECT PRIMARY TUNNEL] 715 --> 720[HEAD-END NODE REQUESTS BACKUP TUNNEL PATH QUALITY NOTIFICATION] 720 --> 725[PLRs OF PRIMARY TUNNEL COLLECT PATH QUALITY INFORMATION (e.g., METRICS/STATISTICS) FOR PRIMARY AND BACKUP TUNNELS (e.g., FROM PRIMARY AND/OR BACKUP NODES)] 725 --> 730[PLRs MAY SEND PATH QUALITY NOTIFICATION TO HEAD-END NODE] 730 --> 735[PLRs MONITOR PROTECTED SEGMENT OF PRIMARY TUNNEL FOR FAILURE] 735 --> 740{DETECT FAILURE ?} 740 -- N --> 705 740 -- Y --> A((A)) </pre> <p>FIG. 7A</p> <p>Vasseur '879, FIG. 7A (annotated).</p>

No.	'821 Patent Claim 7	The Reference
		<pre> graph TD A(()) --> B[DETECTING PLR DIVERTS PRIMARY TUNNEL TRAFFIC TO BACKUP TUNNEL AND SENDS ERROR MESSAGE TO HEAD-END NODE] B --> C[PLR CONTINUES TO COLLECT PATH QUALITY INFORMATION FOR BACKUP TUNNEL] C --> D{PLR SEND NOTIFICATION TO HEAD-END NODE ?} D -- N --> E{TIMER EXPIRED ?} E -- Y --> F[HEAD-END NODE DETERMINES WHETHER TO REESTABLISH PRIMARY TUNNEL BASED ON BACKUP TUNNEL PATH QUALITY NOTIFICATION] F --> G{ACCEPTABLE PATH QUALITY ?} G -- Y --> H[HEAD-END NODE ATTEMPTS TO REESTABLISH PRIMARY TUNNEL] G -- N --> I(END) H --> I D -- Y --> F </pre> <p>FIG. 7B</p> <p>Vasseur '879, FIG. 7B.</p>

No.	'821 Patent Claim 7	The Reference
		<p>“The present invention relates to computer networks and more particularly to dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 1:10-14.</p> <p>“Since management of interconnected computer networks can prove burdensome, smaller groups of computer networks may be maintained as routing domains or autonomous systems. The networks within an autonomous system (AS) are typically coupled together by conventional “intradomain” routers configured to execute intradomain routing protocols, and are generally subject to a common authority. To improve routing scalability, a service provider (e.g., an ISP) may divide an AS into multiple “areas.” It may be desirable, however, to increase the number of nodes capable of exchanging data; in this case, interdomain routers executing interdomain routing protocols are used to interconnect nodes of the various ASes. Moreover, it may be desirable to interconnect various ASes that operate under different administrative domains. As used herein, an AS or an area is generally referred to as a “domain,” and a router that interconnects different domains together is generally referred to as a ‘border router.’” Vasseur '879, 1:40-56.</p> <p>“An example of an interdomain routing protocol is the Border Gateway Protocol version 4 (BGP), which performs routing between domains (ASes) by exchanging routing and reachability information among neighboring interdomain routers of the systems. An adjacency is a relationship formed between selected neighboring (peer) routers for the purpose of exchanging routing information messages and abstracting the network topology. The routing information exchanged by BGP peer routers typically includes destination address prefixes, i.e., the portions of destination addresses used by the routing protocol to render routing (“next hop”) decisions. Examples of such destination addresses include IP version 4 (IPv4) and version 6 (IPv6) addresses. BGP generally operates over a reliable transport protocol, such as TCP, to establish a TCP connection/session. The BGP protocol is well known and generally described in Request for Comments (RFC) 1771, entitled A Border Gateway Protocol 4 (BGP-4), published March 1995.” Vasseur '879, 1:57-2:7.</p>

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		<p>“Examples of an intradomain routing protocol, or an interior gateway protocol (IGP), are the Open Shortest Path First (OSPF) routing protocol and the Intermediate-System-to-Intermediate-System (IS-IS) routing protocol. The OSPF and IS-IS protocols are based on link-state technology and, therefore, are commonly referred to as link-state routing protocols. Link-state protocols define the manner with which routing information and network-topology information are exchanged and processed in a domain. This information is generally directed to an intradomain router's local state (e.g., the router's usable interfaces and reachable neighbors or adjacencies). The OSPF protocol is described in RFC 2328, entitled OSPF Version 2, dated April 1998 and the IS-IS protocol used in the context of IP is described in RFC 1195, entitled Use of OSI IS-IS for routing in TCP/IP and Dual Environments, dated December 1990, both of which are hereby incorporated by reference.” Vasseur '879, 2:8-24.</p> <p>“An intermediate network node often stores its routing information in a routing table maintained and managed by a routing information base (RIB). The routing table is a searchable data structure in which network addresses are mapped to their associated routing information. However, those skilled in the art will understand that the routing table need not be organized as a table, and alternatively may be another type of searchable data structure. Although the intermediate network node's routing table may be configured with a predetermined set of routing information, the node also may dynamically acquire (“learn”) network routing information as it sends and receives data packets. When a packet is received at the intermediate network node, the packet's destination address (e.g., stored in a header of the packet) may be used to identify a routing table entry containing routing information associated with the received packet. Among other things, the packet's routing information indicates the packet's next-hop address.” Vasseur '879, 2:25-41.</p> <p>“Multi-Protocol Label Switching (MPLS) Traffic Engineering has been developed to meet data networking requirements such as guaranteed available bandwidth or fast restoration. MPLS Traffic Engineering exploits modern label switching techniques to build guaranteed bandwidth end-to-end tunnels through an IP/MPLS network of label switched routers (LSRs). These tunnels are a type of label switched path (LSP) and thus are generally referred to as MPLS Traffic Engineering (TE) LSPs. Examples of MPLS TE can be found in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels dated December 2001, RFC 3784</p>

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		<p>entitled Intermediate-System-to-Intermediate-System (IS-IS) Extensions for Traffic Engineering (TE) dated June 2004, and RFC 3630, entitled Traffic Engineering (TE) Extensions to OSPF Version 2 dated September 2003, the contents of all of which are hereby incorporated by reference in their entirety.” Vasseur '879, 2:58-3:6.</p> <p>“Establishment of an MPLS TE-LSP from a head-end LSR to a tail-end LSR involves computation of a path through a network of LSRs. Optimally, the computed path is the “shortest” path, as measured in some metric, that satisfies all relevant LSP Traffic Engineering constraints such as e.g., required bandwidth, “affinities” (administrative constraints to avoid or include certain links), etc. Path computation can either be performed by the head-end LSR or by some other entity operating as a path computation element (PCE) not co-located on the head-end LSR. The head-end LSR (or a PCE) exploits its knowledge of network topology and resources available on each link to perform the path computation according to the LSP Traffic Engineering constraints. Various path computation methodologies are available including CSPF (constrained shortest path first). MPLS TE-LSPs can be configured within a single domain, e.g., area, level, or AS, or may also span multiple domains, e.g., areas, levels, or ASes.” Vasseur '879, 3:7-24.</p> <p>“The PCE is an entity having the capability to compute paths between any nodes of which the PCE is aware in an AS or area. PCEs are especially useful in that they are more cognizant of network traffic and path selection within their AS or area, and thus may be used for more optimal path computation. A head-end LSR may further operate as a path computation client (PCC) configured to send a path computation request to the PCE, and receive a response with the computed path, potentially taking into consideration other path computation requests from other PCCs. It is important to note that when one PCE sends a request to another PCE, it acts as a PCC.” Vasseur '879, 3:25-36.</p> <p>“Some applications may incorporate unidirectional data flows configured to transfer time-sensitive traffic from a source (sender) in a computer network to a destination (receiver) in the network in accordance with a certain “quality of service” (QoS). Here, network resources may be reserved for the unidirectional flow to ensure that the QoS associated with the data flow is maintained. The Resource ReSerVation Protocol (RSVP) is a network-control protocol that</p>

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		<p>enables applications to reserve resources in order to obtain special QoS for their data flows. RSVP works in conjunction with routing protocols to, e.g., reserve resources for a data flow in a computer network in order to establish a level of QoS required by the data flow. RSVP is defined in R. Braden, et al., Resource ReSerVation Protocol (RSVP), RFC 2205, the contents of which are hereby incorporated by reference in its entirety. In the case of traffic engineering applications, RSVP signaling (with Traffic Engineering extensions) is used to establish a TE-LSP and to convey various TE-LSP attributes to routers, such as border routers, along the TE-LSP obeying the set of required constraints whose path may have been computed by various means.” Vasseur ’879, 3:37-57.</p> <p>“Generally, a tunnel is a logical structure that encapsulates a packet (a header and data) of one protocol inside a data field of another protocol packet with a new header. In this manner, the encapsulated data may be transmitted through networks that it would otherwise not be capable of traversing. More importantly, a tunnel creates a transparent virtual network link between two network nodes that is generally unaffected by physical network links or devices (i.e., the physical network links or devices merely forward the encapsulated packet based on the new header). While one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur ’879, 3:58-4:3.</p> <p>“Occasionally, a network element (e.g., a node or link) will fail, causing redirection of the traffic that originally traversed the failed network element to other network elements that bypass the failure. Generally, notice of this failure is relayed to the nodes in the network through an advertisement of the new network topology, e.g., an IGP or BGP Advertisement, and routing tables are updated to avoid the failure accordingly. Reconfiguring a network in response to a network element failure using, e.g., pure IP rerouting, can be time consuming. Many recovery techniques, however, are available to provide fast recovery and/or network configuration in the event of a network element failure, including, inter alia, “Fast Reroute”, e.g., MPLS TE Fast Reroute. An example of MPLS TE Fast Reroute is described in Pan, et al., Fast Reroute Extensions to RSVP-TE for LSP Tunnels, RFC 4090, May 2005, which is hereby incorporated by reference as though fully set forth herein.” Vasseur ’879, 4:4-21.</p>

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		<p>“Fast Reroute (or FRR) has been widely deployed to protect against network element failures, where “backup tunnels” are created to bypass one or more protected network elements (e.g., links, shared risk link groups (SRLGs), and nodes). When the network element fails, traffic is quickly diverted (“Fast Rerouted”) over a backup tunnel to bypass the failed element, or more particularly, in the case of MPLS, a set of primary TE-LSPs (tunnels) is quickly diverted. Specifically, the point of local repair (PLR) node configured to reroute the traffic inserts (“pushes”) a new label for the backup tunnel, and the traffic is diverted accordingly. Once the failed element is bypassed, the backup tunnel label is removed (“popped”), and the traffic is routed along the original path according to the next label (e.g., that of the original TE-LSP). Notably, the backup tunnel, in addition to bypassing the failed element along a protected primary TE-LSP, also intersects the primary TE-LSP, i.e., it begins and ends at nodes along the protected primary TE-LSP.” Vasseur '879, 4:22-39.</p> <p>“To offer maximum protection, e.g., guaranteed bandwidth, during Fast Reroute, backup tunnels may reserve a configurable amount of bandwidth to ensure that Fast Rerouted traffic from the primary tunnel has a reserved path to follow. For example, the bandwidth reserved for the primary tunnel may also be reserved on the backup tunnel. While this approach provides maximum protection, it also requires a non-negligible amount of network resources (e.g., capacity/bandwidth) and may increase operational complexity.” Vasseur '879, 4:40-48.</p> <p>“Certain techniques are available to efficiently minimize the amount of resources required by the establishment and maintenance of the backup tunnels for Fast Reroute. One such technique is to create zero-bandwidth (“0-BW”) backup tunnels (i.e., tunnels that reserve no bandwidth) to protect non-0-BW primary tunnels. This “best effort” approach does not guarantee that the path followed by the backup tunnel will have enough bandwidth to support the diverted primary tunnel at the time of failure without QoS degradation, however in many situations the path quality of the backup tunnel is sufficient. For instance, if the network is not overly congested, or the backup tunnel follows a non-congested path, there may be enough available bandwidth along the backup tunnel to support the newly rerouted traffic. Also, because primary tunnels often reserve bandwidth in response to “peak” traffic utilization, the amount of traffic over the primary tunnel at the time of failure may be far less than the reserved bandwidth (e.g., at “off-peak” times). Notably, those skilled in the art will understand that in</p>

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		<p>the absence of the above exceptions, a 0-BW backup tunnel may have unacceptable bandwidth (e.g., affecting path quality) to support the diverted traffic.” Vasseur ’879, 4:49-5:2.</p> <p>“Currently, head-end nodes (LSRs) may be configured to systematically reroute the primary tunnels affected by the network element failure (e.g., diverted primary tunnels), especially in the case with 0-BW backup tunnels, such as, e.g., by reestablishing a new primary tunnel that follows a path excluding the failed network element. In particular, 0-BW backup tunnels represent a best effort attempt to allow the head-end node to more gracefully reestablish the primary tunnel in response to a failure, since the backup tunnels may not be able to support the diverted traffic without QoS degradation. The systematic reestablishing may potentially result in the reestablishment of a large number of primary tunnels (e.g., up to 3000 for a single network element failure in today’s networks). Notably, reestablishing diverted primary tunnels may be undesirable for the network, such as by creating traffic churn, jitter, control plane overloads, etc., as will be understood by those skilled in the art. However, as noted above, there are situations where the backup tunnel may provide acceptable bandwidth, at least, for example, for a period of time (e.g., possibly short) until the failed network element is restored. In these situations, then, it may have been unnecessary to reestablish the diverted primary tunnels. There remains a need, therefore, for a technique that dynamically determines whether to reestablish a diverted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur ’879, 5:3-28.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 5:32-47.</p>

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		<p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as Resource ReSerVation Protocol (RSVP) Traffic Engineering (TE) signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or type/length/value (TLV) encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object of the RSVP messages to convey the path quality notification.” Vasseur '879, 5:48-58.</p> <p>“In accordance with one aspect of the present invention, the head-end node requests the establishment of the primary tunnel (e.g., a TE-Label Switched Path, TE-LSP), along with a request for Fast Reroute protection of one or more network elements (e.g., with zero-bandwidth, 0-BW backup tunnels) at a PLR node. In addition, the head-end node may include a request for backup tunnel path quality notification, such as, e.g., through the use of the novel Feedback sub-object. The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and sends an error message (e.g., an RSVP PathErr) to the head end node indicating the “Fast Rerouting” of the primary tunnel.” Vasseur '879, 5:59-6:6.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, the PLR node may collect metrics/statistics (e.g., packet drops, path cost, jitter, etc.) of the primary and/or backup tunnels. Once the primary tunnel is Fast Rerouted, the PLR node continues to collect metrics of the backup tunnel, and may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. Notably, the PLR node may be configured to send path quality notifications to the head-end node once, continually, periodically, in response to configurable changes in path quality, etc. Also, as in the case where multiple primary tunnels are Fast Rerouted, the path quality notification may include an indication of which Fast Rerouted primary tunnels in particular have been effected by the changed path quality.” Vasseur '879, 6:7-23.</p>

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		<p>“In accordance with yet another aspect of the present invention, upon receiving the error message (PathErr), the head-end node may wait for the path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. Notably, in the event the head-end node does not receive a path quality notification for the backup tunnel (e.g., within a configurable time limit), the head-end node may attempt to reestablish the new primary tunnel. Moreover, where the head-end node has multiple primary tunnels being Fast Rerouted, a configurable subset of the primary tunnels may be reestablished, e.g., to reduce congestion of the backup tunnels, and/or to limit the number of reestablished primary tunnels within a given period of time.” Vasseur '879, 6:24-43.</p> <p>“Advantageously, the novel technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. By providing the head-end node of the primary tunnel with path quality feedback of the backup tunnel, the novel technique avoids reestablishing a potentially large number of tunnels over one or more alternate paths after a failure (and Fast Reroute) if the backup tunnels have acceptable path quality. In particular, the backup tunnels, e.g., 0-BW backup tunnels, may not be congested or subsequently burdened by the Fast Rerouted traffic of the primary tunnel. Also, the failed network element (thus the primary tunnel) may be quickly restored; therefore by not reestablishing the primary tunnel, network jitter, churn, etc., may be avoided. Further, the dynamic nature of the novel technique alleviates the need for cumbersome manual configuration.” Vasseur '879, 6:44-59.</p> <p>“FIG. 3 is schematic block diagram of an exemplary signaling (RSVP) message that may be advantageously used with the present invention.” Vasseur '879, 7:6-8.</p> <p>“FIG. 5 is a schematic block diagram of the computer network in FIG. 1 showing Fast Reroute protection of a primary tunnel using a backup tunnel in accordance with the present invention.” Vasseur '879, 7:12-15.</p>

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		<p>“FIG. 6 is a schematic block diagram of the computer network in FIG. 5 showing an unacceptable backup tunnel path quality and resultant reestablishing of the primary tunnel in accordance with the present invention.” Vasseur '879, 7:16-19.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention.” Vasseur '879, 7:20-24.</p> <p>“FIG. 1 is a schematic block diagram of an exemplary computer network 100 comprising a plurality of nodes A-F, such as routers or other network devices, interconnected as shown. The nodes may be a part of one or more autonomous systems, routing domains, or other networks or subnetworks. For instance, routers A and E may be provider edge (PE) devices of a provider network, (e.g., a service provider network) that are interconnected to one or more customer networks through customer edge (CE) devices (not shown, while the remaining nodes B-D and F may be core provider (P) devices, as will be understood by those skilled in the art. Those skilled in the art will also understand that the nodes A-F may be any nodes within any arrangement of computer networks, and that the view shown herein is merely an example. For example, the nodes may be configured as connections to/from one or more virtual private networks (VPNs), as will be understood by those skilled in the art. These examples are merely representative. Those skilled in the art will understand that any number of routers, nodes, links, etc. may be used in the computer network 100 and connected in a variety of ways, and that the view shown herein is for simplicity.” Vasseur '879, 7:29-49.</p> <p>“Data packets may be exchanged among the computer network 100 using predefined network communication protocols such as the Transmission Control Protocol/Internet Protocol (TCP/IP), User Datagram Protocol (UDP), Asynchronous Transfer Mode (ATM) protocol, Frame Relay protocol, Internet Packet Exchange (IPX) protocol, etc. Routing information may be distributed among the routers of the computer network using predetermined Interior Gateway Protocols (IGPs), such as conventional distance-vector protocols or, illustratively, link-state protocols, through the use of IGP Advertisements.” Vasseur '879, 7:50-60.</p>

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		<p>“FIG. 2 is a schematic block diagram of an exemplary router 200 that may be advantageously used with the present invention, e.g., as an edge router or a core router. The router comprises a plurality of network interfaces 210, a processor 220, and a memory 240 interconnected by a system bus 250. The network interfaces 210 contain the mechanical, electrical and signaling circuitry for communicating data over physical links coupled to the network 100. The network interfaces may be configured to transmit and/or receive data using a variety of different communication protocols, including, inter alia, TCP/IP, UDP, ATM, synchronous optical networks (SONET), wireless protocols, Frame Relay, Ethernet, Fiber Distributed Data Interface (FDDI), etc.” Vasseur '879, 7:61-8:6.</p> <p>“The memory 240 comprises a plurality of storage locations that are addressable by the processor 220 and the network interfaces 210 for storing software programs and data structures associated with the present invention. The processor 220 may comprise necessary elements or logic adapted to execute the software programs and manipulate the data structures. A router operating system 242 (e.g., the Internetworking Operating System, or IOS™, of Cisco Systems, Inc.), portions of which is typically resident in memory 240 and executed by the processor, functionally organizes the router by, inter alia, invoking network operations in support of software processes and/or services executing on the router. These software processes and/or services may comprise routing services 247, Traffic Engineering (TE) services 244, and RSVP services 249. It will be apparent to those skilled in the art that other processor and memory means, including various computer-readable media, may be used to store and execute program instructions pertaining to the inventive technique described herein.” Vasseur '879, 8:7-26.</p> <p>“Routing services 247 contain computer executable instructions executed by processor 220 to perform functions provided by one or more routing protocols, such as IGP (e.g., OSPF and IS-IS), IP, BGP, etc. These functions may be configured to manage a forwarding information database (not shown) containing, e.g., data used to make forwarding decisions. Routing services 247 may also perform functions related to virtual routing protocols, such as maintaining VRF instances (not shown) as will be understood by those skilled in the art.” Vasseur '879, 8:27-36.</p>

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		<p>“RSVP services 249 contain computer executable instructions for implementing RSVP and processing RSVP messages in accordance with the present invention. RSVP is described in RFC 2205, entitled Resource ReSerVation Protocol (RSVP), and in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels, both as incorporated above.” Vasseur '879, 8:37-42.</p> <p>“TE services 244 contain computer executable instructions for operating TE functions in accordance with the present invention. Examples of Traffic Engineering are described in RFC 3209, RFC 3784, and RFC 3630 as incorporated above, and in RFC 3473, entitled, Generalized Multi-Protocol Label Switching (GMPLS) Signaling Resource ReSerVation Protocol-Traffic Engineering (RSVP-TE) Extensions dated January 2003, which is hereby incorporated by reference in its entirety. A TE database (TED, not shown) may be illustratively resident in memory 240 and used to store TE information provided by the routing protocols, such as IGP, BGP, and/or RSVP (with TE extensions, e.g., as described herein), including, inter alia, path quality information as described herein. The TED may be illustratively maintained and managed by TE services 244.” Vasseur '879, 8:43-57.</p> <p>“Changes in the network topology may be communicated among routers 200 using a link-state protocol, such as the conventional OSPF and IS-IS protocols. Suppose, for example, that a communication link fails or a cost value associated with a network node changes. Once the change in the network's state is detected by one of the routers, that router may flood an IGP Advertisement communicating the change to the other routers in the network. In this manner, each of the routers eventually “converges” to an identical view of the network topology.” Vasseur '879, 8:58-67.</p> <p>“In one embodiment, the routers described herein are IP routers that implement Multi-Protocol Label Switching (MPLS) and operate as label switched routers (LSRs). In one simple MPLS scenario, at an ingress to a network, a label is assigned to each incoming packet based on its forwarding equivalence class before forwarding the packet to a next-hop router. At each router, a forwarding selection and a new substitute label are determined by using the label found in the incoming packet as a reference to a label forwarding table that includes this information. At the network egress, a forwarding decision is made based on the incoming label</p>

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		<p>but optionally no label is included when the packet is sent on to the next hop. In some network configurations, one hop prior to the network egress, a penultimate hop popping (PHP) operation may be performed. Particularly, because the hop prior to the network egress (the penultimate hop) is attached to the network egress, the label is no longer needed to assure that the traffic follows a particular path to the network egress. As such, the PHP-enabled device “pops” the labels from the traffic before forwarding the traffic to the network egress, e.g., using conventional or native (IP) routing, thereby alleviating the task of removing the labels at the network egress.” Vasseur '879, 9:1-23.</p> <p>“The paths taken by packets that traverse the network in this manner are referred to as label switched paths (LSPs) or Traffic Engineering (TE)-LSPs. An example TE-LSP is shown as the thick line and arrow (T1) between a head-end node (router A) and a tailend node (router E) in FIG. 1. Establishment of a TE-LSP requires computation of a path, signaling along the path, and modification of forwarding tables along the path. MPLS TE establishes LSPs that have guaranteed bandwidth under certain conditions. Illustratively, the TE-LSPs may be signaled through the use of the RSVP protocol (with Traffic Engineering extensions), and in particular, RSVP TE signaling messages. Notably, when incorporating the use of PCEs (described below), the path computation request (and response) between PCC and PCE can be exchanged in accordance with a protocol specified in Vasseur, et al., Path Computation Element (PCE) Communication Protocol (PCEP)—Version 1—<draft-vasseur-pce-pcep-02.txt>, Internet Draft, September 2005, the contents of which are hereby incorporated by reference in its entirety. It should be understood that the use of RSVP or PCEP serves only as an example, and that other communication protocols may be used in accordance with the present invention.” Vasseur '879, 9:24-45.</p> <p>“In accordance with RSVP, to request a data flow (TE-LSP) between a sender and a receiver, the sender may send an RSVP path request (Path) message downstream to the receiver along a path (e.g., a unicast route) to identify the sender and indicate e.g., bandwidth needed to accommodate the data flow, along with other attributes of the TE-LSP. The Path message may contain various information about the data flow including, e.g., traffic characteristics of the data flow. Also in accordance with the RSVP, a receiver establishes the TE-LSP between the sender and receiver by responding to the sender's Path message with a reservation request</p>

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		<p>(Resv) message. The reservation request message travels upstream hop-by-hop along the flow from the receiver to the sender. The reservation request message contains information that is used by intermediate nodes along the flow to reserve resources for the data flow between the sender and the receiver, to confirm the attributes of the TE-LSP, and provide a TE-LSP label. If an intermediate node in the path between the sender and receiver acquires a Path message or Resv message for a new or established reservation (TE-LSP) and encounters an error (e.g., insufficient resources, failed network element, etc.), the intermediate node generates and forwards a path or reservation error (PathErr or ResvErr, hereinafter Error) message to the sender or receiver, respectively.” Vasseur '879, 9:46-10:2.</p> <p>“FIG. 3 is a schematic block diagram of portions of a signaling message 300 (e.g., RSVP message, such as Path, Resv or Error) that may be advantageously used with the present invention. Message 300 contains, inter alia, a common header 310 and one or more signaling protocol specific objects 320, such as an LSP-ATTRIBUTE object 330. The common header 310 may comprise a source address 312 and destination address 314, denoting the origination and requested termination of the message 300. Protocol specific objects 320 contain objects necessary for each type of message 300 (e.g., Path, Resv, Error, etc.). For instance, a Path message may have a sender template object, Tspec object, Previous-hop object, etc. The LSP-ATTRIBUTE object 330, for instance, may be used to signal attributes and/or information regarding an LSP (tunnel). To communicate this information, LSP-ATTRIBUTE object 330 (as well as specific objects 320) may include various type/length/value (TLV) encoding formats and/or flags, as will be understood by those skilled in the art. An example of an LSP-ATTRIBUTE object is further described in Farrel, et al., Encoding of Attributes for Multiprotocol Label Switching (MPLS) Label Switched Path (LSP) Establishment Using RSVP-TE <draft-ietf-mpls-rsvpte-attributes-05.txt>, Internet Draft, May 2005, which is hereby incorporated by reference as though fully set forth herein. A Resv message, on the other hand, may have specific objects 320 for a label object, session object, filter spec object, etc., in addition to the LSP-ATTRIBUTE object 330. Error messages 300 (e.g., PathErr or ResvErr), may also have specific objects 320, such as for defining the type of error, etc.” Vasseur '879, 10:3-31.</p>

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		<p>“It should be noted that in accordance with RSVP signaling, the state of the TE-LSP is refreshed on a timed interval, e.g., every thirty seconds, in which RSVP Path and Resv messages are exchanged. This timed interval is configurable by a system administrator. Moreover, various methods understood by those skilled in the art may be utilized to protect against route record objects (RROs) contained in signaling messages for a TE-LSP in the event security/privacy is desired. Such RRO filtering prevents a head-end node of the TE-LSP from learning of the nodes along the TE-LSP, i.e., nodes within the provider network.” Vasseur '879, 10:4-42.</p> <p>“Although the illustrative embodiment described herein is directed to MPLS, it should also be noted that the present invention may advantageously apply to Generalized MPLS (GMPLS), which pertains not only to packet and cell-based networks, but also to Time Division Multiplexed (TDM) and optical networks. GMPLS is well known and described in RFC 3945, entitled Generalized Multi-Protocol Label Switching (GMPLS) Architecture, dated October 2004, and RFC 3946, entitled Generalized Multi-Protocol Label Switching (GMPLS) Extensions for Synchronous Optical Network (SONET) and Synchronous Digital Hierarchy (SDH) Control, dated October 2004, the contents of both of which are hereby incorporated by reference in their entirety.” Vasseur '879, 10:43-55.</p> <p>“To obviate delays associated with updating routing tables when attempting to avoid a failed network element (i.e., during convergence), some networks have employed MPLS TE Fast Reroute (FRR). MPLS Fast Reroute is a technique that may be used to quickly divert (“Fast Reroute”) traffic around failed network elements in a TE-LSP. MPLS Fast Reroute is further described, for example, by Fast Reroute Extensions to RSVP-TE for LSP Tunnels, as incorporated by reference above. According to the technique, one or more network elements (e.g. links or nodes) in a network are protected by backup tunnels following an alternate path. If a failure occurs on a protected link or node, TE-LSPs (and consequently the traffic that they carry) are locally diverted onto an appropriate alternate path (e.g., a “backup tunnel”) by the node immediately upstream from the failure. The backup tunnel acts as a Fast Reroute path for the primary TE-LSP and obviates delays associated with other measures, such as tearing down the primary TE-LSP after having gracefully diverted the TE-LSPs affected by the failure, should an alternate path around the failed network element exist. In the event of a</p>

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		<p>failure of a protected element the head-end node of the backup tunnel (or a “point of local repair,” PLR node) may quickly begin diverting traffic over the backup tunnel with minimal disruption to traffic flow. Those skilled in the art will understand that MPLS Fast Reroute is one example of link or node failure protection, and that other known correction mechanisms may be used in accordance with the present invention. As mentioned above, however, the head-end node of the Fast Rerouted primary tunnel may attempt to reestablish the primary tunnel in response to learning of the protected element failure, particularly in the case where the backup tunnel is a zero-bandwidth (0-BW) tunnel. The attempt to reestablish the primary tunnel has conventionally been a systematic response to Fast Rerouting (diverting) of the primary tunnel, regardless of the path quality of the backup tunnel.” Vasseur '879, 10:56-11:23.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, 11:24-39.</p> <p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as RSVP TE signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or TLV encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object 330 of the RSVP messages 300 to convey the path quality notification.” Vasseur '879, 11:40-49.</p>

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		<p>“FIG. 4 is a schematic block diagram illustrating the format of an extension object (TLV) 400, such as a Fast Reroute Feedback object, that may be advantageously used with the present invention. The extension object (or sub-object) 400 is illustratively embodied as a TLV contained in an LSP-ATTRIBUTE object 330 of an RSVP message 300 and is extended to carry backup (and/or primary) tunnel path quality information. To that end, the “Feedback” object 400 is organized to include a Type field 405 containing a predetermined type value signifying the specific content of the object 400. The Length field 410 is a variable length value. The TLV encoded format may also comprise one or more non-ordered sub-TLVs 450 carried within the TLV “payload” (e.g. Value field 415), each having a Type field 455, Length field 460, and Value field 465. The fields of the TLV 400 and sub-TLV(s) 450 are used in a variety of manners, including as described herein, according to the present invention.” Vasseur '879, 11:59-12:8.</p> <p>“In accordance with one aspect of the present invention, the head-end node (e.g., router A) requests the establishment of the primary tunnel (e.g., T1), such as a TE-LSP. Notably, the head-end node may be a head-end node for multiple primary tunnels, as will be understood by those skilled in the art. Along with the primary tunnel establishment, the head-end node may also request Fast Reroute protection of one or more network elements (e.g., all intermediate network elements) at a PLR node (e.g., router B as shown). Note that each intermediate node along the primary tunnel may act as a PLR node, and that router B is shown merely for simplicity. Illustratively, the Fast Reroute protection may be embodied as one or more zero-bandwidth (0-BW) backup tunnels at the PLR node (e.g., BT1). Those skilled in the art will also understand that the PLR node may protect more than one primary tunnel originating at more than one corresponding head-end node (not shown).” Vasseur '879, 12:9-25.</p> <p>“The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. For example, various connectivity verification protocols, such as, e.g., Bidirectional Forwarding Detection (BFD), IGP “Hello” packets, BGP KEEPALIVE messages, etc., may be used to detect a failure of a network element, as will be understood by those skilled in the art. Furthermore, other lower-layer failure detection mechanisms (e.g. optical or SONET/SDH alarms) may be used to detect a network element failure. Once failure is detected, the PLR node diverts the</p>

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		<p>traffic onto the backup tunnel, and may send an error message (e.g., an RSVP PathErr 300, such as a conventional “tunnel locally repaired” message) to the head end node indicating the “Fast Rerouting” of the primary tunnel. FIG. 5 is a schematic block diagram of the computer network 100 in FIG. 1 showing Fast Reroute protection of the primary tunnel T1 (e.g., in response to a protected network element, router C, failure, indicated with an overlaid “X”) using a backup tunnel BT1 in accordance with the present invention. Traffic originally received at the PLR node (router B) over the primary tunnel is now diverted over the backup tunnel to a remerge point (router D) of the primary tunnel, as will be understood by those skilled in the art.” Vasseur '879, 12:42-65.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, each PLR node may collect metrics/statistics of the primary and/or backup tunnels. For instance, example metrics may comprise, inter alia, packet drops, path cost, jitter, delay, bandwidth, etc. The PLR node may collect the metrics through traffic monitoring, probes, independent calculations, and/or through cooperation with protected nodes of the primary tunnel (primary nodes) and nodes of the backup tunnel (backup nodes), e.g., transmitting path quality notifications. Once the primary tunnel is Fast Rerouted (i.e., diverted after failure of a protected network element), the PLR node continues to collect metrics of the backup tunnel. (Alternatively, metrics of the backup tunnel may be collected only after Fast Reroute, and not prior to Fast Reroute.)” Vasseur '879, 12:66-13:12.</p> <p>“For example, a path cost increase from the primary tunnel to the backup tunnel may be computed (and transmitted, below) by the PLR node prior to the failure (or during the failure while the primary TE-LSP is diverted onto the backup tunnel) using its own routing tables. The path cost increase may be calculated as a difference between the entire length (head-end node to tail-end node) of the primary and backup tunnels, or just the difference between the protected segment of the primary tunnel and the backup tunnel (PLR node to remerge point). Also, a jitter increase of the primary and backup tunnels, which may be generally described as a difference between inter-arrival of packets, may be monitored using various known techniques, such as, e.g., sending probe packets (probes) from the PLR node to the remerge point. For instance, probes may determine that packets arrive at the remerge point along the primary tunnel from the PLR node consistently, e.g., every 10 milliseconds (ms) (e.g., an</p>

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		<p>average value). After Fast Reroute, however, probes may determine that packets do not arrive at the remerge point along the backup tunnel from the PLR node consistently, e.g., one may arrive in 10 ms, another in 100 ms, another in 50 ms, etc. The non-constant rate of received packets (jitter) may be undesirable, e.g., in particular for voice over IP (VoIP) traffic, as will be understood by those skilled in the art.” Vasseur '879, 13:13-36.</p> <p>“As a further example, packet dropping may be monitored for the primary and backup tunnels prior to and after Fast Reroute (respectively). For instance, based on the tunnel label of the dropped packet, primary nodes and/or backup nodes may be able to distinguish which tunnel corresponds to the dropped packets. Each of the primary and/or backup nodes collect packet drop data, and periodically inform the PLR node of the number of dropped packets (e.g., through a corresponding Feedback object 400). In the case of a backup node, the PLR node receiving the notification may interpret the association of the backup tunnel label and the primary tunnel label to reference an appropriate primary tunnel. Those skilled in the art will understand that the above path quality metrics are merely examples, and that any other metrics/statistics useful for determining path quality of the backup tunnel may be used in accordance with the present invention (e.g., delay, bandwidth, etc.). Further, the path quality information may be measured and compared in a variety of manners, such as, e.g., as a difference between primary and backup tunnels before and after Fast Reroute, or simply the difference between the backup tunnel before and after Fast Reroute, etc.” Vasseur '879, 13:37-58.</p> <p>“Also after the primary tunnel is Fast Rerouted, the PLR node may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. For instance, the novel Feedback object 400 may include one or more sub-TLVs 450 corresponding to metrics/statistics, as described above. Notably, the path quality information pertaining to a particular metric/statistic may be transmitted as total values for interpretation by the head-end node (e.g., to determine the difference), or as PLR-node-computed differences (e.g., between the primary and backup tunnels, or before and after Fast Reroute). For instance, if the delay of the primary tunnel (along the protected segment) prior to Fast Reroute were 2 ms, and after Fast Reroute the delay of the backup tunnel were 5 ms, the notification may be configured to</p>

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		<p>include both values 2 ms and 5 ms, or instead simply the difference, i.e., an increase of 3 ms.” Vasseur '879, 13:59-14:8.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message 300 (PathErr), the head-end node may wait for at least one path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. Also, any number of metrics may be used in the determination, e.g., as configured by a system administrator. For example, using the metrics described above, a head-end node may be configured to reestablish the primary tunnel in response to i) a certain number of packet drops, ii) a percent increase in packet drops, iii) a number of packet drops and a percent increase in path cost, iv) a percent increase in path cost and a percent increase in jitter, etc. Those skilled in the art will understand that these are merely examples of possible path quality comparisons and reestablishment determinations, and that any comparisons to any metrics at any configurable changes may be used in accordance with the present invention.” Vasseur '879, 14:41-59.</p> <p>“If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. FIG. 6 is a schematic block diagram of the computer network 100 in FIG. 5 showing an unacceptable backup tunnel path quality (dotted line and arrow) and resultant reestablishment of the primary tunnel in accordance with the present invention. Those skilled in the art will understand that the reestablished primary tunnel may traverse one or more primary nodes (not shown), and that it may be computed specifically to avoid the failed network element and any network elements of the unacceptable backup tunnel. Those skilled in the art will also understand that the attempt to reestablish the primary tunnel may not be able to find an acceptable path, in which case the head-end node may continue to use the unacceptable backup tunnel or other unacceptable rerouted path.” Vasseur '879, 14:60-15:9.</p>

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		<p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention. The procedure 700 starts at step 705, and continues to step 710, where a head-end node (e.g., router A) establishes a primary tunnel (e.g., T1) to a destination tail-end node (e.g., router E). During or after establishment, the head-end node requests protection of the primary tunnel at step 715, and in response, PLR nodes along the primary tunnel (e.g., router B) create backup tunnels (e.g., BT1) to protect the primary tunnel in step 720. (Those skilled in the art will understand that backup tunnels around the protected network elements may already exist at the PLR node, and that “creating backup tunnels” in step 720 implies an association with pre-existing backup tunnels.) As mentioned above, these backup tunnels may illustratively be embodied as 0-BW backup tunnels. In accordance with the present invention, the head-end node may additionally request backup tunnel path quality notification from the PLR nodes in step 725, such as, e.g., through the use of empty corresponding Feedback objects in RSVP (Path) messages 300, as described above.” Vasseur '879, 15:37-58.</p> <p>“The procedure 700 continues to FIG. 7B (step “A”), where in step 750 the PLR node detecting the failure diverts (“Fast Reroutes”) the primary tunnel traffic to the backup tunnel and sends an error message (e.g., an RSVP (Error) message 300) to the head end node, e.g., a “tunnel locally repaired” message. The detecting PLR node continues to collect path quality information for the backup tunnel in step 755 and at step 760 determines whether to send the path quality notification to the head-end node. For example, as mentioned above, the PLR node may be configured to continually send notifications, or periodically, or in response to a configurable change in path quality, etc. Also as mentioned above, the PLR node may be configured to send either the actual path quality information or the change (difference) in path quality. (As further mentioned above, metrics of the backup tunnel may alternatively be collected only after Fast Reroute, and not prior to Fast Reroute.).” Vasseur '879, 16:4-20.</p> <p>“If the PLR node decides to send the notification in step 760, then the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification in step 765, e.g., based on one or more configurable thresholds, percentages, etc., as described above. If the backup tunnel is currently maintaining an acceptable quality for the</p>

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		<p>traffic flow in step 770, the head-end node may continue to utilize the backup tunnel, and the PLR node continues to collect path quality information in step 755 to detect any change in quality. Otherwise, if the backup tunnel quality is not acceptable in step 770, the head-end node may attempt to reestablish the primary tunnel in step 775. Notably, as mentioned above, if the PLR node has not sent any notification (step 760) within a configurable period of time in step 780, e.g., due to a backup tunnel failure, over-congestion, etc., then the head-end node may also attempt to reestablish the primary tunnel in step 775 accordingly. Moreover, as described above, in the event more than one primary tunnel is Fast Rerouted for the head-end node, various techniques to reestablish one or more of the primary tunnels may be used (e.g., as many tunnels as necessary, a configurable subset of tunnels, all tunnels, the congested tunnels, etc.). The procedure 700 ends in step 785.” Vasseur '879, 16:21-43.</p> <p>“While there has been shown and described an illustrative embodiment that dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network, it is to be understood that various other adaptations and modifications may be made within the spirit and scope of the present invention. For example, the invention has been shown and described herein using “Fast Reroute,” e.g., MPLS TE Fast Reroute (FRR). However, the invention in its broader sense is not so limited, and may, in fact, be used with other network element protection and failure correction mechanisms as will be understood by those skilled in the art. Moreover, while the above description describes performing the technique at the head-end node and PLR node, the invention may also be advantageously used with PCEs. In addition, while one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 16:63-17:13.</p> <p>Rustogi discloses:</p> <p>“An example method includes identifying a fault condition in a network, and evaluating pseudowires affected by the fault condition in order to make a determination as to whether an aggregate failure occurred in the network for a group of pseudowires. The method also includes communicating a group message indicating that the group of pseudowires is associated with the fault condition. The group message includes a group identification (ID),</p>

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		<p>which identifies the group of pseudowires, and the group message includes a pseudowire group label identifying an in-band aggregate channel. More specifically, the pseudowire group label can be applicable to static pseudowires. In more detailed embodiments, the group ID identifies the group of pseudowires that are associated with an attachment circuit, a label switched path, or a port. Internal mappings can be maintained such that a plurality of pseudowires is mapped to individual interfaces of network elements in the network.” Rustogi, Abstract.</p> <p>FIG. 1A</p> <p>Rustogi, FIG. 1A.</p>

No.	'821 Patent Claim 7	The Reference
		<pre> graph TD START([START]) --> 100[100 ~ A GIVEN NETWORK ELEMENT CAN IDENTIFY A FAULT CONDITION IT RECEIVES] 100 --> 102[102 ~ THE NETWORK ELEMENT EVALUATES PSEUDOWIRES IN ORDER TO DETERMINE WHETHER A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED] 102 --> 104[104 ~ IF ONLY A FEW PSEUDOWIRES ARE AFFECTED BY THE FAULT CONDITION, THE GROUPING PROTOCOL MAY OPTIONALY NOT BE USED, WHERE A MORE ROUTINE MESSAGING PROTOCOL COULD BE EMPLOYED] 104 --> 106[106 ~ IF A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED, THE GROUPING PROTOCOL IS EMPLOYED TO MINIMIZE THE MESSAGES THAT ARE SENT, RECEIVED, AND PROCESSED IN THE NETWORK] 106 --> 108[108 ~ IN THE CASE OF AN AGGREGATE FAILURE, AN AGGREGATE CHANNEL CAN BE USED TO OFFER APPROPRIATE GROUP MESSAGING. THE INDIVIDUAL MESSAGES THAT CONVEY GROUP IDENTIFICATIONS (IDS) CAN QUICKLY SIGNIFY THE FAULT CONDITION TO NETWORK PEERS] 108 --> END([END]) </pre> <p>FIG. 1B</p> <p>Rustogi, FIG. 1B.</p>

No.	'821 Patent Claim 7	The Reference
		<p>FIG. 2</p> <p>Rustogi, FIG. 2.</p> <p>Detailed description of FIG. 2:</p> <ul style="list-style-type: none"> Nodes: There are six nodes labeled: SWITCHING PE 1, TERMINATING PE 1, SWITCHING PE 2, TERMINATING PE 2, TERMINATING PE 3, and SWITCHING PE 3. Connections: <ul style="list-style-type: none"> SWITCHING PE 1 connects to TERMINATING PE 1 via two paths: one labeled "PSEUDOWIRE GROUP LABEL A" (Group Identification A) and another labeled "PSEUDOWIRE GROUP LABEL C" (Group Identification C). TERMINATING PE 1 connects to TERMINATING PE 2 via two paths: one labeled "PSEUDOWIRE GROUP LABEL A" (Group Identification A) and another labeled "PSEUDOWIRE GROUP LABEL D" (Group Identification D). TERMINATING PE 2 connects to TERMINATING PE 3 via two paths: one labeled "PSEUDOWIRE GROUP LABEL C" (Group Identification C) and another labeled "PSEUDOWIRE GROUP LABEL D" (Group Identification D). SWITCHING PE 2 connects to TERMINATING PE 3 via two paths: one labeled "PSEUDOWIRE GROUP LABEL D" (Group Identification D) and another labeled "PSEUDOWIRE GROUP LABEL E" (Group Identification E). Labels: <ul style="list-style-type: none"> Group ID A → C, D Group ID B → E Group Identification C Group Identification D Group Identification E PSEUDOWIRE GROUP LABEL A PSEUDOWIRE GROUP LABEL C PSEUDOWIRE GROUP LABEL D PSEUDOWIRE GROUP LABEL E Failure Indicators: <ul style="list-style-type: none"> INTERFACE A AND B FAIL (near TERMINATING PE 1)

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		<p>The diagram shows a network topology with three PE nodes: TERMINATING PE 1 (62), SWITCHING PE 1 (68), and TERMINATING PE 2 (64). PE 1 has two interfaces, A and B, with a label 1 indicating "INTERFACE A AND B FAIL". PE 2 has one interface, C, with a label 3 indicating "PSEUDOWIRE GROUP LABEL C". PE 3 has one interface, D, with a label 66. The connections between them are labeled with pseudowire group labels A through E. Labels A and B connect PE 1 to PE 2, with labels 2 indicating "PSEUDOWIRE GROUP LABEL A" and "PSEUDOWIRE GROUP LABEL B". Labels C and D connect PE 1 to PE 3, with labels 2 indicating "PSEUDOWIRE GROUP LABEL C" and "PSEUDOWIRE GROUP LABEL D". Label E connects PE 2 to PE 3, with a label 2 indicating "PSEUDOWIRE GROUP LABEL E". A callout box at the top right indicates "GROUP ID A → C, D" and "GROUP ID B → E". Another callout box indicates "GROUP ID C → PSEUDOWIRE GROUP LABEL C" and "GROUP ID D → PSEUDOWIRE GROUP LABEL D". A third callout box indicates "GROUP ID E → PSEUDOWIRE GROUP LABEL E".</p>

No.	'821 Patent Claim 7	The Reference
		<p>FIG. 4</p> <p>Rustogi, FIG. 4.</p>

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		<p>FIG. 5</p> <p>80</p> <p>Rustogi, FIG. 5.</p>

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		<p style="text-align: center;">74</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>AC PORT</th><th>LSP</th><th>PW GROUP</th><th>PW GROUP LABEL IDENTIFICATION</th></tr> </thead> <tbody> <tr> <td>INTERFACE A</td><td>LSP 1</td><td>A</td><td>A</td></tr> <tr> <td>INTERFACE B</td><td>LSP 1</td><td>B</td><td>B</td></tr> <tr> <td>REMOTE INTERFACE C ON TERMINATING PE 2</td><td>LSP 2</td><td>F</td><td>F</td></tr> <tr> <td>REMOTE INTERFACE D ON TERMINATING PE 3</td><td>LSP 2</td><td>G, H</td><td>G, H</td></tr> </tbody> </table> <p style="text-align: center;">FIG. 6</p> <p>Rustogi, FIG. 6.</p> <p>“The field of communications has become increasingly important in today's society. In particular, the ability to quickly and to effectively provision connections presents a significant challenge to component manufacturers, system designers, and network operators. Multiprotocol Label Switching (MPLS) is a mechanism in telecommunications networks that carries data from one network node to the next. Layer 2 services (such as Frame Relay, Asynchronous Transfer Mode, and Ethernet) can be emulated over an MPLS backbone by encapsulating the Layer 2 Protocol Data Units (PDUs) and transmitting them over pseudowires. Protocols exist for establishing and maintaining the pseudowires. Certain issues have arisen in pseudowire scenarios, where faults are detected in the network.” Rustogi, ¶ [0002].</p> <p>“FIG. 1A is a simplified block diagram of a communication system for providing pseudowire group labels in a network environment in accordance with one embodiment of the present disclosure.” Rustogi, ¶ [0004].</p>	AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION	INTERFACE A	LSP 1	A	A	INTERFACE B	LSP 1	B	B	REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F	REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H
AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION																			
INTERFACE A	LSP 1	A	A																			
INTERFACE B	LSP 1	B	B																			
REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F																			
REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H																			

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		<p>“FIG. 1B is a simplified flowchart depicting one possible, generic operational flow associated with the communication system.” Rustogi, ¶ [0005].</p> <p>“FIG. 2 is a simplified block diagram of an example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0006].</p> <p>“FIG. 3 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0007].</p> <p>“FIG. 4 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0008].</p> <p>“FIG. 5 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0009].</p> <p>“FIG. 6 is a simplified table of an example set of pseudowire group provisioning parameters in accordance with one embodiment.” Rustogi, ¶ [0010].</p> <p>“FIG. 1A is a simplified block diagram of a communication system 10 for providing pseudowire group labels in accordance with one example of the present disclosure. FIG. 1A includes a customer edge 1 (CE1) 12, a CE2 14, and a CE3 16, where a number of faults 18 are shown as propagating in the network. Typically, when an error or a failure occurs in the network (e.g., an interface failure, a pulled cable, a switch failure, hardware/software failures generally, etc.), messages are sent to various network devices in order to inform them of these fault conditions. Faults 18 of FIG. 1A are indicative of such messages, where the underlying fault condition (being signaled by the messages) can occur virtually anywhere in a network (e.g., in a customer edge, in provider equipment, etc.). FIG. 1A also includes a terminating provider equipment 1 (TPE1) 20, a TPE2 22, a TPE3 24, a switching provider edge 1 (SPE1) 30, and a SPE2 32. In one particular example implementation, the TPEs and SPEs of FIG. 1A are switches that are configured to exchange data in a network environment.” Rustogi, ¶ [0012].</p>

No.	'821 Patent Claim 7	The Reference
		<p>“SPE1 30 may include a pseudowire (PW) group module 54 a, a processor 56 a, and a memory element 58 a. In a similar fashion, TPE2 22 may include a pseudowire group module 54 b, a processor 56 b, and a memory element 58 b. FIG. 1A also includes a number of static pseudowires 42, 44, and 46. A set of static/dynamic pseudowires 48, 50 is also provided. Note that the group labeling protocol discussed herein can be executed between individual SPEs, TPEs, or between any combinations of these elements.” Rustogi, ¶ [0013].</p> <p>“In one particular arrangement, communication system 10 is provided in conjunction with a Layer-2 virtual private networks (L2VPN)/operation, administration, and maintenance (OAM) L2VPN/OAM framework. The OAM framework is intended to provide OAM layering across L2VPN services, pseudowires, and packet switched network (PSN) tunnels. Communication system 10 may include any suitable networking protocol or arrangement that provides a communicative platform for communication system 10. Thus, communication system 10 may include a configuration capable of transmission control protocol/internet protocol (TCP/IP) communications for the transmission and/or reception of packets in a network. Communication system 10 may also operate in conjunction with a user datagram protocol/IP (UDP/IP) or any other suitable protocol where appropriate and based on particular needs.” Rustogi, ¶ [0014].</p> <p>“Failure detection and failure notification for static pseudowires is inadequate, where sluggish signaling can result in poor scalability for failover performance. Typically, static pseudowires are manually configured at respective endpoints, where control channels are absent for providing group level signaling messages. Aggregate channels are significant tools for providing suitable scalability in the network, but no such aggregate channel exists for static pseudowires. For dynamic pseudowires, such an aggregate channel may be present in the form of a label distribution protocol (LDP) directed session. However, no such protocol exists for static pseudowire configurations such that an in-band aggregate channel would be available for static pseudowires.” Rustogi, ¶ [0016].</p> <p>“Communication system 10 can address the aforementioned issues (and others) by offering a pseudowire group label that can represent an aggregate channel for groups of static pseudowires. The aggregate channel of communication system 10 can allow for improved</p>

No.	'821 Patent Claim 7	The Reference
		<p>scalability of failover performance. In accordance with one potential configuration of communication system 10, a pseudowire group label is representative of a group of static pseudowires transported over a label switched path (LSP). The pseudowire group label can identify the aggregate channel, which captures the hierarchy relevant to OAM mechanisms. Additionally, the groups represented by the group identification (ID) can be mutually exclusive, where a pseudowire is part of only one group. In other embodiments, a pseudowire can be part of multiple groups, or be configured in any other suitable manner based on particular network arrangements.” Rustogi, ¶ [0017].</p> <p>“During operations, and with brief reference to FIG. 1B, a given network element can identify a fault condition it receives (at step 100) and, subsequently, evaluate pseudowires in order to determine whether a sufficient number of pseudowires have been affected. This is reflected by step 102. If only a few pseudowires are affected by the fault condition, the grouping protocol outlined herein may have only nominal value, where there could be an option to simply communicate the fault condition in a more routine manner, as outlined in step 104. However, if a sufficient number of pseudowires have been affected, the grouping protocol outlined herein can be employed to minimize the messages that are sent, received, and processed in the network. This is reflected as step 106. Note that the determination (as to whether a sufficient number of pseudowires have been impacted by the fault condition) can involve accessing internal tables such that a quick mapping can occur to determine if an aggregate failure has occurred. As used herein, the term ‘aggregate failure’ simply connotes that a sufficient number of pseudowires have experienced the fault condition such that an aggregate channel can be employed to offer appropriate group messaging. For the aggregate failure condition, the individual messages that convey Group identifications (IDs) can quickly signify the fault condition to network peers, as shown in step 108.” Rustogi, ¶ [0018].</p> <p>“In specific regards to OAM mechanisms, OAM messages typically result from common failures in the network. These fault conditions can be aggregated such that they are signaled as a single message, which could represent a group of failed pseudowires (as opposed to sending individual messages for each failed pseudowire). Hence, a single message could be sent to represent all the relevant OAM messages propagating in communication system 10. The group label that propagates in communication system 10 provides an architecture with a</p>

No.	'821 Patent Claim 7	The Reference
		<p>significant level of aggregation for failed pseudowires (i.e., pseudowires being affected by a given fault condition), particularly for OAM messaging. Moreover, the in-band aggregate channel of communication system 10 is based (at least in part) on the evolving trends of OAM mechanisms, which are required to be fast, responsive, and capable of being implemented in hardware or software. Additionally, in-band OAM protocols are a better measure of the path availability.” Rustogi, ¶ [0020].</p> <p>“In operation of one example implementation, a group label can represent the tuple <attachment circuit (AC) port level grouping, LSP>. This could signify that all pseudowires on an AC port (sought for aggregation) traverse a given LSP. Multiple pseudowire groups can exist within an LSP. Similarly, pseudowires on the same AC port (that traverse a different LSP) can use a different pseudowire group label. Alternatively, an administrator may seek to employ a one-to-one mapping between an LSP and a group label. If that were the case, then only one pseudowire group would exist within an LSP. In scenarios where there is no LSP label in the packet (e.g., due to penultimate hop popping), the pseudowire group label can provide the hierarchy that is appropriate.” Rustogi, ¶ [0021].</p> <p>“In one particular example, the group level pseudowire OAM message can be sent with the following label stack: Explicit/Implicit LSP Label+pseudowire group Label+GAL+ACH+pseudowire OAM with grouping TLV (where GAL=Generic Associated Channel Label, ACH=Associated Channel Header, TLV=Type-Length-Value). If there are multiple LSPs, then one group label can be provisioned for each LSP (for each pseudowire group), where per group messages can be sent on each LSP. The group label does not necessarily have a one-to-one mapping to the grouping of pseudowires implied by the Group ID in the grouping TLV. Note also that the group-based aggregate channel is applicable to static pseudowires, as well as for dynamic pseudowires in certain applications.” Rustogi, ¶ [0022].</p> <p>“As discussed herein, the aggregate channel of communication system 10 can be configured in various ways. For example, and with regards to a first option, a separate label may simply be used to identify a pseudowire group within an LSP. The association of an OAM message and a pseudowire group is straightforward. There could potentially be multiple pseudowire</p>

No.	'821 Patent Claim 7	The Reference
		<p>group labels per LSP. As a second option, one group label can be used to identify a common pseudowire group channel on the LSP. In this implementation, one pseudowire group label is provided per LSP. The OAM message association to a pseudowire group is not as simple as the first option. As a third option, one pseudowire is simply designated to convey grouping information (e.g., without using a group label). In this case, there is no need for a pseudowire group label. Again, the OAM message association to a pseudowire group is not as simple as the first option.” Rustogi, ¶ [0023].</p> <p>“Any combination of formatting (for the Group ID and the pseudowire group label) can be used in the group message to be communicated in the network. In one example, only one of these elements is communicated when an aggregate fault condition is detected, or these elements can be combined into a single unique identifier. In the most generic example, a group message would at least include the Group ID (identifying the pseudowires affected by the fault) and a pseudowire group label (identifying an aggregate channel for communicating the group message). In this generic sense, a pipe (the Group ID) within a pipe (the pseudowire group label) is being identified, where the group message is identifying both elements during an aggregate fault condition. Operational details of communication system 10 are described below with reference to FIGS. 2-6. Note that before turning to additional example flows and example embodiments of the present disclosure, a brief overview of the infrastructure of communication system 10 is provided.” Rustogi, ¶ [0024].</p> <p>“CE1 12, CE2 14, and CE3 16 represent devices, infrastructure, equipment, clients, or customers seeking to initiate a data session in communication system 10. These elements may comprise a digital subscriber line access multiplexer (DSLAM), a router, a personal computer, a server, a switch, and/or other devices associated with data propagation. Further, these elements may sit behind, or in front of, one or more of these identified devices. The term ‘CE’ may be inclusive of the devices identified above (e.g., a DSLAM, a switch, etc.), as well as devices used to initiate a communication, such as a console, a proprietary endpoint, a telephone, a cellular telephone, a bridge, a computer, a personal digital assistant (PDA), a laptop or an electronic notebook, or any other device, component, element, or object capable of initiating voice, audio, media, or data exchanges within communication system 10. The customer element may also include any device that seeks to initiate a communication on behalf</p>

No.	'821 Patent Claim 7	The Reference
		<p>of another entity or element, such as a program, a database, or any other component, device, element, or object capable of initiating a voice, a video, text, or a data exchange within communication system 10. Data, as used herein in this document, refers to any type of video, numeric, voice, media, or script data, or any type of source or object code, or any other suitable information in any appropriate format that may be communicated from one point to another.” Rustogi, ¶ [0025].</p> <p>“SPE1 30, SPE2 32, TPE1 20, TPE2 22, and TPE3 24 are network elements that facilitate communications in two directions in a network environment. In one particular example, each of these network elements is a switch configured to exchange data over static and/or dynamic pseudowire links. Further, the traffic exchanged between these components may be directed over an MPLS transport in certain embodiments. As used herein in this Specification, the term ‘network element’ is meant to encompass switches, routers, bridges, gateways, servers, processors, loadbalancers, firewalls, or any other suitable device, component, element, or object operable to exchange or process information in a network environment. Moreover, these network elements may include any suitable hardware, software, components, modules, interfaces, or objects that facilitate the operations thereof. This may be inclusive of appropriate algorithms and communication protocols that allow for the effective exchange of data or information. Along similar design alternatives, any of the internal modules and components of these network elements may be combined in various possible configurations.” Rustogi, ¶ [0029].</p> <p>“Turning to FIG. 2, FIG. 2 is a simplified block diagram of an example system 60 for providing an example use case using per-label switched path (LSP) pseudowire group labels. FIG. 2 includes a TPE1 62, a TPE2 64, a TPE3 66, a SPE1 68, and a SPE2 70. Each pseudowire group is identified, where a group identification (ID) for Group A and Group B is depicted at TPE1 62. Similarly, Groups C, D, and E have Group IDs at SPE1 68. TPE2 64 and TPE3 66 can couple to interfaces C and D, respectively.” Rustogi, ¶ [0031].</p> <p>“In this particular example, interfaces A and B have failed. Note that there is a multitude of attachment circuits (e.g., 1000 attachment circuits) that are being transported over these interfaces A and B, where the attachment circuits are being tunneled into a corresponding</p>

No.	'821 Patent Claim 7	The Reference
		<p>number of pseudowires. For example, there could be 500 attachment circuits on interface A (implicating 500 pseudowires) and 500 attachment circuits on interface B, where the fault condition for the pseudowires should be signaled. In other flawed systems, an architecture would individually signal this fault condition for each pseudowire (e.g., via signaling between TPE1 62 and SPE1 68). Instead of sending 500 messages, a single message can be sent, where a single label (and Group ID) can be used to identify the pseudowires. In this case, the Group ID A is used to signal the fault condition for 300 pseudowires and for 200 pseudowires (i.e., the top two links connecting TPE1 62 and SPE1 68) using a single message (that includes Group Label A and Group ID A). Thus, the status for Group A is quickly communicated to SPE1 68. Similarly, Group ID B can be used to signal the status of the other 500 pseudowires to appropriately convey the status for Group B. More specifically, the message can include Group Label A and Group ID B. Note that all 1000 pseudowires have effectively been accounted for using these Group IDs A and B.” Rustogi, ¶ [0032].</p> <p>“FIG. 3 is a simplified block diagram of an example system 72 for providing another use case for pseudowire group labels. Note that the grouping mechanism outlined herein is not limited to pseudowires that propagate over LSPs. Certain pseudowires can propagate over an LSP and represent one group, where two ports can be provisioned for two different groups (e.g., Group A and Group B). Hence, FIG. 3 is depicting a use case using pseudowire group labels for <port, LSP>mapping. In a general sense, such a configuration is showing how pseudowire mechanics can be used to offer different group signaling, which may be based on various possible implementations. Thus, there is a group level construct corresponding to the group labels that are created such that any OAM protocol can send the appropriate aggregate messages. In this particular example, the signaling for Group ID A, B, C, and D is similar to that of FIG. 2; however, the grouping mechanism has simply changed.” Rustogi, ¶ [0035].</p> <p>“FIG. 4 is a simplified block diagram of an example system 76 for providing another use case for pseudowire group labels. In this particular example, interface C fails (as shown at TPE2 64). Note that the same logical flow occurs in FIG. 4 in terms of the group signaling, as previously discussed. The group labels in two directions do not have to be the same, where the groupings for the messaging are not necessarily symmetrical. In this particular example, TPE2 64 sends a status for Group E with the corresponding group label (i.e., Group ID E for</p>

No.	'821 Patent Claim 7	The Reference
		<p>300 pseudowires), where that message will have a Group Label E and a Group ID E. Hence, this particular signaling is indicative of 300 pseudowires failing in the network. SPE1 68 can send the status for Group F (where the Group ID F is associated with 300 pseudowires) to TPE1 62, where that message includes a Group Label F and a Group ID F.” Rustogi, ¶ [0036].</p> <p>“FIG. 5 is a simplified block diagram of an example system 80 for providing another use case for pseudowire group labels. In this particular example, interface D fails (as shown at TPE3 66), where all 700 pseudowires fail. In one implementation, TPE3 66 does not have a 700 pseudowire Group ID. Instead, the Group IDs can correspond to 200 and 500 pseudowires, when summed together account for the 700 pseudowires. In this particular example, TPE3 66 sends one message for Group I (representing 200 pseudowires) and another message for Group J (representing 500 pseudowires) to SPE1 68. In response, SPE1 68 sends a message for Group G (representing 200 pseudowires) and another message for Group H (representing 500 pseudowires). Again, the signaling being exchanged between these elements is minimal due to the effective grouping of pseudowires. SPE1 68 also sends a single message for Group I (associated with 200 pseudowires) and Group J (associated with 500 pseudowires) to TPE3 66, which is coupled to interface D. Group ID G is associated with 200 pseudowires, whereas Group ID H is representative of 500 pseudowires.” Rustogi, ¶ [0037].</p> <p>“FIG. 6 is a simplified table 74 illustrating an example set of pseudowire group provisioning parameters for TPE1 62, where these particular provisioning parameters could be relevant to the configuration of FIG. 3. At least in one generic sense, FIG. 2 can reflect one approach for mapping a PW group label to a PW Group ID, while FIGS. 3-5 can reflect a second approach for such mappings, where table 74 is associated with that second approach.” Rustogi, ¶ [0038].</p> <p>“In particular, table 74 illustrates the mapping between SPE1 68 and TPE1 62. The first column represents the attachment circuit port (e.g., interface A, interface B, remote interface C on TPE2 64, and remote interface D on TPE3 66). Additionally, table 74 depicts a number of LSPs, a set of pseudowire grouping labels, and a set of pseudowire Group IDs. Note that the Group IDs are provided inside the pseudowire group labels in this example such that these two columns match in table 74. Additionally, note that table 74 is merely representing some</p>

No.	'821 Patent Claim 7	The Reference
		of the possible characteristics in a single direction, where different constructs could be used in the reverse direction. Note that the provisioning as discussed herein can significantly reduce messaging such that these presented concepts offer increased scalability. This is due in part to the nominal processing that occurs in the network, in contrast to the processing required to evaluate a prolific amount of signaling messages associated with particular pseudowires. Additionally, the paradigm discussed herein can afford service providers an adequate amount of downtime after a failure has occurred in the network.” Rustogi, ¶ [0039].

No.	'821 Patent Claim 8	The Reference
8	The method of claim 4, further comprising the step of configuring said working entity as revertive.	<p>The Reference discloses the method of claim 4, further comprising the step of configuring said working entity as revertive.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 9	The Reference
9[preamble]	The method of claim 4, wherein said overall cost function comprises:	<p>The Reference discloses the method of claim 4, wherein said overall cost function comprises.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 4.</i></p>

No.	'821 Patent Claim 9	The Reference
9[a]	selecting a subset of entity pairs wherein each entity pair of said subset has substantially minimum probability of a concurrent failure of said protection entity and said working entity; and	<p>The Reference discloses selecting a subset of entity pairs wherein each entity pair of said subset has substantially minimum probability of a concurrent failure of said protection entity and said working entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>
9[b]	if said subset comprises at least two entity pairs, selecting an entity pair from said subset that minimizes an entity cost function.	<p>The Reference discloses if said subset comprises at least two entity pairs, selecting an entity pair from said subset that minimizes an entity cost function.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p> <p><u>Kurose discloses:</u></p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p> <p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a</p>

No.	'821 Patent Claim 9	The Reference
		packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current purposes, we'll simply take the link costs as a given and won't worry about how they are determined.” Kurose at 280.

No.	'821 Patent Claim 9	The Reference
		<p>Figure 4.4 + Abstract model of a network</p> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 9	The Reference
		<p>“A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network.” Kurose at 281.</p> <p>“In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner.” Kurose at 282.</p>

No.	'821 Patent Claim 10	The Reference
10	<p>The method of claim 9, wherein said entity cost function comprises a predefined metric.</p>	<p>The Reference discloses the method of claim 9, wherein said entity cost function comprises a predefined metric.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p> <p>Kurose discloses:</p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p> <p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current</p>

No.	'821 Patent Claim 10	The Reference
		<p>purposes, we'll simply take the link costs as a given and won't worry about how they are determined." Kurose at 280.</p> <p>Figure 4.4 • Abstract model of a network</p> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 10	The Reference
		<p>“A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network.” Kurose at 281.</p> <p>“In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner.” Kurose at 282.</p>

No.	'821 Patent Claim 11	The Reference
11	<p>The method of claim 10, wherein said predefined metric is selected from the group consisting of interior gateway protocol (IGP) and traffic engineering (TE).</p>	<p>The Reference discloses the method of claim 10, wherein said predefined metric is selected from the group consisting of interior gateway protocol (IGP) and traffic engineering (TE).</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Cisco created and developed the MPLS and MPLS-TE standards and patented technology based on those standards <i>before</i> Orkit utilized such technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Filsfils • Taylor • Vasseur '879 • Rustogi <p>Filsfils discloses:</p> <p>“In one embodiment, forwarding information bases (FIBs) are selectively populated in a packet switch. A packet switching device determines, based on one or more protocol signaling messages, a subset, which is less than all, on which FIBs a lookup operation may be performed for identifying forwarding information for a received particular packet. The packet switching device populates each of these FIBs, but not all of the FIBs of the packet switching device, with forwarding information corresponding to the particular forwarding</p>

No.	'821 Patent Claim 11	The Reference
		<p>value. Thus, FIB resources are consumed for only those FIBs which could actually be used, and not all of the FIBs, for forwarding packets in the data plane of the packet switching device, whether these packets are received on a primary or backup path.” Filsfils, Abstract.</p> <p>The diagram illustrates a network topology with four apparatuses (101, 102, 103, 104) connected in series. - Apparatus 101 is labeled '(E.G., PACKET SWITCHING DEVICE, NETWORK DEVICE, APPLIANCE, LABEL SWITCH ROUTER)'. - Apparatus 102 is labeled '(E.G., PACKET SWITCHING DEVICE, NETWORK DEVICE, APPLIANCE, LABEL SWITCH ROUTER)'. - Apparatus 103 is labeled '(E.G., PACKET SWITCHING DEVICE, NETWORK DEVICE, APPLIANCE, LABEL SWITCH ROUTER)' with the note 'WITH MULTIPLE FORWARDING INFORMATION BASES (FIBS), TYPICALLY ONE OR MORE ON EACH OF MULTIPLE LINE CARDS'. - Apparatus 104 is labeled '(E.G., PACKET SWITCHING DEVICE, NETWORK DEVICE, APPLIANCE, LABEL SWITCH ROUTER)'. - A 'PRIMARY PATH' (USE LABEL T1) is shown as a solid black arrow connecting 101, 103, and 104. It is labeled 'LABEL=T1' at the connection to 103 and '111' below the arrow. - A 'SECOND TUNNEL' is indicated by a dashed line from 101 to 102, labeled '(USE LABEL T2 FOR SECOND TUNNEL)'. - A 'BACKUP PATH (SECOND TUNNEL)' is shown as a dashed line connecting 101, 102, and 103, labeled 'LABEL=T1' at the connection to 102 and '121' below the arrow. - A 'LINK' is shown as a dashed line connecting 102 and 103, labeled '122' below the arrow. - Callouts provide additional context: 'PARTICULAR TUNNEL FROM APPARATUS 101 TO 104 WITH PRIMARY PATH 101-111-103-112-104, WITH A BACKUP PATH OF LINK 111 VIA 121-102-122'.</p> <p style="text-align: center;">FIGURE 1</p> <p>Filsfils, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 11	The Reference
		<p>APPARATUS (E.G., PACKET SWITCHING DEVICE APPLIANCE, CORE ROUTER, LABEL SWITCH ROUTER)</p> <p>200</p> <p>PRIMARY AND BACKUP PATHS</p> <p>210</p> <p>211</p> <p>212</p> <p>213</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>ROUTE PROCESSOR CONFIGURED TO CORRELATE PRIMARY AND BACKUP PATHS AND POPULATE FIBS ACCORDINGLY</p> <p>201</p> <p>202</p> <p>203</p> <p>204</p> <p>COMMUNICATION MECHANISM(S) (E.G., BUS, SWITCHING FABRIC, MATRIX)</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>NETWORK(S)</p> <p>NETWORK(S)</p> <p>FIGURE 2</p> <p>Filsfils, FIG. 2 (annotated).</p>

No.	'821 Patent Claim 11	The Reference
		<pre> graph TD START([START]) --> 402[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL] 402 --> 404[DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)] 404 --> 406[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 406 --> END([END]) </pre> <p>The flowchart illustrates a process for establishing primary and backup paths for a tunnel. It begins with a START node, followed by a step to exchange protocol signaling messages to establish the primary path. This is followed by a decision point to determine if a backup path needs to be created. If so, another set of protocol signaling messages are exchanged to establish the backup path. The process concludes with an END node.</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL</p> <p>DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.</p>

FIGURE 4

Filsfils, FIG. 4 (annotated).

No.	'821 Patent Claim 11	The Reference
		<pre> graph TD START([START]) --> S1[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, SENDING A PARTICULAR LABEL FROM TO THE NEIGHBOR UPSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THIS LSR OVER THE PARTICULAR TUNNEL.] S1 --> S2[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF THE PARTICULAR TUNNEL, INCLUDING RECEIVING INFORMATION THAT IT CAN USE TO CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] S2 --> S3[CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL, AND ONLY POPULATE THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, THE LSR KNOWS WHAT ON WHAT INTERFACE(S) PACKETS FROM THE BACKUP PATH COULD BE RECEIVED. THIS CORRELATION MAY INCLUDE USING DATA CONCERNING BUNDLED INTERFACES, AND EVEN RECURSIVE CORRELATION OF BACKUP TUNNELS USED TO BACKUP OTHER BACKUP TUNNELS, AS WELL AS LOAD BALANCING AND OTHER TECHNIQUES TO DETERMINE WHERE BACKUP PATH PACKETS COULD BE RECEIVED, AND THE SUBSET OF FORWARDING INFORMATION BASES IN THE DATA PLANE THAT COULD BE USED TO FORWARD PACKETS OVER THE PARTICULAR TUNNEL, WHETHER VIA A PRIMARY OR BACKUP PATH.] S3 --> END([END]) </pre> <p>FIGURE 5</p> <p>Filsfils, FIG. 5.</p>

No.	'821 Patent Claim 11	The Reference
		<p>“The communications industry is rapidly changing to adjust to emerging technologies and ever increasing customer demand. This customer demand for new applications and increased performance of existing applications is driving communications network and system providers to employ networks and systems having greater speed and capacity (e.g., greater bandwidth). In trying to achieve these goals, a common approach taken by many communications providers is to use packet switching technology, including switching packets on labels especially in the core network using Multiprotocol Label Switching (MPLS).” Filsfils, 1:12-22.</p> <p>“Tunnels, such as MPLS-TE (Traffic Engineering) and MPLS-TP (Transport Profile), are paths established through a network in order to transport packets efficiently through a label switched network. Fast Re-Route (FRR) is a technology that allows backup paths to be established in the network, which can be used in case of a problem with a primary path (original primary path or currently used backup path) of the tunnel. RFC 4090, entitled “Fast Reroute Extensions to RSVP-TE for LSP Tunnels,” provides an extension of the protocol signaling to establish backup label switched path (LSP) tunnels for local repair of LSP tunnels.” Filsfils, 1:23-33.</p> <p>“Expressly turning to the figures, FIG. 1 illustrates a network 100 operating according to one embodiment. Shown are four apparatus 101-104 (e.g., packet switching devices such as a label switch router, network device, and/or appliance). For explanation purposes, each of apparatus 101-104 will be referenced as a label switch router (LSR).” Filsfils, 5:41-46.</p> <p>“As shown, a particular tunnel is established, using a signaling protocol and exchanging of protocol signaling messages. Note, LSR 101 may, or may not, be an endpoint of the particular tunnel (e.g., LSR 101 may be an intermediate LSR on the path of the particular tunnel). The primary path of the particular tunnel includes spans from LSR 101 via link 111 to LSR 103 and via link 112 to LSR 104. Note, LSR 104 may be an intermediate LSR on the path of the particular tunnel, or an endpoint of the particular tunnel. Further, for this example embodiment, LSR 103 signals LSR 101 to use label T1 at the top of the label stack in the header of a packet sent to it on the particular tunnel.” Filsfils, 5:47-58.</p>

No.	'821 Patent Claim 11	The Reference
		<p>“A second tunnel from LSR 101 via link 121 to LSR 102 and via link 122 to LSR 103 is similarly configured using a signaling protocol and exchanging of protocol signaling messages. For example purposes, LSR 102 signals LSR 101 to use label (T2) at the top of the label stack in the header of a packet sent to it on the second tunnel. In one embodiment, LSR 101 creates the second tunnel in response to determining, or being instructed to, create a backup path to protect link 111 and/or protect all or certain tunnels traversing link 111.” Filsfils, 5:59-67.</p> <p>“As shown in FIG. 1, link 111 (primary path of the particular tunnel and/or all or certain tunnels traversing link 111) is protected by LSR 101 using the second tunnel (backup path). When sending packets over the particular tunnel over link 111, LSR 101 includes label T1 at the top of the label stack of these packets. If link 111 cannot be used for communicating packets of the particular tunnel, LSR 101 sends packets over the backup path for the particular tunnel by sending packets to LSR 102, with these packets having a label stack including: label T2 followed by label T1. Thus, LSR 102 will receive these packets, pop the top label (T2) from the label stack of each of these packets, identify based on label T2 to send these packets to LSR 103. After popping the top label, the label at the top of the label stack of these packets is T1, which is the same label LSR 103 expects to receive for the particular tunnel. Therefore, these packets received with label T1 at the top of their label stack, are forwarded (after popping label T1 from their label stack) by LSR 103 over the particular tunnel to LSR 104.” Filsfils, 6:6-24.</p> <p>“One embodiment acquires such additional information by extending Resource Reservation Protocol (RSVP) to provide information which allows a packet switch to correlate primary and backup paths. Thus, a packet switch can use this additional information in determining which of its forwarding information bases (FIBs) could possibly be used in forwarding packets (e.g., in the data plane of the packet switch).” Filsfils, 6:51-57.</p> <p>“In providing this additional information to LSR 103, one embodiment communicates an extended RSVP message (including a new or modified RSVP object) or other message to LSR 103 on the second tunnel. This message designates one or more primary tunnels (e.g., label T1 in our example) and/or a link (e.g., link 111). As LSR 103 knows what interface</p>

No.	'821 Patent Claim 11	The Reference
		<p>that it received this message, LSR 103 knows that it must populate forwarding information for these primary tunnels, either specified (e.g., by a label such as T1), or all labels corresponding to tunnels which could be received over link 111. In one embodiment, the extended RSVP or other message communicated to LSR 103 also includes an identification of the backup tunnel (e.g., T2) over which the RSVP or other message is being received, as the identification the tunnel over which a packet is received is often not communicated in a packet (e.g., in the case of Penultimate Hop Popping).” Filsfils, 7:19-34.</p> <p>“As shown in FIG. 1, one embodiment includes apparatus 103, which populates less than all of its FIBs with forwarding information for a tunnel (although all FIBs may be populated for certain tunnels). One embodiment includes apparatus 101 and/or 102 which communicates, via a signaling protocol (e.g., an extension of RSVP, or using another protocol), information which allows apparatus 103 to determine the relationship between primary and backup paths, such that apparatus 104 can correlate this primary and backup path information (possibly also correlating backup path of backup path information, and/or bundled interfaces and/or bundled links) to identify a minimum subset of the FIBs that could possibly be used in forwarding packets of particular primary paths (e.g., tunnels).” Filsfils, 7:49-62</p> <p>“Turning to FIG. 2, illustrates an apparatus 200 (e.g., packet switching devices such as a label switch router, network device, and/or appliance) operating in one embodiment. As shown, apparatus 200 includes line cards 201, 202 communicatively coupled via communication mechanism(s) 203 (e.g., bus, switching fabric, and/or matrix). Additionally, route processor 204 is configured to correlate primary and backup paths of tunnels, and to populate minimum subsets of FIBs with forwarding information for labels. Again, a minimum subset of FIBs for a particular path or label of the particular path is the set of FIBs that are determined to possibly be used in forwarding packets of a primary path, whether the label is received in a packet over the primary path or over a backup path, and possibly considering backup paths of a backup path and/or the possibly effect of bundled interfaces and/or bundled links.” Filsfils, 7:63-8:11.</p>

No.	'821 Patent Claim 11	The Reference
		<p>“As shown in FIG. 2, apparatus 200 is communicatively coupled via primary and backup paths 211, 212 to networks 210 and 213 (which could be the same network). As illustrated, each of line cards 201, 202 includes one or more FIBs. By correlating on which line card(s) 201, 202 and even within line cards 201, 202 that have multiple FIBs, primary and backup path(s) of tunnels, the number of FIB entries populated in apparatus 200 can typically be reduced, possibly significantly saving memory/storage resources and resources used to populate the FIBs.” Filsfils, 8:12-21.</p> <p>“FIG. 4 illustrates a process performed in one embodiment. Processing begins with process block 400. In process block 402, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes receiving a particular label for a downstream LSR to use when sending packets to the downstream LSR over the particular tunnel.” Filsfils, 8:61-67.</p> <p>“In process block 404, a determination is made to create a backup path from the node (e.g. the node performing these operations). This backup path may be used to protect one or more particular tunnels, and/or may be used to protect a link which is used to carry packet traffic of one or more tunnels.” Filsfils, 9:1-5.</p> <p>“In process block 406, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including providing information to the downstream LSR so that the downstream LSR can correlate primary and backup path(s) of the particular tunnel and substantially only program the FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:6-23.</p>

No.	'821 Patent Claim 11	The Reference
		<p>“FIG. 5 illustrates a process performed in one embodiment. Processing begins with process block 500. In process block 502, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes sending a particular label for an upstream LSR to use when sending packets over the particular tunnel to this apparatus (e.g., an LSR performing these operations).” Filsfils, 9:26-32.</p> <p>“In process block 504, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including receiving information that the LSR can use to correlate primary and backup path(s) of the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:33-47.</p> <p>“In process block 506, the primary and backup path(s) of the particular tunnel are correlated to identify the set of FIBs that could possibly be used in forwarding packets of the particular tunnel. Substantially only those FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel (either through a primary or backup path) are populated with the forwarding information (e.g., an entry corresponding to the label it advertised to use for the particular tunnel) for the particular tunnel. For example, the LSR knows what on what interface(s) packets from the backup path could be received. This correlation may include using data concerning bundled interfaces, and even recursive correlation of backup tunnels used to backup other backup tunnels, as well as load balancing and other techniques to determine where backup path packets could be received, and the subset of forwarding information bases in the data plane that could be used to forward packets over the tunnel, whether via a primary or backup path.” Filsfils, 9:48-65.</p>

No.	'821 Patent Claim 11	The Reference
		<p>Taylor discloses:</p> <p>“Grouping pseudowires based on hardware interfaces and configured control paths enables improved pseudowire failover performance. Signaling status changes (e.g., from standby to active status) is facilitated by using group IDs for the pseudowire groups, thereby enabling improved failover performance when there is disruption in the network.” Taylor, Abstract.</p> <p>The diagram illustrates a network topology with five PE routers (T-PE1, S-PE2, T-PE4, T-PE1 10.1.1.1, S-PE3) connected via various interfaces (e1/0, e4/0, etc.). Each router contains a box detailing its internal pseudowire (PW) configuration, including PW IDs, neighbor addresses, and encapsulation types (encap mpls). The connections between routers are labeled with numbers 1, 2, or 101/102, indicating different control paths.</p> <ul style="list-style-type: none"> T-PE1 (402): <ul style="list-style-type: none"> Interface e1/0.111: xconnect 10.2.2.2 1 encap mpls, backup peer 10.3.3.3 101 Interface e1/0.222: xconnect 10.3.3.3 2 encap mpls, backup peer 10.2.2.2 102 S-PE2 (406): <ul style="list-style-type: none"> 12 vfi one point-to-point: neighbor 10.1.1.1 101 encap mpls, neighbor 10.4.4.4 101 encap mpls 12 vfi two point-to-point: neighbor 10.1.1.1 102 encap mpls, neighbor 10.4.4.4 102 encap mpls T-PE4 (404): <ul style="list-style-type: none"> Interface e4/0.111: xconnect 10.2.2.2 1 encap mpls, backup peer 10.3.3.3 101 Interface e4/0.222: xconnect 10.3.3.3 2 encap mpls, backup peer 10.2.2.2 102 T-PE1 10.1.1.1 (408): <ul style="list-style-type: none"> e1/0: xconnect 10.2.2.2 102 S-PE3 (408): <ul style="list-style-type: none"> 12 vfi one point-to-point: neighbor 10.1.1.1 101 encap mpls, neighbor 10.4.4.4 101 encap mpls 12 vfi two point-to-point: neighbor 10.1.1.1 102 encap mpls, neighbor 10.4.4.4 102 encap mpls

FIG. 4

Taylor, FIG. 4 (annotated).

No.	'821 Patent Claim 11	The Reference
		<p>FIG. 5</p> <p>Taylor, FIG. 5 (annotated).</p> <p>The diagram illustrates a network topology with four nodes: T-PE1 (11.1.1.1), S-PE2 (12.1.1.1), S-PE3 (13.1.1.1), and T-PE4 (14.1.1.1). The connections and configurations are as follows:</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1) -> S-PE2 (12.1.1.1): Gid=1 for 1,2,3,4. This connection is highlighted in yellow. T-PE1 (11.1.1.1) -> S-PE3 (13.1.1.1): Gid=2 for 5,6,7,8. This connection is highlighted in yellow. S-PE2 (12.1.1.1) -> T-PE4 (14.1.1.1): Gid=100 for 1,2. This connection is highlighted in yellow. S-PE2 (12.1.1.1) -> T-PE1 (11.1.1.1): Gid=200 for 1,2,3,4. This connection is highlighted in yellow. S-PE2 (12.1.1.1) -> T-PE1 (11.1.1.1): Gid=201 for 3,4. This connection is highlighted in yellow. S-PE3 (13.1.1.1) -> T-PE5 (15.1.1.1): Gid=50 for 5,6,7,8. This connection is highlighted in yellow. S-PE3 (13.1.1.1) -> T-PE1 (11.1.1.1): Gid=75 for 5,6. This connection is highlighted in yellow. S-PE3 (13.1.1.1) -> T-PE1 (11.1.1.1): Gid=76 for 7,8. This connection is highlighted in yellow. T-PE4 (14.1.1.1) -> S-PE2 (12.1.1.1): Gid=200 for 1,2. This connection is highlighted in yellow. T-PE4 (14.1.1.1) -> S-PE2 (12.1.1.1): Gid=201 for 3,4. This connection is highlighted in yellow. T-PE5 (15.1.1.1) -> S-PE3 (13.1.1.1): Gid=250 for 5,6. This connection is highlighted in yellow. T-PE5 (15.1.1.1) -> S-PE3 (13.1.1.1): Gid=251 for 7,8. This connection is highlighted in yellow. <p>Annotations provide detailed interface configurations for each node:</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1) annotations: <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.1 1 encaps mpls backup peer 13.1.1.15 interface e0/0.200 xconnect 12.1.1.1 2 encaps mpls backup peer 13.1.1.15 interface e0/0.300 xconnect 12.1.1.1 3 encaps mpls backup peer 13.1.1.17 interface e0/0.400 xconnect 12.1.1.1 4 encaps mpls backup peer 13.1.1.18 S-PE2 (12.1.1.1) annotations: <ul style="list-style-type: none"> l2 vif abc point-to-point neighbor 11.1.1.1 1 encaps mpls neighbor 14.1.1.1 1 encaps mpls l2 vif def point-to-point neighbor 11.1.1.1 2 encaps mpls neighbor 14.1.1.1 2 encaps mpls l2 vif ghi point-to-point neighbor 11.1.1.1 3 encaps mpls neighbor 14.1.1.1 3 encaps mpls l2 vif jkl point-to-point neighbor 11.1.1.1 4 encaps mpls neighbor 14.1.1.1 4 encaps mpls S-PE3 (13.1.1.1) annotations: <ul style="list-style-type: none"> l2 vif abc point-to-point neighbor 11.1.1.1 5 encaps mpls neighbor 15.1.1.1 5 encaps mpls l2 vif def point-to-point neighbor 11.1.1.1 6 encaps mpls neighbor 15.1.1.1 6 encaps mpls l2 vif ghi point-to-point neighbor 11.1.1.1 7 encaps mpls neighbor 15.1.1.1 7 encaps mpls l2 vif jkl point-to-point neighbor 11.1.1.1 8 encaps mpls neighbor 15.1.1.1 8 encaps mpls T-PE4 (14.1.1.1) annotations: <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.1 1 encaps mpls interface e0/0.200 xconnect 12.1.1.1 2 encaps mpls interface e1/0.100 xconnect 12.1.1.1 3 encaps mpls interface e1/0.200 xconnect 12.1.1.1 4 encaps mpls T-PE5 (15.1.1.1) annotations: <ul style="list-style-type: none"> interface e0/0.100 xconnect 13.1.1.1 5 encaps mpls interface e0/0.200 xconnect 13.1.1.1 6 encaps mpls interface e1/0.100 xconnect 13.1.1.1 7 encaps mpls interface e1/0.200 xconnect 13.1.1.1 8 encaps mpls

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		<p style="text-align: center;">TRAFFIC FLOW IS TOWER ← CORE</p> <p>The diagram illustrates a network topology with the following components and their connections:</p> <ul style="list-style-type: none"> TOWER T-PE: Represented by a cloud icon containing two parallel bars labeled 804. The top bar contains "VLANs 100-549" and "VLANs 550-999". The bottom bar contains "VLANs 100-112" and "VLANs 110-112". S-PE1: Represented by a hexagonal node connected to the TOWER T-PE via a line labeled 802. It contains two parallel bars labeled 802. The top bar contains "VLANs 100-549" and "VLANs 550-999". The bottom bar contains "VLANs 100-549" and "VLANs 550-999". S-PE2: Represented by a hexagonal node connected to the TOWER T-PE via a line labeled 808. It contains two parallel bars labeled 808. The top bar contains "VLANs 100-112" and "VLANs 110-112". The bottom bar contains "VLANs 100-112" and "VLANs 110-112". CORE T-PE1: Represented by a hexagonal node connected to S-PE1 via a line labeled 802. It contains two parallel bars labeled 802. The top bar contains "VLANs 100-549" and "VLANs 550-999". The bottom bar contains "VLANs 100-549" and "VLANs 550-999". CORE T-PE2: Represented by a hexagonal node connected to S-PE2 via a line labeled 806. It contains two parallel bars labeled 806. The top bar contains "VLANs 550-999" and "VLANs 100-549". The bottom bar contains "VLANs 550-999" and "VLANs 100-549". RNC/BSC: Represented by a vertical rectangle connected to CORE T-PE1 and CORE T-PE2 via lines labeled mLACP. It contains two parallel bars labeled 806. The top bar contains "VLANs 100-999" and "VLANs 550-999". The bottom bar contains "VLANs 100-999" and "VLANs 550-999". <p>Legend:</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td>ACTIVE PW/ACTIVE AC</td> <td>STANDBY PW/ACTIVE AC</td> </tr> <tr> <td>STANDBY* PW/STANDBY AC</td> <td>STANDBY PW/STANDBY AC</td> </tr> <tr> <td colspan="2" style="text-align: center;">* GOES ACTIVE WHEN mLACP FAILS OVER</td> </tr> </table> <p style="text-align: right;">FIG. 8</p> <p>Taylor, FIG. 8 (annotated).</p>	ACTIVE PW/ACTIVE AC	STANDBY PW/ACTIVE AC	STANDBY* PW/STANDBY AC	STANDBY PW/STANDBY AC	* GOES ACTIVE WHEN mLACP FAILS OVER	
ACTIVE PW/ACTIVE AC	STANDBY PW/ACTIVE AC							
STANDBY* PW/STANDBY AC	STANDBY PW/STANDBY AC							
* GOES ACTIVE WHEN mLACP FAILS OVER								

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		<pre> sequenceDiagram participant TOWER as TOWER T-PE participant S1 as S-PE1 participant S2 as S-PE2 participant CORE as CORE T-PE TOWER->>S1: S-PE1 FFD PWs MARKED DOWN activate S1 S1-->>S2: S-PE1 FAILURE deactivate S1 S2-->>CORE: HOT STANDBY → ACTIVE activate CORE CORE-->>S2: HOT STANDBY → ACTIVE deactivate CORE S2-->>CORE: S-PE1 GROUPS MARKED DOWN deactivate S2 </pre> <p><i>FIG. 9</i></p> <p>Taylor, FIG. 9 (annotated).</p>

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		<pre> graph TD 1002((1002)) --> 1004[1004 SPECIFY PHYSICAL INTERFACE AT FIRST PE] 1004 --> 1006[1006 SPECIFY FIRST CONTROL PATH FROM FIRST PE TO SECOND PE] 1006 --> 1008[1008 SPECIFY SECOND CONTROL PATH FROM FIRST PE TO THIRD PE] 1008 --> 1010[1010 SPECIFY REDUNDANT COMBINATIONS OF PWs] 1010 --> 1012[1012 GROUP PWs INTO A PRIMARY PW GROUP AND A BACKUP PW GROUP] 1012 --> 1014[1014 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO PRIMARY/BACKUP PW GROUPS] 1014 --> 1016[1016 DETECT FAILURE ON FIRST CONTROL PATH] 1016 --> 1018[1018 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO BACKUP/PRIMARY PW GROUPS] </pre> <p style="text-align: center;"><i>FIG. 10</i></p> <p>Taylor, FIG. 10 (annotated).</p>

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		<p>“The present disclosure relates generally to communication networks and more particularly to pseudowire configurations in communication networks.” Taylor, 1:8-10.</p> <p>“Virtual Private Network (VPN) services provide secure network connections between different locations. A company, for example, can use a VPN to provide secure connections between geographically dispersed sites that need to access the corporate network so that each customer edge (CE) end point or node can communicate directly and independently with all other CE nodes. Different types of VPNs have been classified by the network layer used to establish the connection between the customer and provider network. For example, Virtual Private LAN Service (VPLS) is an architecture that delivers a multipoint Layer 2 VPN (L2VPN) service that in all respects emulates an Ethernet Local Area Network (LAN) across a wide metropolitan geographic area. All services in a VPLS appear to be on the same LAN, regardless of location. In other words, with VPLS, customers can communicate as if they were connected via a private Ethernet segment, i.e., multipoint Ethernet LAN services.” Taylor, 1:12-28.</p> <p>“In this context, each CE device at a customer site is connected to the service provider network at a provider edge PE) device by an Attachment Circuit (AC) that provides the customer connection to a service provider network, that is, the connection between a CE node and its associated PE node. Within the provider network, each PE device includes a Virtual Switch Instance (VSI) that emulates an Ethernet bridge (i.e., switch) function in terms of Media Access Control (MAC) address learning and forwarding in order to facilitate the provisioning of a multipoint L2VPN. A pseudowire (PW) is a virtual connection between two PE devices that connect two attachment circuits. In the context of the VPLS service, a pseudowire can be thought of as a point-to-point virtual link for each offered service between a pair of VSIs. Therefore, if each VSI can be thought of as a virtual Ethernet switch for a given customer service instance, then each pseudowire can be thought of as a virtual link connecting these virtual switches to each other over a Packet Switched Network (PSN) for that service instance.” Taylor, 1:29-47.</p>

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		<p>“Since the failure of pseudowires obviously degrades network performance, some effort has been directed towards adding system redundancies including redundant pseudowires. However, the presence of redundant pseudowires alone is insufficient to improve overall failover performance, that is, the ability to switch over automatically to a redundant or backup system. Thus, there is a need for improved methods for managing pseudowires to facilitate pseudowire switching and enable improved failover performance.” Taylor, 1:48-56.</p> <p>“FIG. 4 shows details for PW connectivity in an exemplary network for an example embodiment.” Taylor, 1:66-67.</p> <p>“FIG. 5 shows details for PW grouping for an example embodiment.” Taylor, 2:1-2.</p> <p>“FIG. 8 shows an example network including redundant PW connections for an example embodiment.” Taylor, 2:9-10.</p> <p>“FIG. 9 shows an example sequence diagram for a failure mode related to the embodiment shown in FIG. 8.” Taylor, 2:11-12.</p> <p>“FIG. 10 shows a flowchart that illustrates a method of providing improved PW grouping according to an example embodiment.” Taylor, 2:13-15.</p> <p>“According to one embodiment, a method of providing improved pseudowire performance includes specifying a physical interface at a first PE node in a network, a first control path from the first PE node to a second PE node in the network, and a second control path from the first PE node to a third PE node in the network. With these specifications, the method then includes specifying redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node, and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Then these pseudowires can be grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup</p>

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		<p>pseudowires. Group identifiers for the primary PW group and the backup PW group can then be used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths. The method may then include detecting a failure on the first control path, and in response to the detected failure, using the group identifiers to assign the active status to the backup pseudowires and the standby status to the primary pseudowires.” Taylor, 2:27-52.</p> <p>“Pseudowires are used in pseudowire emulation edge-to-edge to provide a Layer 2 Virtual Private Network (L2VPN) connection. When large numbers (e.g., 4,000-6,000) of pseudowires are aggregated together on a single router, failure performance tends to be linear or O(n) where n is the number of pseudowires. While O(n) performance may be acceptable for small numbers of pseudowires, the effect on network outages can be increasingly undesirable as the number of pseudowires increases.” Taylor, 2:54-62.</p> <p>“For example, a cell-site router will typically start an approximately 2-minute procedure if contact with its controller, which is reached via a pseudowire, is lost for more than some threshold amount (e.g., between approximately 0.75 and 1.75 seconds in some cases). This can be a major impediment to the scalability of pseudowire deployments. These issues have become increasingly relevant as providers of Multiservice Broadband Networks (MBNs) are rapidly replacing or augmenting their traditional Synchronous Optical Networking (SONET) equipment with cheaper Ethernet equipment in the evolution towards a 4G (i.e., 4th generation) network.” Taylor, 2:63-3:7.</p> <p>“One aspect of a solution to the problem of pseudowire failure is the deployment of redundant pseudowires. For example, redundant pseudowires have been used in the context of Multiprotocol Label Switching (MPLS) networks, which use a Label Distribution Protocol (LDP) to manage labels for forwarding traffic between routers. In this context, general requirements for redundancy schemes have been developed so that duplicate pseudowires are available when a given pseudowire fails (e.g., by using active/standby status indicators). In addition, more specific implementations for redundant pseudowires have also been developed.” Taylor, 3:8-18.</p>

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		<p>“FIG. 1 shows a reference network model 102 with applications to example embodiments disclosed herein. The reference network model 102 includes an aggregation network 104 of PE nodes and a distribution network 106 of PE nodes between a radio network controller (RNC) (or base station controller (BSC)) 108 on the core side of the model 102 and a radio tower 111 on the tower side of the model 102. Switching provider edge nodes S-PE1 and S-PE2 connect the two networks 104, 106. On the core side, two core terminating provider edges T-PE1 and T-PE2 connect to the RNC/BSC 108 through attachment circuits 110, 112. On the tower side, one tower terminating provider edge T-PE connects to the radio tower 111 through an attachment circuit 114.” Taylor, 3:19-31.</p> <p>“Additionally as noted in FIG. 1, peer-PE monitoring is carried out within each network 104, 106. That is, there is peer-PE monitoring between provider edges that share a segment, for example, by multi-hop bidirectional forwarding detection (BFD). Alternatively, peer monitoring can be accomplished by other means (e.g., MPLS-TP (Transport Protocol) keep-alives). This peer-PE monitoring is used to provide the mechanism for fast failure detection. Once a failure is detected, the network can react by “rerouting” the failed pseudowires to pre-provisioned backup paths and thus provide a minimal disruption in service to the end-user. This rerouting can be accomplished by LDP signaling between provider edges.” Taylor, 3:32-44.</p> <p>“The reference network model 102 may be considered as part of a larger hub-and-spoke model as shown in FIG. 2. A hub-and-spoke distribution model 202 includes a core network 204, distribution networks 206, and aggregation networks 208. Network elements including distribution nodes, aggregation nodes, and towers are also shown with nominal count values (e.g., 30 distribution nodes between the core network 204 and a distribution network 206). In this model 202, tower T-PEs are the spokes while core-PEs constitute the hub. Dozens to hundreds of tower T-PEs connect to a few S-PEs; these S-PEs are quite similar to ASBRs as they act as forwarders between the two distinct MPLS domains, providing isolation and, in the case of mobility, aggregation services. Typically, several aggregation networks 208 are connected to a single distribution network 206, eventually connecting the tower with the core router that connects the tower's ACs to the RNC/BSC. There are typically several</p>

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		<p>distribution networks in a Radio Access Network (RAN) connected to the service provider's core Internet Protocol (IP) network." Taylor, 4:11-30.</p> <p>"With reference to FIG. 1, FIG. 3 shows a variety of failure modes encountered in the reference network model 102. Failure 302 of communications between tower T-PE and the S-PE can be detected via peer monitoring when both the S-PE and the T-PE are still active/alive. For example, this failure may be due to a loss of connectivity when the BFD session goes down. Failure 304 of S-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability (e.g., if the BFD-session hello timers cannot be serviced for the prescribed period of time). Failure 306 of communications between S-PE1 and core T-PE1 can be detected via peer monitoring when both S-PE1 and core T-PE1 are still active/alive. Failure 308 at core T-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability." Taylor, 4:46-60.</p> <p>"Pseudowire connectivity is further illustrated in FIG. 4 where the illustrated network includes four nodes: T-PE1 (10.1.1.1), S-PE2 (10.2.2.2), S-PE3 (10.3.3.3), and T-PEA (10.4.4.4). For the terminating nodes T-PE1 and T-PE4, specifications for VLANs (virtual Local Area Networks) connections (i.e., pseudowires) are shown using the Internet Operating System Command Line Interface (IOS CLI). The specification 402 for T-PE1 defines two VLANs as primary/backup combinations of virtual circuits for the network. The first three lines of the specification 402 define "VLAN 111" beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for "VLAN 111" in the first line. The second line uses the "xconnect" statement to specify a virtual circuit from that interface to S-PE2 (10.2.2.2) with a virtual circuit Identification (VCID) set as VCID=1, and the third line uses the "backup peer" statement to specify another virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=101. The next three lines of the specification 402 define "VLAN 222" beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for "VLAN 222" in the fourth line. The fifth line uses the "xconnect" statement to specify a virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=2, and the sixth line uses the "backup peer" statement to specify another virtual circuit from that interface to S-PE2 (10.2.2.2) with VCID=102. These virtual circuits, VCID=1, VCID=2, VCID=101 and VCID=102 are shown in the figure between T-PE1 and the S-PEs with a</p>

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		<p>solid line for the primary circuits VCID=1 and VCID=2 and a dashed line for the backup circuits VCID=101 and VCID=102.” Taylor, 4:61-5:23.</p> <p>“With respect to T-PE1 in FIG. 4, although “VLAN 111” and “VLAN 222” share the same hardware port, they do not share the same “control path disposition.” That is, “VLAN 111” is primary to S-PE2 (VCID=1) and standby to S-PE3 (VCID=101), while “VLAN 222” has an opposite configuration since it is primary to S-PE3 (VCID=2) and standby to S-PE2 (VCID=102). As discussed below, certain embodiments group pseudowires according to “control path disposition” (e.g., xconnect configuration as well as the hardware interface in order to improve failover performance. That is, to deal with both hardware port failures and switching path failures, the grouping criteria also considers the cross connects. In this case, on T-PE1 as well as T-PE4, there would exist two groups: one for active to S-PE2 and standby to S-PE3 and another for active to S-PE3 and standby to S-PE2 (i.e., the inverse configuration).” Taylor, 5:61-6:9.</p> <p>“First, local connectivity is characterized by local group identifications (Group-IDs), which depend on whether the allocation is done at a T-PE or S-PE. FIG. 5 shows an embodiment that illustrates an allocation of local group IDs in a network including terminating nodes T-PE1 (11.1.1.1), T-PE4 (14.1.1.1), and T-PE5 (15.1.1.1) and switching nodes S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1). The specification 502 for T-PE1 determines corresponding local group IDs 504 based on the hardware interface and the control path. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, and the Group-ID=2 corresponds to VCID=5, VCID=6, VCID=7, and VCID=8.” Taylor, 6:10-21.</p> <p>“Local group IDs are maintained in a database so that pseudowire redundancy is also maintained. First, in a case without pseudowire redundancy, all the xconnect configurations from the same physical interface to the same peer are assigned the same local group ID. So, for example, in Ethernet cases all xconnect configurations under sub-interfaces of the same physical interface to the same peer will be assigned the same local group ID (e.g., e0/0 and e0/1 are sub-interfaces of e0). FIG. 6 shows a database representation for T-PE4 from FIG. 5. From the root node 602 for T-PE4, there is a first interface node 604 for e0/1 and a second interface node 606 for e1/0. The first interface node 604 is configured towards a single peer</p>

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		<p>node (12.1.1.1) 608 and is thus assigned a single local group ID (Group-ID=200) 610. Similarly, the second interface node 606 is configured towards a single peer node (12.1.1.1) 612 and is thus assigned a single local group ID (Group-ID=201) 614. In this case from the assignment of local group IDs 508 in FIG. 5, Group-ID=200 corresponds to VCID=1 and VCID=2, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). Both pseudowires (VCID=1 and VCID=2) are assigned the same local group ID (Group-ID=200) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly, from the assignment of local group IDs 508 in FIG. 5, T-PE4 has VCID=3 and VCID=4 under the physical interface e1/0 going to the same peer S-PE2 (12.1.1.1), and the local Group ID (Group-ID=201) is assigned to these VCs.” Taylor, 6:48-7:8.</p> <p>“For the pseudowire redundancy case, a separate redundancy-group database is maintained by the xconnect application. This redundancy-group database contains the peer IDs in the group and the local group IDs advertised to them. This is needed to maintain a 1:1 mapping between the primary pseudowires and their corresponding backup pseudowires. FIG. 7 shows a database representation for T-PE1 from FIG. 5. From the root node 702 for T-PE1, there is an interface node 704 for e0/0 and a redundancy group node 706 that shows connections for configurations to a first peer node (12.1.1.1) 708, which is assigned a local group ID (Group-ID=1) 710, and a second peer node (13.1.1.1) 712, which is assigned a local group ID (Group-ID=2) 714. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). These pseudowires are assigned the same group ID (Group-ID=1) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly from the assignment of local group IDs 504 in FIG. 5, T-PE1 has VCID=5, VCID=6, VCID=7, and VCID=8 under the physical interface e0/0 going to another peer S-PE3 (13.1.1.1), and the local group ID (Group-ID=2) is assigned to these VCs. In this case these local group IDs are organized as a redundancy group 706.” Taylor, 7:9-35.</p>

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		<p>“Pseudowire grouping allows multiple pseudowires to be aggregated together when signaling either label withdrawals or status changes between segment end-point provider edges. This signaling can be carried out through LDP grouping TLV (Type Length Value). For example, when a PE node fails (e.g., failure 304 of S-PE1), aggregating the inter-segment PE signaling using the grouping TLV can provide significant scaling advantages. This allows all pseudowires sharing a physical port and PW configuration (e.g., xconnect configuration) to be signaled en masse between segment-adjacent provider edges.” Taylor, 7:36-46.</p> <p>“FIG. 8 shows an example based on FIG. 1 where VLAN ACs are shown as grouped by both port/HW-interface and pseudowire-class. The grouping criterion allows all “similar” pseudowires to be signaled together: All the grouped pseudowires share the same port and next-hop provider edge. Additionally, the figure contains many pseudowires, each grouped into a shaded tube. For example, the tube labeled “VLANs 100-549” contains 450 pseudowires grouped together. This figure depicts an incoming Ethernet comprised of 900 VLANs being segmented in two with 450 VLANs (100-549) active to S-PE1 while the other half of the VLANs (550-999) being active to S-PE2. This might be considered a type of manual load balancing. Furthermore, the aggregation network is only showing a single tower and the VLANs associated with it; other VLAN destinations are not shown in the figure.” Taylor, 7:47-62.</p> <p>“‘VLANs 110-112’ are active along a first pseudowire path 802 from Core T-PE1 to S-PE1 and a second pseudowire path 804 from S-PE1 to Tower T-PE. When a failure occurs at S-PE1 (e.g., as the switching node failure 304 shown in FIG. 3), then the standby pseudowires become active for ‘VLANs 110-112’ along a first pseudowire path 806 from Core T-PE1 to S-PE2 and a second pseudowire path 808 from S-PE2 to Tower T-PE.” Taylor, 7:63-8:3.</p> <p>“The standby pseudowires in FIG. 8 can be configured as HSPWs, a configuration that enables ACs to quickly failover to pre-provisioned pseudowires that are in active state but set to not-forwarding. Then when a failure occurs, switching over to these pre-provisioned HSPWs occurs quickly by switching from not-forwarding status to forwarding status.” Taylor, 8:4-9.</p>

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		<p>“FIG. 9 shows a UML (Unified Modeling Language) sequence diagram of signaling events related to the failover procedure illustrated in FIG. 8 for a switching node failure 304. The Tower T-PE and the Core T-PE independently detect a failure at S-PE1 (e.g., BFD forwarding with LDP signaling), and then mark down the status of the currently active pseudowires routed through S-PE1 and mark up the status of the standby pseudowires routed through S-PW2. Other failure modes shown in FIG. 3 can be handled similarly.” Taylor, 8:10-18.</p> <p>“In general, it is desirable for MPLS-based Ethernet networks to react quickly to failures, so proactive detection mechanisms are employed in order to pick up system failures quickly. All proactive monitoring is typically done between PE peers on a single MPLS network. These provider edges on the edges of the MPLS network act similarly to an Autonomous System Boundary Router (ASBR). As a result, related embodiments detect control path failures, which may not be the same as pseudowire data path failures. That is, the data packets and control packets may take different paths between provider edges in a MPLS network although typically these paths are coincident. Thus, when the control and data paths are not coincident, if the control path fails, then all pseudowires utilizing the control path are marked as failed. As a corollary, if the data path fails and the control path remains healthy, then failure will not be detected from monitoring the control path.” Taylor, 8:19-35.</p> <p>“A failure of a monitored provider edge initiates a switchover of all active pseudowires using the failing provider edge to their configured HSPWs (if they exist). Grouping can greatly reduce the number of messages needed between provider edges (Inter-PE Aggregation) and within a single provider edge (Intra-PE Aggregation). Furthermore, the MPLS network itself may be internally resilient deploying technologies such as, but not limited to, MPLS-TE (MPLS Traffic Engineering) and ERR (Fast Reroute). The paths across the MPLS network may recover quickly and might not trip the fault-monitoring systems.” Taylor, 8:36-46.</p> <p>“With reference to the above discussion, FIG. 10 shows a method 1002 of providing improved PW grouping according to an example embodiment. In a first operation 1004 of the method 1002, a physical interface is specified at a first PE node in a network. In a second</p>

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		<p>operation 1006, a first control path is specified from the first PE node to a second PE node in the network. In a third operation 1008, a second control path is specified from the first PE node to a third PE node in the network. These control paths related to a common physical interface can be used to characterize redundant pairs of pseudowires.” Taylor, 8:48-58.</p> <p>“In a fourth operation 1010, redundant combinations of pseudowires are specified, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Although a redundant combination may relate a single backup pseudowire to a given primary pseudowire, in some cases multiple backup pseudowires will be related to a given primary pseudowire for increased redundancy. In a fifth operation 1012, these pseudowires are grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. In a sixth operation 1014, group identifiers for the primary PW group and the backup PW group are used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 8:59-9:11.</p> <p>“In an optional seventh operation 1016, a failure may be detected on the first control path, and in an optional eighth operation 1018, in response to the detected failure, the group identifiers may be used to assign the active status to the backup pseudowires and the standby status to the primary pseudowires. For example, the failure on the first control path may be detected by using BED packet streams between PE nodes of the network. Then the detected failure can be signaled to PE nodes in the network by sending LDP status updates between PE nodes in the network. Then, after receiving the failure detection signals, the group identifiers can be used again to assign the active status to the backup pseudowires and the standby status to the primary pseudowires by sending LDP status updates between PE nodes in the network.” Taylor, 9:12-26.</p>

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		<p>“Typically the network in is an MPLS network and the PE nodes are routers that provide network services to connected CE nodes of a customer network. In general, each control path is an Internet Protocol (IP) routing path between PE nodes in the network and each data path is a label switched path (LSP) between PE nodes in the network.” Taylor, 9:27-32.</p> <p>“FIG. 11 shows a schematic representation of an apparatus 1102, in accordance with an example embodiment. For example, the apparatus 1102 may be used to implement the method 1002 of providing improved pseudowire grouping as described above with reference to FIG. 10. The apparatus 1102 is shown to include a processing system 1104 that may be implemented on a server, client, or other processing device that includes an operating system 1106 for executing software instructions.” Taylor, 10:2-10.</p> <p>“In accordance with an example embodiment, the apparatus 1102 includes a PW management module 1108 that includes a first specification module 1110, a second specification module 1112, third specification module 1114, a fourth specification module 1116, a grouping module 1118, and an assignment module 1120. The first specification module 1110 operates to specify a physical interface at a first PE node in a network. The second specification module 1112 operates to specify a first control path from the first PE node to a second PE node in the network. The third specification module 1114 operates to specify a second control path from the first PE node to a third PE node in the network. The fourth specification module 1116 operates to specify redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node.” Taylor, 10:11-29.</p> <p>“The grouping module 1118 operates to group the pseudowires into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. The assignment module 1120 operates to use group identifiers for the PW groups to assign an active status to the primary pseudowires and a standby status to the backup pseudowires. The active status enables data transfers along corresponding PW data</p>

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		<p>paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 10:30-38.</p> <p>Vasseur '879 discloses:</p> <p>“A technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, Abstract.</p>

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		<p>The diagram illustrates a network topology labeled 100. It features a sequence of nodes connected by a red horizontal line representing a primary tunnel, labeled T1. The nodes are labeled A through E. Node A is the (HEAD-END) NODE, node B is the (PLR) NODE, node C is the (PRIMARY) NODE, node D is the (REMERGE) POINT, and node E is the (TAIL-END) NODE. A bracket above the nodes B, C, and D is labeled PROTECTED NETWORK ELEMENTS. Below node C, a blue V-shaped line represents a backup tunnel, labeled BT1, which connects to a (BACKUP) NODE labeled F. Arrows indicate the flow of traffic from node A through node B to node C, then branching down to node F, and finally merging back up to node D and continuing to node E.</p> <p style="text-align: center;">FIG. 1</p> <p>Vasseur '879, FIG. 1 (annotated).</p>

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	SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)	<p>COMMON HEADER 310</p> <p>SOURCE ADDRESS 312</p> <p>DESTINATION ADDRESS 314</p> <p>SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320</p> <p>LSP-ATTRIBUTE OBJECT 330</p> <p>EXTENSION OBJECT(S) 400</p>

FIG. 3

Vasseur '879, FIG. 3 (annotated).

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		<p style="text-align: center;">FIG. 5</p> <p>Vasseur '879, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 11	The Reference
		<p>FIG. 6</p> <p>Vasseur '879, FIG. 6 (annotated).</p>

No.	'821 Patent Claim 11	The Reference
		<pre> graph TD START([START]) --> 705[HEAD-END NODE ESTABLISHES PRIMARY TUNNEL] 705 --> 710[HEAD-END NODE REQUESTS PROTECTION OF PRIMARY TUNNEL] 710 --> 715[POINTS OF LOCAL REPAIR (PLRs) CREATE BACKUP TUNNELS TO PROTECT PRIMARY TUNNEL] 715 --> 720[HEAD-END NODE REQUESTS BACKUP TUNNEL PATH QUALITY NOTIFICATION] 720 --> 725[PLRs OF PRIMARY TUNNEL COLLECT PATH QUALITY INFORMATION (e.g., METRICS/STATISTICS) FOR PRIMARY AND BACKUP TUNNELS (e.g., FROM PRIMARY AND/OR BACKUP NODES)] 725 --> 730[PLRs MAY SEND PATH QUALITY NOTIFICATION TO HEAD-END NODE] 730 --> 735[PLRs MONITOR PROTECTED SEGMENT OF PRIMARY TUNNEL FOR FAILURE] 735 --> 740{DETECT FAILURE ?} 740 -- N --> 705 740 -- Y --> A((A)) </pre> <p>FIG. 7A Vasseur '879, FIG. 7A (annotated).</p>

No.	'821 Patent Claim 11	The Reference
		<pre> graph TD A((A)) --> B[DETECTING PLR DIVERTS PRIMARY TUNNEL TRAFFIC TO BACKUP TUNNEL AND SENDS ERROR MESSAGE TO HEAD-END NODE] B --> C[PLR CONTINUES TO COLLECT PATH QUALITY INFORMATION FOR BACKUP TUNNEL] C --> D{PLR SEND NOTIFICATION TO HEAD-END NODE ?} D -- N --> E{TIMER EXPIRED ?} E -- Y --> F[HEAD-END NODE DETERMINES WHETHER TO REESTABLISH PRIMARY TUNNEL BASED ON BACKUP TUNNEL PATH QUALITY NOTIFICATION] F --> G{ACCEPTABLE PATH QUALITY ?} G -- Y --> H[HEAD-END NODE ATTEMPTS TO REESTABLISH PRIMARY TUNNEL] G -- N --> I[END] H --> I D -- N --> C D -- Y --> F </pre> <p>Detailed description of FIG. 7B:</p> <ul style="list-style-type: none"> Step 700: A starts at node A. Step 750: Detecting PLR diverts primary tunnel traffic to backup tunnel and sends error message to head-end node. Step 755: PLR continues to collect path quality information for backup tunnel. Decision 760: PLR sends notification to head-end node? <ul style="list-style-type: none"> If No (N), loop back to 755. If Yes (Y), proceed to step 765. Step 765: Head-end node determines whether to reestablish primary tunnel based on backup tunnel path quality notification. Decision 770: Acceptable path quality? <ul style="list-style-type: none"> If Yes (Y), proceed to step 775. If No (N), loop back to 755. Step 775: Head-end node attempts to reestablish primary tunnel. Step 785: End.

FIG. 7B

Vasseur '879, FIG. 7B.

No.	'821 Patent Claim 11	The Reference
		<p>“The present invention relates to computer networks and more particularly to dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 1:10-14.</p> <p>“Since management of interconnected computer networks can prove burdensome, smaller groups of computer networks may be maintained as routing domains or autonomous systems. The networks within an autonomous system (AS) are typically coupled together by conventional “intradomain” routers configured to execute intradomain routing protocols, and are generally subject to a common authority. To improve routing scalability, a service provider (e.g., an ISP) may divide an AS into multiple “areas.” It may be desirable, however, to increase the number of nodes capable of exchanging data; in this case, interdomain routers executing interdomain routing protocols are used to interconnect nodes of the various ASes. Moreover, it may be desirable to interconnect various ASes that operate under different administrative domains. As used herein, an AS or an area is generally referred to as a “domain,” and a router that interconnects different domains together is generally referred to as a ‘border router.’” Vasseur '879, 1:40-56.</p> <p>“An example of an interdomain routing protocol is the Border Gateway Protocol version 4 (BGP), which performs routing between domains (ASes) by exchanging routing and reachability information among neighboring interdomain routers of the systems. An adjacency is a relationship formed between selected neighboring (peer) routers for the purpose of exchanging routing information messages and abstracting the network topology. The routing information exchanged by BGP peer routers typically includes destination address prefixes, i.e., the portions of destination addresses used by the routing protocol to render routing (“next hop”) decisions. Examples of such destination addresses include IP version 4 (IPv4) and version 6 (IPv6) addresses. BGP generally operates over a reliable transport protocol, such as TCP, to establish a TCP connection/session. The BGP protocol is well known and generally described in Request for Comments (RFC) 1771, entitled A Border Gateway Protocol 4 (BGP-4), published March 1995.” Vasseur '879, 1:57-2:7.</p>

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		<p>“Examples of an intradomain routing protocol, or an interior gateway protocol (IGP), are the Open Shortest Path First (OSPF) routing protocol and the Intermediate-System-to-Intermediate-System (IS-IS) routing protocol. The OSPF and IS-IS protocols are based on link-state technology and, therefore, are commonly referred to as link-state routing protocols. Link-state protocols define the manner with which routing information and network-topology information are exchanged and processed in a domain. This information is generally directed to an intradomain router's local state (e.g., the router's usable interfaces and reachable neighbors or adjacencies). The OSPF protocol is described in RFC 2328, entitled OSPF Version 2, dated April 1998 and the IS-IS protocol used in the context of IP is described in RFC 1195, entitled Use of OSI IS-IS for routing in TCP/IP and Dual Environments, dated December 1990, both of which are hereby incorporated by reference.” Vasseur '879, 2:8-24.</p> <p>“An intermediate network node often stores its routing information in a routing table maintained and managed by a routing information base (RIB). The routing table is a searchable data structure in which network addresses are mapped to their associated routing information. However, those skilled in the art will understand that the routing table need not be organized as a table, and alternatively may be another type of searchable data structure. Although the intermediate network node's routing table may be configured with a predetermined set of routing information, the node also may dynamically acquire (“learn”) network routing information as it sends and receives data packets. When a packet is received at the intermediate network node, the packet's destination address (e.g., stored in a header of the packet) may be used to identify a routing table entry containing routing information associated with the received packet. Among other things, the packet's routing information indicates the packet's next-hop address.” Vasseur '879, 2:25-41.</p> <p>“Multi-Protocol Label Switching (MPLS) Traffic Engineering has been developed to meet data networking requirements such as guaranteed available bandwidth or fast restoration. MPLS Traffic Engineering exploits modern label switching techniques to build guaranteed bandwidth end-to-end tunnels through an IP/MPLS network of label switched routers (LSRs). These tunnels are a type of label switched path (LSP) and thus are generally referred to as MPLS Traffic Engineering (TE) LSPs. Examples of MPLS TE can be found in RFC</p>

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		<p>3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels dated December 2001, RFC 3784 entitled Intermediate-System-to-Intermediate-System (IS-IS) Extensions for Traffic Engineering (TE) dated June 2004, and RFC 3630, entitled Traffic Engineering (TE) Extensions to OSPF Version 2 dated September 2003, the contents of all of which are hereby incorporated by reference in their entirety.” Vasseur '879, 2:58-3:6.</p> <p>“Establishment of an MPLS TE-LSP from a head-end LSR to a tail-end LSR involves computation of a path through a network of LSRs. Optimally, the computed path is the “shortest” path, as measured in some metric, that satisfies all relevant LSP Traffic Engineering constraints such as e.g., required bandwidth, “affinities” (administrative constraints to avoid or include certain links), etc. Path computation can either be performed by the head-end LSR or by some other entity operating as a path computation element (PCE) not co-located on the head-end LSR. The head-end LSR (or a PCE) exploits its knowledge of network topology and resources available on each link to perform the path computation according to the LSP Traffic Engineering constraints. Various path computation methodologies are available including CSPF (constrained shortest path first). MPLS TE-LSPs can be configured within a single domain, e.g., area, level, or AS, or may also span multiple domains, e.g., areas, levels, or ASes.” Vasseur '879, 3:7-24.</p> <p>“The PCE is an entity having the capability to compute paths between any nodes of which the PCE is aware in an AS or area. PCEs are especially useful in that they are more cognizant of network traffic and path selection within their AS or area, and thus may be used for more optimal path computation. A head-end LSR may further operate as a path computation client (PCC) configured to send a path computation request to the PCE, and receive a response with the computed path, potentially taking into consideration other path computation requests from other PCCs. It is important to note that when one PCE sends a request to another PCE, it acts as a PCC.” Vasseur '879, 3:25-36.</p> <p>“Some applications may incorporate unidirectional data flows configured to transfer time-sensitive traffic from a source (sender) in a computer network to a destination (receiver) in the network in accordance with a certain “quality of service” (QoS). Here, network resources may be reserved for the unidirectional flow to ensure that the QoS associated with the data</p>

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		<p>flow is maintained. The Resource ReSerVation Protocol (RSVP) is a network-control protocol that enables applications to reserve resources in order to obtain special QoS for their data flows. RSVP works in conjunction with routing protocols to, e.g., reserve resources for a data flow in a computer network in order to establish a level of QoS required by the data flow. RSVP is defined in R. Braden, et al., Resource ReSerVation Protocol (RSVP), RFC 2205, the contents of which are hereby incorporated by reference in its entirety. In the case of traffic engineering applications, RSVP signaling (with Traffic Engineering extensions) is used to establish a TE-LSP and to convey various TE-LSP attributes to routers, such as border routers, along the TE-LSP obeying the set of required constraints whose path may have been computed by various means.” Vasseur '879, 3:37-57.</p> <p>“Generally, a tunnel is a logical structure that encapsulates a packet (a header and data) of one protocol inside a data field of another protocol packet with a new header. In this manner, the encapsulated data may be transmitted through networks that it would otherwise not be capable of traversing. More importantly, a tunnel creates a transparent virtual network link between two network nodes that is generally unaffected by physical network links or devices (i.e., the physical network links or devices merely forward the encapsulated packet based on the new header). While one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 3:58-4:3.</p> <p>“Occasionally, a network element (e.g., a node or link) will fail, causing redirection of the traffic that originally traversed the failed network element to other network elements that bypass the failure. Generally, notice of this failure is relayed to the nodes in the network through an advertisement of the new network topology, e.g., an IGP or BGP Advertisement, and routing tables are updated to avoid the failure accordingly. Reconfiguring a network in response to a network element failure using, e.g., pure IP rerouting, can be time consuming. Many recovery techniques, however, are available to provide fast recovery and/or network configuration in the event of a network element failure, including, inter alia, “Fast Reroute”, e.g., MPLS TE Fast Reroute. An example of MPLS TE Fast Reroute is described in Pan, et al., Fast Reroute Extensions to RSVP-TE for LSP Tunnels, RFC 4090, May 2005, which is hereby incorporated by reference as though fully set forth herein.” Vasseur '879, 4:4-21.</p>

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		<p>“Fast Reroute (or FRR) has been widely deployed to protect against network element failures, where “backup tunnels” are created to bypass one or more protected network elements (e.g., links, shared risk link groups (SRLGs), and nodes). When the network element fails, traffic is quickly diverted (“Fast Rerouted”) over a backup tunnel to bypass the failed element, or more particularly, in the case of MPLS, a set of primary TE-LSPs (tunnels) is quickly diverted. Specifically, the point of local repair (PLR) node configured to reroute the traffic inserts (“pushes”) a new label for the backup tunnel, and the traffic is diverted accordingly. Once the failed element is bypassed, the backup tunnel label is removed (“popped”), and the traffic is routed along the original path according to the next label (e.g., that of the original TE-LSP). Notably, the backup tunnel, in addition to bypassing the failed element along a protected primary TE-LSP, also intersects the primary TE-LSP, i.e., it begins and ends at nodes along the protected primary TE-LSP.” Vasseur '879, 4:22-39.</p> <p>“To offer maximum protection, e.g., guaranteed bandwidth, during Fast Reroute, backup tunnels may reserve a configurable amount of bandwidth to ensure that Fast Rerouted traffic from the primary tunnel has a reserved path to follow. For example, the bandwidth reserved for the primary tunnel may also be reserved on the backup tunnel. While this approach provides maximum protection, it also requires a non-negligible amount of network resources (e.g., capacity/bandwidth) and may increase operational complexity.” Vasseur '879, 4:40-48.</p> <p>“Certain techniques are available to efficiently minimize the amount of resources required by the establishment and maintenance of the backup tunnels for Fast Reroute. One such technique is to create zero-bandwidth (“0-BW”) backup tunnels (i.e., tunnels that reserve no bandwidth) to protect non-0-BW primary tunnels. This “best effort” approach does not guarantee that the path followed by the backup tunnel will have enough bandwidth to support the diverted primary tunnel at the time of failure without QoS degradation, however in many situations the path quality of the backup tunnel is sufficient. For instance, if the network is not overly congested, or the backup tunnel follows a non-congested path, there may be enough available bandwidth along the backup tunnel to support the newly rerouted traffic. Also, because primary tunnels often reserve bandwidth in response to “peak” traffic</p>

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		<p>utilization, the amount of traffic over the primary tunnel at the time of failure may be far less than the reserved bandwidth (e.g., at “off-peak” times). Notably, those skilled in the art will understand that in the absence of the above exceptions, a 0-BW backup tunnel may have unacceptable bandwidth (e.g., affecting path quality) to support the diverted traffic.” Vasseur '879, 4:49-5:2.</p> <p>“Currently, head-end nodes (LSRs) may be configured to systematically reroute the primary tunnels affected by the network element failure (e.g., diverted primary tunnels), especially in the case with 0-BW backup tunnels, such as, e.g., by reestablishing a new primary tunnel that follows a path excluding the failed network element. In particular, 0-BW backup tunnels represent a best effort attempt to allow the head-end node to more gracefully reestablish the primary tunnel in response to a failure, since the backup tunnels may not be able to support the diverted traffic without QoS degradation. The systematic reestablishing may potentially result in the reestablishment of a large number of primary tunnels (e.g., up to 3000 for a single network element failure in today's networks). Notably, reestablishing diverted primary tunnels may be undesirable for the network, such as by creating traffic churn, jitter, control plane overloads, etc., as will be understood by those skilled in the art. However, as noted above, there are situations where the backup tunnel may provide acceptable bandwidth, at least, for example, for a period of time (e.g., possibly short) until the failed network element is restored. In these situations, then, it may have been unnecessary to reestablish the diverted primary tunnels. There remains a need, therefore, for a technique that dynamically determines whether to reestablish a diverted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 5:3-28.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g.,</p>

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		<p>with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, 5:32-47.</p> <p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as Resource ReSerVation Protocol (RSVP) Traffic Engineering (TE) signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or type/length/value (TLV) encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object of the RSVP messages to convey the path quality notification.” Vasseur '879, 5:48-58.</p> <p>“In accordance with one aspect of the present invention, the head-end node requests the establishment of the primary tunnel (e.g., a TE-Label Switched Path, TE-LSP), along with a request for Fast Reroute protection of one or more network elements (e.g., with zero-bandwidth, 0-BW backup tunnels) at a PLR node. In addition, the head-end node may include a request for backup tunnel path quality notification, such as, e.g., through the use of the novel Feedback sub-object. The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and sends an error message (e.g., an RSVP PathErr) to the head end node indicating the “Fast Rerouting” of the primary tunnel.” Vasseur '879, 5:59-6:6.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, the PLR node may collect metrics/statistics (e.g., packet drops, path cost, jitter, etc.) of the primary and/or backup tunnels. Once the primary tunnel is Fast Rerouted, the PLR node continues to collect metrics of the backup tunnel, and may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. Notably, the PLR node may be configured to send path quality notifications to the head-end node once, continually, periodically, in response to configurable changes in path quality, etc. Also, as in the case</p>

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		<p>where multiple primary tunnels are Fast Rerouted, the path quality notification may include an indication of which Fast Rerouted primary tunnels in particular have been effected by the changed path quality.” Vasseur ’879, 6:7-23.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message (PathErr), the head-end node may wait for the path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. Notably, in the event the head-end node does not receive a path quality notification for the backup tunnel (e.g., within a configurable time limit), the head-end node may attempt to reestablish the new primary tunnel. Moreover, where the head-end node has multiple primary tunnels being Fast Rerouted, a configurable subset of the primary tunnels may be reestablished, e.g., to reduce congestion of the backup tunnels, and/or to limit the number of reestablished primary tunnels within a given period of time.” Vasseur ’879, 6:24-43.</p> <p>“Advantageously, the novel technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. By providing the head-end node of the primary tunnel with path quality feedback of the backup tunnel, the novel technique avoids reestablishing a potentially large number of tunnels over one or more alternate paths after a failure (and Fast Reroute) if the backup tunnels have acceptable path quality. In particular, the backup tunnels, e.g., 0-BW backup tunnels, may not be congested or subsequently burdened by the Fast Rerouted traffic of the primary tunnel. Also, the failed network element (thus the primary tunnel) may be quickly restored; therefore by not reestablishing the primary tunnel, network jitter, churn, etc., may be avoided. Further, the dynamic nature of the novel technique alleviates the need for cumbersome manual configuration.” Vasseur ’879, 6:44-59.</p>

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		<p>“FIG. 3 is schematic block diagram of an exemplary signaling (RSVP) message that may be advantageously used with the present invention.” Vasseur '879, 7:6-8.</p> <p>“FIG. 5 is a schematic block diagram of the computer network in FIG. 1 showing Fast Reroute protection of a primary tunnel using a backup tunnel in accordance with the present invention.” Vasseur '879, 7:12-15.</p> <p>“FIG. 6 is a schematic block diagram of the computer network in FIG. 5 showing an unacceptable backup tunnel path quality and resultant reestablishing of the primary tunnel in accordance with the present invention.” Vasseur '879, 7:16-19.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention.” Vasseur '879, 7:20-24.</p> <p>“FIG. 1 is a schematic block diagram of an exemplary computer network 100 comprising a plurality of nodes A-F, such as routers or other network devices, interconnected as shown. The nodes may be a part of one or more autonomous systems, routing domains, or other networks or subnetworks. For instance, routers A and E may be provider edge (PE) devices of a provider network, (e.g., a service provider network) that are interconnected to one or more customer networks through customer edge (CE) devices (not shown, while the remaining nodes B-D and F may be core provider (P) devices, as will be understood by those skilled in the art. Those skilled in the art will also understand that the nodes A-F may be any nodes within any arrangement of computer networks, and that the view shown herein is merely an example. For example, the nodes may be configured as connections to/from one or more virtual private networks (VPNs), as will be understood by those skilled in the art. These examples are merely representative. Those skilled in the art will understand that any number of routers, nodes, links, etc. may be used in the computer network 100 and connected in a variety of ways, and that the view shown herein is for simplicity.” Vasseur '879, 7:29-49.</p>

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		<p>“Data packets may be exchanged among the computer network 100 using predefined network communication protocols such as the Transmission Control Protocol/Internet Protocol (TCP/IP), User Datagram Protocol (UDP), Asynchronous Transfer Mode (ATM) protocol, Frame Relay protocol, Internet Packet Exchange (IPX) protocol, etc. Routing information may be distributed among the routers of the computer network using predetermined Interior Gateway Protocols (IGPs), such as conventional distance-vector protocols or, illustratively, link-state protocols, through the use of IGP Advertisements.” Vasseur '879, 7:50-60.</p> <p>“FIG. 2 is a schematic block diagram of an exemplary router 200 that may be advantageously used with the present invention, e.g., as an edge router or a core router. The router comprises a plurality of network interfaces 210, a processor 220, and a memory 240 interconnected by a system bus 250. The network interfaces 210 contain the mechanical, electrical and signaling circuitry for communicating data over physical links coupled to the network 100. The network interfaces may be configured to transmit and/or receive data using a variety of different communication protocols, including, inter alia, TCP/IP, UDP, ATM, synchronous optical networks (SONET), wireless protocols, Frame Relay, Ethernet, Fiber Distributed Data Interface (FDDI), etc.” Vasseur '879, 7:61-8:6.</p> <p>“The memory 240 comprises a plurality of storage locations that are addressable by the processor 220 and the network interfaces 210 for storing software programs and data structures associated with the present invention. The processor 220 may comprise necessary elements or logic adapted to execute the software programs and manipulate the data structures. A router operating system 242 (e.g., the Internetworking Operating System, or IOS™, of Cisco Systems, Inc.), portions of which is typically resident in memory 240 and executed by the processor, functionally organizes the router by, inter alia, invoking network operations in support of software processes and/or services executing on the router. These software processes and/or services may comprise routing services 247, Traffic Engineering (TE) services 244, and RSVP services 249. It will be apparent to those skilled in the art that other processor and memory means, including various computer-readable media, may be used to store and execute program instructions pertaining to the inventive technique described herein.” Vasseur '879, 8:7-26.</p>

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		<p>“Routing services 247 contain computer executable instructions executed by processor 220 to perform functions provided by one or more routing protocols, such as IGP (e.g., OSPF and IS-IS), IP, BGP, etc. These functions may be configured to manage a forwarding information database (not shown) containing, e.g., data used to make forwarding decisions. Routing services 247 may also perform functions related to virtual routing protocols, such as maintaining VRF instances (not shown) as will be understood by those skilled in the art.” Vasseur ’879, 8:27-36.</p> <p>“RSVP services 249 contain computer executable instructions for implementing RSVP and processing RSVP messages in accordance with the present invention. RSVP is described in RFC 2205, entitled Resource ReSerVation Protocol (RSVP), and in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels, both as incorporated above.” Vasseur ’879, 8:37-42.</p> <p>“TE services 244 contain computer executable instructions for operating TE functions in accordance with the present invention. Examples of Traffic Engineering are described in RFC 3209, RFC 3784, and RFC 3630 as incorporated above, and in RFC 3473, entitled, Generalized Multi-Protocol Label Switching (GMPLS) Signaling Resource ReSerVation Protocol-Traffic Engineering (RSVP-TE) Extensions dated January 2003, which is hereby incorporated by reference in its entirety. A TE database (TED, not shown) may be illustratively resident in memory 240 and used to store TE information provided by the routing protocols, such as IGP, BGP, and/or RSVP (with TE extensions, e.g., as described herein), including, inter alia, path quality information as described herein. The TED may be illustratively maintained and managed by TE services 244.” Vasseur ’879, 8:43-57.</p> <p>“Changes in the network topology may be communicated among routers 200 using a link-state protocol, such as the conventional OSPF and IS-IS protocols. Suppose, for example, that a communication link fails or a cost value associated with a network node changes. Once the change in the network’s state is detected by one of the routers, that router may flood an IGP Advertisement communicating the change to the other routers in the network. In this manner, each of the routers eventually “converges” to an identical view of the network topology.” Vasseur ’879, 8:58-67.</p>

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		<p>“In one embodiment, the routers described herein are IP routers that implement Multi-Protocol Label Switching (MPLS) and operate as label switched routers (LSRs). In one simple MPLS scenario, at an ingress to a network, a label is assigned to each incoming packet based on its forwarding equivalence class before forwarding the packet to a next-hop router. At each router, a forwarding selection and a new substitute label are determined by using the label found in the incoming packet as a reference to a label forwarding table that includes this information. At the network egress, a forwarding decision is made based on the incoming label but optionally no label is included when the packet is sent on to the next hop. In some network configurations, one hop prior to the network egress, a penultimate hop popping (PHP) operation may be performed. Particularly, because the hop prior to the network egress (the penultimate hop) is attached to the network egress, the label is no longer needed to assure that the traffic follows a particular path to the network egress. As such, the PHP-enabled device “pops” the labels from the traffic before forwarding the traffic to the network egress, e.g., using conventional or native (IP) routing, thereby alleviating the task of removing the labels at the network egress.” Vasseur '879, 9:1-23.</p> <p>“The paths taken by packets that traverse the network in this manner are referred to as label switched paths (LSPs) or Traffic Engineering (TE)-LSPs. An example TE-LSP is shown as the thick line and arrow (T1) between a head-end node (router A) and a tailend node (router E) in FIG. 1. Establishment of a TE-LSP requires computation of a path, signaling along the path, and modification of forwarding tables along the path. MPLS TE establishes LSPs that have guaranteed bandwidth under certain conditions. Illustratively, the TE-LSPs may be signaled through the use of the RSVP protocol (with Traffic Engineering extensions), and in particular, RSVP TE signaling messages. Notably, when incorporating the use of PCEs (described below), the path computation request (and response) between PCC and PCE can be exchanged in accordance with a protocol specified in Vasseur, et al., Path Computation Element (PCE) Communication Protocol (PCEP)—Version 1—<draft-vasseur-pce-pcep-02.txt>, Internet Draft, September 2005, the contents of which are hereby incorporated by reference in its entirety. It should be understood that the use of RSVP or PCEP serves only as an example, and that other communication protocols may be used in accordance with the present invention.” Vasseur '879, 9:24-45.</p>

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		<p>“In accordance with RSVP, to request a data flow (TE-LSP) between a sender and a receiver, the sender may send an RSVP path request (Path) message downstream to the receiver along a path (e.g., a unicast route) to identify the sender and indicate e.g., bandwidth needed to accommodate the data flow, along with other attributes of the TE-LSP. The Path message may contain various information about the data flow including, e.g., traffic characteristics of the data flow. Also in accordance with the RSVP, a receiver establishes the TE-LSP between the sender and receiver by responding to the sender's Path message with a reservation request (Resv) message. The reservation request message travels upstream hop-by-hop along the flow from the receiver to the sender. The reservation request message contains information that is used by intermediate nodes along the flow to reserve resources for the data flow between the sender and the receiver, to confirm the attributes of the TE-LSP, and provide a TE-LSP label. If an intermediate node in the path between the sender and receiver acquires a Path message or Resv message for a new or established reservation (TE-LSP) and encounters an error (e.g., insufficient resources, failed network element, etc.), the intermediate node generates and forwards a path or reservation error (PathErr or ResvErr, hereinafter Error) message to the sender or receiver, respectively.” Vasseur '879, 9:46-10:2.</p> <p>“FIG. 3 is a schematic block diagram of portions of a signaling message 300 (e.g., RSVP message, such as Path, Resv or Error) that may be advantageously used with the present invention. Message 300 contains, inter alia, a common header 310 and one or more signaling protocol specific objects 320, such as an LSP-ATTRIBUTE object 330. The common header 310 may comprise a source address 312 and destination address 314, denoting the origination and requested termination of the message 300. Protocol specific objects 320 contain objects necessary for each type of message 300 (e.g., Path, Resv, Error, etc.). For instance, a Path message may have a sender template object, Tspec object, Previous-hop object, etc. The LSP-ATTRIBUTE object 330, for instance, may be used to signal attributes and/or information regarding an LSP (tunnel). To communicate this information, LSP-ATTRIBUTE object 330 (as well as specific objects 320) may include various type/length/value (TLV) encoding formats and/or flags, as will be understood by those skilled in the art. An example of an LSP-ATTRIBUTE object is further described in Farrel, et al., Encoding of Attributes for Multiprotocol Label Switching (MPLS) Label Switched Path (LSP) Establishment Using RSVP-TE <draft-ietf-mpls-rsvpte-attributes-05.txt>,</p>

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		<p>Internet Draft, May 2005, which is hereby incorporated by reference as though fully set forth herein. A Resv message, on the other hand, may have specific objects 320 for a label object, session object, filter spec object, etc., in addition to the LSP-ATTRIBUTE object 330. Error messages 300 (e.g., PathErr or ResvErr), may also have specific objects 320, such as for defining the type of error, etc.” Vasseur '879, 10:3-31.</p> <p>“It should be noted that in accordance with RSVP signaling, the state of the TE-LSP is refreshed on a timed interval, e.g., every thirty seconds, in which RSVP Path and Resv messages are exchanged. This timed interval is configurable by a system administrator. Moreover, various methods understood by those skilled in the art may be utilized to protect against route record objects (RROs) contained in signaling messages for a TE-LSP in the event security/privacy is desired. Such RRO filtering prevents a head-end node of the TE-LSP from learning of the nodes along the TE-LSP, i.e., nodes within the provider network.” Vasseur '879, 10:4-42.</p> <p>“Although the illustrative embodiment described herein is directed to MPLS, it should also be noted that the present invention may advantageously apply to Generalized MPLS (GMPLS), which pertains not only to packet and cell-based networks, but also to Time Division Multiplexed (TDM) and optical networks. GMPLS is well known and described in RFC 3945, entitled Generalized Multi-Protocol Label Switching (GMPLS) Architecture, dated October 2004, and RFC 3946, entitled Generalized Multi-Protocol Label Switching (GMPLS) Extensions for Synchronous Optical Network (SONET) and Synchronous Digital Hierarchy (SDH) Control, dated October 2004, the contents of both of which are hereby incorporated by reference in their entirety.” Vasseur '879, 10:43-55.</p> <p>“To obviate delays associated with updating routing tables when attempting to avoid a failed network element (i.e., during convergence), some networks have employed MPLS TE Fast Reroute (FRR). MPLS Fast Reroute is a technique that may be used to quickly divert (“Fast Reroute”) traffic around failed network elements in a TE-LSP. MPLS Fast Reroute is further described, for example, by Fast Reroute Extensions to RSVP-TE for LSP Tunnels, as incorporated by reference above. According to the technique, one or more network elements (e.g. links or nodes) in a network are protected by backup tunnels following an alternate</p>

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		<p>path. If a failure occurs on a protected link or node, TE-LSPs (and consequently the traffic that they carry) are locally diverted onto an appropriate alternate path (e.g., a “backup tunnel”) by the node immediately upstream from the failure. The backup tunnel acts as a Fast Reroute path for the primary TE-LSP and obviates delays associated with other measures, such as tearing down the primary TE-LSP after having gracefully diverted the TE-LSPs affected by the failure, should an alternate path around the failed network element exist. In the event of a failure of a protected element the head-end node of the backup tunnel (or a “point of local repair,” PLR node) may quickly begin diverting traffic over the backup tunnel with minimal disruption to traffic flow. Those skilled in the art will understand that MPLS Fast Reroute is one example of link or node failure protection, and that other known correction mechanisms may be used in accordance with the present invention. As mentioned above, however, the head-end node of the Fast Rerouted primary tunnel may attempt to reestablish the primary tunnel in response to learning of the protected element failure, particularly in the case where the backup tunnel is a zero-bandwidth (0-BW) tunnel. The attempt to reestablish the primary tunnel has conventionally been a systematic response to Fast Rerouting (diverting) of the primary tunnel, regardless of the path quality of the backup tunnel.” Vasseur ’879, 10:56-11:23.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 11:24-39.</p>

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		<p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as RSVP TE signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or TLV encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object 330 of the RSVP messages 300 to convey the path quality notification.” Vasseur '879, 11:40-49.</p> <p>“FIG. 4 is a schematic block diagram illustrating the format of an extension object (TLV) 400, such as a Fast Reroute Feedback object, that may be advantageously used with the present invention. The extension object (or sub-object) 400 is illustratively embodied as a TLV contained in an LSP-ATTRIBUTE object 330 of an RSVP message 300 and is extended to carry backup (and/or primary) tunnel path quality information. To that end, the “Feedback” object 400 is organized to include a Type field 405 containing a predetermined type value signifying the specific content of the object 400. The Length field 410 is a variable length value. The TLV encoded format may also comprise one or more non-ordered sub-TLVs 450 carried within the TLV “payload” (e.g. Value field 415), each having a Type field 455, Length field 460, and Value field 465. The fields of the TLV 400 and sub-TLV(s) 450 are used in a variety of manners, including as described herein, according to the present invention.” Vasseur '879, 11:59-12:8.</p> <p>“In accordance with one aspect of the present invention, the head-end node (e.g., router A) requests the establishment of the primary tunnel (e.g., T1), such as a TE-LSP. Notably, the head-end node may be a head-end node for multiple primary tunnels, as will be understood by those skilled in the art. Along with the primary tunnel establishment, the head-end node may also request Fast Reroute protection of one or more network elements (e.g., all intermediate network elements) at a PLR node (e.g., router B as shown). Note that each intermediate node along the primary tunnel may act as a PLR node, and that router B is shown merely for simplicity. Illustratively, the Fast Reroute protection may be embodied as one or more zero-bandwidth (0-BW) backup tunnels at the PLR node (e.g., BT1). Those skilled in the art will also understand that the PLR node may protect more than one primary</p>

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		<p>tunnel originating at more than one corresponding head-end node (not shown).” Vasseur '879, 12:9-25.</p> <p>“The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. For example, various connectivity verification protocols, such as, e.g., Bidirectional Forwarding Detection (BFD), IGP “Hello” packets, BGP KEEPALIVE messages, etc., may be used to detect a failure of a network element, as will be understood by those skilled in the art. Furthermore, other lower-layer failure detection mechanisms (e.g. optical or SONET/SDH alarms) may be used to detect a network element failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and may send an error message (e.g., an RSVP PathErr 300, such as a conventional “tunnel locally repaired” message) to the head end node indicating the “Fast Rerouting” of the primary tunnel. FIG. 5 is a schematic block diagram of the computer network 100 in FIG. 1 showing Fast Reroute protection of the primary tunnel T1 (e.g., in response to a protected network element, router C, failure, indicated with an overlaid “X”) using a backup tunnel BT1 in accordance with the present invention. Traffic originally received at the PLR node (router B) over the primary tunnel is now diverted over the backup tunnel to a remerge point (router D) of the primary tunnel, as will be understood by those skilled in the art.” Vasseur '879, 12:42-65.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, each PLR node may collect metrics/statistics of the primary and/or backup tunnels. For instance, example metrics may comprise, inter alia, packet drops, path cost, jitter, delay, bandwidth, etc. The PLR node may collect the metrics through traffic monitoring, probes, independent calculations, and/or through cooperation with protected nodes of the primary tunnel (primary nodes) and nodes of the backup tunnel (backup nodes), e.g., transmitting path quality notifications. Once the primary tunnel is Fast Rerouted (i.e., diverted after failure of a protected network element), the PLR node continues to collect metrics of the backup tunnel. (Alternatively, metrics of the backup tunnel may be collected only after Fast Reroute, and not prior to Fast Reroute.)” Vasseur '879, 12:66-13:12.</p>

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		<p>“For example, a path cost increase from the primary tunnel to the backup tunnel may be computed (and transmitted, below) by the PLR node prior to the failure (or during the failure while the primary TE-LSP is diverted onto the backup tunnel) using its own routing tables. The path cost increase may be calculated as a difference between the entire length (head-end node to tail-end node) of the primary and backup tunnels, or just the difference between the protected segment of the primary tunnel and the backup tunnel (PLR node to remerge point). Also, a jitter increase of the primary and backup tunnels, which may be generally described as a difference between inter-arrival of packets, may be monitored using various known techniques, such as, e.g., sending probe packets (probes) from the PLR node to the remerge point. For instance, probes may determine that packets arrive at the remerge point along the primary tunnel from the PLR node consistently, e.g., every 10 milliseconds (ms) (e.g., an average value). After Fast Reroute, however, probes may determine that packets do not arrive at the remerge point along the backup tunnel from the PLR node consistently, e.g., one may arrive in 10 ms, another in 100 ms, another in 50 ms, etc. The non-constant rate of received packets (jitter) may be undesirable, e.g., in particular for voice over IP (VoIP) traffic, as will be understood by those skilled in the art.” Vasseur '879, 13:13-36.</p> <p>“As a further example, packet dropping may be monitored for the primary and backup tunnels prior to and after Fast Reroute (respectively). For instance, based on the tunnel label of the dropped packet, primary nodes and/or backup nodes may be able to distinguish which tunnel corresponds to the dropped packets. Each of the primary and/or backup nodes collect packet drop data, and periodically inform the PLR node of the number of dropped packets (e.g., through a corresponding Feedback object 400). In the case of a backup node, the PLR node receiving the notification may interpret the association of the backup tunnel label and the primary tunnel label to reference an appropriate primary tunnel. Those skilled in the art will understand that the above path quality metrics are merely examples, and that any other metrics/statistics useful for determining path quality of the backup tunnel may be used in accordance with the present invention (e.g., delay, bandwidth, etc.). Further, the path quality information may be measured and compared in a variety of manners, such as, e.g., as a difference between primary and backup tunnels before and after Fast Reroute, or simply the difference between the backup tunnel before and after Fast Reroute, etc.” Vasseur '879, 13:37-58.</p>

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		<p>“Also after the primary tunnel is Fast Rerouted, the PLR node may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. For instance, the novel Feedback object 400 may include one or more sub-TLVs 450 corresponding to metrics/statistics, as described above. Notably, the path quality information pertaining to a particular metric/statistic may be transmitted as total values for interpretation by the head-end node (e.g., to determine the difference), or as PLR-node-computed differences (e.g., between the primary and backup tunnels, or before and after Fast Reroute). For instance, if the delay of the primary tunnel (along the protected segment) prior to Fast Reroute were 2 ms, and after Fast Reroute the delay of the backup tunnel were 5 ms, the notification may be configured to include both values 2 ms and 5 ms, or instead simply the difference, i.e., an increase of 3 ms.” Vasseur '879, 13:59-14:8.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message 300 (PathErr), the head-end node may wait for at least one path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. Also, any number of metrics may be used in the determination, e.g., as configured by a system administrator. For example, using the metrics described above, a head-end node may be configured to reestablish the primary tunnel in response to i) a certain number of packet drops, ii) a percent increase in packet drops, iii) a number of packet drops and a percent increase in path cost, iv) a percent increase in path cost and a percent increase in jitter, etc. Those skilled in the art will understand that these are merely examples of possible path quality comparisons and reestablishment determinations, and that any comparisons to any metrics at any configurable changes may be used in accordance with the present invention.” Vasseur '879, 14:41-59.</p> <p>“If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. FIG. 6 is a schematic block diagram of the computer network 100 in FIG. 5 showing an unacceptable backup tunnel path quality (dotted line and arrow) and resultant reestablishment of the primary tunnel in</p>

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		<p>accordance with the present invention. Those skilled in the art will understand that the reestablished primary tunnel may traverse one or more primary nodes (not shown), and that it may be computed specifically to avoid the failed network element and any network elements of the unacceptable backup tunnel. Those skilled in the art will also understand that the attempt to reestablish the primary tunnel may not be able to find an acceptable path, in which case the head-end node may continue to use the unacceptable backup tunnel or other unacceptable rerouted path.” Vasseur '879, 14:60-15:9.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention. The procedure 700 starts at step 705, and continues to step 710, where a head-end node (e.g., router A) establishes a primary tunnel (e.g., T1) to a destination tail-end node (e.g., router E). During or after establishment, the head-end node requests protection of the primary tunnel at step 715, and in response, PLR nodes along the primary tunnel (e.g., router B) create backup tunnels (e.g., BT1) to protect the primary tunnel in step 720. (Those skilled in the art will understand that backup tunnels around the protected network elements may already exist at the PLR node, and that “creating backup tunnels” in step 720 implies an association with pre-existing backup tunnels.) As mentioned above, these backup tunnels may illustratively be embodied as 0-BW backup tunnels. In accordance with the present invention, the head-end node may additionally request backup tunnel path quality notification from the PLR nodes in step 725, such as, e.g., through the use of empty corresponding Feedback objects in RSVP (Path) messages 300, as described above.” Vasseur '879, 15:37-58.</p> <p>“The procedure 700 continues to FIG. 7B (step “A”), where in step 750 the PLR node detecting the failure diverts (“Fast Reroutes”) the primary tunnel traffic to the backup tunnel and sends an error message (e.g., an RSVP (Error) message 300) to the head end node, e.g., a “tunnel locally repaired” message. The detecting PLR node continues to collect path quality information for the backup tunnel in step 755 and at step 760 determines whether to send the path quality notification to the head-end node. For example, as mentioned above, the PLR node may be configured to continually send notifications, or periodically, or in response to a configurable change in path quality, etc. Also as mentioned above, the PLR</p>

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		<p>node may be configured to send either the actual path quality information or the change (difference) in path quality. (As further mentioned above, metrics of the backup tunnel may alternatively be collected only after Fast Reroute, and not prior to Fast Reroute.).” Vasseur '879, 16:4-20.</p> <p>“If the PLR node decides to send the notification in step 760, then the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification in step 765, e.g., based on one or more configurable thresholds, percentages, etc., as described above. If the backup tunnel is currently maintaining an acceptable quality for the traffic flow in step 770, the head-end node may continue to utilize the backup tunnel, and the PLR node continues to collect path quality information in step 755 to detect any change in quality. Otherwise, if the backup tunnel quality is not acceptable in step 770, the head-end node may attempt to reestablish the primary tunnel in step 775. Notably, as mentioned above, if the PLR node has not sent any notification (step 760) within a configurable period of time in step 780, e.g., due to a backup tunnel failure, over-congestion, etc., then the head-end node may also attempt to reestablish the primary tunnel in step 775 accordingly. Moreover, as described above, in the event more than one primary tunnel is Fast Rerouted for the head-end node, various techniques to reestablish one or more of the primary tunnels may be used (e.g., as many tunnels as necessary, a configurable subset of tunnels, all tunnels, the congested tunnels, etc.). The procedure 700 ends in step 785.” Vasseur '879, 16:21-43.</p> <p>“While there has been shown and described an illustrative embodiment that dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network, it is to be understood that various other adaptations and modifications may be made within the spirit and scope of the present invention. For example, the invention has been shown and described herein using “Fast Reroute,” e.g., MPLS TE Fast Reroute (FRR). However, the invention in its broader sense is not so limited, and may, in fact, be used with other network element protection and failure correction mechanisms as will be understood by those skilled in the art. Moreover, while the above description describes performing the technique at the head-end node and PLR node, the invention may also be advantageously used with PCEs. In addition, while one</p>

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		<p>example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 16:63-17:13.</p> <p><u>Rustogi discloses:</u></p> <p>“An example method includes identifying a fault condition in a network, and evaluating pseudowires affected by the fault condition in order to make a determination as to whether an aggregate failure occurred in the network for a group of pseudowires. The method also includes communicating a group message indicating that the group of pseudowires is associated with the fault condition. The group message includes a group identification (ID), which identifies the group of pseudowires, and the group message includes a pseudowire group label identifying an in-band aggregate channel. More specifically, the pseudowire group label can be applicable to static pseudowires. In more detailed embodiments, the group ID identifies the group of pseudowires that are associated with an attachment circuit, a label switched path, or a port. Internal mappings can be maintained such that a plurality of pseudowires is mapped to individual interfaces of network elements in the network.”</p> <p>Rustogi, Abstract.</p>

No.	'821 Patent Claim 11	The Reference
		<p>FIG. 1A</p> <p>Rustogi, FIG. 1A.</p>

No.	'821 Patent Claim 11	The Reference
		<pre> graph TD START([START]) --> 100[100 ~ A GIVEN NETWORK ELEMENT CAN IDENTIFY A FAULT CONDITION IT RECEIVES] 100 --> 102[102 ~ THE NETWORK ELEMENT EVALUATES PSEUDOWIRES IN ORDER TO DETERMINE WHETHER A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED] 102 --> 104[104 ~ IF ONLY A FEW PSEUDOWIRES ARE AFFECTED BY THE FAULT CONDITION, THE GROUPING PROTOCOL MAY OPTIONALY NOT BE USED, WHERE A MORE ROUTINE MESSAGING PROTOCOL COULD BE EMPLOYED] 104 --> 106[106 ~ IF A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED, THE GROUPING PROTOCOL IS EMPLOYED TO MINIMIZE THE MESSAGES THAT ARE SENT, RECEIVED, AND PROCESSED IN THE NETWORK] 106 --> 108[108 ~ IN THE CASE OF AN AGGREGATE FAILURE, AN AGGREGATE CHANNEL CAN BE USED TO OFFER APPROPRIATE GROUP MESSAGING. THE INDIVIDUAL MESSAGES THAT CONVEY GROUP IDENTIFICATIONS (IDS) CAN QUICKLY SIGNIFY THE FAULT CONDITION TO NETWORK PEERS] 108 --> END([END]) </pre> <p>FIG. 1B</p> <p>Rustogi, FIG. 1B.</p>

No.	'821 Patent Claim 11	The Reference
		<p>Diagram illustrating the network topology and pseudowire mapping:</p> <ul style="list-style-type: none"> Switching PE 1: Handles pseudowires labeled A and E. It maps Group Identification A to pseudowire group label A and Group Identification E to pseudowire group label E. Switching PE 2: Handles pseudowires labeled C and D. It maps Group Identification C to pseudowire group label C and Group Identification D to pseudowire group label D. Switching PE 3: Handles pseudowires labeled B and D. It maps Group Identification B to pseudowire group label B and Group Identification D to pseudowire group label D. Terminating PE 1: Has two interfaces, A and B. Interface A and B fail if both are down. Terminating PE 2: Has one interface C. Terminating PE 3: Has one interface D. <p>Failure detection (1) monitors interfaces A and B. If both fail, it triggers a response (e.g., failover).</p>

Rustogi, FIG. 2.

No.	'821 Patent Claim 11	The Reference
		<p>FIG. 3</p> <p>Rustogi, FIG. 3.</p>

No.	'821 Patent Claim 11	The Reference
		<p>FIG. 4</p> <p>76</p> <p>Rustogi, FIG. 4.</p>

No.	'821 Patent Claim 11	The Reference
		<p>FIG. 5</p> <p>80</p> <p>Rustogi, FIG. 5.</p>

No.	'821 Patent Claim 11	The Reference																				
		<p style="text-align: center;">74</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>AC PORT</th><th>LSP</th><th>PW GROUP</th><th>PW GROUP LABEL IDENTIFICATION</th></tr> </thead> <tbody> <tr> <td>INTERFACE A</td><td>LSP 1</td><td>A</td><td>A</td></tr> <tr> <td>INTERFACE B</td><td>LSP 1</td><td>B</td><td>B</td></tr> <tr> <td>REMOTE INTERFACE C ON TERMINATING PE 2</td><td>LSP 2</td><td>F</td><td>F</td></tr> <tr> <td>REMOTE INTERFACE D ON TERMINATING PE 3</td><td>LSP 2</td><td>G, H</td><td>G, H</td></tr> </tbody> </table> <p style="text-align: center;">FIG. 6</p> <p>Rustogi, FIG. 6.</p> <p>“The field of communications has become increasingly important in today's society. In particular, the ability to quickly and to effectively provision connections presents a significant challenge to component manufacturers, system designers, and network operators. Multiprotocol Label Switching (MPLS) is a mechanism in telecommunications networks that carries data from one network node to the next. Layer 2 services (such as Frame Relay, Asynchronous Transfer Mode, and Ethernet) can be emulated over an MPLS backbone by encapsulating the Layer 2 Protocol Data Units (PDUs) and transmitting them over pseudowires. Protocols exist for establishing and maintaining the pseudowires. Certain issues have arisen in pseudowire scenarios, where faults are detected in the network.” Rustogi, ¶ [0002].</p> <p>“FIG. 1A is a simplified block diagram of a communication system for providing pseudowire group labels in a network environment in accordance with one embodiment of the present disclosure.” Rustogi, ¶ [0004].</p>	AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION	INTERFACE A	LSP 1	A	A	INTERFACE B	LSP 1	B	B	REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F	REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H
AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION																			
INTERFACE A	LSP 1	A	A																			
INTERFACE B	LSP 1	B	B																			
REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F																			
REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H																			

No.	'821 Patent Claim 11	The Reference
		<p>“FIG. 1B is a simplified flowchart depicting one possible, generic operational flow associated with the communication system.” Rustogi, ¶ [0005].</p> <p>“FIG. 2 is a simplified block diagram of an example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0006].</p> <p>“FIG. 3 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0007].</p> <p>“FIG. 4 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0008].</p> <p>“FIG. 5 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0009].</p> <p>“FIG. 6 is a simplified table of an example set of pseudowire group provisioning parameters in accordance with one embodiment.” Rustogi, ¶ [0010].</p> <p>“FIG. 1A is a simplified block diagram of a communication system 10 for providing pseudowire group labels in accordance with one example of the present disclosure. FIG. 1A includes a customer edge 1 (CE1) 12, a CE2 14, and a CE3 16, where a number of faults 18 are shown as propagating in the network. Typically, when an error or a failure occurs in the network (e.g., an interface failure, a pulled cable, a switch failure, hardware/software failures generally, etc.), messages are sent to various network devices in order to inform them of these fault conditions. Faults 18 of FIG. 1A are indicative of such messages, where the underlying fault condition (being signaled by the messages) can occur virtually anywhere in a network (e.g., in a customer edge, in provider equipment, etc.). FIG. 1A also includes a terminating provider equipment 1 (TPE1) 20, a TPE2 22, a TPE3 24, a switching provider edge 1 (SPE1) 30, and a SPE2 32. In one particular example implementation, the TPEs and SPEs of FIG. 1A are switches that are configured to exchange data in a network environment.” Rustogi, ¶ [0012].</p>

No.	'821 Patent Claim 11	The Reference
		<p>“SPE1 30 may include a pseudowire (PW) group module 54 a, a processor 56 a, and a memory element 58 a. In a similar fashion, TPE2 22 may include a pseudowire group module 54 b, a processor 56 b, and a memory element 58 b. FIG. 1A also includes a number of static pseudowires 42, 44, and 46. A set of static/dynamic pseudowires 48, 50 is also provided. Note that the group labeling protocol discussed herein can be executed between individual SPEs, TPEs, or between any combinations of these elements.” Rustogi, ¶ [0013].</p> <p>“In one particular arrangement, communication system 10 is provided in conjunction with a Layer-2 virtual private networks (L2VPN)/operation, administration, and maintenance (OAM) L2VPN/OAM framework. The OAM framework is intended to provide OAM layering across L2VPN services, pseudowires, and packet switched network (PSN) tunnels. Communication system 10 may include any suitable networking protocol or arrangement that provides a communicative platform for communication system 10. Thus, communication system 10 may include a configuration capable of transmission control protocol/internet protocol (TCP/IP) communications for the transmission and/or reception of packets in a network. Communication system 10 may also operate in conjunction with a user datagram protocol/IP (UDP/IP) or any other suitable protocol where appropriate and based on particular needs.” Rustogi, ¶ [0014].</p> <p>“Failure detection and failure notification for static pseudowires is inadequate, where sluggish signaling can result in poor scalability for failover performance. Typically, static pseudowires are manually configured at respective endpoints, where control channels are absent for providing group level signaling messages. Aggregate channels are significant tools for providing suitable scalability in the network, but no such aggregate channel exists for static pseudowires. For dynamic pseudowires, such an aggregate channel may be present in the form of a label distribution protocol (LDP) directed session. However, no such protocol exists for static pseudowire configurations such that an in-band aggregate channel would be available for static pseudowires.” Rustogi, ¶ [0016].</p> <p>“Communication system 10 can address the aforementioned issues (and others) by offering a pseudowire group label that can represent an aggregate channel for groups of static pseudowires. The aggregate channel of communication system 10 can allow for improved</p>

No.	'821 Patent Claim 11	The Reference
		<p>scalability of failover performance. In accordance with one potential configuration of communication system 10, a pseudowire group label is representative of a group of static pseudowires transported over a label switched path (LSP). The pseudowire group label can identify the aggregate channel, which captures the hierarchy relevant to OAM mechanisms. Additionally, the groups represented by the group identification (ID) can be mutually exclusive, where a pseudowire is part of only one group. In other embodiments, a pseudowire can be part of multiple groups, or be configured in any other suitable manner based on particular network arrangements.” Rustogi, ¶ [0017].</p> <p>“During operations, and with brief reference to FIG. 1B, a given network element can identify a fault condition it receives (at step 100) and, subsequently, evaluate pseudowires in order to determine whether a sufficient number of pseudowires have been affected. This is reflected by step 102. If only a few pseudowires are affected by the fault condition, the grouping protocol outlined herein may have only nominal value, where there could be an option to simply communicate the fault condition in a more routine manner, as outlined in step 104. However, if a sufficient number of pseudowires have been affected, the grouping protocol outlined herein can be employed to minimize the messages that are sent, received, and processed in the network. This is reflected as step 106. Note that the determination (as to whether a sufficient number of pseudowires have been impacted by the fault condition) can involve accessing internal tables such that a quick mapping can occur to determine if an aggregate failure has occurred. As used herein, the term ‘aggregate failure’ simply connotes that a sufficient number of pseudowires have experienced the fault condition such that an aggregate channel can be employed to offer appropriate group messaging. For the aggregate failure condition, the individual messages that convey Group identifications (IDs) can quickly signify the fault condition to network peers, as shown in step 108.” Rustogi, ¶ [0018].</p> <p>“In specific regards to OAM mechanisms, OAM messages typically result from common failures in the network. These fault conditions can be aggregated such that they are signaled as a single message, which could represent a group of failed pseudowires (as opposed to sending individual messages for each failed pseudowire). Hence, a single message could be sent to represent all the relevant OAM messages propagating in communication system 10.</p>

No.	'821 Patent Claim 11	The Reference
		<p>The group label that propagates in communication system 10 provides an architecture with a significant level of aggregation for failed pseudowires (i.e., pseudowires being affected by a given fault condition), particularly for OAM messaging. Moreover, the in-band aggregate channel of communication system 10 is based (at least in part) on the evolving trends of OAM mechanisms, which are required to be fast, responsive, and capable of being implemented in hardware or software. Additionally, in-band OAM protocols are a better measure of the path availability.” Rustogi, ¶ [0020].</p> <p>“In operation of one example implementation, a group label can represent the tuple <attachment circuit (AC) port level grouping, LSP>. This could signify that all pseudowires on an AC port (sought for aggregation) traverse a given LSP. Multiple pseudowire groups can exist within an LSP. Similarly, pseudowires on the same AC port (that traverse a different LSP) can use a different pseudowire group label. Alternatively, an administrator may seek to employ a one-to-one mapping between an LSP and a group label. If that were the case, then only one pseudowire group would exist within an LSP. In scenarios where there is no LSP label in the packet (e.g., due to penultimate hop popping), the pseudowire group label can provide the hierarchy that is appropriate.” Rustogi, ¶ [0021].</p> <p>“In one particular example, the group level pseudowire OAM message can be sent with the following label stack: Explicit/Implicit LSP Label+pseudowire group Label+GAL+ACH+pseudowire OAM with grouping TLV (where GAL=Generic Associated Channel Label, ACH=Associated Channel Header, TLV=Type-Length-Value). If there are multiple LSPs, then one group label can be provisioned for each LSP (for each pseudowire group), where per group messages can be sent on each LSP. The group label does not necessarily have a one-to-one mapping to the grouping of pseudowires implied by the Group ID in the grouping TLV. Note also that the group-based aggregate channel is applicable to static pseudowires, as well as for dynamic pseudowires in certain applications.” Rustogi, ¶ [0022].</p> <p>“As discussed herein, the aggregate channel of communication system 10 can be configured in various ways. For example, and with regards to a first option, a separate label may simply be used to identify a pseudowire group within an LSP. The association of an OAM message</p>

No.	'821 Patent Claim 11	The Reference
		<p>and a pseudowire group is straightforward. There could potentially be multiple pseudowire group labels per LSP. As a second option, one group label can be used to identify a common pseudowire group channel on the LSP. In this implementation, one pseudowire group label is provided per LSP. The OAM message association to a pseudowire group is not as simple as the first option. As a third option, one pseudowire is simply designated to convey grouping information (e.g., without using a group label). In this case, there is no need for a pseudowire group label. Again, the OAM message association to a pseudowire group is not as simple as the first option.” Rustogi, ¶ [0023].</p> <p>“Any combination of formatting (for the Group ID and the pseudowire group label) can be used in the group message to be communicated in the network. In one example, only one of these elements is communicated when an aggregate fault condition is detected, or these elements can be combined into a single unique identifier. In the most generic example, a group message would at least include the Group ID (identifying the pseudowires affected by the fault) and a pseudowire group label (identifying an aggregate channel for communicating the group message). In this generic sense, a pipe (the Group ID) within a pipe (the pseudowire group label) is being identified, where the group message is identifying both elements during an aggregate fault condition. Operational details of communication system 10 are described below with reference to FIGS. 2-6. Note that before turning to additional example flows and example embodiments of the present disclosure, a brief overview of the infrastructure of communication system 10 is provided.” Rustogi, ¶ [0024].</p> <p>“CE1 12, CE2 14, and CE3 16 represent devices, infrastructure, equipment, clients, or customers seeking to initiate a data session in communication system 10. These elements may comprise a digital subscriber line access multiplexer (DSLAM), a router, a personal computer, a server, a switch, and/or other devices associated with data propagation. Further, these elements may sit behind, or in front of, one or more of these identified devices. The term ‘CE’ may be inclusive of the devices identified above (e.g., a DSLAM, a switch, etc.), as well as devices used to initiate a communication, such as a console, a proprietary endpoint, a telephone, a cellular telephone, a bridge, a computer, a personal digital assistant (PDA), a laptop or an electronic notebook, or any other device, component, element, or object capable of initiating voice, audio, media, or data exchanges within communication</p>

No.	'821 Patent Claim 11	The Reference
		<p>system 10. The customer element may also include any device that seeks to initiate a communication on behalf of another entity or element, such as a program, a database, or any other component, device, element, or object capable of initiating a voice, a video, text, or a data exchange within communication system 10. Data, as used herein in this document, refers to any type of video, numeric, voice, media, or script data, or any type of source or object code, or any other suitable information in any appropriate format that may be communicated from one point to another.” Rustogi, ¶ [0025].</p> <p>“SPE1 30, SPE2 32, TPE1 20, TPE2 22, and TPE3 24 are network elements that facilitate communications in two directions in a network environment. In one particular example, each of these network elements is a switch configured to exchange data over static and/or dynamic pseudowire links. Further, the traffic exchanged between these components may be directed over an MPLS transport in certain embodiments. As used herein in this Specification, the term ‘network element’ is meant to encompass switches, routers, bridges, gateways, servers, processors, loadbalancers, firewalls, or any other suitable device, component, element, or object operable to exchange or process information in a network environment. Moreover, these network elements may include any suitable hardware, software, components, modules, interfaces, or objects that facilitate the operations thereof. This may be inclusive of appropriate algorithms and communication protocols that allow for the effective exchange of data or information. Along similar design alternatives, any of the internal modules and components of these network elements may be combined in various possible configurations.” Rustogi, ¶ [0029].</p> <p>“Turning to FIG. 2, FIG. 2 is a simplified block diagram of an example system 60 for providing an example use case using per-label switched path (LSP) pseudowire group labels. FIG. 2 includes a TPE1 62, a TPE2 64, a TPE3 66, a SPE1 68, and a SPE2 70. Each pseudowire group is identified, where a group identification (ID) for Group A and Group B is depicted at TPE1 62. Similarly, Groups C, D, and E have Group IDs at SPE1 68. TPE2 64 and TPE3 66 can couple to interfaces C and D, respectively.” Rustogi, ¶ [0031].</p>

No.	'821 Patent Claim 11	The Reference
		<p>“In this particular example, interfaces A and B have failed. Note that there is a multitude of attachment circuits (e.g., 1000 attachment circuits) that are being transported over these interfaces A and B, where the attachment circuits are being tunneled into a corresponding number of pseudowires. For example, there could be 500 attachment circuits on interface A (implicating 500 pseudowires) and 500 attachment circuits on interface B, where the fault condition for the pseudowires should be signaled. In other flawed systems, an architecture would individually signal this fault condition for each pseudowire (e.g., via signaling between TPE1 62 and SPE1 68). Instead of sending 500 messages, a single message can be sent, where a single label (and Group ID) can be used to identify the pseudowires. In this case, the Group ID A is used to signal the fault condition for 300 pseudowires and for 200 pseudowires (i.e., the top two links connecting TPE1 62 and SPE1 68) using a single message (that includes Group Label A and Group ID A). Thus, the status for Group A is quickly communicated to SPE1 68. Similarly, Group ID B can be used to signal the status of the other 500 pseudowires to appropriately convey the status for Group B. More specifically, the message can include Group Label A and Group ID B. Note that all 1000 pseudowires have effectively been accounted for using these Group IDs A and B.” Rustogi, ¶ [0032].</p> <p>“FIG. 3 is a simplified block diagram of an example system 72 for providing another use case for pseudowire group labels. Note that the grouping mechanism outlined herein is not limited to pseudowires that propagate over LSPs. Certain pseudowires can propagate over an LSP and represent one group, where two ports can be provisioned for two different groups (e.g., Group A and Group B). Hence, FIG. 3 is depicting a use case using pseudowire group labels for <port, LSP>mapping. In a general sense, such a configuration is showing how pseudowire mechanics can be used to offer different group signaling, which may be based on various possible implementations. Thus, there is a group level construct corresponding to the group labels that are created such that any OAM protocol can send the appropriate aggregate messages. In this particular example, the signaling for Group ID A, B, C, and D is similar to that of FIG. 2; however, the grouping mechanism has simply changed.” Rustogi, ¶ [0035].</p>

No.	'821 Patent Claim 11	The Reference
		<p>“FIG. 4 is a simplified block diagram of an example system 76 for providing another use case for pseudowire group labels. In this particular example, interface C fails (as shown at TPE2 64). Note that the same logical flow occurs in FIG. 4 in terms of the group signaling, as previously discussed. The group labels in two directions do not have to be the same, where the groupings for the messaging are not necessarily symmetrical. In this particular example, TPE2 64 sends a status for Group E with the corresponding group label (i.e., Group ID E for 300 pseudowires), where that message will have a Group Label E and a Group ID E. Hence, this particular signaling is indicative of 300 pseudowires failing in the network. SPE1 68 can send the status for Group F (where the Group ID F is associated with 300 pseudowires) to TPE1 62, where that message includes a Group Label F and a Group ID F.” Rustogi, ¶ [0036].</p> <p>“FIG. 5 is a simplified block diagram of an example system 80 for providing another use case for pseudowire group labels. In this particular example, interface D fails (as shown at TPE3 66), where all 700 pseudowires fail. In one implementation, TPE3 66 does not have a 700 pseudowire Group ID. Instead, the Group IDs can correspond to 200 and 500 pseudowires, when summed together account for the 700 pseudowires. In this particular example, TPE3 66 sends one message for Group I (representing 200 pseudowires) and another message for Group J (representing 500 pseudowires) to SPE1 68. In response, SPE1 68 sends a message for Group G (representing 200 pseudowires) and another message for Group H (representing 500 pseudowires). Again, the signaling being exchanged between these elements is minimal due to the effective grouping of pseudowires. SPE1 68 also sends a single message for Group I (associated with 200 pseudowires) and Group J (associated with 500 pseudowires) to TPE3 66, which is coupled to interface D. Group ID G is associated with 200 pseudowires, whereas Group ID H is representative of 500 pseudowires.” Rustogi, ¶ [0037].</p> <p>“FIG. 6 is a simplified table 74 illustrating an example set of pseudowire group provisioning parameters for TPE1 62, where these particular provisioning parameters could be relevant to the configuration of FIG. 3. At least in one generic sense, FIG. 2 can reflect one approach for mapping a PW group label to a PW Group ID, while FIGS. 3-5 can reflect a second</p>

No.	'821 Patent Claim 11	The Reference
		<p>approach for such mappings, where table 74 is associated with that second approach.” Rustogi, ¶ [0038].</p> <p>“In particular, table 74 illustrates the mapping between SPE1 68 and TPE1 62. The first column represents the attachment circuit port (e.g., interface A, interface B, remote interface C on TPE2 64, and remote interface D on TPE3 66). Additionally, table 74 depicts a number of LSPs, a set of pseudowire grouping labels, and a set of pseudowire Group IDs. Note that the Group IDs are provided inside the pseudowire group labels in this example such that these two columns match in table 74. Additionally, note that table 74 is merely representing some of the possible characteristics in a single direction, where different constructs could be used in the reverse direction. Note that the provisioning as discussed herein can significantly reduce messaging such that these presented concepts offer increased scalability. This is due in part to the nominal processing that occurs in the network, in contrast to the processing required to evaluate a prolific amount of signaling messages associated with particular pseudowires. Additionally, the paradigm discussed herein can afford service providers an adequate amount of downtime after a failure has occurred in the network.” Rustogi, ¶ [0039].</p>

No.	'821 Patent Claim 12	The Reference
12	The method of claim 10, further comprising the step of configuring said working entity as revertive.	<p>The Reference discloses the method of claim 10, further comprising the step of configuring said working entity as revertive.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 13	The Reference
13	The method of claim 1, further comprising the step of: if said entity pair reselection results in both working and protection entities being replaced, sequentially replacing said working entity and said protection entity.	<p>The Reference discloses the method of claim 1, further comprising the step of: if said entity pair reselection results in both working and protection entities being replaced, sequentially replacing said working entity and said protection entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 14	The Reference
14[preamble]	A system for selecting entities within an MPLS network, comprising:	<p>The Reference discloses a system for selecting entities within an MPLS network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[preamble] and 1[a].</i></p> <p>Cisco created and developed the MPLS and MPLS-TE standards and patented technology based on those standards <i>before</i> Orckit utilized such technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Filsfils • Taylor • Vasseur '879 • Rustogi

No.	'821 Patent Claim 14	The Reference
		<p>Filsfils discloses:</p> <p>“In one embodiment, forwarding information bases (FIBs) are selectively populated in a packet switch. A packet switching device determines, based on one or more protocol signaling messages, a subset, which is less than all, on which FIBs a lookup operation may be performed for identifying forwarding information for a received particular packet. The packet switching device populates each of these FIBs, but not all of the FIBs of the packet switching device, with forwarding information corresponding to the particular forwarding value. Thus, FIB resources are consumed for only those FIBs which could actually be used, and not all of the FIBs, for forwarding packets in the data plane of the packet switching device, whether these packets are received on a primary or backup path.” Filsfils, Abstract.</p> <p>FIGURE 1</p> <p>Filsfils, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<p>APPARATUS (E.G., PACKET SWITCHING DEVICE APPLIANCE, CORE ROUTER, LABEL SWITCH ROUTER) 200</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>ROUTE PROCESSOR CONFIGURED TO CORRELATE PRIMARY AND BACKUP PATHS AND POPULATE FIBS ACCORDINGLY</p> <p>COMMUNICATION MECHANISM(S) (E.G., BUS, SWITCHING FABRIC, MATRIX)</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>NETWORK(S)</p> <p>NETWORK(S)</p> <p>FIGURE 2</p> <p>Filsfils, FIG. 2 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<pre> graph TD START([START]) --> 402[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL] 402 --> 404[DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)] 404 --> 406[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 406 --> END([END]) </pre> <p>The flowchart illustrates a process for establishing primary and backup paths for a tunnel. It begins with a START node, followed by a step to exchange protocol signaling messages to establish the primary path. This is followed by a decision point to determine if a backup path needs to be created. If so, another set of protocol signaling messages are exchanged to establish the backup path. The process concludes with an END node.</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL</p> <p>DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.</p>

FIGURE 4

Filsfils, FIG. 4 (annotated).

No.	'821 Patent Claim 14	The Reference
		<pre> graph TD START([START]) --> S1[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, SENDING A PARTICULAR LABEL FROM TO THE NEIGHBOR UPSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THIS LSR OVER THE PARTICULAR TUNNEL.] S1 --> S2[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF THE PARTICULAR TUNNEL, INCLUDING RECEIVING INFORMATION THAT IT CAN USE TO CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] S2 --> S3[CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL, AND ONLY POPULATE THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, THE LSR KNOWS WHAT ON WHAT INTERFACE(S) PACKETS FROM THE BACKUP PATH COULD BE RECEIVED. THIS CORRELATION MAY INCLUDE USING DATA CONCERNING BUNDLED INTERFACES, AND EVEN RECURSIVE CORRELATION OF BACKUP TUNNELS USED TO BACKUP OTHER BACKUP TUNNELS, AS WELL AS LOAD BALANCING AND OTHER TECHNIQUES TO DETERMINE WHERE BACKUP PATH PACKETS COULD BE RECEIVED, AND THE SUBSET OF FORWARDING INFORMATION BASES IN THE DATA PLANE THAT COULD BE USED TO FORWARD PACKETS OVER THE PARTICULAR TUNNEL, WHETHER VIA A PRIMARY OR BACKUP PATH.] S3 --> END([END]) </pre> <p>FIGURE 5</p> <p>Filsfils, FIG. 5.</p>

No.	'821 Patent Claim 14	The Reference
		<p>“The communications industry is rapidly changing to adjust to emerging technologies and ever increasing customer demand. This customer demand for new applications and increased performance of existing applications is driving communications network and system providers to employ networks and systems having greater speed and capacity (e.g., greater bandwidth). In trying to achieve these goals, a common approach taken by many communications providers is to use packet switching technology, including switching packets on labels especially in the core network using Multiprotocol Label Switching (MPLS).” Filsfils, 1:12-22.</p> <p>“Tunnels, such as MPLS-TE (Traffic Engineering) and MPLS-TP (Transport Profile), are paths established through a network in order to transport packets efficiently through a label switched network. Fast Re-Route (FRR) is a technology that allows backup paths to be established in the network, which can be used in case of a problem with a primary path (original primary path or currently used backup path) of the tunnel. RFC 4090, entitled “Fast Reroute Extensions to RSVP-TE for LSP Tunnels,” provides an extension of the protocol signaling to establish backup label switched path (LSP) tunnels for local repair of LSP tunnels.” Filsfils, 1:23-33.</p> <p>“Expressly turning to the figures, FIG. 1 illustrates a network 100 operating according to one embodiment. Shown are four apparatus 101-104 (e.g., packet switching devices such as a label switch router, network device, and/or appliance). For explanation purposes, each of apparatus 101-104 will be referenced as a label switch router (LSR).” Filsfils, 5:41-46.</p> <p>“As shown, a particular tunnel is established, using a signaling protocol and exchanging of protocol signaling messages. Note, LSR 101 may, or may not, be an endpoint of the particular tunnel (e.g., LSR 101 may be an intermediate LSR on the path of the particular tunnel). The primary path of the particular tunnel includes spans from LSR 101 via link 111 to LSR 103 and via link 112 to LSR 104. Note, LSR 104 may be an intermediate LSR on the path of the particular tunnel, or an endpoint of the particular tunnel. Further, for this example embodiment, LSR 103 signals LSR 101 to use label T1 at the top of the label stack in the header of a packet sent to it on the particular tunnel.” Filsfils, 5:47-58.</p> <p>“A second tunnel from LSR 101 via link 121 to LSR 102 and via link 122 to LSR 103 is similarly configured using a signaling protocol and exchanging of protocol signaling</p>

No.	'821 Patent Claim 14	The Reference
		<p>messages. For example purposes, LSR 102 signals LSR 101 to use label (T2) at the top of the label stack in the header of a packet sent to it on the second tunnel. In one embodiment, LSR 101 creates the second tunnel in response to determining, or being instructed to, create a backup path to protect link 111 and/or protect all or certain tunnels traversing link 111.” Filsfils, 5:59-67.</p> <p>“As shown in FIG. 1, link 111 (primary path of the particular tunnel and/or all or certain tunnels traversing link 111) is protected by LSR 101 using the second tunnel (backup path). When sending packets over the particular tunnel over link 111, LSR 101 includes label T1 at the top of the label stack of these packets. If link 111 cannot be used for communicating packets of the particular tunnel, LSR 101 sends packets over the backup path for the particular tunnel by sending packets to LSR 102, with these packets having a label stack including: label T2 followed by label T1. Thus, LSR 102 will receive these packets, pop the top label (T2) from the label stack of each of these packets, identify based on label T2 to send these packets to LSR 103. After popping the top label, the label at the top of the label stack of these packets is T1, which is the same label LSR 103 expects to receive for the particular tunnel. Therefore, these packets received with label T1 at the top of their label stack, are forwarded (after popping label T1 from their label stack) by LSR 103 over the particular tunnel to LSR 104.” Filsfils, 6:6-24.</p> <p>“One embodiment acquires such additional information by extending Resource Reservation Protocol (RSVP) to provide information which allows a packet switch to correlate primary and backup paths. Thus, a packet switch can use this additional information in determining which of its forwarding information bases (FIBs) could possibly be used in forwarding packets (e.g., in the data plane of the packet switch).” Filsfils, 6:51-57.</p> <p>“In providing this additional information to LSR 103, one embodiment communicates an extended RSVP message (including a new or modified RSVP object) or other message to LSR 103 on the second tunnel. This messages designates one or more primary tunnels (e.g., label T1 in our example) and/or a link (e.g., link 111). As LSR 103 knows what interface that it received this message, LSR 103 knows that it must populate forwarding information for these primary tunnels, either specified (e.g., by a label such as T1), or all labels corresponding to</p>

No.	'821 Patent Claim 14	The Reference
		<p>tunnels which could be received over link 111. In one embodiment, the extended RSVP or other message communicated to LSR 103 also includes an identification of the backup tunnel (e.g., T2) over which the RSVP or other message is being received, as the identification the tunnel over which a packet is received is often not communicated in a packet (e.g., in the case of Penultimate Hop Popping).” Filsfils, 7:19-34.</p> <p>“As shown in FIG. 1, one embodiment includes apparatus 103, which populates less than all of its FIBs with forwarding information for a tunnel (although all FIBs may be populated for certain tunnels). One embodiment includes apparatus 101 and/or 102 which communicates, via a signaling protocol (e.g., an extension of RSVP, or using another protocol), information which allows apparatus 103 to determine the relationship between primary and backup paths, such that apparatus 104 can correlate this primary and backup path information (possibly also correlating backup path of backup path information, and/or bundled interfaces and/or bundled links) to identify a minimum subset of the FIBs that could possibly be used in forwarding packets of particular primary paths (e.g., tunnels).” Filsfils, 7:49-62</p> <p>“Turning to FIG. 2, illustrates an apparatus 200 (e.g., packet switching devices such as a label switch router, network device, and/or appliance) operating in one embodiment. As shown, apparatus 200 includes line cards 201, 202 communicatively coupled via communication mechanism(s) 203 (e.g., bus, switching fabric, and/or matrix). Additionally, route processor 204 is configured to correlate primary and backup paths of tunnels, and to populate minimum subsets of FIBs with forwarding information for labels. Again, a minimum subset of FIBs for a particular path or label of the particular path is the set of FIBs that are determined to possibly be used in forwarding packets of a primary path, whether the label is received in a packet over the primary path or over a backup path, and possibly considering backup paths of a backup path and/or the possibly effect of bundled interfaces and/or bundled links.” Filsfils, 7:63-8:11.</p> <p>“As shown in FIG. 2, apparatus 200 is communicatively coupled via primary and backup paths 211, 212 to networks 210 and 213 (which could be the same network). As illustrated, each of line cards 201, 202 includes one or more FIBs. By correlating on which line card(s) 201, 202 and even within line cards 201, 202 that have multiple FIBs, primary and backup path(s) of tunnels, the number of FIB entries populated in apparatus 200 can typically be reduced,</p>

No.	'821 Patent Claim 14	The Reference
		<p>possibly significantly saving memory/storage resources and resources used to populate the FIBs.” Filsfils, 8:12-21.</p> <p>“FIG. 4 illustrates a process performed in one embodiment. Processing begins with process block 400. In process block 402, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes receiving a particular label for a downstream LSR to use when sending packets to the downstream LSR over the particular tunnel.” Filsfils, 8:61-67.</p> <p>“In process block 404, a determination is made to create a backup path from the node (e.g. the node performing these operations). This backup path may be used to protect one or more particular tunnels, and/or may be used to protect a link which is used to carry packet traffic of one or more tunnels.” Filsfils, 9:1-5.</p> <p>“In process block 406, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including providing information to the downstream LSR so that the downstream LSR can correlate primary and backup path(s) of the particular tunnel and substantially only program the FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:6-23.</p> <p>“FIG. 5 illustrates a process performed in one embodiment. Processing begins with process block 500. In process block 502, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes sending a particular label for an upstream LSR to use when sending packets over the particular tunnel to this apparatus (e.g., an LSR performing these operations).” Filsfils, 9:26-32.</p>

No.	'821 Patent Claim 14	The Reference
		<p>“In process block 504, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including receiving information that the LSR can use to correlate primary and backup path(s) of the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:33-47.</p> <p>“In process block 506, the primary and backup path(s) of the particular tunnel are correlated to identify the set of FIBs that could possibly be used in forwarding packets of the particular tunnel. Substantially only those FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel (either through a primary or backup path) are populated with the forwarding information (e.g., an entry corresponding to the label it advertised to use for the particular tunnel) for the particular tunnel. For example, the LSR knows what on what interface(s) packets from the backup path could be received. This correlation may include using data concerning bundled interfaces, and even recursive correlation of backup tunnels used to backup other backup tunnels, as well as load balancing and other techniques to determine where backup path packets could be received, and the subset of forwarding information bases in the data plane that could be used to forward packets over the tunnel, whether via a primary or backup path.” Filsfils, 9:48-65.</p> <p>Taylor discloses:</p> <p>“Grouping pseudowires based on hardware interfaces and configured control paths enables improved pseudowire failover performance. Signaling status changes (e.g., from standby to active status) is facilitated by using group IDs for the pseudowire groups, thereby enabling improved failover performance when there is disruption in the network.” Taylor, Abstract.</p>

No.	'821 Patent Claim 14	The Reference
		<p>T-PE1</p> <p>Interface e1/0.111 xconnect 10.2.2.2 1 encap mpls backup peer 10.3.3.3 101</p> <p>Interface e1/0.222 xconnect 10.3.3.3 2 encap mpls backup peer 10.2.2.2 102</p> <p>S-PE2 10.2.2.2</p> <p>12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls neighbor 10.4.4.4 102 encap mpls</p> <p>12 vfi two point-to-point neighbor 10.1.1.1 102 encap mpls neighbor 10.4.4.4 102 encap mpls</p> <p>T-PE4 10.4.4.4</p> <p>interface e4/0.111 xconnect 10.2.2.2 1 encap mpls backup peer 10.3.3.3 101</p> <p>interface e4/0.222 xconnect 10.3.3.3 2 encap mpls backup peer 10.2.2.2 102</p> <p>S-PE3 10.3.3.3</p> <p>12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls neighbor 10.4.4.4 101 encap mpls</p> <p>12 vfi two point-to-point neighbor 10.1.1.1 2 encap mpls neighbor 10.4.4.4 2 encap mpls</p>

FIG. 4

Taylor, FIG. 4 (annotated).

No.	'821 Patent Claim 14	The Reference
		<p>The diagram illustrates a network topology with five routers:</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1): Located at the top left. It has two interfaces: e0/0 and e1/0. Configuration box 504 shows: <ul style="list-style-type: none"> T-PE1 → S-PE2: Gid=1 for 1,2,3,4 T-PE1 → S-PE3: Gid=2 for 5,6,7,8 S-PE2 (12.1.1.1): Located at the top center. It has two interfaces: e0/0 and e1/0. Configuration box 514 shows: <ul style="list-style-type: none"> I2 vfi abc point-to-point neighbor 11.1.1.1 encaps mpls neighbor 14.1.1.1 encaps mpls I2 vfi def point-to-point neighbor 11.1.1.2 encaps mpls neighbor 14.1.1.2 encaps mpls I2 vfi ghi point-to-point neighbor 11.1.1.3 encaps mpls neighbor 14.1.1.3 encaps mpls I2 vfi jkl point-to-point neighbor 11.1.1.4 encaps mpls neighbor 14.1.1.4 encaps mpls T-PE4 (14.1.1.1): Located at the top right. It has two interfaces: e0/0 and e1/0. Configuration box 506 shows: <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.11 encaps mpls interface e0/0.200 xconnect 12.1.1.12 encaps mpls interface e1/0.100 xconnect 12.1.1.13 encaps mpls interface e1/0.200 xconnect 12.1.1.14 encaps mpls S-PE3 (13.1.1.1): Located in the middle right. It has two interfaces: e0/0 and e1/0. Configuration box 516 shows: <ul style="list-style-type: none"> S-PE2 → T-PE4: Gid=10 for 1,2,3,4 S-PE2 → T-PE1: Gid=20 for 1,2 S-PE2 → T-PE1: Gid=21 for 3,4 S-PE3 → T-PE5: Gid=50 for 5,6,7,8 S-PE3 → T-PE1: Gid=75 for 5,6 S-PE3 → T-PE1: Gid=76 for 7,8 T-PE5 (15.1.1.1): Located at the bottom right. It has two interfaces: e0/0 and e1/0. Configuration box 518 shows: <ul style="list-style-type: none"> I2 vfi abc point-to-point neighbor 11.1.1.5 encaps mpls neighbor 15.1.1.5 encaps mpls I2 vfi def point-to-point neighbor 11.1.1.6 encaps mpls neighbor 15.1.1.6 encaps mpls I2 vfi ghi point-to-point neighbor 11.1.1.7 encaps mpls neighbor 15.1.1.7 encaps mpls I2 vfi jkl point-to-point neighbor 11.1.1.8 encaps mpls neighbor 15.1.1.8 encaps mpls

FIG. 5

Taylor, FIG. 5 (annotated).

No.	'821 Patent Claim 14	The Reference
		<p>TRAFFIC FLOW IS TOWER ← CORE</p> <p>TOWER T-PE</p> <p>S-PE1</p> <p>CORE T-PE1</p> <p>mLACP</p> <p>RNC/BSC</p> <p>CORE T-PE2</p> <p>S-PE2</p> <p>ACTIVE PW/ACTIVE AC</p> <p>STANDBY PW/ACTIVE AC</p> <p>STANDBY* PW/STANDBY AC</p> <p>STANDBY PW/STANDBY AC</p> <p>* GOES ACTIVE WHEN mLACP FAILS OVER</p> <p>FIG. 8</p> <p>Taylor, FIG. 8 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<pre> sequenceDiagram participant TOWER as TOWER T-PE participant S1 as S-PE1 participant S2 as S-PE2 participant CORE as CORE T-PE TOWER->>S1: S-PE1 FFD PWs MARKED DOWN activate S1 S1-->>S2: S-PE1 FAILURE deactivate S1 S2-->>CORE: HOT STANDBY → ACTIVE activate CORE CORE-->>S2: HOT STANDBY → ACTIVE deactivate CORE S2-->>CORE: S-PE1 GROUPS MARKED DOWN deactivate S2 </pre> <p><i>FIG. 9</i></p> <p>Taylor, FIG. 9 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<p>1002</p> <pre> graph TD A[1004 SPECIFY PHYSICAL INTERFACE AT FIRST PE] --> B[1006 SPECIFY FIRST CONTROL PATH FROM FIRST PE TO SECOND PE] B --> C[1008 SPECIFY SECOND CONTROL PATH FROM FIRST PE TO THIRD PE] C --> D[1010 SPECIFY REDUNDANT COMBINATIONS OF PWs] D --> E[1012 GROUP PWs INTO A PRIMARY PW GROUP AND A BACKUP PW GROUP] E --> F[1014 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO PRIMARY/BACKUP PW GROUPS] F --> G[1016 DETECT FAILURE ON FIRST CONTROL PATH] G --> H[1018 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO BACKUP/PRIMARY PW GROUPS] </pre> <p>FIG. 10</p> <p>Taylor, FIG. 10 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<p>“The present disclosure relates generally to communication networks and more particularly to pseudowire configurations in communication networks.” Taylor, 1:8-10.</p> <p>“Virtual Private Network (VPN) services provide secure network connections between different locations. A company, for example, can use a VPN to provide secure connections between geographically dispersed sites that need to access the corporate network so that each customer edge (CE) end point or node can communicate directly and independently with all other CE nodes. Different types of VPNs have been classified by the network layer used to establish the connection between the customer and provider network. For example, Virtual Private LAN Service (VPLS) is an architecture that delivers a multipoint Layer 2 VPN (L2VPN) service that in all respects emulates an Ethernet Local Area Network (LAN) across a wide metropolitan geographic area. All services in a VPLS appear to be on the same LAN, regardless of location. In other words, with VPLS, customers can communicate as if they were connected via a private Ethernet segment, i.e., multipoint Ethernet LAN services.” Taylor, 1:12-28.</p> <p>“In this context, each CE device at a customer site is connected to the service provider network at a provider edge PE) device by an Attachment Circuit (AC) that provides the customer connection to a service provider network, that is, the connection between a CE node and its associated PE node. Within the provider network, each PE device includes a Virtual Switch Instance (VSI) that emulates an Ethernet bridge (i.e., switch) function in terms of Media Access Control (MAC) address learning and forwarding in order to facilitate the provisioning of a multipoint L2VPN. A pseudowire (PW) is a virtual connection between two PE devices that connect two attachment circuits. In the context of the VPLS service, a pseudowire can be thought of as a point-to-point virtual link for each offered service between a pair of VSIs. Therefore, if each VSI can be thought of as a virtual Ethernet switch for a given customer service instance, then each pseudowire can be thought of as a virtual link connecting these virtual switches to each other over a Packet Switched Network (PSN) for that service instance.” Taylor, 1:29-47.</p>

No.	'821 Patent Claim 14	The Reference
		<p>“Since the failure of pseudowires obviously degrades network performance, some effort has been directed towards adding system redundancies including redundant pseudowires. However, the presence of redundant pseudowires alone is insufficient to improve overall failover performance, that is, the ability to switch over automatically to a redundant or backup system. Thus, there is a need for improved methods for managing pseudowires to facilitate pseudowire switching and enable improved failover performance.” Taylor, 1:48-56.</p> <p>“FIG. 4 shows details for PW connectivity in an exemplary network for an example embodiment.” Taylor, 1:66-67.</p> <p>“FIG. 5 shows details for PW grouping for an example embodiment.” Taylor, 2:1-2.</p> <p>“FIG. 8 shows an example network including redundant PW connections for an example embodiment.” Taylor, 2:9-10.</p> <p>“FIG. 9 shows an example sequence diagram for a failure mode related to the embodiment shown in FIG. 8.” Taylor, 2:11-12.</p> <p>“FIG. 10 shows a flowchart that illustrates a method of providing improved PW grouping according to an example embodiment.” Taylor, 2:13-15.</p> <p>“According to one embodiment, a method of providing improved pseudowire performance includes specifying a physical interface at a first PE node in a network, a first control path from the first PE node to a second PE node in the network, and a second control path from the first PE node to a third PE node in the network. With these specifications, the method then includes specifying redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node, and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Then these pseudowires can be grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. Group identifiers for the primary PW group and the backup PW group can then be used to assign an</p>

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		<p>active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths. The method may then include detecting a failure on the first control path, and in response to the detected failure, using the group identifiers to assign the active status to the backup pseudowires and the standby status to the primary pseudowires.” Taylor, 2:27-52.</p> <p>“Pseudowires are used in pseudowire emulation edge-to-edge to provide a Layer 2 Virtual Private Network (L2VPN) connection. When large numbers (e.g., 4,000-6,000) of pseudowires are aggregated together on a single router, failure performance tends to be linear or O(n) where n is the number of pseudowires. While O(n) performance may be acceptable for small numbers of pseudowires, the effect on network outages can be increasingly undesirable as the number of pseudowires increases.” Taylor, 2:54-62.</p> <p>“For example, a cell-site router will typically start an approximately 2-minute procedure if contact with its controller, which is reached via a pseudowire, is lost for more than some threshold amount (e.g., between approximately 0.75 and 1.75 seconds in some cases). This can be a major impediment to the scalability of pseudowire deployments. These issues have become increasingly relevant as providers of Multiservice Broadband Networks (MBNs) are rapidly replacing or augmenting their traditional Synchronous Optical Networking (SONET) equipment with cheaper Ethernet equipment in the evolution towards a 4G (i.e., 4th generation) network.” Taylor, 2:63-3:7.</p> <p>“One aspect of a solution to the problem of pseudowire failure is the deployment of redundant pseudowires. For example, redundant pseudowires have been used in the context of Multiprotocol Label Switching (MPLS) networks, which use a Label Distribution Protocol (LDP) to manage labels for forwarding traffic between routers. In this context, general requirements for redundancy schemes have been developed so that duplicate pseudowires are available when a given pseudowire fails (e.g., by using active/standby status indicators). In addition, more specific implementations for redundant pseudowires have also been developed.” Taylor, 3:8-18.</p>

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		<p>“FIG. 1 shows a reference network model 102 with applications to example embodiments disclosed herein. The reference network model 102 includes an aggregation network 104 of PE nodes and a distribution network 106 of PE nodes between a radio network controller (RNC) (or base station controller (BSC)) 108 on the core side of the model 102 and a radio tower 111 on the tower side of the model 102. Switching provider edge nodes S-PE1 and S-PE2 connect the two networks 104, 106. On the core side, two core terminating provider edges T-PE1 and T-PE2 connect to the RNC/BSC 108 through attachment circuits 110, 112. On the tower side, one tower terminating provider edge T-PE connects to the radio tower 111 through an attachment circuit 114.” Taylor, 3:19-31.</p> <p>“Additionally as noted in FIG. 1, peer-PE monitoring is carried out within each network 104, 106. That is, there is peer-PE monitoring between provider edges that share a segment, for example, by multi-hop bidirectional forwarding detection (BFD). Alternatively, peer monitoring can be accomplished by other means (e.g., MPLS-TP (Transport Protocol) keep-alives). This peer-PE monitoring is used to provide the mechanism for fast failure detection. Once a failure is detected, the network can react by “rerouting” the failed pseudowires to pre-provisioned backup paths and thus provide a minimal disruption in service to the end-user. This rerouting can be accomplished by LDP signaling between provider edges.” Taylor, 3:32-44.</p> <p>“The reference network model 102 may be considered as part of a larger hub-and-spoke model as shown in FIG. 2. A hub-and-spoke distribution model 202 includes a core network 204, distribution networks 206, and aggregation networks 208. Network elements including distribution nodes, aggregation nodes, and towers are also shown with nominal count values (e.g., 30 distribution nodes between the core network 204 and a distribution network 206). In this model 202, tower T-PEs are the spokes while core-PEs constitute the hub. Dozens to hundreds of tower T-PEs connect to a few S-PEs; these S-PEs are quite similar to ASBRs as they act as forwarders between the two distinct MPLS domains, providing isolation and, in the case of mobility, aggregation services. Typically, several aggregation networks 208 are connected to a single distribution network 206, eventually connecting the tower with the core router that connects the tower's ACs to the RNC/BSC. There are typically several distribution</p>

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		<p>networks in a Radio Access Network (RAN) connected to the service provider's core Internet Protocol (IP) network.” Taylor, 4:11-30.</p> <p>“With reference to FIG. 1, FIG. 3 shows a variety of failure modes encountered in the reference network model 102. Failure 302 of communications between tower T-PE and the S-PE can be detected via peer monitoring when both the S-PE and the T-PE are still active/alive. For example, this failure may be due to a loss of connectivity when the BFD session goes down. Failure 304 of S-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability (e.g., if the BFD-session hello timers cannot be serviced for the prescribed period of time). Failure 306 of communications between S-PE1 and core T-PE1 can be detected via peer monitoring when both S-PE1 and core T-PE1 are still active/alive. Failure 308 at core T-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability.” Taylor, 4:46-60.</p> <p>“Pseudowire connectivity is further illustrated in FIG. 4 where the illustrated network includes four nodes: T-PE1 (10.1.1.1), S-PE2 (10.2.2.2), S-PE3 (10.3.3.3), and T-PEA (10.4.4.4). For the terminating nodes T-PE1 and T-PE4, specifications for VLANs (virtual Local Area Networks) connections (i.e., pseudowires) are shown using the Internet Operating System Command Line Interface (IOS CLI). The specification 402 for T-PE1 defines two VLANs as primary/backup combinations of virtual circuits for the network. The first three lines of the specification 402 define “VLAN 111” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 111” in the first line. The second line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE2 (10.12.2) with a virtual circuit Identification (VCID) set as VCID=1, and the third line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=101. The next three lines of the specification 402 define “VLAN 222” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 222” in the fourth line. The fifth line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=2, and the sixth line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE2 (10.2.2.2) with VCID=102. These virtual circuits, VCID=1, VCID=2, VCID=101 and VCID=102 are shown in the figure between T-PE1 and the S-PEs with a solid line for the primary circuits VCID=1</p>

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		<p>and VCID=2 and a dashed line for the backup circuits VCID=101 and VCID=102.” Taylor, 4:61-5:23.</p> <p>“With respect to T-PE1 in FIG. 4, although “VLAN 111” and “VLAN 222” share the same hardware port, they do not share the same “control path disposition.” That is, “VLAN 111” is primary to S-PE2 (VCID=1) and standby to S-PE3 (VCID=101), while “VLAN 222” has an opposite configuration since it is primary to S-PE3 (VCID=2) and standby to S-PE2 (VCID=102). As discussed below, certain embodiments group pseudowires according to “control path disposition” (e.g., xconnect configuration as well as the hardware interface in order to improve failover performance. That is, to deal with both hardware port failures and switching path failures, the grouping criteria also considers the cross connects. In this case, on T-PE1 as well as T-PE4, there would exist two groups: one for active to S-PE2 and standby to S-PE3 and another for active to S-PE3 and standby to S-PE2 (i.e., the inverse configuration).” Taylor, 5:61-6:9.</p> <p>“First, local connectivity is characterized by local group identifications (Group-IDs), which depend on whether the allocation is done at a T-PE or S-PE. FIG. 5 shows an embodiment that illustrates an allocation of local group IDs in a network including terminating nodes T-PE1 (11.1.1.1), T-PE4 (14.1.1.1), and T-PE5 (15.1.1.1) and switching nodes S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1). The specification 502 for T-PE1 determines corresponding local group IDs 504 based on the hardware interface and the control path. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, and the Group-ID=2 corresponds to VCID=5, VCID=6, VCID=7, and VCID=8.” Taylor, 6:10-21.</p> <p>“Local group IDs are maintained in a database so that pseudowire redundancy is also maintained. First, in a case without pseudowire redundancy, all the xconnect configurations from the same physical interface to the same peer are assigned the same local group ID. So, for example, in Ethernet cases all xconnect configurations under sub-interfaces of the same physical interface to the same peer will be assigned the same local group ID (e.g., e0/0 and e0/1 are sub-interfaces of e0). FIG. 6 shows a database representation for T-PE4 from FIG. 5. From the root node 602 for T-PE4, there is a first interface node 604 for e0/1 and a second interface node 606 for e1/0. The first interface node 604 is configured towards a single peer</p>

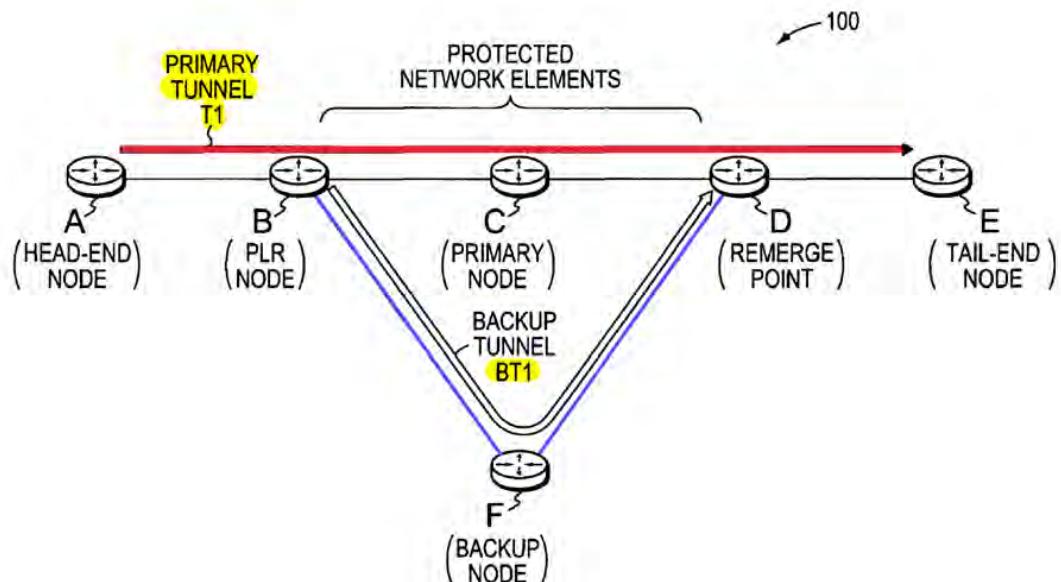
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		<p>node (12.1.1.1) 608 and is thus assigned a single local group ID (Group-ID=200) 610. Similarly, the second interface node 606 is configured towards a single peer node (12.1.1.1) 612 and is thus assigned a single local group ID (Group-ID=201) 614. In this case from the assignment of local group IDs 508 in FIG. 5, Group-ID=200 corresponds to VCID=1 and VCID=2, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). Both pseudowires (VCID=1 and VCID=2) are assigned the same local group ID (Group-ID=200) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly, from the assignment of local group IDs 508 in FIG. 5, T-PE4 has VCID=3 and VCID=4 under the physical interface e1/0 going to the same peer S-PE2 (12.1.1.1), and the local Group ID (Group-ID=201) is assigned to these VCs.” Taylor, 6:48-7:8.</p> <p>“For the pseudowire redundancy case, a separate redundancy-group database is maintained by the xconnect application. This redundancy-group database contains the peer IDs in the group and the local group IDs advertised to them. This is needed to maintain a 1:1 mapping between the primary pseudowires and their corresponding backup pseudowires. FIG. 7 shows a database representation for T-PE1 from FIG. 5. From the root node 702 for T-PE1, there is an interface node 704 for e0/0 and a redundancy group node 706 that shows connections for configurations to a first peer node (12.1.1.1) 708, which is assigned a local group ID (Group-ID=1) 710, and a second peer node (13.1.1.1) 712, which is assigned a local group ID (Group-ID=2) 714. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). These pseudowires are assigned the same group ID (Group-ID=1) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly from the assignment of local group IDs 504 in FIG. 5, T-PE1 has VCID=5, VCID=6, VCID=7, and VCID=8 under the physical interface e0/0 going to another peer S-PE3 (13.1.1.1), and the local group ID (Group-ID=2) is assigned to these VCs. In this case these local group IDs are organized as a redundancy group 706.” Taylor, 7:9-35.</p>

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		<p>“Pseudowire grouping allows multiple pseudowires to be aggregated together when signaling either label withdrawals or status changes between segment end-point provider edges. This signaling can be carried out through LDP grouping TLV (Type Length Value). For example, when a PE node fails (e.g., failure 304 of S-PE1), aggregating the inter-segment PE signaling using the grouping TLV can provide significant scaling advantages. This allows all pseudowires sharing a physical port and PW configuration (e.g., xconnect configuration) to be signaled en masse between segment-adjacent provider edges.” Taylor, 7:36-46.</p> <p>“FIG. 8 shows an example based on FIG. 1 where VLAN ACs are shown as grouped by both port/HW-interface and pseudowire-class. The grouping criterion allows all “similar” pseudowires to be signaled together: All the grouped pseudowires share the same port and next-hop provider edge. Additionally, the figure contains many pseudowires, each grouped into a shaded tube. For example, the tube labeled “VLANs 100-549” contains 450 pseudowires grouped together. This figure depicts an incoming Ethernet comprised of 900 VLANs being segmented in two with 450 VLANs (100-549) active to S-PE1 while the other half of the VLANs (550-999) being active to S-PE2. This might be considered a type of manual load balancing. Furthermore, the aggregation network is only showing a single tower and the VLANs associated with it; other VLAN destinations are not shown in the figure.” Taylor, 7:47-62.</p> <p>“‘VLANs 110-112’ are active along a first pseudowire path 802 from Core T-PE1 to S-PE1 and a second pseudowire path 804 from S-PE1 to Tower T-PE. When a failure occurs at S-PE1 (e.g., as the switching node failure 304 shown in FIG. 3), then the standby pseudowires become active for ‘VLANs 110-112’ along a first pseudowire path 806 from Core T-PE1 to S-PE2 and a second pseudowire path 808 from S-PE2 to Tower T-PE.” Taylor, 7:63-8:3.</p> <p>“The standby pseudowires in FIG. 8 can be configured as HSPWs, a configuration that enables ACs to quickly failover to pre-provisioned pseudowires that are in active state but set to not-forwarding. Then when a failure occurs, switching over to these pre-provisioned HSPWs occurs quickly by switching from not-forwarding status to forwarding status.” Taylor, 8:4-9.</p>

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		<p>“FIG. 9 shows a UML (Unified Modeling Language) sequence diagram of signaling events related to the failover procedure illustrated in FIG. 8 for a switching node failure 304. The Tower T-PE and the Core T-PE independently detect a failure at S-PE1 (e.g., BFD forwarding with LDP signaling), and then mark down the status of the currently active pseudowires routed through S-PE1 and mark up the status of the standby pseudowires routed through S-PW2. Other failure modes shown in FIG. 3 can be handled similarly.” Taylor, 8:10-18.</p> <p>“In general, it is desirable for MPLS-based Ethernet networks to react quickly to failures, so proactive detection mechanisms are employed in order to pick up system failures quickly. All proactive monitoring is typically done between PE peers on a single MPLS network. These provider edges on the edges of the MPLS network act similarly to an Autonomous System Boundary Router (ASBR). As a result, related embodiments detect control path failures, which may not be the same as pseudowire data path failures. That is, the data packets and control packets may take different paths between provider edges in a MPLS network although typically these paths are coincident. Thus, when the control and data paths are not coincident, if the control path fails, then all pseudowires utilizing the control path are marked as failed. As a corollary, if the data path fails and the control path remains healthy, then failure will not be detected from monitoring the control path.” Taylor, 8:19-35.</p> <p>“A failure of a monitored provider edge initiates a switchover of all active pseudowires using the failing provider edge to their configured HSPWs (if they exist). Grouping can greatly reduce the number of messages needed between provider edges (Inter-PE Aggregation) and within a single provider edge (Intra-PE Aggregation). Furthermore, the MPLS network itself may be internally resilient deploying technologies such as, but not limited to, MPLS-TE (MPLS Traffic Engineering) and ERR (Fast Reroute). The paths across the MPLS network may recover quickly and might not trip the fault-monitoring systems.” Taylor, 8:36-46.</p> <p>“With reference to the above discussion, FIG. 10 shows a method 1002 of providing improved PW grouping according to an example embodiment. In a first operation 1004 of the method 1002, a physical interface is specified at a first PE node in a network. In a second operation 1006, a first control path is specified from the first PE node to a second PE node in the network. In a third operation 1008, a second control path is specified from the first PE node to a third</p>

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		<p>PE node in the network. These control paths related to a common physical interface can be used to characterize redundant pairs of pseudowires.” Taylor, 8:48-58.</p> <p>“In a fourth operation 1010, redundant combinations of pseudowires are specified, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Although a redundant combination may relate a single backup pseudowire to a given primary pseudowire, in some cases multiple backup pseudowires will be related to a given primary pseudowire for increased redundancy. In a fifth operation 1012, these pseudowires are grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. In a sixth operation 1014, group identifiers for the primary PW group and the backup PW group are used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 8:59-9:11.</p> <p>“In an optional seventh operation 1016, a failure may be detected on the first control path, and in an optional eighth operation 1018, in response to the detected failure, the group identifiers may be used to assign the active status to the backup pseudowires and the standby status to the primary pseudowires. For example, the failure on the first control path may be detected by using BED packet streams between PE nodes of the network. Then the detected failure can be signaled to PE nodes in the network by sending LDP status updates between PE nodes in the network. Then, after receiving the failure detection signals, the group identifiers can be used again to assign the active status to the backup pseudowires and the standby status to the primary pseudowires by sending LDP status updates between PE nodes in the network.” Taylor, 9:12-26.</p> <p>“Typically the network in is an MPLS network and the PE nodes are routers that provide network services to connected CE nodes of a customer network. In general, each control path is an Internet Protocol (IP) routing path between PE nodes in the network and each data path is a label switched path (LSP) between PE nodes in the network.” Taylor, 9:27-32.</p>

No.	'821 Patent Claim 14	The Reference
		<p>“FIG. 11 shows a schematic representation of an apparatus 1102, in accordance with an example embodiment. For example, the apparatus 1102 may be used to implement the method 1002 of providing improved pseudowire grouping as described above with reference to FIG. 10. The apparatus 1102 is shown to include a processing system 1104 that may be implemented on a server, client, or other processing device that includes an operating system 1106 for executing software instructions.” Taylor, 10:2-10.</p> <p>“In accordance with an example embodiment, the apparatus 1102 includes a PW management module 1108 that includes a first specification module 1110, a second specification module 1112, third specification module 1114, a fourth specification module 1116, a grouping module 1118, and an assignment module 1120. The first specification module 1110 operates to specify a physical interface at a first PE node in a network. The second specification module 1112 operates to specify a first control path from the first PE node to a second PE node in the network. The third specification module 1114 operates to specify a second control path from the first PE node to a third PE node in the network. The fourth specification module 1116 operates to specify redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node.” Taylor, 10:11-29.</p> <p>“The grouping module 1118 operates to group the pseudowires into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. The assignment module 1120 operates to use group identifiers for the PW groups to assign an active status to the primary pseudowires and a standby status to the backup pseudowires. The active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 10:30-38.</p>

No.	'821 Patent Claim 14	The Reference
		<p>Vasseur '879 discloses:</p> <p>"A technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node "Fast Reroutes," i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel." Vasseur '879, Abstract.</p>  <p style="text-align: center;">FIG. 1</p> <p>Vasseur '879, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<p>SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)</p> <p>COMMON HEADER 310</p> <p>SOURCE ADDRESS 312 DESTINATION ADDRESS 314</p> <p>SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320</p> <p>LSP-ATTRIBUTE OBJECT 330</p> <p>EXTENSION OBJECT(S) 400</p>

FIG. 3

Vasseur '879, FIG. 3 (annotated).

No.	'821 Patent Claim 14	The Reference
		<p>FIG. 5</p> <p>Vasseur '879, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<p>FIG. 6</p> <p>Vasseur '879, FIG. 6 (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<pre> graph TD START([START]) --> 705[HEAD-END NODE ESTABLISHES PRIMARY TUNNEL] 705 --> 710[HEAD-END NODE REQUESTS PROTECTION OF PRIMARY TUNNEL] 710 --> 715[POINTS OF LOCAL REPAIR (PLRs) CREATE BACKUP TUNNELS TO PROTECT PRIMARY TUNNEL] 715 --> 720[HEAD-END NODE REQUESTS BACKUP TUNNEL PATH QUALITY NOTIFICATION] 720 --> 725[PLRs OF PRIMARY TUNNEL COLLECT PATH QUALITY INFORMATION (e.g., METRICS/STATISTICS) FOR PRIMARY AND BACKUP TUNNELS (e.g., FROM PRIMARY AND/OR BACKUP NODES)] 725 --> 730[PLRs MAY SEND PATH QUALITY NOTIFICATION TO HEAD-END NODE] 730 --> 735[PLRs MONITOR PROTECTED SEGMENT OF PRIMARY TUNNEL FOR FAILURE] 735 --> 740{DETECT FAILURE ?} 740 -- N --> 705 740 -- Y --> A((A)) </pre> <p>FIG. 7A Vasseur '879, FIG. 7A (annotated).</p>

No.	'821 Patent Claim 14	The Reference
		<pre> graph TD A((A)) --> B[DETECTING PLR DIVERTS PRIMARY TUNNEL TRAFFIC TO BACKUP TUNNEL AND SENDS ERROR MESSAGE TO HEAD-END NODE] B --> C[PLR CONTINUES TO COLLECT PATH QUALITY INFORMATION FOR BACKUP TUNNEL] C --> D{PLR SEND NOTIFICATION TO HEAD-END NODE ?} D -- N --> E{TIMER EXPIRED ?} E -- Y --> F[HEAD-END NODE DETERMINES WHETHER TO REESTABLISH PRIMARY TUNNEL BASED ON BACKUP TUNNEL PATH QUALITY NOTIFICATION] F --> G{ACCEPTABLE PATH QUALITY ?} G -- Y --> H[HEAD-END NODE ATTEMPTS TO REESTABLISH PRIMARY TUNNEL] G -- N --> I(END) D -- Y --> F H --> I </pre> <p>FIG. 7B</p> <p>Vasseur '879, FIG. 7B.</p>

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		<p>“The present invention relates to computer networks and more particularly to dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 1:10-14.</p> <p>“Since management of interconnected computer networks can prove burdensome, smaller groups of computer networks may be maintained as routing domains or autonomous systems. The networks within an autonomous system (AS) are typically coupled together by conventional “intradomain” routers configured to execute intradomain routing protocols, and are generally subject to a common authority. To improve routing scalability, a service provider (e.g., an ISP) may divide an AS into multiple “areas.” It may be desirable, however, to increase the number of nodes capable of exchanging data; in this case, interdomain routers executing interdomain routing protocols are used to interconnect nodes of the various ASes. Moreover, it may be desirable to interconnect various ASes that operate under different administrative domains. As used herein, an AS or an area is generally referred to as a “domain,” and a router that interconnects different domains together is generally referred to as a ‘border router.’” Vasseur '879, 1:40-56.</p> <p>“An example of an interdomain routing protocol is the Border Gateway Protocol version 4 (BGP), which performs routing between domains (ASes) by exchanging routing and reachability information among neighboring interdomain routers of the systems. An adjacency is a relationship formed between selected neighboring (peer) routers for the purpose of exchanging routing information messages and abstracting the network topology. The routing information exchanged by BGP peer routers typically includes destination address prefixes, i.e., the portions of destination addresses used by the routing protocol to render routing (“next hop”) decisions. Examples of such destination addresses include IP version 4 (IPv4) and version 6 (IPv6) addresses. BGP generally operates over a reliable transport protocol, such as TCP, to establish a TCP connection/session. The BGP protocol is well known and generally described in Request for Comments (RFC) 1771, entitled A Border Gateway Protocol 4 (BGP-4), published March 1995.” Vasseur '879, 1:57-2:7.</p>

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		<p>“Examples of an intradomain routing protocol, or an interior gateway protocol (IGP), are the Open Shortest Path First (OSPF) routing protocol and the Intermediate-System-to-Intermediate-System (IS-IS) routing protocol. The OSPF and IS-IS protocols are based on link-state technology and, therefore, are commonly referred to as link-state routing protocols. Link-state protocols define the manner with which routing information and network-topology information are exchanged and processed in a domain. This information is generally directed to an intradomain router's local state (e.g., the router's usable interfaces and reachable neighbors or adjacencies). The OSPF protocol is described in RFC 2328, entitled OSPF Version 2, dated April 1998 and the IS-IS protocol used in the context of IP is described in RFC 1195, entitled Use of OSI IS-IS for routing in TCP/IP and Dual Environments, dated December 1990, both of which are hereby incorporated by reference.” Vasseur '879, 2:8-24.</p> <p>“An intermediate network node often stores its routing information in a routing table maintained and managed by a routing information base (RIB). The routing table is a searchable data structure in which network addresses are mapped to their associated routing information. However, those skilled in the art will understand that the routing table need not be organized as a table, and alternatively may be another type of searchable data structure. Although the intermediate network node's routing table may be configured with a predetermined set of routing information, the node also may dynamically acquire (“learn”) network routing information as it sends and receives data packets. When a packet is received at the intermediate network node, the packet's destination address (e.g., stored in a header of the packet) may be used to identify a routing table entry containing routing information associated with the received packet. Among other things, the packet's routing information indicates the packet's next-hop address.” Vasseur '879, 2:25-41.</p> <p>“Multi-Protocol Label Switching (MPLS) Traffic Engineering has been developed to meet data networking requirements such as guaranteed available bandwidth or fast restoration. MPLS Traffic Engineering exploits modern label switching techniques to build guaranteed bandwidth end-to-end tunnels through an IP/MPLS network of label switched routers (LSRs). These tunnels are a type of label switched path (LSP) and thus are generally referred to as MPLS Traffic Engineering (TE) LSPs. Examples of MPLS TE can be found in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels dated December 2001, RFC 3784</p>

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		<p>entitled Intermediate-System-to-Intermediate-System (IS-IS) Extensions for Traffic Engineering (TE) dated June 2004, and RFC 3630, entitled Traffic Engineering (TE) Extensions to OSPF Version 2 dated September 2003, the contents of all of which are hereby incorporated by reference in their entirety.” Vasseur '879, 2:58-3:6.</p> <p>“Establishment of an MPLS TE-LSP from a head-end LSR to a tail-end LSR involves computation of a path through a network of LSRs. Optimally, the computed path is the “shortest” path, as measured in some metric, that satisfies all relevant LSP Traffic Engineering constraints such as e.g., required bandwidth, “affinities” (administrative constraints to avoid or include certain links), etc. Path computation can either be performed by the head-end LSR or by some other entity operating as a path computation element (PCE) not co-located on the head-end LSR. The head-end LSR (or a PCE) exploits its knowledge of network topology and resources available on each link to perform the path computation according to the LSP Traffic Engineering constraints. Various path computation methodologies are available including CSPF (constrained shortest path first). MPLS TE-LSPs can be configured within a single domain, e.g., area, level, or AS, or may also span multiple domains, e.g., areas, levels, or ASes.” Vasseur '879, 3:7-24.</p> <p>“The PCE is an entity having the capability to compute paths between any nodes of which the PCE is aware in an AS or area. PCEs are especially useful in that they are more cognizant of network traffic and path selection within their AS or area, and thus may be used for more optimal path computation. A head-end LSR may further operate as a path computation client (PCC) configured to send a path computation request to the PCE, and receive a response with the computed path, potentially taking into consideration other path computation requests from other PCCs. It is important to note that when one PCE sends a request to another PCE, it acts as a PCC.” Vasseur '879, 3:25-36.</p> <p>“Some applications may incorporate unidirectional data flows configured to transfer time-sensitive traffic from a source (sender) in a computer network to a destination (receiver) in the network in accordance with a certain “quality of service” (QoS). Here, network resources may be reserved for the unidirectional flow to ensure that the QoS associated with the data flow is maintained. The Resource ReSerVation Protocol (RSVP) is a network-control protocol that</p>

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		<p>enables applications to reserve resources in order to obtain special QoS for their data flows. RSVP works in conjunction with routing protocols to, e.g., reserve resources for a data flow in a computer network in order to establish a level of QoS required by the data flow. RSVP is defined in R. Braden, et al., Resource ReSerVation Protocol (RSVP), RFC 2205, the contents of which are hereby incorporated by reference in its entirety. In the case of traffic engineering applications, RSVP signaling (with Traffic Engineering extensions) is used to establish a TE-LSP and to convey various TE-LSP attributes to routers, such as border routers, along the TE-LSP obeying the set of required constraints whose path may have been computed by various means.” Vasseur ’879, 3:37-57.</p> <p>“Generally, a tunnel is a logical structure that encapsulates a packet (a header and data) of one protocol inside a data field of another protocol packet with a new header. In this manner, the encapsulated data may be transmitted through networks that it would otherwise not be capable of traversing. More importantly, a tunnel creates a transparent virtual network link between two network nodes that is generally unaffected by physical network links or devices (i.e., the physical network links or devices merely forward the encapsulated packet based on the new header). While one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur ’879, 3:58-4:3.</p> <p>“Occasionally, a network element (e.g., a node or link) will fail, causing redirection of the traffic that originally traversed the failed network element to other network elements that bypass the failure. Generally, notice of this failure is relayed to the nodes in the network through an advertisement of the new network topology, e.g., an IGP or BGP Advertisement, and routing tables are updated to avoid the failure accordingly. Reconfiguring a network in response to a network element failure using, e.g., pure IP rerouting, can be time consuming. Many recovery techniques, however, are available to provide fast recovery and/or network configuration in the event of a network element failure, including, inter alia, “Fast Reroute”, e.g., MPLS TE Fast Reroute. An example of MPLS TE Fast Reroute is described in Pan, et al., Fast Reroute Extensions to RSVP-TE for LSP Tunnels, RFC 4090, May 2005, which is hereby incorporated by reference as though fully set forth herein.” Vasseur ’879, 4:4-21.</p>

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		<p>“Fast Reroute (or FRR) has been widely deployed to protect against network element failures, where “backup tunnels” are created to bypass one or more protected network elements (e.g., links, shared risk link groups (SRLGs), and nodes). When the network element fails, traffic is quickly diverted (“Fast Rerouted”) over a backup tunnel to bypass the failed element, or more particularly, in the case of MPLS, a set of primary TE-LSPs (tunnels) is quickly diverted. Specifically, the point of local repair (PLR) node configured to reroute the traffic inserts (“pushes”) a new label for the backup tunnel, and the traffic is diverted accordingly. Once the failed element is bypassed, the backup tunnel label is removed (“popped”), and the traffic is routed along the original path according to the next label (e.g., that of the original TE-LSP). Notably, the backup tunnel, in addition to bypassing the failed element along a protected primary TE-LSP, also intersects the primary TE-LSP, i.e., it begins and ends at nodes along the protected primary TE-LSP.” Vasseur '879, 4:22-39.</p> <p>“To offer maximum protection, e.g., guaranteed bandwidth, during Fast Reroute, backup tunnels may reserve a configurable amount of bandwidth to ensure that Fast Rerouted traffic from the primary tunnel has a reserved path to follow. For example, the bandwidth reserved for the primary tunnel may also be reserved on the backup tunnel. While this approach provides maximum protection, it also requires a non-negligible amount of network resources (e.g., capacity/bandwidth) and may increase operational complexity.” Vasseur '879, 4:40-48.</p> <p>“Certain techniques are available to efficiently minimize the amount of resources required by the establishment and maintenance of the backup tunnels for Fast Reroute. One such technique is to create zero-bandwidth (“0-BW”) backup tunnels (i.e., tunnels that reserve no bandwidth) to protect non-0-BW primary tunnels. This “best effort” approach does not guarantee that the path followed by the backup tunnel will have enough bandwidth to support the diverted primary tunnel at the time of failure without QoS degradation, however in many situations the path quality of the backup tunnel is sufficient. For instance, if the network is not overly congested, or the backup tunnel follows a non-congested path, there may be enough available bandwidth along the backup tunnel to support the newly rerouted traffic. Also, because primary tunnels often reserve bandwidth in response to “peak” traffic utilization, the amount of traffic over the primary tunnel at the time of failure may be far less than the reserved bandwidth (e.g., at “off-peak” times). Notably, those skilled in the art will understand that in</p>

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		<p>the absence of the above exceptions, a 0-BW backup tunnel may have unacceptable bandwidth (e.g., affecting path quality) to support the diverted traffic.” Vasseur ’879, 4:49-5:2.</p> <p>“Currently, head-end nodes (LSRs) may be configured to systematically reroute the primary tunnels affected by the network element failure (e.g., diverted primary tunnels), especially in the case with 0-BW backup tunnels, such as, e.g., by reestablishing a new primary tunnel that follows a path excluding the failed network element. In particular, 0-BW backup tunnels represent a best effort attempt to allow the head-end node to more gracefully reestablish the primary tunnel in response to a failure, since the backup tunnels may not be able to support the diverted traffic without QoS degradation. The systematic reestablishing may potentially result in the reestablishment of a large number of primary tunnels (e.g., up to 3000 for a single network element failure in today’s networks). Notably, reestablishing diverted primary tunnels may be undesirable for the network, such as by creating traffic churn, jitter, control plane overloads, etc., as will be understood by those skilled in the art. However, as noted above, there are situations where the backup tunnel may provide acceptable bandwidth, at least, for example, for a period of time (e.g., possibly short) until the failed network element is restored. In these situations, then, it may have been unnecessary to reestablish the diverted primary tunnels. There remains a need, therefore, for a technique that dynamically determines whether to reestablish a diverted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur ’879, 5:3-28.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 5:32-47.</p>

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		<p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as Resource ReSerVation Protocol (RSVP) Traffic Engineering (TE) signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or type/length/value (TLV) encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object of the RSVP messages to convey the path quality notification.” Vasseur '879, 5:48-58.</p> <p>“In accordance with one aspect of the present invention, the head-end node requests the establishment of the primary tunnel (e.g., a TE-Label Switched Path, TE-LSP), along with a request for Fast Reroute protection of one or more network elements (e.g., with zero-bandwidth, 0-BW backup tunnels) at a PLR node. In addition, the head-end node may include a request for backup tunnel path quality notification, such as, e.g., through the use of the novel Feedback sub-object. The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and sends an error message (e.g., an RSVP PathErr) to the head end node indicating the “Fast Rerouting” of the primary tunnel.” Vasseur '879, 5:59-6:6.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, the PLR node may collect metrics/statistics (e.g., packet drops, path cost, jitter, etc.) of the primary and/or backup tunnels. Once the primary tunnel is Fast Rerouted, the PLR node continues to collect metrics of the backup tunnel, and may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. Notably, the PLR node may be configured to send path quality notifications to the head-end node once, continually, periodically, in response to configurable changes in path quality, etc. Also, as in the case where multiple primary tunnels are Fast Rerouted, the path quality notification may include an indication of which Fast Rerouted primary tunnels in particular have been effected by the changed path quality.” Vasseur '879, 6:7-23.</p>

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		<p>“In accordance with yet another aspect of the present invention, upon receiving the error message (PathErr), the head-end node may wait for the path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. Notably, in the event the head-end node does not receive a path quality notification for the backup tunnel (e.g., within a configurable time limit), the head-end node may attempt to reestablish the new primary tunnel. Moreover, where the head-end node has multiple primary tunnels being Fast Rerouted, a configurable subset of the primary tunnels may be reestablished, e.g., to reduce congestion of the backup tunnels, and/or to limit the number of reestablished primary tunnels within a given period of time.” Vasseur '879, 6:24-43.</p> <p>“Advantageously, the novel technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. By providing the head-end node of the primary tunnel with path quality feedback of the backup tunnel, the novel technique avoids reestablishing a potentially large number of tunnels over one or more alternate paths after a failure (and Fast Reroute) if the backup tunnels have acceptable path quality. In particular, the backup tunnels, e.g., 0-BW backup tunnels, may not be congested or subsequently burdened by the Fast Rerouted traffic of the primary tunnel. Also, the failed network element (thus the primary tunnel) may be quickly restored; therefore by not reestablishing the primary tunnel, network jitter, churn, etc., may be avoided. Further, the dynamic nature of the novel technique alleviates the need for cumbersome manual configuration.” Vasseur '879, 6:44-59.</p> <p>“FIG. 3 is schematic block diagram of an exemplary signaling (RSVP) message that may be advantageously used with the present invention.” Vasseur '879, 7:6-8.</p> <p>“FIG. 5 is a schematic block diagram of the computer network in FIG. 1 showing Fast Reroute protection of a primary tunnel using a backup tunnel in accordance with the present invention.” Vasseur '879, 7:12-15.</p>

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		<p>“FIG. 6 is a schematic block diagram of the computer network in FIG. 5 showing an unacceptable backup tunnel path quality and resultant reestablishing of the primary tunnel in accordance with the present invention.” Vasseur '879, 7:16-19.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention.” Vasseur '879, 7:20-24.</p> <p>“FIG. 1 is a schematic block diagram of an exemplary computer network 100 comprising a plurality of nodes A-F, such as routers or other network devices, interconnected as shown. The nodes may be a part of one or more autonomous systems, routing domains, or other networks or subnetworks. For instance, routers A and E may be provider edge (PE) devices of a provider network, (e.g., a service provider network) that are interconnected to one or more customer networks through customer edge (CE) devices (not shown, while the remaining nodes B-D and F may be core provider (P) devices, as will be understood by those skilled in the art. Those skilled in the art will also understand that the nodes A-F may be any nodes within any arrangement of computer networks, and that the view shown herein is merely an example. For example, the nodes may be configured as connections to/from one or more virtual private networks (VPNs), as will be understood by those skilled in the art. These examples are merely representative. Those skilled in the art will understand that any number of routers, nodes, links, etc. may be used in the computer network 100 and connected in a variety of ways, and that the view shown herein is for simplicity.” Vasseur '879, 7:29-49.</p> <p>“Data packets may be exchanged among the computer network 100 using predefined network communication protocols such as the Transmission Control Protocol/Internet Protocol (TCP/IP), User Datagram Protocol (UDP), Asynchronous Transfer Mode (ATM) protocol, Frame Relay protocol, Internet Packet Exchange (IPX) protocol, etc. Routing information may be distributed among the routers of the computer network using predetermined Interior Gateway Protocols (IGPs), such as conventional distance-vector protocols or, illustratively, link-state protocols, through the use of IGP Advertisements.” Vasseur '879, 7:50-60.</p>

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		<p>“FIG. 2 is a schematic block diagram of an exemplary router 200 that may be advantageously used with the present invention, e.g., as an edge router or a core router. The router comprises a plurality of network interfaces 210, a processor 220, and a memory 240 interconnected by a system bus 250. The network interfaces 210 contain the mechanical, electrical and signaling circuitry for communicating data over physical links coupled to the network 100. The network interfaces may be configured to transmit and/or receive data using a variety of different communication protocols, including, inter alia, TCP/IP, UDP, ATM, synchronous optical networks (SONET), wireless protocols, Frame Relay, Ethernet, Fiber Distributed Data Interface (FDDI), etc.” Vasseur '879, 7:61-8:6.</p> <p>“The memory 240 comprises a plurality of storage locations that are addressable by the processor 220 and the network interfaces 210 for storing software programs and data structures associated with the present invention. The processor 220 may comprise necessary elements or logic adapted to execute the software programs and manipulate the data structures. A router operating system 242 (e.g., the Internetworking Operating System, or IOS™, of Cisco Systems, Inc.), portions of which is typically resident in memory 240 and executed by the processor, functionally organizes the router by, inter alia, invoking network operations in support of software processes and/or services executing on the router. These software processes and/or services may comprise routing services 247, Traffic Engineering (TE) services 244, and RSVP services 249. It will be apparent to those skilled in the art that other processor and memory means, including various computer-readable media, may be used to store and execute program instructions pertaining to the inventive technique described herein.” Vasseur '879, 8:7-26.</p> <p>“Routing services 247 contain computer executable instructions executed by processor 220 to perform functions provided by one or more routing protocols, such as IGP (e.g., OSPF and IS-IS), IP, BGP, etc. These functions may be configured to manage a forwarding information database (not shown) containing, e.g., data used to make forwarding decisions. Routing services 247 may also perform functions related to virtual routing protocols, such as maintaining VRF instances (not shown) as will be understood by those skilled in the art.” Vasseur '879, 8:27-36.</p>

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		<p>“RSVP services 249 contain computer executable instructions for implementing RSVP and processing RSVP messages in accordance with the present invention. RSVP is described in RFC 2205, entitled Resource ReSerVation Protocol (RSVP), and in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels, both as incorporated above.” Vasseur '879, 8:37-42.</p> <p>“TE services 244 contain computer executable instructions for operating TE functions in accordance with the present invention. Examples of Traffic Engineering are described in RFC 3209, RFC 3784, and RFC 3630 as incorporated above, and in RFC 3473, entitled, Generalized Multi-Protocol Label Switching (GMPLS) Signaling Resource ReSerVation Protocol-Traffic Engineering (RSVP-TE) Extensions dated January 2003, which is hereby incorporated by reference in its entirety. A TE database (TED, not shown) may be illustratively resident in memory 240 and used to store TE information provided by the routing protocols, such as IGP, BGP, and/or RSVP (with TE extensions, e.g., as described herein), including, inter alia, path quality information as described herein. The TED may be illustratively maintained and managed by TE services 244.” Vasseur '879, 8:43-57.</p> <p>“Changes in the network topology may be communicated among routers 200 using a link-state protocol, such as the conventional OSPF and IS-IS protocols. Suppose, for example, that a communication link fails or a cost value associated with a network node changes. Once the change in the network's state is detected by one of the routers, that router may flood an IGP Advertisement communicating the change to the other routers in the network. In this manner, each of the routers eventually “converges” to an identical view of the network topology.” Vasseur '879, 8:58-67.</p> <p>“In one embodiment, the routers described herein are IP routers that implement Multi-Protocol Label Switching (MPLS) and operate as label switched routers (LSRs). In one simple MPLS scenario, at an ingress to a network, a label is assigned to each incoming packet based on its forwarding equivalence class before forwarding the packet to a next-hop router. At each router, a forwarding selection and a new substitute label are determined by using the label found in the incoming packet as a reference to a label forwarding table that includes this information. At the network egress, a forwarding decision is made based on the incoming label</p>

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		<p>but optionally no label is included when the packet is sent on to the next hop. In some network configurations, one hop prior to the network egress, a penultimate hop popping (PHP) operation may be performed. Particularly, because the hop prior to the network egress (the penultimate hop) is attached to the network egress, the label is no longer needed to assure that the traffic follows a particular path to the network egress. As such, the PHP-enabled device “pops” the labels from the traffic before forwarding the traffic to the network egress, e.g., using conventional or native (IP) routing, thereby alleviating the task of removing the labels at the network egress.” Vasseur '879, 9:1-23.</p> <p>“The paths taken by packets that traverse the network in this manner are referred to as label switched paths (LSPs) or Traffic Engineering (TE)-LSPs. An example TE-LSP is shown as the thick line and arrow (T1) between a head-end node (router A) and a tailend node (router E) in FIG. 1. Establishment of a TE-LSP requires computation of a path, signaling along the path, and modification of forwarding tables along the path. MPLS TE establishes LSPs that have guaranteed bandwidth under certain conditions. Illustratively, the TE-LSPs may be signaled through the use of the RSVP protocol (with Traffic Engineering extensions), and in particular, RSVP TE signaling messages. Notably, when incorporating the use of PCEs (described below), the path computation request (and response) between PCC and PCE can be exchanged in accordance with a protocol specified in Vasseur, et al., Path Computation Element (PCE) Communication Protocol (PCEP)—Version 1—<draft-vasseur-pce-pcep-02.txt>, Internet Draft, September 2005, the contents of which are hereby incorporated by reference in its entirety. It should be understood that the use of RSVP or PCEP serves only as an example, and that other communication protocols may be used in accordance with the present invention.” Vasseur '879, 9:24-45.</p> <p>“In accordance with RSVP, to request a data flow (TE-LSP) between a sender and a receiver, the sender may send an RSVP path request (Path) message downstream to the receiver along a path (e.g., a unicast route) to identify the sender and indicate e.g., bandwidth needed to accommodate the data flow, along with other attributes of the TE-LSP. The Path message may contain various information about the data flow including, e.g., traffic characteristics of the data flow. Also in accordance with the RSVP, a receiver establishes the TE-LSP between the sender and receiver by responding to the sender's Path message with a reservation request</p>

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		<p>(Resv) message. The reservation request message travels upstream hop-by-hop along the flow from the receiver to the sender. The reservation request message contains information that is used by intermediate nodes along the flow to reserve resources for the data flow between the sender and the receiver, to confirm the attributes of the TE-LSP, and provide a TE-LSP label. If an intermediate node in the path between the sender and receiver acquires a Path message or Resv message for a new or established reservation (TE-LSP) and encounters an error (e.g., insufficient resources, failed network element, etc.), the intermediate node generates and forwards a path or reservation error (PathErr or ResvErr, hereinafter Error) message to the sender or receiver, respectively.” Vasseur '879, 9:46-10:2.</p> <p>“FIG. 3 is a schematic block diagram of portions of a signaling message 300 (e.g., RSVP message, such as Path, Resv or Error) that may be advantageously used with the present invention. Message 300 contains, inter alia, a common header 310 and one or more signaling protocol specific objects 320, such as an LSP-ATTRIBUTE object 330. The common header 310 may comprise a source address 312 and destination address 314, denoting the origination and requested termination of the message 300. Protocol specific objects 320 contain objects necessary for each type of message 300 (e.g., Path, Resv, Error, etc.). For instance, a Path message may have a sender template object, Tspec object, Previous-hop object, etc. The LSP-ATTRIBUTE object 330, for instance, may be used to signal attributes and/or information regarding an LSP (tunnel). To communicate this information, LSP-ATTRIBUTE object 330 (as well as specific objects 320) may include various type/length/value (TLV) encoding formats and/or flags, as will be understood by those skilled in the art. An example of an LSP-ATTRIBUTE object is further described in Farrel, et al., Encoding of Attributes for Multiprotocol Label Switching (MPLS) Label Switched Path (LSP) Establishment Using RSVP-TE <draft-ietf-mpls-rsvpte-attributes-05.txt>, Internet Draft, May 2005, which is hereby incorporated by reference as though fully set forth herein. A Resv message, on the other hand, may have specific objects 320 for a label object, session object, filter spec object, etc., in addition to the LSP-ATTRIBUTE object 330. Error messages 300 (e.g., PathErr or ResvErr), may also have specific objects 320, such as for defining the type of error, etc.” Vasseur '879, 10:3-31.</p>

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		<p>“It should be noted that in accordance with RSVP signaling, the state of the TE-LSP is refreshed on a timed interval, e.g., every thirty seconds, in which RSVP Path and Resv messages are exchanged. This timed interval is configurable by a system administrator. Moreover, various methods understood by those skilled in the art may be utilized to protect against route record objects (RROs) contained in signaling messages for a TE-LSP in the event security/privacy is desired. Such RRO filtering prevents a head-end node of the TE-LSP from learning of the nodes along the TE-LSP, i.e., nodes within the provider network.” Vasseur '879, 10:4-42.</p> <p>“Although the illustrative embodiment described herein is directed to MPLS, it should also be noted that the present invention may advantageously apply to Generalized MPLS (GMPLS), which pertains not only to packet and cell-based networks, but also to Time Division Multiplexed (TDM) and optical networks. GMPLS is well known and described in RFC 3945, entitled Generalized Multi-Protocol Label Switching (GMPLS) Architecture, dated October 2004, and RFC 3946, entitled Generalized Multi-Protocol Label Switching (GMPLS) Extensions for Synchronous Optical Network (SONET) and Synchronous Digital Hierarchy (SDH) Control, dated October 2004, the contents of both of which are hereby incorporated by reference in their entirety.” Vasseur '879, 10:43-55.</p> <p>“To obviate delays associated with updating routing tables when attempting to avoid a failed network element (i.e., during convergence), some networks have employed MPLS TE Fast Reroute (FRR). MPLS Fast Reroute is a technique that may be used to quickly divert (“Fast Reroute”) traffic around failed network elements in a TE-LSP. MPLS Fast Reroute is further described, for example, by Fast Reroute Extensions to RSVP-TE for LSP Tunnels, as incorporated by reference above. According to the technique, one or more network elements (e.g. links or nodes) in a network are protected by backup tunnels following an alternate path. If a failure occurs on a protected link or node, TE-LSPs (and consequently the traffic that they carry) are locally diverted onto an appropriate alternate path (e.g., a “backup tunnel”) by the node immediately upstream from the failure. The backup tunnel acts as a Fast Reroute path for the primary TE-LSP and obviates delays associated with other measures, such as tearing down the primary TE-LSP after having gracefully diverted the TE-LSPs affected by the failure, should an alternate path around the failed network element exist. In the event of a</p>

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		<p>failure of a protected element the head-end node of the backup tunnel (or a “point of local repair,” PLR node) may quickly begin diverting traffic over the backup tunnel with minimal disruption to traffic flow. Those skilled in the art will understand that MPLS Fast Reroute is one example of link or node failure protection, and that other known correction mechanisms may be used in accordance with the present invention. As mentioned above, however, the head-end node of the Fast Rerouted primary tunnel may attempt to reestablish the primary tunnel in response to learning of the protected element failure, particularly in the case where the backup tunnel is a zero-bandwidth (0-BW) tunnel. The attempt to reestablish the primary tunnel has conventionally been a systematic response to Fast Rerouting (diverting) of the primary tunnel, regardless of the path quality of the backup tunnel.” Vasseur ’879, 10:56-11:23.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 11:24-39.</p> <p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as RSVP TE signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or TLV encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object 330 of the RSVP messages 300 to convey the path quality notification.” Vasseur ’879, 11:40-49.</p>

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		<p>“FIG. 4 is a schematic block diagram illustrating the format of an extension object (TLV) 400, such as a Fast Reroute Feedback object, that may be advantageously used with the present invention. The extension object (or sub-object) 400 is illustratively embodied as a TLV contained in an LSP-ATTRIBUTE object 330 of an RSVP message 300 and is extended to carry backup (and/or primary) tunnel path quality information. To that end, the “Feedback” object 400 is organized to include a Type field 405 containing a predetermined type value signifying the specific content of the object 400. The Length field 410 is a variable length value. The TLV encoded format may also comprise one or more non-ordered sub-TLVs 450 carried within the TLV “payload” (e.g. Value field 415), each having a Type field 455, Length field 460, and Value field 465. The fields of the TLV 400 and sub-TLV(s) 450 are used in a variety of manners, including as described herein, according to the present invention.” Vasseur '879, 11:59-12:8.</p> <p>“In accordance with one aspect of the present invention, the head-end node (e.g., router A) requests the establishment of the primary tunnel (e.g., T1), such as a TE-LSP. Notably, the head-end node may be a head-end node for multiple primary tunnels, as will be understood by those skilled in the art. Along with the primary tunnel establishment, the head-end node may also request Fast Reroute protection of one or more network elements (e.g., all intermediate network elements) at a PLR node (e.g., router B as shown). Note that each intermediate node along the primary tunnel may act as a PLR node, and that router B is shown merely for simplicity. Illustratively, the Fast Reroute protection may be embodied as one or more zero-bandwidth (0-BW) backup tunnels at the PLR node (e.g., BT1). Those skilled in the art will also understand that the PLR node may protect more than one primary tunnel originating at more than one corresponding head-end node (not shown).” Vasseur '879, 12:9-25.</p> <p>“The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. For example, various connectivity verification protocols, such as, e.g., Bidirectional Forwarding Detection (BFD), IGP “Hello” packets, BGP KEEPALIVE messages, etc., may be used to detect a failure of a network element, as will be understood by those skilled in the art. Furthermore, other lower-layer failure detection mechanisms (e.g. optical or SONET/SDH alarms) may be used to detect a network element failure. Once failure is detected, the PLR node diverts the</p>

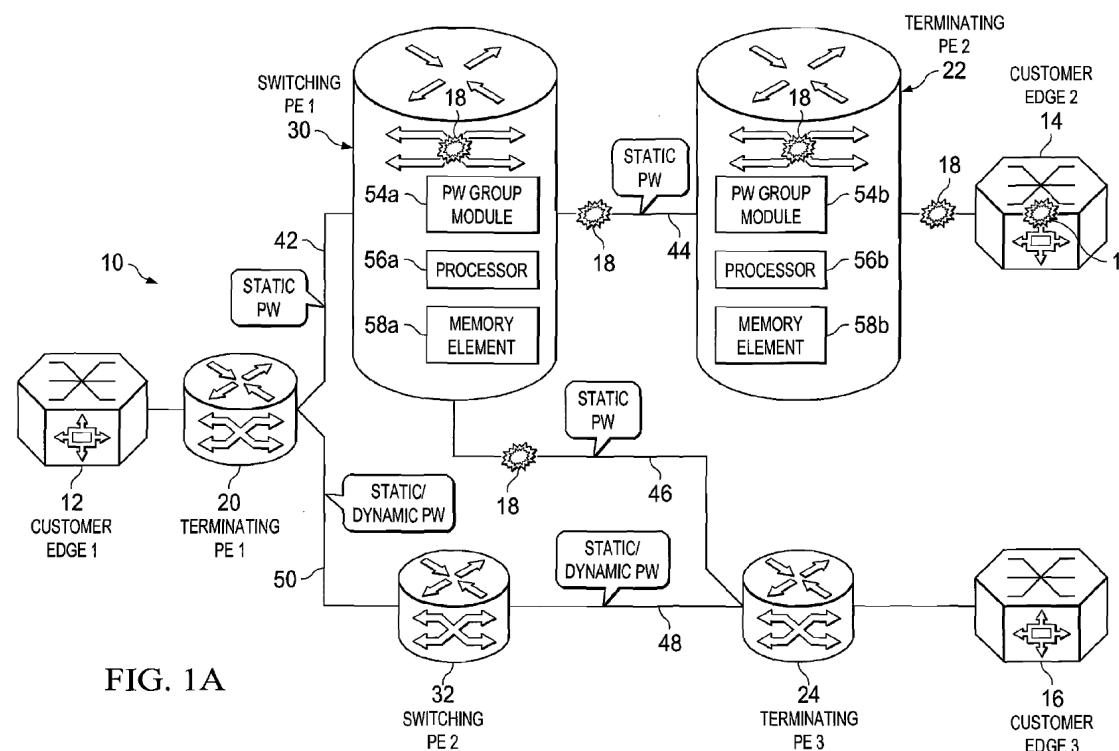
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		<p>traffic onto the backup tunnel, and may send an error message (e.g., an RSVP PathErr 300, such as a conventional “tunnel locally repaired” message) to the head end node indicating the “Fast Rerouting” of the primary tunnel. FIG. 5 is a schematic block diagram of the computer network 100 in FIG. 1 showing Fast Reroute protection of the primary tunnel T1 (e.g., in response to a protected network element, router C, failure, indicated with an overlaid “X”) using a backup tunnel BT1 in accordance with the present invention. Traffic originally received at the PLR node (router B) over the primary tunnel is now diverted over the backup tunnel to a remerge point (router D) of the primary tunnel, as will be understood by those skilled in the art.” Vasseur '879, 12:42-65.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, each PLR node may collect metrics/statistics of the primary and/or backup tunnels. For instance, example metrics may comprise, inter alia, packet drops, path cost, jitter, delay, bandwidth, etc. The PLR node may collect the metrics through traffic monitoring, probes, independent calculations, and/or through cooperation with protected nodes of the primary tunnel (primary nodes) and nodes of the backup tunnel (backup nodes), e.g., transmitting path quality notifications. Once the primary tunnel is Fast Rerouted (i.e., diverted after failure of a protected network element), the PLR node continues to collect metrics of the backup tunnel. (Alternatively, metrics of the backup tunnel may be collected only after Fast Reroute, and not prior to Fast Reroute.)” Vasseur '879, 12:66-13:12.</p> <p>“For example, a path cost increase from the primary tunnel to the backup tunnel may be computed (and transmitted, below) by the PLR node prior to the failure (or during the failure while the primary TE-LSP is diverted onto the backup tunnel) using its own routing tables. The path cost increase may be calculated as a difference between the entire length (head-end node to tail-end node) of the primary and backup tunnels, or just the difference between the protected segment of the primary tunnel and the backup tunnel (PLR node to remerge point). Also, a jitter increase of the primary and backup tunnels, which may be generally described as a difference between inter-arrival of packets, may be monitored using various known techniques, such as, e.g., sending probe packets (probes) from the PLR node to the remerge point. For instance, probes may determine that packets arrive at the remerge point along the primary tunnel from the PLR node consistently, e.g., every 10 milliseconds (ms) (e.g., an</p>

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		<p>average value). After Fast Reroute, however, probes may determine that packets do not arrive at the remerge point along the backup tunnel from the PLR node consistently, e.g., one may arrive in 10 ms, another in 100 ms, another in 50 ms, etc. The non-constant rate of received packets (jitter) may be undesirable, e.g., in particular for voice over IP (VoIP) traffic, as will be understood by those skilled in the art.” Vasseur '879, 13:13-36.</p> <p>“As a further example, packet dropping may be monitored for the primary and backup tunnels prior to and after Fast Reroute (respectively). For instance, based on the tunnel label of the dropped packet, primary nodes and/or backup nodes may be able to distinguish which tunnel corresponds to the dropped packets. Each of the primary and/or backup nodes collect packet drop data, and periodically inform the PLR node of the number of dropped packets (e.g., through a corresponding Feedback object 400). In the case of a backup node, the PLR node receiving the notification may interpret the association of the backup tunnel label and the primary tunnel label to reference an appropriate primary tunnel. Those skilled in the art will understand that the above path quality metrics are merely examples, and that any other metrics/statistics useful for determining path quality of the backup tunnel may be used in accordance with the present invention (e.g., delay, bandwidth, etc.). Further, the path quality information may be measured and compared in a variety of manners, such as, e.g., as a difference between primary and backup tunnels before and after Fast Reroute, or simply the difference between the backup tunnel before and after Fast Reroute, etc.” Vasseur '879, 13:37-58.</p> <p>“Also after the primary tunnel is Fast Rerouted, the PLR node may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. For instance, the novel Feedback object 400 may include one or more sub-TLVs 450 corresponding to metrics/statistics, as described above. Notably, the path quality information pertaining to a particular metric/statistic may be transmitted as total values for interpretation by the head-end node (e.g., to determine the difference), or as PLR-node-computed differences (e.g., between the primary and backup tunnels, or before and after Fast Reroute). For instance, if the delay of the primary tunnel (along the protected segment) prior to Fast Reroute were 2 ms, and after Fast Reroute the delay of the backup tunnel were 5 ms, the notification may be configured to</p>

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		<p>include both values 2 ms and 5 ms, or instead simply the difference, i.e., an increase of 3 ms.” Vasseur '879, 13:59-14:8.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message 300 (PathErr), the head-end node may wait for at least one path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. Also, any number of metrics may be used in the determination, e.g., as configured by a system administrator. For example, using the metrics described above, a head-end node may be configured to reestablish the primary tunnel in response to i) a certain number of packet drops, ii) a percent increase in packet drops, iii) a number of packet drops and a percent increase in path cost, iv) a percent increase in path cost and a percent increase in jitter, etc. Those skilled in the art will understand that these are merely examples of possible path quality comparisons and reestablishment determinations, and that any comparisons to any metrics at any configurable changes may be used in accordance with the present invention.” Vasseur '879, 14:41-59.</p> <p>“If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. FIG. 6 is a schematic block diagram of the computer network 100 in FIG. 5 showing an unacceptable backup tunnel path quality (dotted line and arrow) and resultant reestablishment of the primary tunnel in accordance with the present invention. Those skilled in the art will understand that the reestablished primary tunnel may traverse one or more primary nodes (not shown), and that it may be computed specifically to avoid the failed network element and any network elements of the unacceptable backup tunnel. Those skilled in the art will also understand that the attempt to reestablish the primary tunnel may not be able to find an acceptable path, in which case the head-end node may continue to use the unacceptable backup tunnel or other unacceptable rerouted path.” Vasseur '879, 14:60-15:9.</p>

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		<p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention. The procedure 700 starts at step 705, and continues to step 710, where a head-end node (e.g., router A) establishes a primary tunnel (e.g., T1) to a destination tail-end node (e.g., router E). During or after establishment, the head-end node requests protection of the primary tunnel at step 715, and in response, PLR nodes along the primary tunnel (e.g., router B) create backup tunnels (e.g., BT1) to protect the primary tunnel in step 720. (Those skilled in the art will understand that backup tunnels around the protected network elements may already exist at the PLR node, and that “creating backup tunnels” in step 720 implies an association with pre-existing backup tunnels.) As mentioned above, these backup tunnels may illustratively be embodied as 0-BW backup tunnels. In accordance with the present invention, the head-end node may additionally request backup tunnel path quality notification from the PLR nodes in step 725, such as, e.g., through the use of empty corresponding Feedback objects in RSVP (Path) messages 300, as described above.” Vasseur '879, 15:37-58.</p> <p>“The procedure 700 continues to FIG. 7B (step “A”), where in step 750 the PLR node detecting the failure diverts (“Fast Reroutes”) the primary tunnel traffic to the backup tunnel and sends an error message (e.g., an RSVP (Error) message 300) to the head end node, e.g., a “tunnel locally repaired” message. The detecting PLR node continues to collect path quality information for the backup tunnel in step 755 and at step 760 determines whether to send the path quality notification to the head-end node. For example, as mentioned above, the PLR node may be configured to continually send notifications, or periodically, or in response to a configurable change in path quality, etc. Also as mentioned above, the PLR node may be configured to send either the actual path quality information or the change (difference) in path quality. (As further mentioned above, metrics of the backup tunnel may alternatively be collected only after Fast Reroute, and not prior to Fast Reroute.).” Vasseur '879, 16:4-20.</p> <p>“If the PLR node decides to send the notification in step 760, then the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification in step 765, e.g., based on one or more configurable thresholds, percentages, etc., as described above. If the backup tunnel is currently maintaining an acceptable quality for the</p>

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		<p>traffic flow in step 770, the head-end node may continue to utilize the backup tunnel, and the PLR node continues to collect path quality information in step 755 to detect any change in quality. Otherwise, if the backup tunnel quality is not acceptable in step 770, the head-end node may attempt to reestablish the primary tunnel in step 775. Notably, as mentioned above, if the PLR node has not sent any notification (step 760) within a configurable period of time in step 780, e.g., due to a backup tunnel failure, over-congestion, etc., then the head-end node may also attempt to reestablish the primary tunnel in step 775 accordingly. Moreover, as described above, in the event more than one primary tunnel is Fast Rerouted for the head-end node, various techniques to reestablish one or more of the primary tunnels may be used (e.g., as many tunnels as necessary, a configurable subset of tunnels, all tunnels, the congested tunnels, etc.). The procedure 700 ends in step 785.” Vasseur '879, 16:21-43.</p> <p>“While there has been shown and described an illustrative embodiment that dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network, it is to be understood that various other adaptations and modifications may be made within the spirit and scope of the present invention. For example, the invention has been shown and described herein using “Fast Reroute,” e.g., MPLS TE Fast Reroute (FRR). However, the invention in its broader sense is not so limited, and may, in fact, be used with other network element protection and failure correction mechanisms as will be understood by those skilled in the art. Moreover, while the above description describes performing the technique at the head-end node and PLR node, the invention may also be advantageously used with PCEs. In addition, while one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 16:63-17:13.</p> <p>Rustogi discloses:</p> <p>“An example method includes identifying a fault condition in a network, and evaluating pseudowires affected by the fault condition in order to make a determination as to whether an aggregate failure occurred in the network for a group of pseudowires. The method also includes communicating a group message indicating that the group of pseudowires is associated with the fault condition. The group message includes a group identification (ID),</p>

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		<p>which identifies the group of pseudowires, and the group message includes a pseudowire group label identifying an in-band aggregate channel. More specifically, the pseudowire group label can be applicable to static pseudowires. In more detailed embodiments, the group ID identifies the group of pseudowires that are associated with an attachment circuit, a label switched path, or a port. Internal mappings can be maintained such that a plurality of pseudowires is mapped to individual interfaces of network elements in the network.” Rustogi, Abstract.</p>  <p>FIG. 1A illustrates a network architecture. It shows three Customer Edge nodes (12, 14, 16) connected to a network via terminating PE nodes (20, 22, 24). These terminating PE nodes are connected to switching PE nodes (30, 32, 34). The switching PE nodes contain PW Group Modules (54a, 54b), Processors (56a, 56b), and Memory Elements (58a, 58b). Arrows indicate the flow of static and dynamic pseudowires (PW) between the Customer Edge nodes and the terminating PE nodes, and between the terminating PE nodes and the switching PE nodes. The diagram also shows internal connections within the switching PE nodes, including bidirectional arrows between the PW Group Modules, Processors, and Memory Elements.</p> <p>Rustogi, FIG. 1A.</p>

No.	'821 Patent Claim 14	The Reference
		<pre> graph TD START([START]) --> 100[100 ~ A GIVEN NETWORK ELEMENT CAN IDENTIFY A FAULT CONDITION IT RECEIVES] 100 --> 102[102 ~ THE NETWORK ELEMENT EVALUATES PSEUDOWIRES IN ORDER TO DETERMINE WHETHER A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED] 102 --> 104[104 ~ IF ONLY A FEW PSEUDOWIRES ARE AFFECTED BY THE FAULT CONDITION, THE GROUPING PROTOCOL MAY OPTIONALY NOT BE USED, WHERE A MORE ROUTINE MESSAGING PROTOCOL COULD BE EMPLOYED] 104 --> 106[106 ~ IF A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED, THE GROUPING PROTOCOL IS EMPLOYED TO MINIMIZE THE MESSAGES THAT ARE SENT, RECEIVED, AND PROCESSED IN THE NETWORK] 106 --> 108[108 ~ IN THE CASE OF AN AGGREGATE FAILURE, AN AGGREGATE CHANNEL CAN BE USED TO OFFER APPROPRIATE GROUP MESSAGING. THE INDIVIDUAL MESSAGES THAT CONVEY GROUP IDENTIFICATIONS (IDS) CAN QUICKLY SIGNIFY THE FAULT CONDITION TO NETWORK PEERS] 108 --> END([END]) </pre> <p>FIG. 1B</p> <p>Rustogi, FIG. 1B.</p>

No.	'821 Patent Claim 14	The Reference
		<p>FIG. 2</p> <p>Rustogi, FIG. 2.</p>

No.	'821 Patent Claim 14	The Reference
		<p>The diagram illustrates a network topology with three PE nodes (PE1, PE2, PE3) connected via switching nodes (SW1, SW2) and terminating nodes (TERMINATING PE1, TERMINATING PE2, TERMINATING PE3). The connections are labeled with pseudowire group labels and group identifications:</p> <ul style="list-style-type: none"> Between PE1 and SW1: Pseudowire Group Label A (Group Identification A), Pseudowire Group Label B (Group Identification B). Between SW1 and PE2: Pseudowire Group Label C (Group Identification C), Pseudowire Group Label D (Group Identification D). Between SW2 and PE3: Pseudowire Group Label E (Group Identification E), Pseudowire Group Label F (Group Identification F). Between PE2 and PE3: Pseudowire Group Label G (Group Identification G). <p>Terminating nodes (TERMINATING PE1, TERMINATING PE2, TERMINATING PE3) are connected to their respective PE nodes. The diagram also shows interfaces A, B, C, and D, and a failure condition for interface A and B.</p>

Rustogi, FIG. 3.

No.	'821 Patent Claim 14	The Reference
		<p>The diagram illustrates a network topology involving three PE (Provider Edge) nodes and two switching nodes:</p> <ul style="list-style-type: none"> TERMINATING PE 1 (represented by circle 62) has interfaces A and B. SWITCHING PE 1 (represented by circle 68) has interfaces E and F. It receives a PSEUDOWIRE GROUP LABEL F from PE1 and sends a PSEUDOWIRE GROUP LABEL E to PE2. SWITCHING PE 2 (represented by circle 70) has interface G. TERMINATING PE 2 (represented by circle 64) has interface C. An annotation indicates INTERFACE C FAILS. TERMINATING PE 3 (represented by circle 66) has interface D. <p>Annotations include:</p> <ul style="list-style-type: none"> GROUP ID E → F: Between PE1 and PE2. GROUP IDENTIFICATION E: Associated with interface E. GROUP IDENTIFICATION F: Associated with interface F. GROUP IDENTIFICATION G: Associated with interface G. GROUP IDENTIFICATION H: Associated with interface B. GROUP IDENTIFICATION I: Associated with interface C. GROUP IDENTIFICATION J: Associated with interface D. PSEUDOWIRE GROUP LABEL F: Received by PE1. PSEUDOWIRE GROUP LABEL E: Sent by PE1. INTERFACE C FAILS: An annotation for interface C.

Rustogi, FIG. 4.

No.	'821 Patent Claim 14	The Reference
		<p>FIG. 5</p> <p>80</p> <p>Rustogi, FIG. 5.</p> <p>Detailed description of FIG. 5: The diagram shows a network topology with three PE nodes (PE1, PE2, PE3) connected via switching nodes (68, 70) and terminating nodes (62, 64). - PE1 is connected to 68 (via A) and 70 (via B). - PE2 is connected to 68 (via E) and 64 (via C). - PE3 is connected to 70 (via D FAILS) and 66 (via D). - 68 connects to 62 (via G) and 64 (via E). - 70 connects to 62 (via H) and 66 (via J). - 62 connects to 64 (via F). - 66 connects to 64 (via I). - Labels include: GROUP ID I → G, GROUP ID J → H, GROUP IDENTIFICATION E, GROUP IDENTIFICATION F, GROUP IDENTIFICATION G, GROUP IDENTIFICATION H, GROUP IDENTIFICATION I, GROUP IDENTIFICATION J, PSEUDOWIRE GROUP LABEL G, PSEUDOWIRE GROUP LABEL H, PSEUDOWIRE GROUP LABEL I, PSEUDOWIRE GROUP LABEL J, and INTERFACE A, B, C, D, D FAILS.</p>

No.	'821 Patent Claim 14	The Reference																				
		<p style="text-align: center;">74</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>AC PORT</th><th>LSP</th><th>PW GROUP</th><th>PW GROUP LABEL IDENTIFICATION</th></tr> </thead> <tbody> <tr> <td>INTERFACE A</td><td>LSP 1</td><td>A</td><td>A</td></tr> <tr> <td>INTERFACE B</td><td>LSP 1</td><td>B</td><td>B</td></tr> <tr> <td>REMOTE INTERFACE C ON TERMINATING PE 2</td><td>LSP 2</td><td>F</td><td>F</td></tr> <tr> <td>REMOTE INTERFACE D ON TERMINATING PE 3</td><td>LSP 2</td><td>G, H</td><td>G, H</td></tr> </tbody> </table> <p style="text-align: center;">FIG. 6</p> <p>Rustogi, FIG. 6.</p> <p>“The field of communications has become increasingly important in today's society. In particular, the ability to quickly and to effectively provision connections presents a significant challenge to component manufacturers, system designers, and network operators. Multiprotocol Label Switching (MPLS) is a mechanism in telecommunications networks that carries data from one network node to the next. Layer 2 services (such as Frame Relay, Asynchronous Transfer Mode, and Ethernet) can be emulated over an MPLS backbone by encapsulating the Layer 2 Protocol Data Units (PDUs) and transmitting them over pseudowires. Protocols exist for establishing and maintaining the pseudowires. Certain issues have arisen in pseudowire scenarios, where faults are detected in the network.” Rustogi, ¶ [0002].</p> <p>“FIG. 1A is a simplified block diagram of a communication system for providing pseudowire group labels in a network environment in accordance with one embodiment of the present disclosure.” Rustogi, ¶ [0004].</p>	AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION	INTERFACE A	LSP 1	A	A	INTERFACE B	LSP 1	B	B	REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F	REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H
AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION																			
INTERFACE A	LSP 1	A	A																			
INTERFACE B	LSP 1	B	B																			
REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F																			
REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H																			

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		<p>“FIG. 1B is a simplified flowchart depicting one possible, generic operational flow associated with the communication system.” Rustogi, ¶ [0005].</p> <p>“FIG. 2 is a simplified block diagram of an example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0006].</p> <p>“FIG. 3 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0007].</p> <p>“FIG. 4 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0008].</p> <p>“FIG. 5 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0009].</p> <p>“FIG. 6 is a simplified table of an example set of pseudowire group provisioning parameters in accordance with one embodiment.” Rustogi, ¶ [0010].</p> <p>“FIG. 1A is a simplified block diagram of a communication system 10 for providing pseudowire group labels in accordance with one example of the present disclosure. FIG. 1A includes a customer edge 1 (CE1) 12, a CE2 14, and a CE3 16, where a number of faults 18 are shown as propagating in the network. Typically, when an error or a failure occurs in the network (e.g., an interface failure, a pulled cable, a switch failure, hardware/software failures generally, etc.), messages are sent to various network devices in order to inform them of these fault conditions. Faults 18 of FIG. 1A are indicative of such messages, where the underlying fault condition (being signaled by the messages) can occur virtually anywhere in a network (e.g., in a customer edge, in provider equipment, etc.). FIG. 1A also includes a terminating provider equipment 1 (TPE1) 20, a TPE2 22, a TPE3 24, a switching provider edge 1 (SPE1) 30, and a SPE2 32. In one particular example implementation, the TPEs and SPEs of FIG. 1A are switches that are configured to exchange data in a network environment.” Rustogi, ¶ [0012].</p>

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		<p>“SPE1 30 may include a pseudowire (PW) group module 54 a, a processor 56 a, and a memory element 58 a. In a similar fashion, TPE2 22 may include a pseudowire group module 54 b, a processor 56 b, and a memory element 58 b. FIG. 1A also includes a number of static pseudowires 42, 44, and 46. A set of static/dynamic pseudowires 48, 50 is also provided. Note that the group labeling protocol discussed herein can be executed between individual SPEs, TPEs, or between any combinations of these elements.” Rustogi, ¶ [0013].</p> <p>“In one particular arrangement, communication system 10 is provided in conjunction with a Layer-2 virtual private networks (L2VPN)/operation, administration, and maintenance (OAM) L2VPN/OAM framework. The OAM framework is intended to provide OAM layering across L2VPN services, pseudowires, and packet switched network (PSN) tunnels. Communication system 10 may include any suitable networking protocol or arrangement that provides a communicative platform for communication system 10. Thus, communication system 10 may include a configuration capable of transmission control protocol/internet protocol (TCP/IP) communications for the transmission and/or reception of packets in a network. Communication system 10 may also operate in conjunction with a user datagram protocol/IP (UDP/IP) or any other suitable protocol where appropriate and based on particular needs.” Rustogi, ¶ [0014].</p> <p>“Failure detection and failure notification for static pseudowires is inadequate, where sluggish signaling can result in poor scalability for failover performance. Typically, static pseudowires are manually configured at respective endpoints, where control channels are absent for providing group level signaling messages. Aggregate channels are significant tools for providing suitable scalability in the network, but no such aggregate channel exists for static pseudowires. For dynamic pseudowires, such an aggregate channel may be present in the form of a label distribution protocol (LDP) directed session. However, no such protocol exists for static pseudowire configurations such that an in-band aggregate channel would be available for static pseudowires.” Rustogi, ¶ [0016].</p>

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		<p>“Communication system 10 can address the aforementioned issues (and others) by offering a pseudowire group label that can represent an aggregate channel for groups of static pseudowires. The aggregate channel of communication system 10 can allow for improved scalability of failover performance. In accordance with one potential configuration of communication system 10, a pseudowire group label is representative of a group of static pseudowires transported over a label switched path (LSP). The pseudowire group label can identify the aggregate channel, which captures the hierarchy relevant to OAM mechanisms. Additionally, the groups represented by the group identification (ID) can be mutually exclusive, where a pseudowire is part of only one group. In other embodiments, a pseudowire can be part of multiple groups, or be configured in any other suitable manner based on particular network arrangements.” Rustogi, ¶ [0017].</p> <p>“During operations, and with brief reference to FIG. 1B, a given network element can identify a fault condition it receives (at step 100) and, subsequently, evaluate pseudowires in order to determine whether a sufficient number of pseudowires have been affected. This is reflected by step 102. If only a few pseudowires are affected by the fault condition, the grouping protocol outlined herein may have only nominal value, where there could be an option to simply communicate the fault condition in a more routine manner, as outlined in step 104. However, if a sufficient number of pseudowires have been affected, the grouping protocol outlined herein can be employed to minimize the messages that are sent, received, and processed in the network. This is reflected as step 106. Note that the determination (as to whether a sufficient number of pseudowires have been impacted by the fault condition) can involve accessing internal tables such that a quick mapping can occur to determine if an aggregate failure has occurred. As used herein, the term ‘aggregate failure’ simply connotes that a sufficient number of pseudowires have experienced the fault condition such that an aggregate channel can be employed to offer appropriate group messaging. For the aggregate failure condition, the individual messages that convey Group identifications (IDs) can quickly signify the fault condition to network peers, as shown in step 108.” Rustogi, ¶ [0018].</p>

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		<p>“In specific regards to OAM mechanisms, OAM messages typically result from common failures in the network. These fault conditions can be aggregated such that they are signaled as a single message, which could represent a group of failed pseudowires (as opposed to sending individual messages for each failed pseudowire). Hence, a single message could be sent to represent all the relevant OAM messages propagating in communication system 10. The group label that propagates in communication system 10 provides an architecture with a significant level of aggregation for failed pseudowires (i.e., pseudowires being affected by a given fault condition), particularly for OAM messaging. Moreover, the in-band aggregate channel of communication system 10 is based (at least in part) on the evolving trends of OAM mechanisms, which are required to be fast, responsive, and capable of being implemented in hardware or software. Additionally, in-band OAM protocols are a better measure of the path availability.” Rustogi, ¶ [0020].</p> <p>“In operation of one example implementation, a group label can represent the tuple <attachment circuit (AC) port level grouping, LSP>. This could signify that all pseudowires on an AC port (sought for aggregation) traverse a given LSP. Multiple pseudowire groups can exist within an LSP. Similarly, pseudowires on the same AC port (that traverse a different LSP) can use a different pseudowire group label. Alternatively, an administrator may seek to employ a one-to-one mapping between an LSP and a group label. If that were the case, then only one pseudowire group would exist within an LSP. In scenarios where there is no LSP label in the packet (e.g., due to penultimate hop popping), the pseudowire group label can provide the hierarchy that is appropriate.” Rustogi, ¶ [0021].</p> <p>“In one particular example, the group level pseudowire OAM message can be sent with the following label stack: Explicit/Implicit LSP Label+pseudowire group Label+GAL+ACH+pseudowire OAM with grouping TLV (where GAL=Generic Associated Channel Label, ACH=Associated Channel Header, TLV=Type-Length-Value). If there are multiple LSPs, then one group label can be provisioned for each LSP (for each pseudowire group), where per group messages can be sent on each LSP. The group label does not necessarily have a one-to-one mapping to the grouping of pseudowires implied by the Group ID in the grouping TLV. Note also that the group-based aggregate channel is applicable to</p>

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		<p>static pseudowires, as well as for dynamic pseudowires in certain applications.” Rustogi, ¶ [0022].</p> <p>“As discussed herein, the aggregate channel of communication system 10 can be configured in various ways. For example, and with regards to a first option, a separate label may simply be used to identify a pseudowire group within an LSP. The association of an OAM message and a pseudowire group is straightforward. There could potentially be multiple pseudowire group labels per LSP. As a second option, one group label can be used to identify a common pseudowire group channel on the LSP. In this implementation, one pseudowire group label is provided per LSP. The OAM message association to a pseudowire group is not as simple as the first option. As a third option, one pseudowire is simply designated to convey grouping information (e.g., without using a group label). In this case, there is no need for a pseudowire group label. Again, the OAM message association to a pseudowire group is not as simple as the first option.” Rustogi, ¶ [0023].</p> <p>“Any combination of formatting (for the Group ID and the pseudowire group label) can be used in the group message to be communicated in the network. In one example, only one of these elements is communicated when an aggregate fault condition is detected, or these elements can be combined into a single unique identifier. In the most generic example, a group message would at least include the Group ID (identifying the pseudowires affected by the fault) and a pseudowire group label (identifying an aggregate channel for communicating the group message). In this generic sense, a pipe (the Group ID) within a pipe (the pseudowire group label) is being identified, where the group message is identifying both elements during an aggregate fault condition. Operational details of communication system 10 are described below with reference to FIGS. 2-6. Note that before turning to additional example flows and example embodiments of the present disclosure, a brief overview of the infrastructure of communication system 10 is provided.” Rustogi, ¶ [0024].</p> <p>“CE1 12, CE2 14, and CE3 16 represent devices, infrastructure, equipment, clients, or customers seeking to initiate a data session in communication system 10. These elements may comprise a digital subscriber line access multiplexer (DSLAM), a router, a personal computer, a server, a switch, and/or other devices associated with data propagation. Further,</p>

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		<p>these elements may sit behind, or in front of, one or more of these identified devices. The term ‘CE’ may be inclusive of the devices identified above (e.g., a DSLAM, a switch, etc.), as well as devices used to initiate a communication, such as a console, a proprietary endpoint, a telephone, a cellular telephone, a bridge, a computer, a personal digital assistant (PDA), a laptop or an electronic notebook, or any other device, component, element, or object capable of initiating voice, audio, media, or data exchanges within communication system 10. The customer element may also include any device that seeks to initiate a communication on behalf of another entity or element, such as a program, a database, or any other component, device, element, or object capable of initiating a voice, a video, text, or a data exchange within communication system 10. Data, as used herein in this document, refers to any type of video, numeric, voice, media, or script data, or any type of source or object code, or any other suitable information in any appropriate format that may be communicated from one point to another.” Rustogi, ¶ [0025].</p> <p>“SPE1 30, SPE2 32, TPE1 20, TPE2 22, and TPE3 24 are network elements that facilitate communications in two directions in a network environment. In one particular example, each of these network elements is a switch configured to exchange data over static and/or dynamic pseudowire links. Further, the traffic exchanged between these components may be directed over an MPLS transport in certain embodiments. As used herein in this Specification, the term ‘network element’ is meant to encompass switches, routers, bridges, gateways, servers, processors, loadbalancers, firewalls, or any other suitable device, component, element, or object operable to exchange or process information in a network environment. Moreover, these network elements may include any suitable hardware, software, components, modules, interfaces, or objects that facilitate the operations thereof. This may be inclusive of appropriate algorithms and communication protocols that allow for the effective exchange of data or information. Along similar design alternatives, any of the internal modules and components of these network elements may be combined in various possible configurations.” Rustogi, ¶ [0029].</p>

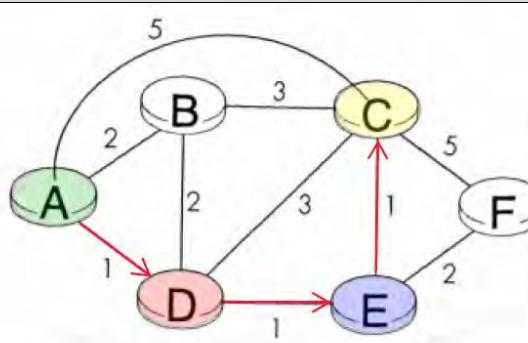
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		<p>“Turning to FIG. 2, FIG. 2 is a simplified block diagram of an example system 60 for providing an example use case using per-label switched path (LSP) pseudowire group labels. FIG. 2 includes a TPE1 62, a TPE2 64, a TPE3 66, a SPE1 68, and a SPE2 70. Each pseudowire group is identified, where a group identification (ID) for Group A and Group B is depicted at TPE1 62. Similarly, Groups C, D, and E have Group IDs at SPE1 68. TPE2 64 and TPE3 66 can couple to interfaces C and D, respectively.” Rustogi, ¶ [0031].</p> <p>“In this particular example, interfaces A and B have failed. Note that there is a multitude of attachment circuits (e.g., 1000 attachment circuits) that are being transported over these interfaces A and B, where the attachment circuits are being tunneled into a corresponding number of pseudowires. For example, there could be 500 attachment circuits on interface A (implicating 500 pseudowires) and 500 attachment circuits on interface B, where the fault condition for the pseudowires should be signaled. In other flawed systems, an architecture would individually signal this fault condition for each pseudowire (e.g., via signaling between TPE1 62 and SPE1 68). Instead of sending 500 messages, a single message can be sent, where a single label (and Group ID) can be used to identify the pseudowires. In this case, the Group ID A is used to signal the fault condition for 300 pseudowires and for 200 pseudowires (i.e., the top two links connecting TPE1 62 and SPE1 68) using a single message (that includes Group Label A and Group ID A). Thus, the status for Group A is quickly communicated to SPE1 68. Similarly, Group ID B can be used to signal the status of the other 500 pseudowires to appropriately convey the status for Group B. More specifically, the message can include Group Label A and Group ID B. Note that all 1000 pseudowires have effectively been accounted for using these Group IDs A and B.” Rustogi, ¶ [0032].</p> <p>“FIG. 3 is a simplified block diagram of an example system 72 for providing another use case for pseudowire group labels. Note that the grouping mechanism outlined herein is not limited to pseudowires that propagate over LSPs. Certain pseudowires can propagate over an LSP and represent one group, where two ports can be provisioned for two different groups (e.g., Group A and Group B). Hence, FIG. 3 is depicting a use case using pseudowire group labels for <port, LSP>mapping. In a general sense, such a configuration is showing how pseudowire mechanics can be used to offer different group signaling, which may be based on various possible implementations. Thus, there is a group level construct corresponding to the group</p>

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		<p>labels that are created such that any OAM protocol can send the appropriate aggregate messages. In this particular example, the signaling for Group ID A, B, C, and D is similar to that of FIG. 2; however, the grouping mechanism has simply changed.” Rustogi, ¶ [0035].</p> <p>“FIG. 4 is a simplified block diagram of an example system 76 for providing another use case for pseudowire group labels. In this particular example, interface C fails (as shown at TPE2 64). Note that the same logical flow occurs in FIG. 4 in terms of the group signaling, as previously discussed. The group labels in two directions do not have to be the same, where the groupings for the messaging are not necessarily symmetrical. In this particular example, TPE2 64 sends a status for Group E with the corresponding group label (i.e., Group ID E for 300 pseudowires), where that message will have a Group Label E and a Group ID E. Hence, this particular signaling is indicative of 300 pseudowires failing in the network. SPE1 68 can send the status for Group F (where the Group ID F is associated with 300 pseudowires) to TPE1 62, where that message includes a Group Label F and a Group ID F.” Rustogi, ¶ [0036].</p> <p>“FIG. 5 is a simplified block diagram of an example system 80 for providing another use case for pseudowire group labels. In this particular example, interface D fails (as shown at TPE3 66), where all 700 pseudowires fail. In one implementation, TPE3 66 does not have a 700 pseudowire Group ID. Instead, the Group IDs can correspond to 200 and 500 pseudowires, when summed together account for the 700 pseudowires. In this particular example, TPE3 66 sends one message for Group I (representing 200 pseudowires) and another message for Group J (representing 500 pseudowires) to SPE1 68. In response, SPE1 68 sends a message for Group G (representing 200 pseudowires) and another message for Group H (representing 500 pseudowires). Again, the signaling being exchanged between these elements is minimal due to the effective grouping of pseudowires. SPE1 68 also sends a single message for Group I (associated with 200 pseudowires) and Group J (associated with 500 pseudowires) to TPE3 66, which is coupled to interface D. Group ID G is associated with 200 pseudowires, whereas Group ID H is representative of 500 pseudowires.” Rustogi, ¶ [0037].</p>

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		<p>“FIG. 6 is a simplified table 74 illustrating an example set of pseudowire group provisioning parameters for TPE1 62, where these particular provisioning parameters could be relevant to the configuration of FIG. 3. At least in one generic sense, FIG. 2 can reflect one approach for mapping a PW group label to a PW Group ID, while FIGS. 3-5 can reflect a second approach for such mappings, where table 74 is associated with that second approach.” Rustogi, ¶ [0038].</p> <p>“In particular, table 74 illustrates the mapping between SPE1 68 and TPE1 62. The first column represents the attachment circuit port (e.g., interface A, interface B, remote interface C on TPE2 64, and remote interface D on TPE3 66). Additionally, table 74 depicts a number of LSPs, a set of pseudowire grouping labels, and a set of pseudowire Group IDs. Note that the Group IDs are provided inside the pseudowire group labels in this example such that these two columns match in table 74. Additionally, note that table 74 is merely representing some of the possible characteristics in a single direction, where different constructs could be used in the reverse direction. Note that the provisioning as discussed herein can significantly reduce messaging such that these presented concepts offer increased scalability. This is due in part to the nominal processing that occurs in the network, in contrast to the processing required to evaluate a prolific amount of signaling messages associated with particular pseudowires. Additionally, the paradigm discussed herein can afford service providers an adequate amount of downtime after a failure has occurred in the network.” Rustogi, ¶ [0039].</p>
14[a]	a data structure comprising a plurality of transport entity descriptors;	<p>The Reference discloses a data structure comprising a plurality of transport entity descriptors.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

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14[b]	an entity protection switch configured to switch between a working entity and a protection entity; and	<p>The Reference discloses an entity protection switch configured to switch between a working entity and a protection entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>
14[c]	digital logic configured to select said working entity and said protection entity from said plurality of transport entity descriptors, comprising:	<p>The Reference discloses digital logic configured to select said working entity and said protection entity from said plurality of transport entity descriptors, comprising.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See infra</i> claim14[d]-[e].</p>
14[d]	logic configured to determine a probability of concurrent failure of said working entity and said protection entity;	<p>The Reference discloses logic configured to determine a probability of concurrent failure of said working entity and said protection entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

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14[e]	<p>logic configured to determine an entity cost of said plurality of transport entity descriptors: and</p>	<p>The Reference discloses logic configured to determine an entity cost of said plurality of transport entity descriptors.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p>Below are examples of such references.</p> <p><u>Kurose discloses:</u></p> <p>For example, Kurose discloses the well-known algorithm of calculating the least-cost between devices when forming a network path.</p> <p>“The purpose of a routing algorithm is simple: given a set of routers, with links connecting the routers, a routing algorithm finds a ‘good’ path from source to destination. Typically, a ‘good’ path is one that has ‘least cost.’” Kurose at 280.</p> <p>“A link also has a value representing the ‘cost’ of sending a packet across the link. The cost may reflect the level of congestion on that link (for example, the current average delay for a packet across that link) or the physical distance traversed by that link (for example, a transoceanic link might have a higher cost than a short-haul terrestrial link). For our current purposes, we’ll simply take the link costs as a given and won’t worry about how they are determined.” Kurose at 280.</p>

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		 <p>Figure 4.4 Abstract model of a network</p> <ul style="list-style-type: none"> ◆ the first link in the path is connected to the source ◆ the last link in the path is connected to the destination ◆ for all i, the i and $i-1$st link in the path are connected to the same node ◆ for the least-cost path, the sum of the cost of the links on the path is the minimum over all possible paths between the source and destination. Note that if all link costs are the same, the least-cost path is also the shortest path (that is, the path crossing the smallest number of links between the source and the destination). <p>In Figure 4.4, for example, the least-cost path between nodes A (source) and C (destination) is along the path ADEC. (We will find it notationally easier to refer to the path in terms of the nodes on the path, rather than the links on the path.)</p> <p>As a simple exercise, try finding the least-cost path from nodes A to F, and reflect for a moment on how you calculated that path. If you are like most people, you found the path from A to F by examining Figure 4.4, tracing a few routes from A to F, and somehow convincing yourself that the path you had chosen had the least cost among all possible paths. (Did you check all of the 12 possible paths between A and F? Probably not!) Such a calculation is an example of a centralized routing algorithm—the routing algorithm was run in one location, your brain, with complete information about the network. Broadly, one way in which we can classify routing algorithms is according to whether they are global or decentralized:</p> <p>Kurose at 281.</p>

No.	'821 Patent Claim 14	The Reference
		<p>“A global routing algorithm computes the least-cost path between a source and destination using complete global knowledge about the network.” Kurose at 281.</p> <p>“In a decentralized routing algorithm, the calculation of the least-cost path is carried out in an iterative, distributed manner.” Kurose at 282.</p>
14[f]	logic configured to reselect said working entity and said protection entity from said plurality of transport entity descriptors upon a reselection event,	<p>The Reference discloses logic configured to reselect said working entity and said protection entity from said plurality of transport entity descriptors upon a reselection event.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[d].</i></p>
14[g]	wherein said reselection event is selected from a group consisting of adding an entity to said plurality of transport entities, removing an entity from said plurality of transport entities, an operational status change for one of said plurality of transport entities, and a change in overall cost for one	<p>The Reference discloses wherein said reselection event is selected from a group consisting of adding an entity to said plurality of transport entities, removing an entity from said plurality of transport entities, an operational status change for one of said plurality of transport entities, and a change in overall cost for one of said plurality of transport entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[e].</i></p>

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	of said plurality of transport entities.	

No.	'821 Patent Claim 15	The Reference
15	The system of claim 14 wherein said entity protection switch comprises a 1:1 switch.	<p>The Reference discloses the system of claim 14 wherein said entity protection switch comprises a 1:1 switch.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 16	The Reference
16	The system of claim 14 wherein said entity protection switch comprises a 1+1 switch.	<p>The Reference discloses the system of claim 14 wherein said entity protection switch comprises a 1+1 switch.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 17	The Reference
17[preamble]	Non-transitory computer readable media configured to perform a method comprising the steps of:	<p>The Reference discloses non-transitory computer readable media configured to perform a method comprising the steps of:</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>
17[a]	providing a plurality of MPLS transport entities between a first endpoint and a second endpoint;	<p>The Reference discloses providing a plurality of MPLS transport entities between a first endpoint and a second endpoint.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[a].</i></p> <p>Cisco created and developed the MPLS and MPLS-TE standards and patented technology based on those standards <i>before</i> Orckit utilized such technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Filsfils • Taylor • Vasseur '879 • Rustogi

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		<p>Filsfils discloses:</p> <p>“In one embodiment, forwarding information bases (FIBs) are selectively populated in a packet switch. A packet switching device determines, based on one or more protocol signaling messages, a subset, which is less than all, on which FIBs a lookup operation may be performed for identifying forwarding information for a received particular packet. The packet switching device populates each of these FIBs, but not all of the FIBs of the packet switching device, with forwarding information corresponding to the particular forwarding value. Thus, FIB resources are consumed for only those FIBs which could actually be used, and not all of the FIBs, for forwarding packets in the data plane of the packet switching device, whether these packets are received on a primary or backup path.” Filsfils, Abstract.</p> <p>FIGURE 1</p> <p>Filsfils, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<p>APPARATUS (E.G., PACKET SWITCHING DEVICE APPLIANCE, CORE ROUTER, LABEL SWITCH ROUTER) 200</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>ROUTE PROCESSOR CONFIGURED TO CORRELATE PRIMARY AND BACKUP PATHS AND POPULATE FIBS ACCORDINGLY</p> <p>COMMUNICATION MECHANISM(S) (E.G., BUS, SWITCHING FABRIC, MATRIX)</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>NETWORK(S)</p> <p>NETWORK(S)</p> <p>FIGURE 2</p> <p>Filsfils, FIG. 2 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<pre> graph TD START([START]) --> 402[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL] 402 --> 404[DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)] 404 --> 406[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 406 --> END([END]) </pre> <p>The flowchart illustrates a process for establishing primary and backup paths for a tunnel. It begins with a START node, followed by a step to exchange protocol signaling messages to establish the primary path. This is followed by a decision point to determine if a backup path needs to be created. If so, another set of protocol signaling messages are exchanged to establish the backup path. The process concludes with an END node.</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL</p> <p>DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.</p>

FIGURE 4

Filsfils, FIG. 4 (annotated).

No.	'821 Patent Claim 17	The Reference
		<pre> graph TD START([START]) --> 502[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, SENDING A PARTICULAR LABEL FROM THE NEIGHBOR UPSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THIS LSR OVER THE PARTICULAR TUNNEL.] 502 --> 504[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF THE PARTICULAR TUNNEL, INCLUDING RECEIVING INFORMATION THAT IT CAN USE TO CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 504 --> 506[CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL, AND ONLY POPULATE THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, THE LSR KNOWS WHAT ON WHAT INTERFACE(S) PACKETS FROM THE BACKUP PATH COULD BE RECEIVED. THIS CORRELATION MAY INCLUDE USING DATA CONCERNING BUNDLED INTERFACES, AND EVEN RECURSIVE CORRELATION OF BACKUP TUNNELS USED TO BACKUP OTHER BACKUP TUNNELS, AS WELL AS LOAD BALANCING AND OTHER TECHNIQUES TO DETERMINE WHERE BACKUP PATH PACKETS COULD BE RECEIVED, AND THE SUBSET OF FORWARDING INFORMATION BASES IN THE DATA PLANE THAT COULD BE USED TO FORWARD PACKETS OVER THE PARTICULAR TUNNEL, WHETHER VIA A PRIMARY OR BACKUP PATH.] 506 --> END([END]) </pre> <p>FIGURE 5</p> <p>Filsfils, FIG. 5.</p>

No.	'821 Patent Claim 17	The Reference
		<p>“The communications industry is rapidly changing to adjust to emerging technologies and ever increasing customer demand. This customer demand for new applications and increased performance of existing applications is driving communications network and system providers to employ networks and systems having greater speed and capacity (e.g., greater bandwidth). In trying to achieve these goals, a common approach taken by many communications providers is to use packet switching technology, including switching packets on labels especially in the core network using Multiprotocol Label Switching (MPLS).” Filsfils, 1:12-22.</p> <p>“Tunnels, such as MPLS-TE (Traffic Engineering) and MPLS-TP (Transport Profile), are paths established through a network in order to transport packets efficiently through a label switched network. Fast Re-Route (FRR) is a technology that allows backup paths to be established in the network, which can be used in case of a problem with a primary path (original primary path or currently used backup path) of the tunnel. RFC 4090, entitled “Fast Reroute Extensions to RSVP-TE for LSP Tunnels,” provides an extension of the protocol signaling to establish backup label switched path (LSP) tunnels for local repair of LSP tunnels.” Filsfils, 1:23-33.</p> <p>“Expressly turning to the figures, FIG. 1 illustrates a network 100 operating according to one embodiment. Shown are four apparatus 101-104 (e.g., packet switching devices such as a label switch router, network device, and/or appliance). For explanation purposes, each of apparatus 101-104 will be referenced as a label switch router (LSR).” Filsfils, 5:41-46.</p> <p>“As shown, a particular tunnel is established, using a signaling protocol and exchanging of protocol signaling messages. Note, LSR 101 may, or may not, be an endpoint of the particular tunnel (e.g., LSR 101 may be an intermediate LSR on the path of the particular tunnel). The primary path of the particular tunnel includes spans from LSR 101 via link 111 to LSR 103 and via link 112 to LSR 104. Note, LSR 104 may be an intermediate LSR on the path of the particular tunnel, or an endpoint of the particular tunnel. Further, for this example embodiment, LSR 103 signals LSR 101 to use label T1 at the top of the label stack in the header of a packet sent to it on the particular tunnel.” Filsfils, 5:47-58.</p> <p>“A second tunnel from LSR 101 via link 121 to LSR 102 and via link 122 to LSR 103 is similarly configured using a signaling protocol and exchanging of protocol signaling</p>

No.	'821 Patent Claim 17	The Reference
		<p>messages. For example purposes, LSR 102 signals LSR 101 to use label (T2) at the top of the label stack in the header of a packet sent to it on the second tunnel. In one embodiment, LSR 101 creates the second tunnel in response to determining, or being instructed to, create a backup path to protect link 111 and/or protect all or certain tunnels traversing link 111.” Filsfils, 5:59-67.</p> <p>“As shown in FIG. 1, link 111 (primary path of the particular tunnel and/or all or certain tunnels traversing link 111) is protected by LSR 101 using the second tunnel (backup path). When sending packets over the particular tunnel over link 111, LSR 101 includes label T1 at the top of the label stack of these packets. If link 111 cannot be used for communicating packets of the particular tunnel, LSR 101 sends packets over the backup path for the particular tunnel by sending packets to LSR 102, with these packets having a label stack including: label T2 followed by label T1. Thus, LSR 102 will receive these packets, pop the top label (T2) from the label stack of each of these packets, identify based on label T2 to send these packets to LSR 103. After popping the top label, the label at the top of the label stack of these packets is T1, which is the same label LSR 103 expects to receive for the particular tunnel. Therefore, these packets received with label T1 at the top of their label stack, are forwarded (after popping label T1 from their label stack) by LSR 103 over the particular tunnel to LSR 104.” Filsfils, 6:6-24.</p> <p>“One embodiment acquires such additional information by extending Resource Reservation Protocol (RSVP) to provide information which allows a packet switch to correlate primary and backup paths. Thus, a packet switch can use this additional information in determining which of its forwarding information bases (FIBs) could possibly be used in forwarding packets (e.g., in the data plane of the packet switch).” Filsfils, 6:51-57.</p> <p>“In providing this additional information to LSR 103, one embodiment communicates an extended RSVP message (including a new or modified RSVP object) or other message to LSR 103 on the second tunnel. This messages designates one or more primary tunnels (e.g., label T1 in our example) and/or a link (e.g., link 111). As LSR 103 knows what interface that it received this message, LSR 103 knows that it must populate forwarding information for these primary tunnels, either specified (e.g., by a label such as T1), or all labels corresponding to</p>

No.	'821 Patent Claim 17	The Reference
		<p>tunnels which could be received over link 111. In one embodiment, the extended RSVP or other message communicated to LSR 103 also includes an identification of the backup tunnel (e.g., T2) over which the RSVP or other message is being received, as the identification the tunnel over which a packet is received is often not communicated in a packet (e.g., in the case of Penultimate Hop Popping).” Filsfils, 7:19-34.</p> <p>“As shown in FIG. 1, one embodiment includes apparatus 103, which populates less than all of its FIBs with forwarding information for a tunnel (although all FIBs may be populated for certain tunnels). One embodiment includes apparatus 101 and/or 102 which communicates, via a signaling protocol (e.g., an extension of RSVP, or using another protocol), information which allows apparatus 103 to determine the relationship between primary and backup paths, such that apparatus 104 can correlate this primary and backup path information (possibly also correlating backup path of backup path information, and/or bundled interfaces and/or bundled links) to identify a minimum subset of the FIBs that could possibly be used in forwarding packets of particular primary paths (e.g., tunnels).” Filsfils, 7:49-62</p> <p>“Turning to FIG. 2, illustrates an apparatus 200 (e.g., packet switching devices such as a label switch router, network device, and/or appliance) operating in one embodiment. As shown, apparatus 200 includes line cards 201, 202 communicatively coupled via communication mechanism(s) 203 (e.g., bus, switching fabric, and/or matrix). Additionally, route processor 204 is configured to correlate primary and backup paths of tunnels, and to populate minimum subsets of FIBs with forwarding information for labels. Again, a minimum subset of FIBs for a particular path or label of the particular path is the set of FIBs that are determined to possibly be used in forwarding packets of a primary path, whether the label is received in a packet over the primary path or over a backup path, and possibly considering backup paths of a backup path and/or the possibly effect of bundled interfaces and/or bundled links.” Filsfils, 7:63-8:11.</p> <p>“As shown in FIG. 2, apparatus 200 is communicatively coupled via primary and backup paths 211, 212 to networks 210 and 213 (which could be the same network). As illustrated, each of line cards 201, 202 includes one or more FIBs. By correlating on which line card(s) 201, 202 and even within line cards 201, 202 that have multiple FIBs, primary and backup path(s) of tunnels, the number of FIB entries populated in apparatus 200 can typically be reduced,</p>

No.	'821 Patent Claim 17	The Reference
		<p>possibly significantly saving memory/storage resources and resources used to populate the FIBs.” Filsfils, 8:12-21.</p> <p>“FIG. 4 illustrates a process performed in one embodiment. Processing begins with process block 400. In process block 402, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes receiving a particular label for a downstream LSR to use when sending packets to the downstream LSR over the particular tunnel.” Filsfils, 8:61-67.</p> <p>“In process block 404, a determination is made to create a backup path from the node (e.g. the node performing these operations). This backup path may be used to protect one or more particular tunnels, and/or may be used to protect a link which is used to carry packet traffic of one or more tunnels.” Filsfils, 9:1-5.</p> <p>“In process block 406, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including providing information to the downstream LSR so that the downstream LSR can correlate primary and backup path(s) of the particular tunnel and substantially only program the FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:6-23.</p> <p>“FIG. 5 illustrates a process performed in one embodiment. Processing begins with process block 500. In process block 502, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes sending a particular label for an upstream LSR to use when sending packets over the particular tunnel to this apparatus (e.g., an LSR performing these operations).” Filsfils, 9:26-32.</p>

No.	'821 Patent Claim 17	The Reference
		<p>“In process block 504, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including receiving information that the LSR can use to correlate primary and backup path(s) of the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:33-47.</p> <p>“In process block 506, the primary and backup path(s) of the particular tunnel are correlated to identify the set of FIBs that could possibly be used in forwarding packets of the particular tunnel. Substantially only those FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel (either through a primary or backup path) are populated with the forwarding information (e.g., an entry corresponding to the label it advertised to use for the particular tunnel) for the particular tunnel. For example, the LSR knows what on what interface(s) packets from the backup path could be received. This correlation may include using data concerning bundled interfaces, and even recursive correlation of backup tunnels used to backup other backup tunnels, as well as load balancing and other techniques to determine where backup path packets could be received, and the subset of forwarding information bases in the data plane that could be used to forward packets over the tunnel, whether via a primary or backup path.” Filsfils, 9:48-65.</p> <p>Taylor discloses:</p> <p>“Grouping pseudowires based on hardware interfaces and configured control paths enables improved pseudowire failover performance. Signaling status changes (e.g., from standby to active status) is facilitated by using group IDs for the pseudowire groups, thereby enabling improved failover performance when there is disruption in the network.” Taylor, Abstract.</p>

No.	'821 Patent Claim 17	The Reference
		<p>The diagram illustrates a network topology with four PE routers (T-PE1, T-PE2, T-PE3, T-PE4) connected via S-PE routers (S-PE1, S-PE2, S-PE3, S-PE4). The connections are labeled with numbers 1, 2, and 101.</p> <ul style="list-style-type: none"> T-PE1: <ul style="list-style-type: none"> Interface e1/0.111: xconnect 10.2.2.2 1 encap mpls; backup peer 10.3.3.3 101 Interface e1/0.222: xconnect 10.3.3.3 2 encap mpls; backup peer 10.2.2.2 102 S-PE2 10.2.2.2: <ul style="list-style-type: none"> 12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls 12 vfi two point-to-point neighbor 10.1.1.1 102 encap mpls T-PE2 10.4.4.4: <ul style="list-style-type: none"> 12 vfi one point-to-point neighbor 10.1.1.1 102 encap mpls 12 vfi two point-to-point neighbor 10.4.4.4 102 encap mpls T-PE3 10.1.1.1: <ul style="list-style-type: none"> e1/0 S-PE3 10.3.3.3: <ul style="list-style-type: none"> 12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls 12 vfi two point-to-point neighbor 10.1.1.1 2 encap mpls T-PE4 10.4.4.4: <ul style="list-style-type: none"> e4/0 T-PE4: <ul style="list-style-type: none"> interface e4/0.111: xconnect 10.2.2.2 1 encap mpls; backup peer 10.3.3.3 101 interface e4/0.222: xconnect 10.3.3.3 2 encap mpls; backup peer 10.2.2.2 102

FIG. 4

Taylor, FIG. 4 (annotated).

No.	'821 Patent Claim 17	The Reference
		<p>The diagram illustrates a network topology with five routers:</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1): Located at the top left. It has two e0/0 interfaces (labeled 1 and 2) connected to S-PE2 and S-PE3 respectively. It also has two e1/0 interfaces (labeled 3 and 4) connected to T-PE4 and T-PE5 respectively. A configuration box labeled 504 contains the following entries: <ul style="list-style-type: none"> T-PE1 → S-PE2: Gid=1 for 1,2,3,4 T-PE1 → S-PE3: Gid=2 for 5,6,7,8 S-PE2 (12.1.1.1): Located at the top center. It has four e0/0 interfaces (labeled 1 through 4) connected to T-PE1, T-PE4, T-PE5, and T-PE3 respectively. A configuration box labeled 514 contains the following entries: <ul style="list-style-type: none"> I2 vif abc point-to-point neighbor 11.1.1.1 encaps mpls neighbor 14.1.1.1 encaps mpls I2 vif def point-to-point neighbor 11.1.1.2 encaps mpls neighbor 14.1.1.2 encaps mpls I2 vif ghi point-to-point neighbor 11.1.1.3 encaps mpls neighbor 14.1.1.3 encaps mpls I2 vif jkl point-to-point neighbor 11.1.1.4 encaps mpls neighbor 14.1.1.4 encaps mpls T-PE4 (14.1.1.1): Located at the top right. It has two e0/0 interfaces (labeled 1 and 2) connected to S-PE2 and S-PE3 respectively. It also has two e1/0 interfaces (labeled 3 and 4) connected to T-PE1 and T-PE5 respectively. A configuration box labeled 506 contains the following entries: <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.11 encaps mpls interface e0/0.200 xconnect 12.1.1.12 encaps mpls interface e1/0.100 xconnect 12.1.1.13 encaps mpls interface e1/0.200 xconnect 12.1.1.14 encaps mpls S-PE3 (13.1.1.1): Located at the bottom left. It has two e0/0 interfaces (labeled 5 and 6) connected to T-PE1 and T-PE5 respectively. It also has two e1/0 interfaces (labeled 7 and 8) connected to T-PE2 and T-PE4 respectively. A configuration box labeled 502 contains the following entries: <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.11 encaps mpls backup peer 13.1.1.15 interface e0/0.200 xconnect 12.1.1.12 encaps mpls backup peer 13.1.1.15 interface e0/0.300 xconnect 12.1.1.13 encaps mpls backup peer 13.1.1.17 interface e0/0.400 xconnect 12.1.1.14 encaps mpls backup peer 13.1.1.18 T-PE5 (15.1.1.1): Located at the bottom right. It has two e0/0 interfaces (labeled 5 and 6) connected to S-PE3 and S-PE4 respectively. It also has two e1/0 interfaces (labeled 7 and 8) connected to S-PE2 and S-PE3 respectively. A configuration box labeled 512 contains the following entries: <ul style="list-style-type: none"> I2 vif abc point-to-point neighbor 11.1.1.5 encaps mpls neighbor 15.1.1.5 encaps mpls I2 vif def point-to-point neighbor 11.1.1.6 encaps mpls neighbor 15.1.1.6 encaps mpls I2 vif ghi point-to-point neighbor 11.1.1.7 encaps mpls neighbor 15.1.1.7 encaps mpls I2 vif jkl point-to-point neighbor 11.1.1.8 encaps mpls neighbor 15.1.1.8 encaps mpls <p>FIG. 5</p> <p>Taylor, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 17	The Reference				
		<p style="text-align: center;">TRAFFIC FLOW IS TOWER ← CORE</p> <p>TOWER T-PE</p> <p>S-PE1</p> <p>CORE T-PE1</p> <p>mLACP</p> <p>RNC/BSC</p> <p>CORE T-PE2</p> <p>S-PE2</p> <p>Legend:</p> <table border="1"> <tr> <td>ACTIVE PW/ACTIVE AC</td> <td>STANDBY PW/ACTIVE AC</td> </tr> <tr> <td>STANDBY* PW/STANDBY AC</td> <td>STANDBY PW/STANDBY AC</td> </tr> </table> <p>* GOES ACTIVE WHEN mLACP FAILS OVER</p> <p>FIG. 8</p> <p>Taylor, FIG. 8 (annotated).</p>	ACTIVE PW/ACTIVE AC	STANDBY PW/ACTIVE AC	STANDBY* PW/STANDBY AC	STANDBY PW/STANDBY AC
ACTIVE PW/ACTIVE AC	STANDBY PW/ACTIVE AC					
STANDBY* PW/STANDBY AC	STANDBY PW/STANDBY AC					

No.	'821 Patent Claim 17	The Reference
		<pre> sequenceDiagram participant TOWER as TOWER T-PE participant S1 as S-PE1 participant S2 as S-PE2 participant CORE as CORE T-PE TOWER->>S1: S-PE1 FFD PWs MARKED DOWN activate S1 S1-->>S2: S-PE1 FAILURE deactivate S1 S2-->>CORE: HOT STANDBY → ACTIVE activate CORE CORE-->>S2: HOT STANDBY → ACTIVE deactivate CORE S2-->>CORE: S-PE1 GROUPS MARKED DOWN deactivate S2 </pre> <p><i>FIG. 9</i></p> <p>Taylor, FIG. 9 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<p>1002 ↗</p> <pre> graph TD A[1004 SPECIFY PHYSICAL INTERFACE AT FIRST PE] --> B[1006 SPECIFY FIRST CONTROL PATH FROM FIRST PE TO SECOND PE] B --> C[1008 SPECIFY SECOND CONTROL PATH FROM FIRST PE TO THIRD PE] C --> D[1010 SPECIFY REDUNDANT COMBINATIONS OF PWs] D --> E[1012 GROUP PWs INTO A PRIMARY PW GROUP AND A BACKUP PW GROUP] E --> F[1014 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO PRIMARY/BACKUP PW GROUPS] F --> G[1016 DETECT FAILURE ON FIRST CONTROL PATH] G --> H[1018 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO BACKUP/PRIMARY PW GROUPS] </pre> <p>FIG. 10</p> <p>Taylor, FIG. 10 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<p>“The present disclosure relates generally to communication networks and more particularly to pseudowire configurations in communication networks.” Taylor, 1:8-10.</p> <p>“Virtual Private Network (VPN) services provide secure network connections between different locations. A company, for example, can use a VPN to provide secure connections between geographically dispersed sites that need to access the corporate network so that each customer edge (CE) end point or node can communicate directly and independently with all other CE nodes. Different types of VPNs have been classified by the network layer used to establish the connection between the customer and provider network. For example, Virtual Private LAN Service (VPLS) is an architecture that delivers a multipoint Layer 2 VPN (L2VPN) service that in all respects emulates an Ethernet Local Area Network (LAN) across a wide metropolitan geographic area. All services in a VPLS appear to be on the same LAN, regardless of location. In other words, with VPLS, customers can communicate as if they were connected via a private Ethernet segment, i.e., multipoint Ethernet LAN services.” Taylor, 1:12-28.</p> <p>“In this context, each CE device at a customer site is connected to the service provider network at a provider edge PE) device by an Attachment Circuit (AC) that provides the customer connection to a service provider network, that is, the connection between a CE node and its associated PE node. Within the provider network, each PE device includes a Virtual Switch Instance (VSI) that emulates an Ethernet bridge (i.e., switch) function in terms of Media Access Control (MAC) address learning and forwarding in order to facilitate the provisioning of a multipoint L2VPN. A pseudowire (PW) is a virtual connection between two PE devices that connect two attachment circuits. In the context of the VPLS service, a pseudowire can be thought of as a point-to-point virtual link for each offered service between a pair of VSIs. Therefore, if each VSI can be thought of as a virtual Ethernet switch for a given customer service instance, then each pseudowire can be thought of as a virtual link connecting these virtual switches to each other over a Packet Switched Network (PSN) for that service instance.” Taylor, 1:29-47.</p>

No.	'821 Patent Claim 17	The Reference
		<p>“Since the failure of pseudowires obviously degrades network performance, some effort has been directed towards adding system redundancies including redundant pseudowires. However, the presence of redundant pseudowires alone is insufficient to improve overall failover performance, that is, the ability to switch over automatically to a redundant or backup system. Thus, there is a need for improved methods for managing pseudowires to facilitate pseudowire switching and enable improved failover performance.” Taylor, 1:48-56.</p> <p>“FIG. 4 shows details for PW connectivity in an exemplary network for an example embodiment.” Taylor, 1:66-67.</p> <p>“FIG. 5 shows details for PW grouping for an example embodiment.” Taylor, 2:1-2.</p> <p>“FIG. 8 shows an example network including redundant PW connections for an example embodiment.” Taylor, 2:9-10.</p> <p>“FIG. 9 shows an example sequence diagram for a failure mode related to the embodiment shown in FIG. 8.” Taylor, 2:11-12.</p> <p>“FIG. 10 shows a flowchart that illustrates a method of providing improved PW grouping according to an example embodiment.” Taylor, 2:13-15.</p> <p>“According to one embodiment, a method of providing improved pseudowire performance includes specifying a physical interface at a first PE node in a network, a first control path from the first PE node to a second PE node in the network, and a second control path from the first PE node to a third PE node in the network. With these specifications, the method then includes specifying redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node, and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Then these pseudowires can be grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. Group identifiers for the primary PW group and the backup PW group can then be used to assign an</p>

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		<p>active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths. The method may then include detecting a failure on the first control path, and in response to the detected failure, using the group identifiers to assign the active status to the backup pseudowires and the standby status to the primary pseudowires.” Taylor, 2:27-52.</p> <p>“Pseudowires are used in pseudowire emulation edge-to-edge to provide a Layer 2 Virtual Private Network (L2VPN) connection. When large numbers (e.g., 4,000-6,000) of pseudowires are aggregated together on a single router, failure performance tends to be linear or O(n) where n is the number of pseudowires. While O(n) performance may be acceptable for small numbers of pseudowires, the effect on network outages can be increasingly undesirable as the number of pseudowires increases.” Taylor, 2:54-62.</p> <p>“For example, a cell-site router will typically start an approximately 2-minute procedure if contact with its controller, which is reached via a pseudowire, is lost for more than some threshold amount (e.g., between approximately 0.75 and 1.75 seconds in some cases). This can be a major impediment to the scalability of pseudowire deployments. These issues have become increasingly relevant as providers of Multiservice Broadband Networks (MBNs) are rapidly replacing or augmenting their traditional Synchronous Optical Networking (SONET) equipment with cheaper Ethernet equipment in the evolution towards a 4G (i.e., 4th generation) network.” Taylor, 2:63-3:7.</p> <p>“One aspect of a solution to the problem of pseudowire failure is the deployment of redundant pseudowires. For example, redundant pseudowires have been used in the context of Multiprotocol Label Switching (MPLS) networks, which use a Label Distribution Protocol (LDP) to manage labels for forwarding traffic between routers. In this context, general requirements for redundancy schemes have been developed so that duplicate pseudowires are available when a given pseudowire fails (e.g., by using active/standby status indicators). In addition, more specific implementations for redundant pseudowires have also been developed.” Taylor, 3:8-18.</p>

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		<p>“FIG. 1 shows a reference network model 102 with applications to example embodiments disclosed herein. The reference network model 102 includes an aggregation network 104 of PE nodes and a distribution network 106 of PE nodes between a radio network controller (RNC) (or base station controller (BSC)) 108 on the core side of the model 102 and a radio tower 111 on the tower side of the model 102. Switching provider edge nodes S-PE1 and S-PE2 connect the two networks 104, 106. On the core side, two core terminating provider edges T-PE1 and T-PE2 connect to the RNC/BSC 108 through attachment circuits 110, 112. On the tower side, one tower terminating provider edge T-PE connects to the radio tower 111 through an attachment circuit 114.” Taylor, 3:19-31.</p> <p>“Additionally as noted in FIG. 1, peer-PE monitoring is carried out within each network 104, 106. That is, there is peer-PE monitoring between provider edges that share a segment, for example, by multi-hop bidirectional forwarding detection (BFD). Alternatively, peer monitoring can be accomplished by other means (e.g., MPLS-TP (Transport Protocol) keep-alives). This peer-PE monitoring is used to provide the mechanism for fast failure detection. Once a failure is detected, the network can react by “rerouting” the failed pseudowires to pre-provisioned backup paths and thus provide a minimal disruption in service to the end-user. This rerouting can be accomplished by LDP signaling between provider edges.” Taylor, 3:32-44.</p> <p>“The reference network model 102 may be considered as part of a larger hub-and-spoke model as shown in FIG. 2. A hub-and-spoke distribution model 202 includes a core network 204, distribution networks 206, and aggregation networks 208. Network elements including distribution nodes, aggregation nodes, and towers are also shown with nominal count values (e.g., 30 distribution nodes between the core network 204 and a distribution network 206). In this model 202, tower T-PEs are the spokes while core-PEs constitute the hub. Dozens to hundreds of tower T-PEs connect to a few S-PEs; these S-PEs are quite similar to ASBRs as they act as forwarders between the two distinct MPLS domains, providing isolation and, in the case of mobility, aggregation services. Typically, several aggregation networks 208 are connected to a single distribution network 206, eventually connecting the tower with the core router that connects the tower's ACs to the RNC/BSC. There are typically several distribution</p>

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		<p>networks in a Radio Access Network (RAN) connected to the service provider's core Internet Protocol (IP) network.” Taylor, 4:11-30.</p> <p>“With reference to FIG. 1, FIG. 3 shows a variety of failure modes encountered in the reference network model 102. Failure 302 of communications between tower T-PE and the S-PE can be detected via peer monitoring when both the S-PE and the T-PE are still active/alive. For example, this failure may be due to a loss of connectivity when the BFD session goes down. Failure 304 of S-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability (e.g., if the BFD-session hello timers cannot be serviced for the prescribed period of time). Failure 306 of communications between S-PE1 and core T-PE1 can be detected via peer monitoring when both S-PE1 and core T-PE1 are still active/alive. Failure 308 at core T-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability.” Taylor, 4:46-60.</p> <p>“Pseudowire connectivity is further illustrated in FIG. 4 where the illustrated network includes four nodes: T-PE1 (10.1.1.1), S-PE2 (10.2.2.2), S-PE3 (10.3.3.3), and T-PEA (10.4.4.4). For the terminating nodes T-PE1 and T-PE4, specifications for VLANs (virtual Local Area Networks) connections (i.e., pseudowires) are shown using the Internet Operating System Command Line Interface (IOS CLI). The specification 402 for T-PE1 defines two VLANs as primary/backup combinations of virtual circuits for the network. The first three lines of the specification 402 define “VLAN 111” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 111” in the first line. The second line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE2 (10.12.2) with a virtual circuit Identification (VCID) set as VCID=1, and the third line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=101. The next three lines of the specification 402 define “VLAN 222” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 222” in the fourth line. The fifth line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=2, and the sixth line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE2 (10.2.2.2) with VCID=102. These virtual circuits, VCID=1, VCID=2, VCID=101 and VCID=102 are shown in the figure between T-PE1 and the S-PEs with a solid line for the primary circuits VCID=1</p>

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		<p>and VCID=2 and a dashed line for the backup circuits VCID=101 and VCID=102.” Taylor, 4:61-5:23.</p> <p>“With respect to T-PE1 in FIG. 4, although “VLAN 111” and “VLAN 222” share the same hardware port, they do not share the same “control path disposition.” That is, “VLAN 111” is primary to S-PE2 (VCID=1) and standby to S-PE3 (VCID=101), while “VLAN 222” has an opposite configuration since it is primary to S-PE3 (VCID=2) and standby to S-PE2 (VCID=102). As discussed below, certain embodiments group pseudowires according to “control path disposition” (e.g., xconnect configuration as well as the hardware interface in order to improve failover performance. That is, to deal with both hardware port failures and switching path failures, the grouping criteria also considers the cross connects. In this case, on T-PE1 as well as T-PE4, there would exist two groups: one for active to S-PE2 and standby to S-PE3 and another for active to S-PE3 and standby to S-PE2 (i.e., the inverse configuration).” Taylor, 5:61-6:9.</p> <p>“First, local connectivity is characterized by local group identifications (Group-IDs), which depend on whether the allocation is done at a T-PE or S-PE. FIG. 5 shows an embodiment that illustrates an allocation of local group IDs in a network including terminating nodes T-PE1 (11.1.1.1), T-PE4 (14.1.1.1), and T-PE5 (15.1.1.1) and switching nodes S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1). The specification 502 for T-PE1 determines corresponding local group IDs 504 based on the hardware interface and the control path. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, and the Group-ID=2 corresponds to VCID=5, VCID=6, VCID=7, and VCID=8.” Taylor, 6:10-21.</p> <p>“Local group IDs are maintained in a database so that pseudowire redundancy is also maintained. First, in a case without pseudowire redundancy, all the xconnect configurations from the same physical interface to the same peer are assigned the same local group ID. So, for example, in Ethernet cases all xconnect configurations under sub-interfaces of the same physical interface to the same peer will be assigned the same local group ID (e.g., e0/0 and e0/1 are sub-interfaces of e0). FIG. 6 shows a database representation for T-PE4 from FIG. 5. From the root node 602 for T-PE4, there is a first interface node 604 for e0/1 and a second interface node 606 for e1/0. The first interface node 604 is configured towards a single peer</p>

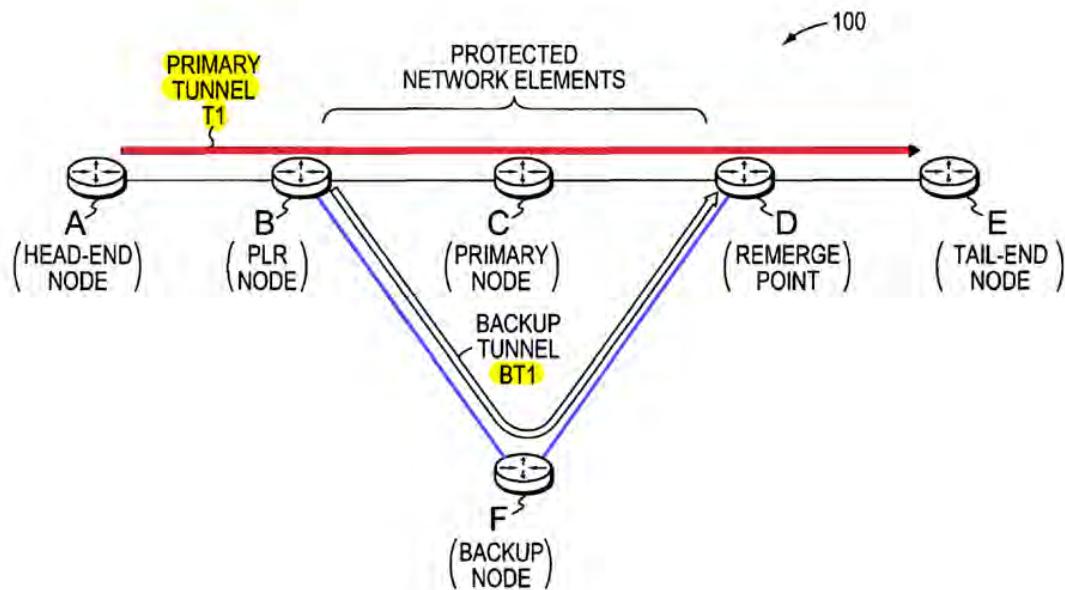
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		<p>node (12.1.1.1) 608 and is thus assigned a single local group ID (Group-ID=200) 610. Similarly, the second interface node 606 is configured towards a single peer node (12.1.1.1) 612 and is thus assigned a single local group ID (Group-ID=201) 614. In this case from the assignment of local group IDs 508 in FIG. 5, Group-ID=200 corresponds to VCID=1 and VCID=2, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). Both pseudowires (VCID=1 and VCID=2) are assigned the same local group ID (Group-ID=200) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly, from the assignment of local group IDs 508 in FIG. 5, T-PE4 has VCID=3 and VCID=4 under the physical interface e1/0 going to the same peer S-PE2 (12.1.1.1), and the local Group ID (Group-ID=201) is assigned to these VCs.” Taylor, 6:48-7:8.</p> <p>“For the pseudowire redundancy case, a separate redundancy-group database is maintained by the xconnect application. This redundancy-group database contains the peer IDs in the group and the local group IDs advertised to them. This is needed to maintain a 1:1 mapping between the primary pseudowires and their corresponding backup pseudowires. FIG. 7 shows a database representation for T-PE1 from FIG. 5. From the root node 702 for T-PE1, there is an interface node 704 for e0/0 and a redundancy group node 706 that shows connections for configurations to a first peer node (12.1.1.1) 708, which is assigned a local group ID (Group-ID=1) 710, and a second peer node (13.1.1.1) 712, which is assigned a local group ID (Group-ID=2) 714. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). These pseudowires are assigned the same group ID (Group-ID=1) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly from the assignment of local group IDs 504 in FIG. 5, T-PE1 has VCID=5, VCID=6, VCID=7, and VCID=8 under the physical interface e0/0 going to another peer S-PE3 (13.1.1.1), and the local group ID (Group-ID=2) is assigned to these VCs. In this case these local group IDs are organized as a redundancy group 706.” Taylor, 7:9-35.</p>

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		<p>“Pseudowire grouping allows multiple pseudowires to be aggregated together when signaling either label withdrawals or status changes between segment end-point provider edges. This signaling can be carried out through LDP grouping TLV (Type Length Value). For example, when a PE node fails (e.g., failure 304 of S-PE1), aggregating the inter-segment PE signaling using the grouping TLV can provide significant scaling advantages. This allows all pseudowires sharing a physical port and PW configuration (e.g., xconnect configuration) to be signaled en masse between segment-adjacent provider edges.” Taylor, 7:36-46.</p> <p>“FIG. 8 shows an example based on FIG. 1 where VLAN ACs are shown as grouped by both port/HW-interface and pseudowire-class. The grouping criterion allows all “similar” pseudowires to be signaled together: All the grouped pseudowires share the same port and next-hop provider edge. Additionally, the figure contains many pseudowires, each grouped into a shaded tube. For example, the tube labeled “VLANs 100-549” contains 450 pseudowires grouped together. This figure depicts an incoming Ethernet comprised of 900 VLANs being segmented in two with 450 VLANs (100-549) active to S-PE1 while the other half of the VLANs (550-999) being active to S-PE2. This might be considered a type of manual load balancing. Furthermore, the aggregation network is only showing a single tower and the VLANs associated with it; other VLAN destinations are not shown in the figure.” Taylor, 7:47-62.</p> <p>“‘VLANs 110-112’ are active along a first pseudowire path 802 from Core T-PE1 to S-PE1 and a second pseudowire path 804 from S-PE1 to Tower T-PE. When a failure occurs at S-PE1 (e.g., as the switching node failure 304 shown in FIG. 3), then the standby pseudowires become active for ‘VLANs 110-112’ along a first pseudowire path 806 from Core T-PE1 to S-PE2 and a second pseudowire path 808 from S-PE2 to Tower T-PE.” Taylor, 7:63-8:3.</p> <p>“The standby pseudowires in FIG. 8 can be configured as HSPWs, a configuration that enables ACs to quickly failover to pre-provisioned pseudowires that are in active state but set to not-forwarding. Then when a failure occurs, switching over to these pre-provisioned HSPWs occurs quickly by switching from not-forwarding status to forwarding status.” Taylor, 8:4-9.</p>

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		<p>“FIG. 9 shows a UML (Unified Modeling Language) sequence diagram of signaling events related to the failover procedure illustrated in FIG. 8 for a switching node failure 304. The Tower T-PE and the Core T-PE independently detect a failure at S-PE1 (e.g., BFD forwarding with LDP signaling), and then mark down the status of the currently active pseudowires routed through S-PE1 and mark up the status of the standby pseudowires routed through S-PW2. Other failure modes shown in FIG. 3 can be handled similarly.” Taylor, 8:10-18.</p> <p>“In general, it is desirable for MPLS-based Ethernet networks to react quickly to failures, so proactive detection mechanisms are employed in order to pick up system failures quickly. All proactive monitoring is typically done between PE peers on a single MPLS network. These provider edges on the edges of the MPLS network act similarly to an Autonomous System Boundary Router (ASBR). As a result, related embodiments detect control path failures, which may not be the same as pseudowire data path failures. That is, the data packets and control packets may take different paths between provider edges in a MPLS network although typically these paths are coincident. Thus, when the control and data paths are not coincident, if the control path fails, then all pseudowires utilizing the control path are marked as failed. As a corollary, if the data path fails and the control path remains healthy, then failure will not be detected from monitoring the control path.” Taylor, 8:19-35.</p> <p>“A failure of a monitored provider edge initiates a switchover of all active pseudowires using the failing provider edge to their configured HSPWs (if they exist). Grouping can greatly reduce the number of messages needed between provider edges (Inter-PE Aggregation) and within a single provider edge (Intra-PE Aggregation). Furthermore, the MPLS network itself may be internally resilient deploying technologies such as, but not limited to, MPLS-TE (MPLS Traffic Engineering) and ERR (Fast Reroute). The paths across the MPLS network may recover quickly and might not trip the fault-monitoring systems.” Taylor, 8:36-46.</p> <p>“With reference to the above discussion, FIG. 10 shows a method 1002 of providing improved PW grouping according to an example embodiment. In a first operation 1004 of the method 1002, a physical interface is specified at a first PE node in a network. In a second operation 1006, a first control path is specified from the first PE node to a second PE node in the network. In a third operation 1008, a second control path is specified from the first PE node to a third</p>

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		<p>PE node in the network. These control paths related to a common physical interface can be used to characterize redundant pairs of pseudowires.” Taylor, 8:48-58.</p> <p>“In a fourth operation 1010, redundant combinations of pseudowires are specified, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Although a redundant combination may relate a single backup pseudowire to a given primary pseudowire, in some cases multiple backup pseudowires will be related to a given primary pseudowire for increased redundancy. In a fifth operation 1012, these pseudowires are grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. In a sixth operation 1014, group identifiers for the primary PW group and the backup PW group are used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 8:59-9:11.</p> <p>“In an optional seventh operation 1016, a failure may be detected on the first control path, and in an optional eighth operation 1018, in response to the detected failure, the group identifiers may be used to assign the active status to the backup pseudowires and the standby status to the primary pseudowires. For example, the failure on the first control path may be detected by using BED packet streams between PE nodes of the network. Then the detected failure can be signaled to PE nodes in the network by sending LDP status updates between PE nodes in the network. Then, after receiving the failure detection signals, the group identifiers can be used again to assign the active status to the backup pseudowires and the standby status to the primary pseudowires by sending LDP status updates between PE nodes in the network.” Taylor, 9:12-26.</p> <p>“Typically the network in is an MPLS network and the PE nodes are routers that provide network services to connected CE nodes of a customer network. In general, each control path is an Internet Protocol (IP) routing path between PE nodes in the network and each data path is a label switched path (LSP) between PE nodes in the network.” Taylor, 9:27-32.</p>

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		<p>“FIG. 11 shows a schematic representation of an apparatus 1102, in accordance with an example embodiment. For example, the apparatus 1102 may be used to implement the method 1002 of providing improved pseudowire grouping as described above with reference to FIG. 10. The apparatus 1102 is shown to include a processing system 1104 that may be implemented on a server, client, or other processing device that includes an operating system 1106 for executing software instructions.” Taylor, 10:2-10.</p> <p>“In accordance with an example embodiment, the apparatus 1102 includes a PW management module 1108 that includes a first specification module 1110, a second specification module 1112, third specification module 1114, a fourth specification module 1116, a grouping module 1118, and an assignment module 1120. The first specification module 1110 operates to specify a physical interface at a first PE node in a network. The second specification module 1112 operates to specify a first control path from the first PE node to a second PE node in the network. The third specification module 1114 operates to specify a second control path from the first PE node to a third PE node in the network. The fourth specification module 1116 operates to specify redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node.” Taylor, 10:11-29.</p> <p>“The grouping module 1118 operates to group the pseudowires into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. The assignment module 1120 operates to use group identifiers for the PW groups to assign an active status to the primary pseudowires and a standby status to the backup pseudowires. The active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 10:30-38.</p>

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		<p>Vasseur '879 discloses:</p> <p>“A technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, Abstract.</p>  <p style="text-align: center;">FIG. 1</p> <p>Vasseur '879, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<p>SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)</p> <p>COMMON HEADER 310</p> <p>SOURCE ADDRESS 312</p> <p>DESTINATION ADDRESS 314</p> <p>SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320</p> <p>LSP-ATTRIBUTE OBJECT 330</p> <p>EXTENSION OBJECT(S) 400</p>

FIG. 3

Vasseur '879, FIG. 3 (annotated).

No.	'821 Patent Claim 17	The Reference
		<p style="text-align: center;">FIG. 5</p> <p>Vasseur '879, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<p>FIG. 6</p> <p>Vasseur '879, FIG. 6 (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<pre> graph TD START([START]) --> 705[HEAD-END NODE ESTABLISHES PRIMARY TUNNEL] 705 --> 710[HEAD-END NODE REQUESTS PROTECTION OF PRIMARY TUNNEL] 710 --> 715[POINTS OF LOCAL REPAIR (PLRs) CREATE BACKUP TUNNELS TO PROTECT PRIMARY TUNNEL] 715 --> 720[HEAD-END NODE REQUESTS BACKUP TUNNEL PATH QUALITY NOTIFICATION] 720 --> 725[PLRs OF PRIMARY TUNNEL COLLECT PATH QUALITY INFORMATION (e.g., METRICS/STATISTICS) FOR PRIMARY AND BACKUP TUNNELS (e.g., FROM PRIMARY AND/OR BACKUP NODES)] 725 --> 730[PLRs MAY SEND PATH QUALITY NOTIFICATION TO HEAD-END NODE] 730 --> 735[PLRs MONITOR PROTECTED SEGMENT OF PRIMARY TUNNEL FOR FAILURE] 735 --> 740{DETECT FAILURE ?} 740 -- N --> 705 740 -- Y --> A((A)) </pre> <p>FIG. 7A Vasseur '879, FIG. 7A (annotated).</p>

No.	'821 Patent Claim 17	The Reference
		<pre> graph TD A((A)) --> B[DETECTING PLR DIVERTS PRIMARY TUNNEL TRAFFIC TO BACKUP TUNNEL AND SENDS ERROR MESSAGE TO HEAD-END NODE] B --> C[PLR CONTINUES TO COLLECT PATH QUALITY INFORMATION FOR BACKUP TUNNEL] C --> D{PLR SEND NOTIFICATION TO HEAD-END NODE ?} D -- N --> E{TIMER EXPIRED ?} E -- Y --> F[HEAD-END NODE DETERMINES WHETHER TO REESTABLISH PRIMARY TUNNEL BASED ON BACKUP TUNNEL PATH QUALITY NOTIFICATION] F --> G{ACCEPTABLE PATH QUALITY ?} G -- Y --> H[HEAD-END NODE ATTEMPTS TO REESTABLISH PRIMARY TUNNEL] G -- N --> I(END) H --> I D -- N --> C D -- Y --> F </pre> <p>The flowchart describes a process for handling a primary tunnel diversion. It begins with step 700 (A), which involves detecting that a PLR has diverted primary tunnel traffic to a backup tunnel and sending an error message to a head-end node. Step 750 follows, where the PLR continues to collect path quality information for the backup tunnel. A decision diamond 760 asks if the PLR should send a notification to the head-end node. If the answer is 'N', the process loops back to step 755. If the answer is 'Y', it proceeds to step 765, where the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification. Another decision diamond 770 then checks if the path quality is acceptable. If 'Y', the process moves to step 775, where the head-end node attempts to reestablish the primary tunnel. If 'N', the process ends at step 785. A timer expiration check (step 780) is also present, which loops back to step 755 if 'N' and proceeds to step 765 if 'Y'.</p> <p>FIG. 7B</p> <p>Vasseur '879, FIG. 7B.</p>

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		<p>“The present invention relates to computer networks and more particularly to dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 1:10-14.</p> <p>“Since management of interconnected computer networks can prove burdensome, smaller groups of computer networks may be maintained as routing domains or autonomous systems. The networks within an autonomous system (AS) are typically coupled together by conventional “intradomain” routers configured to execute intradomain routing protocols, and are generally subject to a common authority. To improve routing scalability, a service provider (e.g., an ISP) may divide an AS into multiple “areas.” It may be desirable, however, to increase the number of nodes capable of exchanging data; in this case, interdomain routers executing interdomain routing protocols are used to interconnect nodes of the various ASes. Moreover, it may be desirable to interconnect various ASes that operate under different administrative domains. As used herein, an AS or an area is generally referred to as a “domain,” and a router that interconnects different domains together is generally referred to as a ‘border router.’” Vasseur '879, 1:40-56.</p> <p>“An example of an interdomain routing protocol is the Border Gateway Protocol version 4 (BGP), which performs routing between domains (ASes) by exchanging routing and reachability information among neighboring interdomain routers of the systems. An adjacency is a relationship formed between selected neighboring (peer) routers for the purpose of exchanging routing information messages and abstracting the network topology. The routing information exchanged by BGP peer routers typically includes destination address prefixes, i.e., the portions of destination addresses used by the routing protocol to render routing (“next hop”) decisions. Examples of such destination addresses include IP version 4 (IPv4) and version 6 (IPv6) addresses. BGP generally operates over a reliable transport protocol, such as TCP, to establish a TCP connection/session. The BGP protocol is well known and generally described in Request for Comments (RFC) 1771, entitled A Border Gateway Protocol 4 (BGP-4), published March 1995.” Vasseur '879, 1:57-2:7.</p>

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		<p>“Examples of an intradomain routing protocol, or an interior gateway protocol (IGP), are the Open Shortest Path First (OSPF) routing protocol and the Intermediate-System-to-Intermediate-System (IS-IS) routing protocol. The OSPF and IS-IS protocols are based on link-state technology and, therefore, are commonly referred to as link-state routing protocols. Link-state protocols define the manner with which routing information and network-topology information are exchanged and processed in a domain. This information is generally directed to an intradomain router's local state (e.g., the router's usable interfaces and reachable neighbors or adjacencies). The OSPF protocol is described in RFC 2328, entitled OSPF Version 2, dated April 1998 and the IS-IS protocol used in the context of IP is described in RFC 1195, entitled Use of OSI IS-IS for routing in TCP/IP and Dual Environments, dated December 1990, both of which are hereby incorporated by reference.” Vasseur '879, 2:8-24.</p> <p>“An intermediate network node often stores its routing information in a routing table maintained and managed by a routing information base (RIB). The routing table is a searchable data structure in which network addresses are mapped to their associated routing information. However, those skilled in the art will understand that the routing table need not be organized as a table, and alternatively may be another type of searchable data structure. Although the intermediate network node's routing table may be configured with a predetermined set of routing information, the node also may dynamically acquire (“learn”) network routing information as it sends and receives data packets. When a packet is received at the intermediate network node, the packet's destination address (e.g., stored in a header of the packet) may be used to identify a routing table entry containing routing information associated with the received packet. Among other things, the packet's routing information indicates the packet's next-hop address.” Vasseur '879, 2:25-41.</p> <p>“Multi-Protocol Label Switching (MPLS) Traffic Engineering has been developed to meet data networking requirements such as guaranteed available bandwidth or fast restoration. MPLS Traffic Engineering exploits modern label switching techniques to build guaranteed bandwidth end-to-end tunnels through an IP/MPLS network of label switched routers (LSRs). These tunnels are a type of label switched path (LSP) and thus are generally referred to as MPLS Traffic Engineering (TE) LSPs. Examples of MPLS TE can be found in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels dated December 2001, RFC 3784</p>

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		<p>entitled Intermediate-System-to-Intermediate-System (IS-IS) Extensions for Traffic Engineering (TE) dated June 2004, and RFC 3630, entitled Traffic Engineering (TE) Extensions to OSPF Version 2 dated September 2003, the contents of all of which are hereby incorporated by reference in their entirety.” Vasseur '879, 2:58-3:6.</p> <p>“Establishment of an MPLS TE-LSP from a head-end LSR to a tail-end LSR involves computation of a path through a network of LSRs. Optimally, the computed path is the “shortest” path, as measured in some metric, that satisfies all relevant LSP Traffic Engineering constraints such as e.g., required bandwidth, “affinities” (administrative constraints to avoid or include certain links), etc. Path computation can either be performed by the head-end LSR or by some other entity operating as a path computation element (PCE) not co-located on the head-end LSR. The head-end LSR (or a PCE) exploits its knowledge of network topology and resources available on each link to perform the path computation according to the LSP Traffic Engineering constraints. Various path computation methodologies are available including CSPF (constrained shortest path first). MPLS TE-LSPs can be configured within a single domain, e.g., area, level, or AS, or may also span multiple domains, e.g., areas, levels, or ASes.” Vasseur '879, 3:7-24.</p> <p>“The PCE is an entity having the capability to compute paths between any nodes of which the PCE is aware in an AS or area. PCEs are especially useful in that they are more cognizant of network traffic and path selection within their AS or area, and thus may be used for more optimal path computation. A head-end LSR may further operate as a path computation client (PCC) configured to send a path computation request to the PCE, and receive a response with the computed path, potentially taking into consideration other path computation requests from other PCCs. It is important to note that when one PCE sends a request to another PCE, it acts as a PCC.” Vasseur '879, 3:25-36.</p> <p>“Some applications may incorporate unidirectional data flows configured to transfer time-sensitive traffic from a source (sender) in a computer network to a destination (receiver) in the network in accordance with a certain “quality of service” (QoS). Here, network resources may be reserved for the unidirectional flow to ensure that the QoS associated with the data flow is maintained. The Resource ReSerVation Protocol (RSVP) is a network-control protocol that</p>

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		<p>enables applications to reserve resources in order to obtain special QoS for their data flows. RSVP works in conjunction with routing protocols to, e.g., reserve resources for a data flow in a computer network in order to establish a level of QoS required by the data flow. RSVP is defined in R. Braden, et al., Resource ReSerVation Protocol (RSVP), RFC 2205, the contents of which are hereby incorporated by reference in its entirety. In the case of traffic engineering applications, RSVP signaling (with Traffic Engineering extensions) is used to establish a TE-LSP and to convey various TE-LSP attributes to routers, such as border routers, along the TE-LSP obeying the set of required constraints whose path may have been computed by various means.” Vasseur ’879, 3:37-57.</p> <p>“Generally, a tunnel is a logical structure that encapsulates a packet (a header and data) of one protocol inside a data field of another protocol packet with a new header. In this manner, the encapsulated data may be transmitted through networks that it would otherwise not be capable of traversing. More importantly, a tunnel creates a transparent virtual network link between two network nodes that is generally unaffected by physical network links or devices (i.e., the physical network links or devices merely forward the encapsulated packet based on the new header). While one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur ’879, 3:58-4:3.</p> <p>“Occasionally, a network element (e.g., a node or link) will fail, causing redirection of the traffic that originally traversed the failed network element to other network elements that bypass the failure. Generally, notice of this failure is relayed to the nodes in the network through an advertisement of the new network topology, e.g., an IGP or BGP Advertisement, and routing tables are updated to avoid the failure accordingly. Reconfiguring a network in response to a network element failure using, e.g., pure IP rerouting, can be time consuming. Many recovery techniques, however, are available to provide fast recovery and/or network configuration in the event of a network element failure, including, inter alia, “Fast Reroute”, e.g., MPLS TE Fast Reroute. An example of MPLS TE Fast Reroute is described in Pan, et al., Fast Reroute Extensions to RSVP-TE for LSP Tunnels, RFC 4090, May 2005, which is hereby incorporated by reference as though fully set forth herein.” Vasseur ’879, 4:4-21.</p>

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		<p>“Fast Reroute (or FRR) has been widely deployed to protect against network element failures, where “backup tunnels” are created to bypass one or more protected network elements (e.g., links, shared risk link groups (SRLGs), and nodes). When the network element fails, traffic is quickly diverted (“Fast Rerouted”) over a backup tunnel to bypass the failed element, or more particularly, in the case of MPLS, a set of primary TE-LSPs (tunnels) is quickly diverted. Specifically, the point of local repair (PLR) node configured to reroute the traffic inserts (“pushes”) a new label for the backup tunnel, and the traffic is diverted accordingly. Once the failed element is bypassed, the backup tunnel label is removed (“popped”), and the traffic is routed along the original path according to the next label (e.g., that of the original TE-LSP). Notably, the backup tunnel, in addition to bypassing the failed element along a protected primary TE-LSP, also intersects the primary TE-LSP, i.e., it begins and ends at nodes along the protected primary TE-LSP.” Vasseur '879, 4:22-39.</p> <p>“To offer maximum protection, e.g., guaranteed bandwidth, during Fast Reroute, backup tunnels may reserve a configurable amount of bandwidth to ensure that Fast Rerouted traffic from the primary tunnel has a reserved path to follow. For example, the bandwidth reserved for the primary tunnel may also be reserved on the backup tunnel. While this approach provides maximum protection, it also requires a non-negligible amount of network resources (e.g., capacity/bandwidth) and may increase operational complexity.” Vasseur '879, 4:40-48.</p> <p>“Certain techniques are available to efficiently minimize the amount of resources required by the establishment and maintenance of the backup tunnels for Fast Reroute. One such technique is to create zero-bandwidth (“0-BW”) backup tunnels (i.e., tunnels that reserve no bandwidth) to protect non-0-BW primary tunnels. This “best effort” approach does not guarantee that the path followed by the backup tunnel will have enough bandwidth to support the diverted primary tunnel at the time of failure without QoS degradation, however in many situations the path quality of the backup tunnel is sufficient. For instance, if the network is not overly congested, or the backup tunnel follows a non-congested path, there may be enough available bandwidth along the backup tunnel to support the newly rerouted traffic. Also, because primary tunnels often reserve bandwidth in response to “peak” traffic utilization, the amount of traffic over the primary tunnel at the time of failure may be far less than the reserved bandwidth (e.g., at “off-peak” times). Notably, those skilled in the art will understand that in</p>

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		<p>the absence of the above exceptions, a 0-BW backup tunnel may have unacceptable bandwidth (e.g., affecting path quality) to support the diverted traffic.” Vasseur ’879, 4:49-5:2.</p> <p>“Currently, head-end nodes (LSRs) may be configured to systematically reroute the primary tunnels affected by the network element failure (e.g., diverted primary tunnels), especially in the case with 0-BW backup tunnels, such as, e.g., by reestablishing a new primary tunnel that follows a path excluding the failed network element. In particular, 0-BW backup tunnels represent a best effort attempt to allow the head-end node to more gracefully reestablish the primary tunnel in response to a failure, since the backup tunnels may not be able to support the diverted traffic without QoS degradation. The systematic reestablishing may potentially result in the reestablishment of a large number of primary tunnels (e.g., up to 3000 for a single network element failure in today’s networks). Notably, reestablishing diverted primary tunnels may be undesirable for the network, such as by creating traffic churn, jitter, control plane overloads, etc., as will be understood by those skilled in the art. However, as noted above, there are situations where the backup tunnel may provide acceptable bandwidth, at least, for example, for a period of time (e.g., possibly short) until the failed network element is restored. In these situations, then, it may have been unnecessary to reestablish the diverted primary tunnels. There remains a need, therefore, for a technique that dynamically determines whether to reestablish a diverted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur ’879, 5:3-28.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 5:32-47.</p>

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		<p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as Resource ReSerVation Protocol (RSVP) Traffic Engineering (TE) signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or type/length/value (TLV) encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object of the RSVP messages to convey the path quality notification.” Vasseur '879, 5:48-58.</p> <p>“In accordance with one aspect of the present invention, the head-end node requests the establishment of the primary tunnel (e.g., a TE-Label Switched Path, TE-LSP), along with a request for Fast Reroute protection of one or more network elements (e.g., with zero-bandwidth, 0-BW backup tunnels) at a PLR node. In addition, the head-end node may include a request for backup tunnel path quality notification, such as, e.g., through the use of the novel Feedback sub-object. The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and sends an error message (e.g., an RSVP PathErr) to the head end node indicating the “Fast Rerouting” of the primary tunnel.” Vasseur '879, 5:59-6:6.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, the PLR node may collect metrics/statistics (e.g., packet drops, path cost, jitter, etc.) of the primary and/or backup tunnels. Once the primary tunnel is Fast Rerouted, the PLR node continues to collect metrics of the backup tunnel, and may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. Notably, the PLR node may be configured to send path quality notifications to the head-end node once, continually, periodically, in response to configurable changes in path quality, etc. Also, as in the case where multiple primary tunnels are Fast Rerouted, the path quality notification may include an indication of which Fast Rerouted primary tunnels in particular have been effected by the changed path quality.” Vasseur '879, 6:7-23.</p>

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		<p>“In accordance with yet another aspect of the present invention, upon receiving the error message (PathErr), the head-end node may wait for the path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. Notably, in the event the head-end node does not receive a path quality notification for the backup tunnel (e.g., within a configurable time limit), the head-end node may attempt to reestablish the new primary tunnel. Moreover, where the head-end node has multiple primary tunnels being Fast Rerouted, a configurable subset of the primary tunnels may be reestablished, e.g., to reduce congestion of the backup tunnels, and/or to limit the number of reestablished primary tunnels within a given period of time.” Vasseur '879, 6:24-43.</p> <p>“Advantageously, the novel technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. By providing the head-end node of the primary tunnel with path quality feedback of the backup tunnel, the novel technique avoids reestablishing a potentially large number of tunnels over one or more alternate paths after a failure (and Fast Reroute) if the backup tunnels have acceptable path quality. In particular, the backup tunnels, e.g., 0-BW backup tunnels, may not be congested or subsequently burdened by the Fast Rerouted traffic of the primary tunnel. Also, the failed network element (thus the primary tunnel) may be quickly restored; therefore by not reestablishing the primary tunnel, network jitter, churn, etc., may be avoided. Further, the dynamic nature of the novel technique alleviates the need for cumbersome manual configuration.” Vasseur '879, 6:44-59.</p> <p>“FIG. 3 is schematic block diagram of an exemplary signaling (RSVP) message that may be advantageously used with the present invention.” Vasseur '879, 7:6-8.</p> <p>“FIG. 5 is a schematic block diagram of the computer network in FIG. 1 showing Fast Reroute protection of a primary tunnel using a backup tunnel in accordance with the present invention.” Vasseur '879, 7:12-15.</p>

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		<p>“FIG. 6 is a schematic block diagram of the computer network in FIG. 5 showing an unacceptable backup tunnel path quality and resultant reestablishing of the primary tunnel in accordance with the present invention.” Vasseur '879, 7:16-19.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention.” Vasseur '879, 7:20-24.</p> <p>“FIG. 1 is a schematic block diagram of an exemplary computer network 100 comprising a plurality of nodes A-F, such as routers or other network devices, interconnected as shown. The nodes may be a part of one or more autonomous systems, routing domains, or other networks or subnetworks. For instance, routers A and E may be provider edge (PE) devices of a provider network, (e.g., a service provider network) that are interconnected to one or more customer networks through customer edge (CE) devices (not shown, while the remaining nodes B-D and F may be core provider (P) devices, as will be understood by those skilled in the art. Those skilled in the art will also understand that the nodes A-F may be any nodes within any arrangement of computer networks, and that the view shown herein is merely an example. For example, the nodes may be configured as connections to/from one or more virtual private networks (VPNs), as will be understood by those skilled in the art. These examples are merely representative. Those skilled in the art will understand that any number of routers, nodes, links, etc. may be used in the computer network 100 and connected in a variety of ways, and that the view shown herein is for simplicity.” Vasseur '879, 7:29-49.</p> <p>“Data packets may be exchanged among the computer network 100 using predefined network communication protocols such as the Transmission Control Protocol/Internet Protocol (TCP/IP), User Datagram Protocol (UDP), Asynchronous Transfer Mode (ATM) protocol, Frame Relay protocol, Internet Packet Exchange (IPX) protocol, etc. Routing information may be distributed among the routers of the computer network using predetermined Interior Gateway Protocols (IGPs), such as conventional distance-vector protocols or, illustratively, link-state protocols, through the use of IGP Advertisements.” Vasseur '879, 7:50-60.</p>

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		<p>“FIG. 2 is a schematic block diagram of an exemplary router 200 that may be advantageously used with the present invention, e.g., as an edge router or a core router. The router comprises a plurality of network interfaces 210, a processor 220, and a memory 240 interconnected by a system bus 250. The network interfaces 210 contain the mechanical, electrical and signaling circuitry for communicating data over physical links coupled to the network 100. The network interfaces may be configured to transmit and/or receive data using a variety of different communication protocols, including, inter alia, TCP/IP, UDP, ATM, synchronous optical networks (SONET), wireless protocols, Frame Relay, Ethernet, Fiber Distributed Data Interface (FDDI), etc.” Vasseur '879, 7:61-8:6.</p> <p>“The memory 240 comprises a plurality of storage locations that are addressable by the processor 220 and the network interfaces 210 for storing software programs and data structures associated with the present invention. The processor 220 may comprise necessary elements or logic adapted to execute the software programs and manipulate the data structures. A router operating system 242 (e.g., the Internetworking Operating System, or IOS™, of Cisco Systems, Inc.), portions of which is typically resident in memory 240 and executed by the processor, functionally organizes the router by, inter alia, invoking network operations in support of software processes and/or services executing on the router. These software processes and/or services may comprise routing services 247, Traffic Engineering (TE) services 244, and RSVP services 249. It will be apparent to those skilled in the art that other processor and memory means, including various computer-readable media, may be used to store and execute program instructions pertaining to the inventive technique described herein.” Vasseur '879, 8:7-26.</p> <p>“Routing services 247 contain computer executable instructions executed by processor 220 to perform functions provided by one or more routing protocols, such as IGP (e.g., OSPF and IS-IS), IP, BGP, etc. These functions may be configured to manage a forwarding information database (not shown) containing, e.g., data used to make forwarding decisions. Routing services 247 may also perform functions related to virtual routing protocols, such as maintaining VRF instances (not shown) as will be understood by those skilled in the art.” Vasseur '879, 8:27-36.</p>

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		<p>“RSVP services 249 contain computer executable instructions for implementing RSVP and processing RSVP messages in accordance with the present invention. RSVP is described in RFC 2205, entitled Resource ReSerVation Protocol (RSVP), and in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels, both as incorporated above.” Vasseur '879, 8:37-42.</p> <p>“TE services 244 contain computer executable instructions for operating TE functions in accordance with the present invention. Examples of Traffic Engineering are described in RFC 3209, RFC 3784, and RFC 3630 as incorporated above, and in RFC 3473, entitled, Generalized Multi-Protocol Label Switching (GMPLS) Signaling Resource ReSerVation Protocol-Traffic Engineering (RSVP-TE) Extensions dated January 2003, which is hereby incorporated by reference in its entirety. A TE database (TED, not shown) may be illustratively resident in memory 240 and used to store TE information provided by the routing protocols, such as IGP, BGP, and/or RSVP (with TE extensions, e.g., as described herein), including, inter alia, path quality information as described herein. The TED may be illustratively maintained and managed by TE services 244.” Vasseur '879, 8:43-57.</p> <p>“Changes in the network topology may be communicated among routers 200 using a link-state protocol, such as the conventional OSPF and IS-IS protocols. Suppose, for example, that a communication link fails or a cost value associated with a network node changes. Once the change in the network's state is detected by one of the routers, that router may flood an IGP Advertisement communicating the change to the other routers in the network. In this manner, each of the routers eventually “converges” to an identical view of the network topology.” Vasseur '879, 8:58-67.</p> <p>“In one embodiment, the routers described herein are IP routers that implement Multi-Protocol Label Switching (MPLS) and operate as label switched routers (LSRs). In one simple MPLS scenario, at an ingress to a network, a label is assigned to each incoming packet based on its forwarding equivalence class before forwarding the packet to a next-hop router. At each router, a forwarding selection and a new substitute label are determined by using the label found in the incoming packet as a reference to a label forwarding table that includes this information. At the network egress, a forwarding decision is made based on the incoming label</p>

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		<p>but optionally no label is included when the packet is sent on to the next hop. In some network configurations, one hop prior to the network egress, a penultimate hop popping (PHP) operation may be performed. Particularly, because the hop prior to the network egress (the penultimate hop) is attached to the network egress, the label is no longer needed to assure that the traffic follows a particular path to the network egress. As such, the PHP-enabled device “pops” the labels from the traffic before forwarding the traffic to the network egress, e.g., using conventional or native (IP) routing, thereby alleviating the task of removing the labels at the network egress.” Vasseur '879, 9:1-23.</p> <p>“The paths taken by packets that traverse the network in this manner are referred to as label switched paths (LSPs) or Traffic Engineering (TE)-LSPs. An example TE-LSP is shown as the thick line and arrow (T1) between a head-end node (router A) and a tailend node (router E) in FIG. 1. Establishment of a TE-LSP requires computation of a path, signaling along the path, and modification of forwarding tables along the path. MPLS TE establishes LSPs that have guaranteed bandwidth under certain conditions. Illustratively, the TE-LSPs may be signaled through the use of the RSVP protocol (with Traffic Engineering extensions), and in particular, RSVP TE signaling messages. Notably, when incorporating the use of PCEs (described below), the path computation request (and response) between PCC and PCE can be exchanged in accordance with a protocol specified in Vasseur, et al., Path Computation Element (PCE) Communication Protocol (PCEP)—Version 1—<draft-vasseur-pce-pcep-02.txt>, Internet Draft, September 2005, the contents of which are hereby incorporated by reference in its entirety. It should be understood that the use of RSVP or PCEP serves only as an example, and that other communication protocols may be used in accordance with the present invention.” Vasseur '879, 9:24-45.</p> <p>“In accordance with RSVP, to request a data flow (TE-LSP) between a sender and a receiver, the sender may send an RSVP path request (Path) message downstream to the receiver along a path (e.g., a unicast route) to identify the sender and indicate e.g., bandwidth needed to accommodate the data flow, along with other attributes of the TE-LSP. The Path message may contain various information about the data flow including, e.g., traffic characteristics of the data flow. Also in accordance with the RSVP, a receiver establishes the TE-LSP between the sender and receiver by responding to the sender's Path message with a reservation request</p>

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		<p>(Resv) message. The reservation request message travels upstream hop-by-hop along the flow from the receiver to the sender. The reservation request message contains information that is used by intermediate nodes along the flow to reserve resources for the data flow between the sender and the receiver, to confirm the attributes of the TE-LSP, and provide a TE-LSP label. If an intermediate node in the path between the sender and receiver acquires a Path message or Resv message for a new or established reservation (TE-LSP) and encounters an error (e.g., insufficient resources, failed network element, etc.), the intermediate node generates and forwards a path or reservation error (PathErr or ResvErr, hereinafter Error) message to the sender or receiver, respectively.” Vasseur '879, 9:46-10:2.</p> <p>“FIG. 3 is a schematic block diagram of portions of a signaling message 300 (e.g., RSVP message, such as Path, Resv or Error) that may be advantageously used with the present invention. Message 300 contains, inter alia, a common header 310 and one or more signaling protocol specific objects 320, such as an LSP-ATTRIBUTE object 330. The common header 310 may comprise a source address 312 and destination address 314, denoting the origination and requested termination of the message 300. Protocol specific objects 320 contain objects necessary for each type of message 300 (e.g., Path, Resv, Error, etc.). For instance, a Path message may have a sender template object, Tspec object, Previous-hop object, etc. The LSP-ATTRIBUTE object 330, for instance, may be used to signal attributes and/or information regarding an LSP (tunnel). To communicate this information, LSP-ATTRIBUTE object 330 (as well as specific objects 320) may include various type/length/value (TLV) encoding formats and/or flags, as will be understood by those skilled in the art. An example of an LSP-ATTRIBUTE object is further described in Farrel, et al., Encoding of Attributes for Multiprotocol Label Switching (MPLS) Label Switched Path (LSP) Establishment Using RSVP-TE <draft-ietf-mpls-rsvpte-attributes-05.txt>, Internet Draft, May 2005, which is hereby incorporated by reference as though fully set forth herein. A Resv message, on the other hand, may have specific objects 320 for a label object, session object, filter spec object, etc., in addition to the LSP-ATTRIBUTE object 330. Error messages 300 (e.g., PathErr or ResvErr), may also have specific objects 320, such as for defining the type of error, etc.” Vasseur '879, 10:3-31.</p>

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		<p>“It should be noted that in accordance with RSVP signaling, the state of the TE-LSP is refreshed on a timed interval, e.g., every thirty seconds, in which RSVP Path and Resv messages are exchanged. This timed interval is configurable by a system administrator. Moreover, various methods understood by those skilled in the art may be utilized to protect against route record objects (RROs) contained in signaling messages for a TE-LSP in the event security/privacy is desired. Such RRO filtering prevents a head-end node of the TE-LSP from learning of the nodes along the TE-LSP, i.e., nodes within the provider network.” Vasseur '879, 10:4-42.</p> <p>“Although the illustrative embodiment described herein is directed to MPLS, it should also be noted that the present invention may advantageously apply to Generalized MPLS (GMPLS), which pertains not only to packet and cell-based networks, but also to Time Division Multiplexed (TDM) and optical networks. GMPLS is well known and described in RFC 3945, entitled Generalized Multi-Protocol Label Switching (GMPLS) Architecture, dated October 2004, and RFC 3946, entitled Generalized Multi-Protocol Label Switching (GMPLS) Extensions for Synchronous Optical Network (SONET) and Synchronous Digital Hierarchy (SDH) Control, dated October 2004, the contents of both of which are hereby incorporated by reference in their entirety.” Vasseur '879, 10:43-55.</p> <p>“To obviate delays associated with updating routing tables when attempting to avoid a failed network element (i.e., during convergence), some networks have employed MPLS TE Fast Reroute (FRR). MPLS Fast Reroute is a technique that may be used to quickly divert (“Fast Reroute”) traffic around failed network elements in a TE-LSP. MPLS Fast Reroute is further described, for example, by Fast Reroute Extensions to RSVP-TE for LSP Tunnels, as incorporated by reference above. According to the technique, one or more network elements (e.g. links or nodes) in a network are protected by backup tunnels following an alternate path. If a failure occurs on a protected link or node, TE-LSPs (and consequently the traffic that they carry) are locally diverted onto an appropriate alternate path (e.g., a “backup tunnel”) by the node immediately upstream from the failure. The backup tunnel acts as a Fast Reroute path for the primary TE-LSP and obviates delays associated with other measures, such as tearing down the primary TE-LSP after having gracefully diverted the TE-LSPs affected by the failure, should an alternate path around the failed network element exist. In the event of a</p>

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		<p>failure of a protected element the head-end node of the backup tunnel (or a “point of local repair,” PLR node) may quickly begin diverting traffic over the backup tunnel with minimal disruption to traffic flow. Those skilled in the art will understand that MPLS Fast Reroute is one example of link or node failure protection, and that other known correction mechanisms may be used in accordance with the present invention. As mentioned above, however, the head-end node of the Fast Rerouted primary tunnel may attempt to reestablish the primary tunnel in response to learning of the protected element failure, particularly in the case where the backup tunnel is a zero-bandwidth (0-BW) tunnel. The attempt to reestablish the primary tunnel has conventionally been a systematic response to Fast Rerouting (diverting) of the primary tunnel, regardless of the path quality of the backup tunnel.” Vasseur '879, 10:56-11:23.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, 11:24-39.</p> <p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as RSVP TE signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or TLV encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object 330 of the RSVP messages 300 to convey the path quality notification.” Vasseur '879, 11:40-49.</p>

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		<p>“FIG. 4 is a schematic block diagram illustrating the format of an extension object (TLV) 400, such as a Fast Reroute Feedback object, that may be advantageously used with the present invention. The extension object (or sub-object) 400 is illustratively embodied as a TLV contained in an LSP-ATTRIBUTE object 330 of an RSVP message 300 and is extended to carry backup (and/or primary) tunnel path quality information. To that end, the “Feedback” object 400 is organized to include a Type field 405 containing a predetermined type value signifying the specific content of the object 400. The Length field 410 is a variable length value. The TLV encoded format may also comprise one or more non-ordered sub-TLVs 450 carried within the TLV “payload” (e.g. Value field 415), each having a Type field 455, Length field 460, and Value field 465. The fields of the TLV 400 and sub-TLV(s) 450 are used in a variety of manners, including as described herein, according to the present invention.” Vasseur '879, 11:59-12:8.</p> <p>“In accordance with one aspect of the present invention, the head-end node (e.g., router A) requests the establishment of the primary tunnel (e.g., T1), such as a TE-LSP. Notably, the head-end node may be a head-end node for multiple primary tunnels, as will be understood by those skilled in the art. Along with the primary tunnel establishment, the head-end node may also request Fast Reroute protection of one or more network elements (e.g., all intermediate network elements) at a PLR node (e.g., router B as shown). Note that each intermediate node along the primary tunnel may act as a PLR node, and that router B is shown merely for simplicity. Illustratively, the Fast Reroute protection may be embodied as one or more zero-bandwidth (0-BW) backup tunnels at the PLR node (e.g., BT1). Those skilled in the art will also understand that the PLR node may protect more than one primary tunnel originating at more than one corresponding head-end node (not shown).” Vasseur '879, 12:9-25.</p> <p>“The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. For example, various connectivity verification protocols, such as, e.g., Bidirectional Forwarding Detection (BFD), IGP “Hello” packets, BGP KEEPALIVE messages, etc., may be used to detect a failure of a network element, as will be understood by those skilled in the art. Furthermore, other lower-layer failure detection mechanisms (e.g. optical or SONET/SDH alarms) may be used to detect a network element failure. Once failure is detected, the PLR node diverts the</p>

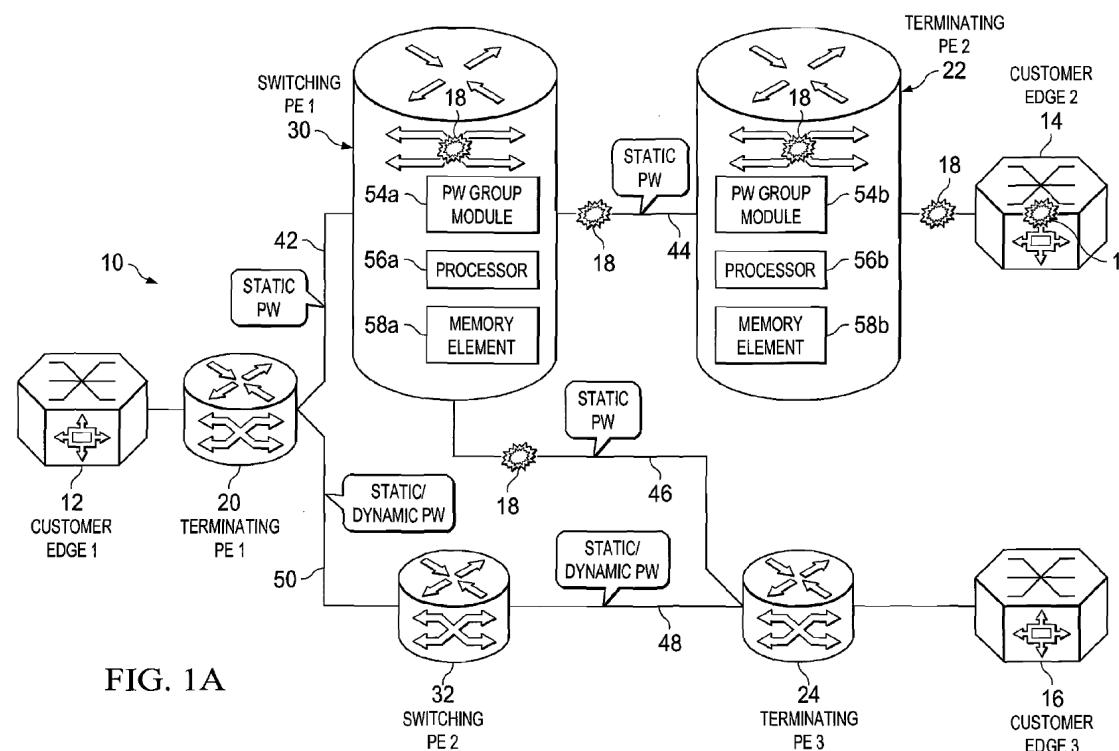
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		<p>traffic onto the backup tunnel, and may send an error message (e.g., an RSVP PathErr 300, such as a conventional “tunnel locally repaired” message) to the head end node indicating the “Fast Rerouting” of the primary tunnel. FIG. 5 is a schematic block diagram of the computer network 100 in FIG. 1 showing Fast Reroute protection of the primary tunnel T1 (e.g., in response to a protected network element, router C, failure, indicated with an overlaid “X”) using a backup tunnel BT1 in accordance with the present invention. Traffic originally received at the PLR node (router B) over the primary tunnel is now diverted over the backup tunnel to a remerge point (router D) of the primary tunnel, as will be understood by those skilled in the art.” Vasseur '879, 12:42-65.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, each PLR node may collect metrics/statistics of the primary and/or backup tunnels. For instance, example metrics may comprise, inter alia, packet drops, path cost, jitter, delay, bandwidth, etc. The PLR node may collect the metrics through traffic monitoring, probes, independent calculations, and/or through cooperation with protected nodes of the primary tunnel (primary nodes) and nodes of the backup tunnel (backup nodes), e.g., transmitting path quality notifications. Once the primary tunnel is Fast Rerouted (i.e., diverted after failure of a protected network element), the PLR node continues to collect metrics of the backup tunnel. (Alternatively, metrics of the backup tunnel may be collected only after Fast Reroute, and not prior to Fast Reroute.)” Vasseur '879, 12:66-13:12.</p> <p>“For example, a path cost increase from the primary tunnel to the backup tunnel may be computed (and transmitted, below) by the PLR node prior to the failure (or during the failure while the primary TE-LSP is diverted onto the backup tunnel) using its own routing tables. The path cost increase may be calculated as a difference between the entire length (head-end node to tail-end node) of the primary and backup tunnels, or just the difference between the protected segment of the primary tunnel and the backup tunnel (PLR node to remerge point). Also, a jitter increase of the primary and backup tunnels, which may be generally described as a difference between inter-arrival of packets, may be monitored using various known techniques, such as, e.g., sending probe packets (probes) from the PLR node to the remerge point. For instance, probes may determine that packets arrive at the remerge point along the primary tunnel from the PLR node consistently, e.g., every 10 milliseconds (ms) (e.g., an</p>

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		<p>average value). After Fast Reroute, however, probes may determine that packets do not arrive at the remerge point along the backup tunnel from the PLR node consistently, e.g., one may arrive in 10 ms, another in 100 ms, another in 50 ms, etc. The non-constant rate of received packets (jitter) may be undesirable, e.g., in particular for voice over IP (VoIP) traffic, as will be understood by those skilled in the art.” Vasseur '879, 13:13-36.</p> <p>“As a further example, packet dropping may be monitored for the primary and backup tunnels prior to and after Fast Reroute (respectively). For instance, based on the tunnel label of the dropped packet, primary nodes and/or backup nodes may be able to distinguish which tunnel corresponds to the dropped packets. Each of the primary and/or backup nodes collect packet drop data, and periodically inform the PLR node of the number of dropped packets (e.g., through a corresponding Feedback object 400). In the case of a backup node, the PLR node receiving the notification may interpret the association of the backup tunnel label and the primary tunnel label to reference an appropriate primary tunnel. Those skilled in the art will understand that the above path quality metrics are merely examples, and that any other metrics/statistics useful for determining path quality of the backup tunnel may be used in accordance with the present invention (e.g., delay, bandwidth, etc.). Further, the path quality information may be measured and compared in a variety of manners, such as, e.g., as a difference between primary and backup tunnels before and after Fast Reroute, or simply the difference between the backup tunnel before and after Fast Reroute, etc.” Vasseur '879, 13:37-58.</p> <p>“Also after the primary tunnel is Fast Rerouted, the PLR node may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. For instance, the novel Feedback object 400 may include one or more sub-TLVs 450 corresponding to metrics/statistics, as described above. Notably, the path quality information pertaining to a particular metric/statistic may be transmitted as total values for interpretation by the head-end node (e.g., to determine the difference), or as PLR-node-computed differences (e.g., between the primary and backup tunnels, or before and after Fast Reroute). For instance, if the delay of the primary tunnel (along the protected segment) prior to Fast Reroute were 2 ms, and after Fast Reroute the delay of the backup tunnel were 5 ms, the notification may be configured to</p>

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		<p>include both values 2 ms and 5 ms, or instead simply the difference, i.e., an increase of 3 ms.” Vasseur '879, 13:59-14:8.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message 300 (PathErr), the head-end node may wait for at least one path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. Also, any number of metrics may be used in the determination, e.g., as configured by a system administrator. For example, using the metrics described above, a head-end node may be configured to reestablish the primary tunnel in response to i) a certain number of packet drops, ii) a percent increase in packet drops, iii) a number of packet drops and a percent increase in path cost, iv) a percent increase in path cost and a percent increase in jitter, etc. Those skilled in the art will understand that these are merely examples of possible path quality comparisons and reestablishment determinations, and that any comparisons to any metrics at any configurable changes may be used in accordance with the present invention.” Vasseur '879, 14:41-59.</p> <p>“If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. FIG. 6 is a schematic block diagram of the computer network 100 in FIG. 5 showing an unacceptable backup tunnel path quality (dotted line and arrow) and resultant reestablishment of the primary tunnel in accordance with the present invention. Those skilled in the art will understand that the reestablished primary tunnel may traverse one or more primary nodes (not shown), and that it may be computed specifically to avoid the failed network element and any network elements of the unacceptable backup tunnel. Those skilled in the art will also understand that the attempt to reestablish the primary tunnel may not be able to find an acceptable path, in which case the head-end node may continue to use the unacceptable backup tunnel or other unacceptable rerouted path.” Vasseur '879, 14:60-15:9.</p>

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		<p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention. The procedure 700 starts at step 705, and continues to step 710, where a head-end node (e.g., router A) establishes a primary tunnel (e.g., T1) to a destination tail-end node (e.g., router E). During or after establishment, the head-end node requests protection of the primary tunnel at step 715, and in response, PLR nodes along the primary tunnel (e.g., router B) create backup tunnels (e.g., BT1) to protect the primary tunnel in step 720. (Those skilled in the art will understand that backup tunnels around the protected network elements may already exist at the PLR node, and that “creating backup tunnels” in step 720 implies an association with pre-existing backup tunnels.) As mentioned above, these backup tunnels may illustratively be embodied as 0-BW backup tunnels. In accordance with the present invention, the head-end node may additionally request backup tunnel path quality notification from the PLR nodes in step 725, such as, e.g., through the use of empty corresponding Feedback objects in RSVP (Path) messages 300, as described above.” Vasseur '879, 15:37-58.</p> <p>“The procedure 700 continues to FIG. 7B (step “A”), where in step 750 the PLR node detecting the failure diverts (“Fast Reroutes”) the primary tunnel traffic to the backup tunnel and sends an error message (e.g., an RSVP (Error) message 300) to the head end node, e.g., a “tunnel locally repaired” message. The detecting PLR node continues to collect path quality information for the backup tunnel in step 755 and at step 760 determines whether to send the path quality notification to the head-end node. For example, as mentioned above, the PLR node may be configured to continually send notifications, or periodically, or in response to a configurable change in path quality, etc. Also as mentioned above, the PLR node may be configured to send either the actual path quality information or the change (difference) in path quality. (As further mentioned above, metrics of the backup tunnel may alternatively be collected only after Fast Reroute, and not prior to Fast Reroute.).” Vasseur '879, 16:4-20.</p> <p>“If the PLR node decides to send the notification in step 760, then the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification in step 765, e.g., based on one or more configurable thresholds, percentages, etc., as described above. If the backup tunnel is currently maintaining an acceptable quality for the</p>

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		<p>traffic flow in step 770, the head-end node may continue to utilize the backup tunnel, and the PLR node continues to collect path quality information in step 755 to detect any change in quality. Otherwise, if the backup tunnel quality is not acceptable in step 770, the head-end node may attempt to reestablish the primary tunnel in step 775. Notably, as mentioned above, if the PLR node has not sent any notification (step 760) within a configurable period of time in step 780, e.g., due to a backup tunnel failure, over-congestion, etc., then the head-end node may also attempt to reestablish the primary tunnel in step 775 accordingly. Moreover, as described above, in the event more than one primary tunnel is Fast Rerouted for the head-end node, various techniques to reestablish one or more of the primary tunnels may be used (e.g., as many tunnels as necessary, a configurable subset of tunnels, all tunnels, the congested tunnels, etc.). The procedure 700 ends in step 785.” Vasseur '879, 16:21-43.</p> <p>“While there has been shown and described an illustrative embodiment that dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network, it is to be understood that various other adaptations and modifications may be made within the spirit and scope of the present invention. For example, the invention has been shown and described herein using “Fast Reroute,” e.g., MPLS TE Fast Reroute (FRR). However, the invention in its broader sense is not so limited, and may, in fact, be used with other network element protection and failure correction mechanisms as will be understood by those skilled in the art. Moreover, while the above description describes performing the technique at the head-end node and PLR node, the invention may also be advantageously used with PCEs. In addition, while one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 16:63-17:13.</p> <p>Rustogi discloses:</p> <p>“An example method includes identifying a fault condition in a network, and evaluating pseudowires affected by the fault condition in order to make a determination as to whether an aggregate failure occurred in the network for a group of pseudowires. The method also includes communicating a group message indicating that the group of pseudowires is associated with the fault condition. The group message includes a group identification (ID),</p>

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		<p>which identifies the group of pseudowires, and the group message includes a pseudowire group label identifying an in-band aggregate channel. More specifically, the pseudowire group label can be applicable to static pseudowires. In more detailed embodiments, the group ID identifies the group of pseudowires that are associated with an attachment circuit, a label switched path, or a port. Internal mappings can be maintained such that a plurality of pseudowires is mapped to individual interfaces of network elements in the network.” Rustogi, Abstract.</p>  <p>FIG. 1A</p> <p>Rustogi, FIG. 1A.</p>

No.	'821 Patent Claim 17	The Reference
		<pre> graph TD START([START]) --> 100[100 ~ A GIVEN NETWORK ELEMENT CAN IDENTIFY A FAULT CONDITION IT RECEIVES] 100 --> 102[102 ~ THE NETWORK ELEMENT EVALUATES PSEUDOWIRES IN ORDER TO DETERMINE WHETHER A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED] 102 --> 104[104 ~ IF ONLY A FEW PSEUDOWIRES ARE AFFECTED BY THE FAULT CONDITION, THE GROUPING PROTOCOL MAY OPTIONALY NOT BE USED, WHERE A MORE ROUTINE MESSAGING PROTOCOL COULD BE EMPLOYED] 104 --> 106[106 ~ IF A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED, THE GROUPING PROTOCOL IS EMPLOYED TO MINIMIZE THE MESSAGES THAT ARE SENT, RECEIVED, AND PROCESSED IN THE NETWORK] 106 --> 108[108 ~ IN THE CASE OF AN AGGREGATE FAILURE, AN AGGREGATE CHANNEL CAN BE USED TO OFFER APPROPRIATE GROUP MESSAGING. THE INDIVIDUAL MESSAGES THAT CONVEY GROUP IDENTIFICATIONS (IDS) CAN QUICKLY SIGNIFY THE FAULT CONDITION TO NETWORK PEERS] 108 --> END([END]) </pre> <p>FIG. 1B</p> <p>Rustogi, FIG. 1B.</p>

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		<p>FIG. 2</p> <p>Rustogi, FIG. 2.</p>

No.	'821 Patent Claim 17	The Reference
		<p>FIG. 3</p> <p>Rustogi, FIG. 3.</p> <p>Detailed description of FIG. 3: The diagram shows a network topology with three PE nodes (PE1, PE2, PE3) and three corresponding terminating nodes (TERMINATING PE1, TERMINATING PE2, TERMINATING PE3). PE1 is connected to SW1 (labeled 68) and TERMINATING PE1 (labeled 62). PE2 is connected to SW1 and SW2 (labeled 70). PE3 is connected to SW2 and TERMINATING PE3 (labeled 66). SW1 has five outgoing connections labeled GROUP IDENTIFICATION A, B, C, D, E. SW2 has four outgoing connections labeled GROUP IDENTIFICATION C, D, E, F. The terminating nodes have interfaces labeled A, B, C, D. Callouts indicate: GROUP ID A → C, D; GROUP ID B → E; GROUP IDENTIFICATION C; PSEUDOWIRE GROUP LABEL C → 3; PSEUDOWIRE GROUP LABEL A → 2; PSEUDOWIRE GROUP LABEL B → 2; PSEUDOWIRE GROUP LABEL E → 4; PSEUDOWIRE GROUP LABEL D → 4; PSEUDOWIRE GROUP LABEL F → 4.</p>

No.	'821 Patent Claim 17	The Reference
		<p>The diagram illustrates a network topology involving three PE (Provider Edge) nodes and two switching nodes:</p> <ul style="list-style-type: none"> TERMINATING PE 1 (represented by circle 62) has interfaces A and B. SWITCHING PE 1 (represented by circle 68) has interfaces E and F. It receives a PSEUDOWIRE GROUP LABEL F from PE1 and sends a PSEUDOWIRE GROUP LABEL E to PE2. SWITCHING PE 2 (represented by circle 70) has interface G. TERMINATING PE 2 (represented by circle 64) has interface C. An annotation indicates INTERFACE C FAILS. TERMINATING PE 3 (represented by circle 66) has interface D. <p>Annotations include:</p> <ul style="list-style-type: none"> GROUP ID E → F: Between PE1 and PE2. GROUP IDENTIFICATION E: Associated with interface E. GROUP IDENTIFICATION F: Associated with interface F. GROUP IDENTIFICATION G: Associated with interface G. GROUP IDENTIFICATION H: Associated with interface B. GROUP IDENTIFICATION I: Associated with interface C. GROUP IDENTIFICATION J: Associated with interface D. INTERFACE A: On PE1. INTERFACE B: On PE1. INTERFACE C: On PE2, marked with a crossed-out symbol. INTERFACE C FAILS: Annotation for interface C. INTERFACE D: On PE3. INTERFACE: A general label for the connection between PE1 and PE2. PSEUDOWIRE GROUP LABEL F: Label sent from PE1 to PE2. PSEUDOWIRE GROUP LABEL E: Label sent from PE2 to PE1. <p style="text-align: center;">FIG. 4</p> <p style="text-align: right;">76</p>

Rustogi, FIG. 4.

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		<p>FIG. 5</p> <p>80</p> <p>Rustogi, FIG. 5.</p> <p>Detailed description of FIG. 5: The diagram shows a network topology with three PE nodes (PE1, PE2, PE3) connected via switching nodes (68, 70) and terminating nodes (62, 64). - PE1 is connected to 68 and 70 via interfaces A and B respectively. - PE2 is connected to 68 and 70 via interfaces C and D respectively. - PE3 is connected to 70 via interface D. - 68 connects to PE1 via interface A and to PE2 via interface C. - 70 connects to PE2 via interface D and to PE3 via interface D. - Labels indicate various pseudowire group labels (e.g., G, H, I, J, L, M) and group identifications (e.g., GROUP ID I → G, GROUP ID J → H, etc.) associated with the connections between nodes.</p>

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		<p style="text-align: center;">74</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>AC PORT</th><th>LSP</th><th>PW GROUP</th><th>PW GROUP LABEL IDENTIFICATION</th></tr> </thead> <tbody> <tr> <td>INTERFACE A</td><td>LSP 1</td><td>A</td><td>A</td></tr> <tr> <td>INTERFACE B</td><td>LSP 1</td><td>B</td><td>B</td></tr> <tr> <td>REMOTE INTERFACE C ON TERMINATING PE 2</td><td>LSP 2</td><td>F</td><td>F</td></tr> <tr> <td>REMOTE INTERFACE D ON TERMINATING PE 3</td><td>LSP 2</td><td>G, H</td><td>G, H</td></tr> </tbody> </table> <p style="text-align: center;">FIG. 6</p> <p>Rustogi, FIG. 6.</p> <p>“The field of communications has become increasingly important in today's society. In particular, the ability to quickly and to effectively provision connections presents a significant challenge to component manufacturers, system designers, and network operators. Multiprotocol Label Switching (MPLS) is a mechanism in telecommunications networks that carries data from one network node to the next. Layer 2 services (such as Frame Relay, Asynchronous Transfer Mode, and Ethernet) can be emulated over an MPLS backbone by encapsulating the Layer 2 Protocol Data Units (PDUs) and transmitting them over pseudowires. Protocols exist for establishing and maintaining the pseudowires. Certain issues have arisen in pseudowire scenarios, where faults are detected in the network.” Rustogi, ¶ [0002].</p> <p>“FIG. 1A is a simplified block diagram of a communication system for providing pseudowire group labels in a network environment in accordance with one embodiment of the present disclosure.” Rustogi, ¶ [0004].</p>	AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION	INTERFACE A	LSP 1	A	A	INTERFACE B	LSP 1	B	B	REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F	REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H
AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION																			
INTERFACE A	LSP 1	A	A																			
INTERFACE B	LSP 1	B	B																			
REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F																			
REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H																			

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		<p>“FIG. 1B is a simplified flowchart depicting one possible, generic operational flow associated with the communication system.” Rustogi, ¶ [0005].</p> <p>“FIG. 2 is a simplified block diagram of an example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0006].</p> <p>“FIG. 3 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0007].</p> <p>“FIG. 4 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0008].</p> <p>“FIG. 5 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0009].</p> <p>“FIG. 6 is a simplified table of an example set of pseudowire group provisioning parameters in accordance with one embodiment.” Rustogi, ¶ [0010].</p> <p>“FIG. 1A is a simplified block diagram of a communication system 10 for providing pseudowire group labels in accordance with one example of the present disclosure. FIG. 1A includes a customer edge 1 (CE1) 12, a CE2 14, and a CE3 16, where a number of faults 18 are shown as propagating in the network. Typically, when an error or a failure occurs in the network (e.g., an interface failure, a pulled cable, a switch failure, hardware/software failures generally, etc.), messages are sent to various network devices in order to inform them of these fault conditions. Faults 18 of FIG. 1A are indicative of such messages, where the underlying fault condition (being signaled by the messages) can occur virtually anywhere in a network (e.g., in a customer edge, in provider equipment, etc.). FIG. 1A also includes a terminating provider equipment 1 (TPE1) 20, a TPE2 22, a TPE3 24, a switching provider edge 1 (SPE1) 30, and a SPE2 32. In one particular example implementation, the TPEs and SPEs of FIG. 1A are switches that are configured to exchange data in a network environment.” Rustogi, ¶ [0012].</p>

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		<p>“SPE1 30 may include a pseudowire (PW) group module 54 a, a processor 56 a, and a memory element 58 a. In a similar fashion, TPE2 22 may include a pseudowire group module 54 b, a processor 56 b, and a memory element 58 b. FIG. 1A also includes a number of static pseudowires 42, 44, and 46. A set of static/dynamic pseudowires 48, 50 is also provided. Note that the group labeling protocol discussed herein can be executed between individual SPEs, TPEs, or between any combinations of these elements.” Rustogi, ¶ [0013].</p> <p>“In one particular arrangement, communication system 10 is provided in conjunction with a Layer-2 virtual private networks (L2VPN)/operation, administration, and maintenance (OAM) L2VPN/OAM framework. The OAM framework is intended to provide OAM layering across L2VPN services, pseudowires, and packet switched network (PSN) tunnels. Communication system 10 may include any suitable networking protocol or arrangement that provides a communicative platform for communication system 10. Thus, communication system 10 may include a configuration capable of transmission control protocol/internet protocol (TCP/IP) communications for the transmission and/or reception of packets in a network. Communication system 10 may also operate in conjunction with a user datagram protocol/IP (UDP/IP) or any other suitable protocol where appropriate and based on particular needs.” Rustogi, ¶ [0014].</p> <p>“Failure detection and failure notification for static pseudowires is inadequate, where sluggish signaling can result in poor scalability for failover performance. Typically, static pseudowires are manually configured at respective endpoints, where control channels are absent for providing group level signaling messages. Aggregate channels are significant tools for providing suitable scalability in the network, but no such aggregate channel exists for static pseudowires. For dynamic pseudowires, such an aggregate channel may be present in the form of a label distribution protocol (LDP) directed session. However, no such protocol exists for static pseudowire configurations such that an in-band aggregate channel would be available for static pseudowires.” Rustogi, ¶ [0016].</p>

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		<p>“Communication system 10 can address the aforementioned issues (and others) by offering a pseudowire group label that can represent an aggregate channel for groups of static pseudowires. The aggregate channel of communication system 10 can allow for improved scalability of failover performance. In accordance with one potential configuration of communication system 10, a pseudowire group label is representative of a group of static pseudowires transported over a label switched path (LSP). The pseudowire group label can identify the aggregate channel, which captures the hierarchy relevant to OAM mechanisms. Additionally, the groups represented by the group identification (ID) can be mutually exclusive, where a pseudowire is part of only one group. In other embodiments, a pseudowire can be part of multiple groups, or be configured in any other suitable manner based on particular network arrangements.” Rustogi, ¶ [0017].</p> <p>“During operations, and with brief reference to FIG. 1B, a given network element can identify a fault condition it receives (at step 100) and, subsequently, evaluate pseudowires in order to determine whether a sufficient number of pseudowires have been affected. This is reflected by step 102. If only a few pseudowires are affected by the fault condition, the grouping protocol outlined herein may have only nominal value, where there could be an option to simply communicate the fault condition in a more routine manner, as outlined in step 104. However, if a sufficient number of pseudowires have been affected, the grouping protocol outlined herein can be employed to minimize the messages that are sent, received, and processed in the network. This is reflected as step 106. Note that the determination (as to whether a sufficient number of pseudowires have been impacted by the fault condition) can involve accessing internal tables such that a quick mapping can occur to determine if an aggregate failure has occurred. As used herein, the term ‘aggregate failure’ simply connotes that a sufficient number of pseudowires have experienced the fault condition such that an aggregate channel can be employed to offer appropriate group messaging. For the aggregate failure condition, the individual messages that convey Group identifications (IDs) can quickly signify the fault condition to network peers, as shown in step 108.” Rustogi, ¶ [0018].</p> <p>“In specific regards to OAM mechanisms, OAM messages typically result from common failures in the network. These fault conditions can be aggregated such that they are signaled as a single message, which could represent a group of failed pseudowires (as opposed to</p>

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		<p>sending individual messages for each failed pseudowire). Hence, a single message could be sent to represent all the relevant OAM messages propagating in communication system 10. The group label that propagates in communication system 10 provides an architecture with a significant level of aggregation for failed pseudowires (i.e., pseudowires being affected by a given fault condition), particularly for OAM messaging. Moreover, the in-band aggregate channel of communication system 10 is based (at least in part) on the evolving trends of OAM mechanisms, which are required to be fast, responsive, and capable of being implemented in hardware or software. Additionally, in-band OAM protocols are a better measure of the path availability.” Rustogi, ¶ [0020].</p> <p>“In operation of one example implementation, a group label can represent the tuple <attachment circuit (AC) port level grouping, LSP>. This could signify that all pseudowires on an AC port (sought for aggregation) traverse a given LSP. Multiple pseudowire groups can exist within an LSP. Similarly, pseudowires on the same AC port (that traverse a different LSP) can use a different pseudowire group label. Alternatively, an administrator may seek to employ a one-to-one mapping between an LSP and a group label. If that were the case, then only one pseudowire group would exist within an LSP. In scenarios where there is no LSP label in the packet (e.g., due to penultimate hop popping), the pseudowire group label can provide the hierarchy that is appropriate.” Rustogi, ¶ [0021].</p> <p>“In one particular example, the group level pseudowire OAM message can be sent with the following label stack: Explicit/Implicit LSP Label+pseudowire group Label+GAL+ACH+pseudowire OAM with grouping TLV (where GAL=Generic Associated Channel Label, ACH=Associated Channel Header, TLV=Type-Length-Value). If there are multiple LSPs, then one group label can be provisioned for each LSP (for each pseudowire group), where per group messages can be sent on each LSP. The group label does not necessarily have a one-to-one mapping to the grouping of pseudowires implied by the Group ID in the grouping TLV. Note also that the group-based aggregate channel is applicable to static pseudowires, as well as for dynamic pseudowires in certain applications.” Rustogi, ¶ [0022].</p>

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		<p>“As discussed herein, the aggregate channel of communication system 10 can be configured in various ways. For example, and with regards to a first option, a separate label may simply be used to identify a pseudowire group within an LSP. The association of an OAM message and a pseudowire group is straightforward. There could potentially be multiple pseudowire group labels per LSP. As a second option, one group label can be used to identify a common pseudowire group channel on the LSP. In this implementation, one pseudowire group label is provided per LSP. The OAM message association to a pseudowire group is not as simple as the first option. As a third option, one pseudowire is simply designated to convey grouping information (e.g., without using a group label). In this case, there is no need for a pseudowire group label. Again, the OAM message association to a pseudowire group is not as simple as the first option.” Rustogi, ¶ [0023].</p> <p>“Any combination of formatting (for the Group ID and the pseudowire group label) can be used in the group message to be communicated in the network. In one example, only one of these elements is communicated when an aggregate fault condition is detected, or these elements can be combined into a single unique identifier. In the most generic example, a group message would at least include the Group ID (identifying the pseudowires affected by the fault) and a pseudowire group label (identifying an aggregate channel for communicating the group message). In this generic sense, a pipe (the Group ID) within a pipe (the pseudowire group label) is being identified, where the group message is identifying both elements during an aggregate fault condition. Operational details of communication system 10 are described below with reference to FIGS. 2-6. Note that before turning to additional example flows and example embodiments of the present disclosure, a brief overview of the infrastructure of communication system 10 is provided.” Rustogi, ¶ [0024].</p> <p>“CE1 12, CE2 14, and CE3 16 represent devices, infrastructure, equipment, clients, or customers seeking to initiate a data session in communication system 10. These elements may comprise a digital subscriber line access multiplexer (DSLAM), a router, a personal computer, a server, a switch, and/or other devices associated with data propagation. Further, these elements may sit behind, or in front of, one or more of these identified devices. The term ‘CE’ may be inclusive of the devices identified above (e.g., a DSLAM, a switch, etc.), as well as devices used to initiate a communication, such as a console, a proprietary endpoint, a</p>

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		<p>telephone, a cellular telephone, a bridge, a computer, a personal digital assistant (PDA), a laptop or an electronic notebook, or any other device, component, element, or object capable of initiating voice, audio, media, or data exchanges within communication system 10. The customer element may also include any device that seeks to initiate a communication on behalf of another entity or element, such as a program, a database, or any other component, device, element, or object capable of initiating a voice, a video, text, or a data exchange within communication system 10. Data, as used herein in this document, refers to any type of video, numeric, voice, media, or script data, or any type of source or object code, or any other suitable information in any appropriate format that may be communicated from one point to another.” Rustogi, ¶ [0025].</p> <p>“SPE1 30, SPE2 32, TPE1 20, TPE2 22, and TPE3 24 are network elements that facilitate communications in two directions in a network environment. In one particular example, each of these network elements is a switch configured to exchange data over static and/or dynamic pseudowire links. Further, the traffic exchanged between these components may be directed over an MPLS transport in certain embodiments. As used herein in this Specification, the term ‘network element’ is meant to encompass switches, routers, bridges, gateways, servers, processors, loadbalancers, firewalls, or any other suitable device, component, element, or object operable to exchange or process information in a network environment. Moreover, these network elements may include any suitable hardware, software, components, modules, interfaces, or objects that facilitate the operations thereof. This may be inclusive of appropriate algorithms and communication protocols that allow for the effective exchange of data or information. Along similar design alternatives, any of the internal modules and components of these network elements may be combined in various possible configurations.” Rustogi, ¶ [0029].</p> <p>“Turning to FIG. 2, FIG. 2 is a simplified block diagram of an example system 60 for providing an example use case using per-label switched path (LSP) pseudowire group labels. FIG. 2 includes a TPE1 62, a TPE2 64, a TPE3 66, a SPE1 68, and a SPE2 70. Each pseudowire group is identified, where a group identification (ID) for Group A and Group B is depicted at TPE1 62. Similarly, Groups C, D, and E have Group IDs at SPE1 68. TPE2 64 and TPE3 66 can couple to interfaces C and D, respectively.” Rustogi, ¶ [0031].</p>

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		<p>“In this particular example, interfaces A and B have failed. Note that there is a multitude of attachment circuits (e.g., 1000 attachment circuits) that are being transported over these interfaces A and B, where the attachment circuits are being tunneled into a corresponding number of pseudowires. For example, there could be 500 attachment circuits on interface A (implicating 500 pseudowires) and 500 attachment circuits on interface B, where the fault condition for the pseudowires should be signaled. In other flawed systems, an architecture would individually signal this fault condition for each pseudowire (e.g., via signaling between TPE1 62 and SPE1 68). Instead of sending 500 messages, a single message can be sent, where a single label (and Group ID) can be used to identify the pseudowires. In this case, the Group ID A is used to signal the fault condition for 300 pseudowires and for 200 pseudowires (i.e., the top two links connecting TPE1 62 and SPE1 68) using a single message (that includes Group Label A and Group ID A). Thus, the status for Group A is quickly communicated to SPE1 68. Similarly, Group ID B can be used to signal the status of the other 500 pseudowires to appropriately convey the status for Group B. More specifically, the message can include Group Label A and Group ID B. Note that all 1000 pseudowires have effectively been accounted for using these Group IDs A and B.” Rustogi, ¶ [0032].</p> <p>“FIG. 3 is a simplified block diagram of an example system 72 for providing another use case for pseudowire group labels. Note that the grouping mechanism outlined herein is not limited to pseudowires that propagate over LSPs. Certain pseudowires can propagate over an LSP and represent one group, where two ports can be provisioned for two different groups (e.g., Group A and Group B). Hence, FIG. 3 is depicting a use case using pseudowire group labels for <port, LSP>mapping. In a general sense, such a configuration is showing how pseudowire mechanics can be used to offer different group signaling, which may be based on various possible implementations. Thus, there is a group level construct corresponding to the group labels that are created such that any OAM protocol can send the appropriate aggregate messages. In this particular example, the signaling for Group ID A, B, C, and D is similar to that of FIG. 2; however, the grouping mechanism has simply changed.” Rustogi, ¶ [0035].</p>

No.	'821 Patent Claim 17	The Reference
		<p>“FIG. 4 is a simplified block diagram of an example system 76 for providing another use case for pseudowire group labels. In this particular example, interface C fails (as shown at TPE2 64). Note that the same logical flow occurs in FIG. 4 in terms of the group signaling, as previously discussed. The group labels in two directions do not have to be the same, where the groupings for the messaging are not necessarily symmetrical. In this particular example, TPE2 64 sends a status for Group E with the corresponding group label (i.e., Group ID E for 300 pseudowires), where that message will have a Group Label E and a Group ID E. Hence, this particular signaling is indicative of 300 pseudowires failing in the network. SPE1 68 can send the status for Group F (where the Group ID F is associated with 300 pseudowires) to TPE1 62, where that message includes a Group Label F and a Group ID F.” Rustogi, ¶ [0036].</p> <p>“FIG. 5 is a simplified block diagram of an example system 80 for providing another use case for pseudowire group labels. In this particular example, interface D fails (as shown at TPE3 66), where all 700 pseudowires fail. In one implementation, TPE3 66 does not have a 700 pseudowire Group ID. Instead, the Group IDs can correspond to 200 and 500 pseudowires, when summed together account for the 700 pseudowires. In this particular example, TPE3 66 sends one message for Group I (representing 200 pseudowires) and another message for Group J (representing 500 pseudowires) to SPE1 68. In response, SPE1 68 sends a message for Group G (representing 200 pseudowires) and another message for Group H (representing 500 pseudowires). Again, the signaling being exchanged between these elements is minimal due to the effective grouping of pseudowires. SPE1 68 also sends a single message for Group I (associated with 200 pseudowires) and Group J (associated with 500 pseudowires) to TPE3 66, which is coupled to interface D. Group ID G is associated with 200 pseudowires, whereas Group ID H is representative of 500 pseudowires.” Rustogi, ¶ [0037].</p> <p>“FIG. 6 is a simplified table 74 illustrating an example set of pseudowire group provisioning parameters for TPE1 62, where these particular provisioning parameters could be relevant to the configuration of FIG. 3. At least in one generic sense, FIG. 2 can reflect one approach for mapping a PW group label to a PW Group ID, while FIGS. 3-5 can reflect a second approach for such mappings, where table 74 is associated with that second approach.” Rustogi, ¶ [0038].</p>

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		<p>“In particular, table 74 illustrates the mapping between SPE1 68 and TPE1 62. The first column represents the attachment circuit port (e.g., interface A, interface B, remote interface C on TPE2 64, and remote interface D on TPE3 66). Additionally, table 74 depicts a number of LSPs, a set of pseudowire grouping labels, and a set of pseudowire Group IDs. Note that the Group IDs are provided inside the pseudowire group labels in this example such that these two columns match in table 74. Additionally, note that table 74 is merely representing some of the possible characteristics in a single direction, where different constructs could be used in the reverse direction. Note that the provisioning as discussed herein can significantly reduce messaging such that these presented concepts offer increased scalability. This is due in part to the nominal processing that occurs in the network, in contrast to the processing required to evaluate a prolific amount of signaling messages associated with particular pseudowires. Additionally, the paradigm discussed herein can afford service providers an adequate amount of downtime after a failure has occurred in the network.” Rustogi, ¶ [0039].</p>
17[b]	determining an overall cost for each entity pair of said plurality of entities:	<p>The Reference discloses determining an overall cost for each entity pair of said plurality of entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[b].</i></p>

No.	'821 Patent Claim 17	The Reference
17[c]	selecting an entity pair from said plurality of transport entities based at least in part upon said overall cost; and	<p>The Reference discloses selecting an entity pair from said plurality of transport entities based at least in part upon said overall cost.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[c].</i></p>
17[d]	if an entity pair reselection event occurs, reselecting said entity pair from the group consisting of said entity pair and a replacement entity pair comprising at least one entity distinct from the entities of said entity pair,	<p>The Reference discloses if an entity pair reselection event occurs, reselecting said entity pair from the group consisting of said entity pair and a replacement entity pair comprising at least one entity distinct from the entities of said entity pair.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[d].</i></p>

No.	'821 Patent Claim 17	The Reference
17[e]	<p>wherein said entity pair reselection event is selected from a group consisting of adding an entity to said plurality of transport entities, removing an entity from said plurality of transport entities, an operational status change for one of said plurality of transport entities, and a change in overall cost for one of said plurality of transport entities.</p>	<p>The Reference discloses wherein said entity pair reselection event is selected from a group consisting of adding an entity to said plurality of transport entities, removing an entity from said plurality of transport entities, an operational status change for one of said plurality of transport entities, and a change in overall cost for one of said plurality of transport entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 1[e].</i></p>

No.	'821 Patent Claim 18	The Reference
18[preamble]	<p>The non-transitory computer readable media of claim 17, wherein said step of selecting an entity pair further comprises:</p>	<p>The Reference discloses the non-transitory computer readable media of claim 17, wherein said step of selecting an entity pair further comprises.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 18	The Reference
18[a]	selecting a working entity from said plurality of transport entities;	<p>The Reference discloses selecting a working entity from said plurality of transport entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 2[a].</i></p>
18[b]	selecting a protection entity from said plurality of transport entities; and	<p>The Reference discloses selecting a protection entity from said plurality of transport entities.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 2[b].</i></p>
18[c]	selecting an active entity from the set consisting of said working entity and said protection entity.	<p>The Reference discloses selecting an active entity from the set consisting of said working entity and said protection entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 3.</i></p>

No.	'821 Patent Claim 19	The Reference
19	The non-transitory readable media of claim 18, wherein said step of selecting an entity pair further comprises minimizing an overall cost function.	<p>The Reference discloses the non-transitory readable media of claim 18, wherein said step of selecting an entity pair further comprises minimizing an overall cost function.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 4.</i></p>

No.	'821 Patent Claim 20	The Reference
20[preamble]	The non-transitory readable media of claim 19, wherein said overall cost function comprises:	<p>The Reference discloses the non-transitory readable media of claim 19, wherein said overall cost function comprises.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>
20[a]	minimizing a probability of concurrent failure of said protection entity and said working entity; and	<p>The Reference discloses minimizing a probability of concurrent failure of said protection entity and said working entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p>

No.	'821 Patent Claim 20	The Reference
20[b]	a predefined metric selected from the group consisting of interior gateway protocol (IGP) and traffic engineering (TE).	<p><i>See Claim 5.</i></p> <p>The Reference discloses a predefined metric selected from the group consisting of interior gateway protocol (IGP) and traffic engineering (TE).</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Cisco IOS system, Juniper IOS System, IETF MPLS-TP System, Doshi '239, Sivabalan '928, and Zamfir '948.</p> <p><i>See Claim 7.</i></p> <p>Cisco created and developed the MPLS and MPLS-TE standards and patented technology based on those standards <i>before</i> Orckit utilized such technology. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Filsfils • Taylor • Vasseur '879 • Rustogi <p><u>Filsfils discloses:</u></p> <p>"In one embodiment, forwarding information bases (FIBs) are selectively populated in a packet switch. A packet switching device determines, based on one or more protocol signaling messages, a subset, which is less than all, on which FIBs a lookup operation may be performed for identifying forwarding information for a received particular packet. The packet switching device populates each of these FIBs, but not all of the FIBs of the packet switching device, with forwarding information corresponding to the particular forwarding value. Thus, FIB resources are consumed for only those FIBs which could actually be used, and not all of the FIBs, for forwarding packets in the data plane of the packet switching device, whether these packets are received on a primary or backup path." Filsfils, Abstract.</p>

No.	'821 Patent Claim 20	The Reference
		<p>The diagram illustrates a network path between four apparatuses (101, 102, 103, 104). Apparatus 101 is connected to 102 via a primary path (111) labeled 'LABEL=T1' and a backup path (121) labeled '(USE LABEL T2 FOR SECOND TUNNEL)'. Apparatus 102 is connected to 103 via a path labeled 'LABEL=T1'. Apparatus 103 is connected to 104 via a path labeled '112'. The text below the diagram specifies a particular tunnel from 101 to 104, detailing the primary path (101-111-103-112-104) and the backup path (111 via 121-102-122).</p> <p>FIGURE 1</p> <p>Filsfils, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<p>APPARATUS (E.G., PACKET SWITCHING DEVICE APPLIANCE, CORE ROUTER, LABEL SWITCH ROUTER)</p> <p>ROUTE PROCESSOR CONFIGURED TO CORRELATE PRIMARY AND BACKUP PATHS AND POPULATE FIBS ACCORDINGLY</p> <p>COMMUNICATION MECHANISM(S) (E.G., BUS, SWITCHING FABRIC, MATRIX)</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>LINE CARD CONFIGURED WITH ONE OR MORE FIBS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>PRIMARY AND BACKUP PATHS</p> <p>NETWORK(S)</p> <p>FIGURE 2</p> <p>Filsfils, FIG. 2 (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<pre> graph TD START([START]) --> 402[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL] 402 --> 404[DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)] 404 --> 406[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 406 --> END([END]) </pre> <p>The flowchart illustrates a process for establishing primary and backup paths for a tunnel. It begins with a START node, followed by a step to exchange protocol signaling messages to establish the primary path. This is followed by a decision point to determine if a backup path needs to be created. If so, another set of protocol signaling messages are exchanged to establish the backup path. The process concludes with an END node.</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, INCLUDING RECEIVING A PARTICULAR LABEL FROM A DOWNSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THE DOWNSTREAM LSR OVER THE PARTICULAR TUNNEL</p> <p>DETERMINE TO CREATE A BACKUP PATH FROM THE NODE TO PROTECT A PORTION OF THE PARTICULAR TUNNEL, OR TO PROTECT A LINK OVER WHICH THE PARTICULAR TUNNEL MAY TRAVERSE (E.G., OVER THE PRIMARY OR A BACKUP PATH)</p> <p>EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF A PATH OF THE PARTICULAR TUNNEL, INCLUDING PROVIDING INFORMATION TO THE DOWNSTREAM LSR SO THAT IT CAN CORRELATE PRIMARY AND BACKUP PATH(S) OF THE TUNNEL, SO THAT IT CAN ONLY PROGRAM THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.</p>

FIGURE 4

Filsfils, FIG. 4 (annotated).

No.	'821 Patent Claim 20	The Reference
		<pre> graph TD START([START]) --> 502[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH THE PRIMARY PATH FOR A PARTICULAR TUNNEL, SENDING A PARTICULAR LABEL FROM THE NEIGHBOR UPSTREAM LABEL SWITCH ROUTER (LSR) TO USE WHEN SENDING PACKETS (IN THE DATA PLANE) TO THIS LSR OVER THE PARTICULAR TUNNEL.] 502 --> 504[EXCHANGE PROTOCOL SIGNALING MESSAGES TO ESTABLISH A BACKUP PATH FOR A PORTION OF THE PARTICULAR TUNNEL, INCLUDING RECEIVING INFORMATION THAT IT CAN USE TO CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING THE PRIMARY PATH OF THE PARTICULAR TUNNEL. FOR EXAMPLE, ONE OR MORE OF THE PROTOCOL SIGNALING MESSAGES (E.G. RSVP) INFORMS AN LSR THAT A BACKUP PATH (E.G., A SECOND TUNNEL) IS PROTECTING A LINK OVER WHICH THE PARTICULAR TUNNEL (AND POSSIBLY MANY OTHER TUNNELS) MAY TRAVERSE.] 504 --> 506[CORRELATE PRIMARY AND BACKUP PATH(S) OF THE PARTICULAR TUNNEL, AND ONLY POPULATE THE FORWARDING INFORMATION BASES THAT COULD BE USED IN THE DATA PLANE FOR FORWARDING PACKETS OVER THE TUNNEL. FOR EXAMPLE, THE LSR KNOWS WHAT ON WHAT INTERFACE(S) PACKETS FROM THE BACKUP PATH COULD BE RECEIVED. THIS CORRELATION MAY INCLUDE USING DATA CONCERNING BUNDLED INTERFACES, AND EVEN RECURSIVE CORRELATION OF BACKUP TUNNELS USED TO BACKUP OTHER BACKUP TUNNELS, AS WELL AS LOAD BALANCING AND OTHER TECHNIQUES TO DETERMINE WHERE BACKUP PATH PACKETS COULD BE RECEIVED, AND THE SUBSET OF FORWARDING INFORMATION BASES IN THE DATA PLANE THAT COULD BE USED TO FORWARD PACKETS OVER THE PARTICULAR TUNNEL, WHETHER VIA A PRIMARY OR BACKUP PATH.] 506 --> END([END]) </pre> <p>FIGURE 5</p> <p>Filsfils, FIG. 5.</p>

No.	'821 Patent Claim 20	The Reference
		<p>“The communications industry is rapidly changing to adjust to emerging technologies and ever increasing customer demand. This customer demand for new applications and increased performance of existing applications is driving communications network and system providers to employ networks and systems having greater speed and capacity (e.g., greater bandwidth). In trying to achieve these goals, a common approach taken by many communications providers is to use packet switching technology, including switching packets on labels especially in the core network using Multiprotocol Label Switching (MPLS).” Filsfils, 1:12-22.</p> <p>“Tunnels, such as MPLS-TE (Traffic Engineering) and MPLS-TP (Transport Profile), are paths established through a network in order to transport packets efficiently through a label switched network. Fast Re-Route (FRR) is a technology that allows backup paths to be established in the network, which can be used in case of a problem with a primary path (original primary path or currently used backup path) of the tunnel. RFC 4090, entitled “Fast Reroute Extensions to RSVP-TE for LSP Tunnels,” provides an extension of the protocol signaling to establish backup label switched path (LSP) tunnels for local repair of LSP tunnels.” Filsfils, 1:23-33.</p> <p>“Expressly turning to the figures, FIG. 1 illustrates a network 100 operating according to one embodiment. Shown are four apparatus 101-104 (e.g., packet switching devices such as a label switch router, network device, and/or appliance). For explanation purposes, each of apparatus 101-104 will be referenced as a label switch router (LSR).” Filsfils, 5:41-46.</p> <p>“As shown, a particular tunnel is established, using a signaling protocol and exchanging of protocol signaling messages. Note, LSR 101 may, or may not, be an endpoint of the particular tunnel (e.g., LSR 101 may be an intermediate LSR on the path of the particular tunnel). The primary path of the particular tunnel includes spans from LSR 101 via link 111 to LSR 103 and via link 112 to LSR 104. Note, LSR 104 may be an intermediate LSR on the path of the particular tunnel, or an endpoint of the particular tunnel. Further, for this example embodiment, LSR 103 signals LSR 101 to use label T1 at the top of the label stack in the header of a packet sent to it on the particular tunnel.” Filsfils, 5:47-58.</p> <p>“A second tunnel from LSR 101 via link 121 to LSR 102 and via link 122 to LSR 103 is similarly configured using a signaling protocol and exchanging of protocol signaling</p>

No.	'821 Patent Claim 20	The Reference
		<p>messages. For example purposes, LSR 102 signals LSR 101 to use label (T2) at the top of the label stack in the header of a packet sent to it on the second tunnel. In one embodiment, LSR 101 creates the second tunnel in response to determining, or being instructed to, create a backup path to protect link 111 and/or protect all or certain tunnels traversing link 111.” Filsfils, 5:59-67.</p> <p>“As shown in FIG. 1, link 111 (primary path of the particular tunnel and/or all or certain tunnels traversing link 111) is protected by LSR 101 using the second tunnel (backup path). When sending packets over the particular tunnel over link 111, LSR 101 includes label T1 at the top of the label stack of these packets. If link 111 cannot be used for communicating packets of the particular tunnel, LSR 101 sends packets over the backup path for the particular tunnel by sending packets to LSR 102, with these packets having a label stack including: label T2 followed by label T1. Thus, LSR 102 will receive these packets, pop the top label (T2) from the label stack of each of these packets, identify based on label T2 to send these packets to LSR 103. After popping the top label, the label at the top of the label stack of these packets is T1, which is the same label LSR 103 expects to receive for the particular tunnel. Therefore, these packets received with label T1 at the top of their label stack, are forwarded (after popping label T1 from their label stack) by LSR 103 over the particular tunnel to LSR 104.” Filsfils, 6:6-24.</p> <p>“One embodiment acquires such additional information by extending Resource Reservation Protocol (RSVP) to provide information which allows a packet switch to correlate primary and backup paths. Thus, a packet switch can use this additional information in determining which of its forwarding information bases (FIBs) could possibly be used in forwarding packets (e.g., in the data plane of the packet switch).” Filsfils, 6:51-57.</p> <p>“In providing this additional information to LSR 103, one embodiment communicates an extended RSVP message (including a new or modified RSVP object) or other message to LSR 103 on the second tunnel. This messages designates one or more primary tunnels (e.g., label T1 in our example) and/or a link (e.g., link 111). As LSR 103 knows what interface that it received this message, LSR 103 knows that it must populate forwarding information for these primary tunnels, either specified (e.g., by a label such as T1), or all labels corresponding to</p>

No.	'821 Patent Claim 20	The Reference
		<p>tunnels which could be received over link 111. In one embodiment, the extended RSVP or other message communicated to LSR 103 also includes an identification of the backup tunnel (e.g., T2) over which the RSVP or other message is being received, as the identification the tunnel over which a packet is received is often not communicated in a packet (e.g., in the case of Penultimate Hop Popping).” Filsfils, 7:19-34.</p> <p>“As shown in FIG. 1, one embodiment includes apparatus 103, which populates less than all of its FIBs with forwarding information for a tunnel (although all FIBs may be populated for certain tunnels). One embodiment includes apparatus 101 and/or 102 which communicates, via a signaling protocol (e.g., an extension of RSVP, or using another protocol), information which allows apparatus 103 to determine the relationship between primary and backup paths, such that apparatus 104 can correlate this primary and backup path information (possibly also correlating backup path of backup path information, and/or bundled interfaces and/or bundled links) to identify a minimum subset of the FIBs that could possibly be used in forwarding packets of particular primary paths (e.g., tunnels).” Filsfils, 7:49-62</p> <p>“Turning to FIG. 2, illustrates an apparatus 200 (e.g., packet switching devices such as a label switch router, network device, and/or appliance) operating in one embodiment. As shown, apparatus 200 includes line cards 201, 202 communicatively coupled via communication mechanism(s) 203 (e.g., bus, switching fabric, and/or matrix). Additionally, route processor 204 is configured to correlate primary and backup paths of tunnels, and to populate minimum subsets of FIBs with forwarding information for labels. Again, a minimum subset of FIBs for a particular path or label of the particular path is the set of FIBs that are determined to possibly be used in forwarding packets of a primary path, whether the label is received in a packet over the primary path or over a backup path, and possibly considering backup paths of a backup path and/or the possibly effect of bundled interfaces and/or bundled links.” Filsfils, 7:63-8:11.</p> <p>“As shown in FIG. 2, apparatus 200 is communicatively coupled via primary and backup paths 211, 212 to networks 210 and 213 (which could be the same network). As illustrated, each of line cards 201, 202 includes one or more FIBs. By correlating on which line card(s) 201, 202 and even within line cards 201, 202 that have multiple FIBs, primary and backup path(s) of tunnels, the number of FIB entries populated in apparatus 200 can typically be reduced,</p>

No.	'821 Patent Claim 20	The Reference
		<p>possibly significantly saving memory/storage resources and resources used to populate the FIBs.” Filsfils, 8:12-21.</p> <p>“FIG. 4 illustrates a process performed in one embodiment. Processing begins with process block 400. In process block 402, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes receiving a particular label for a downstream LSR to use when sending packets to the downstream LSR over the particular tunnel.” Filsfils, 8:61-67.</p> <p>“In process block 404, a determination is made to create a backup path from the node (e.g. the node performing these operations). This backup path may be used to protect one or more particular tunnels, and/or may be used to protect a link which is used to carry packet traffic of one or more tunnels.” Filsfils, 9:1-5.</p> <p>“In process block 406, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including providing information to the downstream LSR so that the downstream LSR can correlate primary and backup path(s) of the particular tunnel and substantially only program the FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs an LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:6-23.</p> <p>“FIG. 5 illustrates a process performed in one embodiment. Processing begins with process block 500. In process block 502, protocol signaling messages are exchanged to establish the primary path for a particular tunnel, which typically includes sending a particular label for an upstream LSR to use when sending packets over the particular tunnel to this apparatus (e.g., an LSR performing these operations).” Filsfils, 9:26-32.</p>

No.	'821 Patent Claim 20	The Reference
		<p>“In process block 504, protocol signaling messages are exchanged to establish a backup path for a portion of the particular tunnel, including receiving information that the LSR can use to correlate primary and backup path(s) of the particular tunnel. For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting the primary path of the particular tunnel (and the LSR knows on which interface and/or link this protocol signaling message was received) For example, one or more of the protocol signaling messages (e.g. RSVP) informs the LSR that a backup path (e.g., a second tunnel) is protecting a link over which the particular tunnel (and possibly many other tunnels) may traverse (and the LSR knows on which interface and/or link this protocol signaling message was received).” Filsfils, 9:33-47.</p> <p>“In process block 506, the primary and backup path(s) of the particular tunnel are correlated to identify the set of FIBs that could possibly be used in forwarding packets of the particular tunnel. Substantially only those FIBs that could potentially be used in the data plane for forwarding packets over the particular tunnel (either through a primary or backup path) are populated with the forwarding information (e.g., an entry corresponding to the label it advertised to use for the particular tunnel) for the particular tunnel. For example, the LSR knows what on what interface(s) packets from the backup path could be received. This correlation may include using data concerning bundled interfaces, and even recursive correlation of backup tunnels used to backup other backup tunnels, as well as load balancing and other techniques to determine where backup path packets could be received, and the subset of forwarding information bases in the data plane that could be used to forward packets over the tunnel, whether via a primary or backup path.” Filsfils, 9:48-65.</p> <p>Taylor discloses:</p> <p>“Grouping pseudowires based on hardware interfaces and configured control paths enables improved pseudowire failover performance. Signaling status changes (e.g., from standby to active status) is facilitated by using group IDs for the pseudowire groups, thereby enabling improved failover performance when there is disruption in the network.” Taylor, Abstract.</p>

No.	'821 Patent Claim 20	The Reference
		<p>The diagram illustrates a network topology with four PE routers (T-PE1, T-PE2, T-PE3, T-PE4) connected via S-PE routers (S-PE1, S-PE2, S-PE3, S-PE4). The connections are labeled with numbers 1, 2, and 101.</p> <ul style="list-style-type: none"> T-PE1: <ul style="list-style-type: none"> Interface e1/0.111: xconnect 10.2.2.2 1 encap mpls; backup peer 10.3.3.3 101 Interface e1/0.222: xconnect 10.3.3.3 2 encap mpls; backup peer 10.2.2.2 102 S-PE2 10.2.2.2: <ul style="list-style-type: none"> 12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls 12 vfi two point-to-point neighbor 10.1.1.1 102 encap mpls T-PE2 10.4.4.4: <ul style="list-style-type: none"> 12 vfi one point-to-point neighbor 10.1.1.1 102 encap mpls 12 vfi two point-to-point neighbor 10.4.4.4 102 encap mpls T-PE3 10.1.1.1: <ul style="list-style-type: none"> e1/0 S-PE3 10.3.3.3: <ul style="list-style-type: none"> 12 vfi one point-to-point neighbor 10.1.1.1 101 encap mpls 12 vfi two point-to-point neighbor 10.1.1.1 2 encap mpls T-PE4 10.4.4.4: <ul style="list-style-type: none"> e4/0 T-PE4: <ul style="list-style-type: none"> interface e4/0.111: xconnect 10.2.2.2 1 encap mpls; backup peer 10.3.3.3 101 interface e4/0.222: xconnect 10.3.3.3 2 encap mpls; backup peer 10.2.2.2 102

FIG. 4

Taylor, FIG. 4 (annotated).

No.	'821 Patent Claim 20	The Reference
		<p>The diagram illustrates a network topology with five routers:</p> <ul style="list-style-type: none"> T-PE1 (11.1.1.1): Located at the top left. It has two e0/0 interfaces (labeled 1 and 2) connected to S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1) respectively. It also has two e1/0 interfaces (labeled 3 and 4) connected to T-PE4 (14.1.1.1) and T-PE5 (15.1.1.1) respectively. S-PE2 (12.1.1.1): Located at the top center. It has four e0/0 interfaces (labeled 1 through 4) connected to T-PE1, T-PE4, and T-PE5. T-PE4 (14.1.1.1): Located at the top right. It has two e0/0 interfaces (labeled 1 and 2) connected to S-PE2 and S-PE3 respectively. It also has two e1/0 interfaces (labeled 3 and 4) connected to T-PE1 and T-PE5 respectively. S-PE3 (13.1.1.1): Located at the bottom center. It has four e0/0 interfaces (labeled 5 through 8) connected to T-PE1, T-PE4, and T-PE5. T-PE5 (15.1.1.1): Located at the bottom right. It has two e0/0 interfaces (labeled 5 and 6) connected to S-PE2 and S-PE3 respectively. It also has two e1/0 interfaces (labeled 7 and 8) connected to T-PE1 and T-PE4 respectively. <p>Configuration Boxes (Annotated Text):</p> <ul style="list-style-type: none"> T-PE1 → S-PE2: Gid=1 for 1,2,3,4 T-PE1 → S-PE3: Gid=2 for 5,6,7,8 (Box 504) interface e0/0.100 xconnect 12.1.1.1 1 encaps mpls backup peer 13.1.1.15 interface e0/0.200 xconnect 12.1.1.1 2 encaps mpls backup peer 13.1.1.15 interface e0/0.300 xconnect 12.1.1.1 3 encaps mpls backup peer 13.1.1.17 interface e0/0.400 xconnect 12.1.1.1 4 encaps mpls backup peer 13.1.1.18 (Box 502) 514 <ul style="list-style-type: none"> l2 vfi abc point-to-point neighbor 11.1.1.1 1 encaps mpls neighbor 14.1.1.1 1 encaps mpls l2 vfi def point-to-point neighbor 11.1.1.1 2 encaps mpls neighbor 14.1.1.1 2 encaps mpls l2 vfi ghi point-to-point neighbor 11.1.1.1 3 encaps mpls neighbor 14.1.1.1 3 encaps mpls l2 vfi jkl point-to-point neighbor 11.1.1.1 4 encaps mpls neighbor 14.1.1.1 4 encaps mpls 506 <ul style="list-style-type: none"> interface e0/0.100 xconnect 12.1.1.1 1 encaps mpls interface e0/0.200 xconnect 12.1.1.1 2 encaps mpls interface e1/0.100 xconnect 12.1.1.1 3 encaps mpls interface e1/0.200 xconnect 12.1.1.1 4 encaps mpls 508 <ul style="list-style-type: none"> T-PE4 → S-PE2: Gid=200 for 1,2 T-PE4 → S-PE2: Gid=201 for 3,4 516 <ul style="list-style-type: none"> S-PE2 → T-PE4: Gid=10 for 1,2,3,4 S-PE2 → T-PE1: Gid=20 for 1,2 S-PE2 → T-PE1: Gid=21 for 3,4 520 <ul style="list-style-type: none"> S-PE3 → T-PE5: Gid=50 for 5,6,7,8 S-PE3 → T-PE1: Gid=75 for 5,6 S-PE3 → T-PE1: Gid=76 for 7,8 512 <ul style="list-style-type: none"> T-PE5 → S-PE3: Gid=250 for 5,6 T-PE5 → S-PE3: Gid=251 for 7,8 518 <ul style="list-style-type: none"> interface e0/0.100 xconnect 13.1.1.1 5 encaps mpls interface e0/0.200 xconnect 13.1.1.1 6 encaps mpls interface e1/0.100 xconnect 13.1.1.1 7 encaps mpls interface e1/0.200 xconnect 13.1.1.1 8 encaps mpls 510

FIG. 5

Taylor, FIG. 5 (annotated).

No.	'821 Patent Claim 20	The Reference
		<p>TRAFFIC FLOW IS TOWER ← CORE</p> <p>TOWER T-PE</p> <p>S-PE1</p> <p>CORE T-PE1</p> <p>mLACP</p> <p>RNC/BSC</p> <p>CORE T-PE2</p> <p>ACTIVE PW/ACTIVE AC</p> <p>STANDBY PW/ACTIVE AC</p> <p>STANDBY* PW/STANDBY AC</p> <p>STANDBY PW/STANDBY AC</p> <p>* GOES ACTIVE WHEN mLACP FAILS OVER</p> <p>FIG. 8</p>

Taylor, FIG. 8 (annotated).

No.	'821 Patent Claim 20	The Reference
		<pre> sequenceDiagram participant TOWER as TOWER T-PE participant S1 as S-PE1 participant S2 as S-PE2 participant CORE as CORE T-PE TOWER->>S1: S-PE1 FFD PWs MARKED DOWN S1->>S1: S-PE1 FAILURE S1->>S2: HOT STANDBY → ACTIVE S2->>CORE: HOT STANDBY → ACTIVE CORE->>S2: S-PE1 GROUPS MARKED DOWN </pre> <p><i>Annotations:</i></p> <ul style="list-style-type: none"> S-PE1 FFD PWs MARKED DOWN S-PE1 FAILURE (boxed) HOT STANDBY → ACTIVE (yellow boxes) S-PE1 GROUPS MARKED DOWN

FIG. 9

Taylor, FIG. 9 (annotated).

No.	'821 Patent Claim 20	The Reference
		<p>1002 ↗</p> <pre> graph TD A[1004 SPECIFY PHYSICAL INTERFACE AT FIRST PE] --> B[1006 SPECIFY FIRST CONTROL PATH FROM FIRST PE TO SECOND PE] B --> C[1008 SPECIFY SECOND CONTROL PATH FROM FIRST PE TO THIRD PE] C --> D[1010 SPECIFY REDUNDANT COMBINATIONS OF PWs] D --> E[1012 GROUP PWs INTO A PRIMARY PW GROUP AND A BACKUP PW GROUP] E --> F[1014 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO PRIMARY/BACKUP PW GROUPS] F --> G[1016 DETECT FAILURE ON FIRST CONTROL PATH] G --> H[1018 USE GROUP IDs TO ASSIGN ACTIVE/STANDBY STATUS TO BACKUP/PRIMARY PW GROUPS] </pre> <p>FIG. 10</p> <p>Taylor, FIG. 10 (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<p>“The present disclosure relates generally to communication networks and more particularly to pseudowire configurations in communication networks.” Taylor, 1:8-10.</p> <p>“Virtual Private Network (VPN) services provide secure network connections between different locations. A company, for example, can use a VPN to provide secure connections between geographically dispersed sites that need to access the corporate network so that each customer edge (CE) end point or node can communicate directly and independently with all other CE nodes. Different types of VPNs have been classified by the network layer used to establish the connection between the customer and provider network. For example, Virtual Private LAN Service (VPLS) is an architecture that delivers a multipoint Layer 2 VPN (L2VPN) service that in all respects emulates an Ethernet Local Area Network (LAN) across a wide metropolitan geographic area. All services in a VPLS appear to be on the same LAN, regardless of location. In other words, with VPLS, customers can communicate as if they were connected via a private Ethernet segment, i.e., multipoint Ethernet LAN services.” Taylor, 1:12-28.</p> <p>“In this context, each CE device at a customer site is connected to the service provider network at a provider edge PE) device by an Attachment Circuit (AC) that provides the customer connection to a service provider network, that is, the connection between a CE node and its associated PE node. Within the provider network, each PE device includes a Virtual Switch Instance (VSI) that emulates an Ethernet bridge (i.e., switch) function in terms of Media Access Control (MAC) address learning and forwarding in order to facilitate the provisioning of a multipoint L2VPN. A pseudowire (PW) is a virtual connection between two PE devices that connect two attachment circuits. In the context of the VPLS service, a pseudowire can be thought of as a point-to-point virtual link for each offered service between a pair of VSIs. Therefore, if each VSI can be thought of as a virtual Ethernet switch for a given customer service instance, then each pseudowire can be thought of as a virtual link connecting these virtual switches to each other over a Packet Switched Network (PSN) for that service instance.” Taylor, 1:29-47.</p>

No.	'821 Patent Claim 20	The Reference
		<p>“Since the failure of pseudowires obviously degrades network performance, some effort has been directed towards adding system redundancies including redundant pseudowires. However, the presence of redundant pseudowires alone is insufficient to improve overall failover performance, that is, the ability to switch over automatically to a redundant or backup system. Thus, there is a need for improved methods for managing pseudowires to facilitate pseudowire switching and enable improved failover performance.” Taylor, 1:48-56.</p> <p>“FIG. 4 shows details for PW connectivity in an exemplary network for an example embodiment.” Taylor, 1:66-67.</p> <p>“FIG. 5 shows details for PW grouping for an example embodiment.” Taylor, 2:1-2.</p> <p>“FIG. 8 shows an example network including redundant PW connections for an example embodiment.” Taylor, 2:9-10.</p> <p>“FIG. 9 shows an example sequence diagram for a failure mode related to the embodiment shown in FIG. 8.” Taylor, 2:11-12.</p> <p>“FIG. 10 shows a flowchart that illustrates a method of providing improved PW grouping according to an example embodiment.” Taylor, 2:13-15.</p> <p>“According to one embodiment, a method of providing improved pseudowire performance includes specifying a physical interface at a first PE node in a network, a first control path from the first PE node to a second PE node in the network, and a second control path from the first PE node to a third PE node in the network. With these specifications, the method then includes specifying redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node, and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Then these pseudowires can be grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. Group identifiers for the primary PW group and the backup PW group can then be used to assign an</p>

No.	'821 Patent Claim 20	The Reference
		<p>active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths. The method may then include detecting a failure on the first control path, and in response to the detected failure, using the group identifiers to assign the active status to the backup pseudowires and the standby status to the primary pseudowires.” Taylor, 2:27-52.</p> <p>“Pseudowires are used in pseudowire emulation edge-to-edge to provide a Layer 2 Virtual Private Network (L2VPN) connection. When large numbers (e.g., 4,000-6,000) of pseudowires are aggregated together on a single router, failure performance tends to be linear or O(n) where n is the number of pseudowires. While O(n) performance may be acceptable for small numbers of pseudowires, the effect on network outages can be increasingly undesirable as the number of pseudowires increases.” Taylor, 2:54-62.</p> <p>“For example, a cell-site router will typically start an approximately 2-minute procedure if contact with its controller, which is reached via a pseudowire, is lost for more than some threshold amount (e.g., between approximately 0.75 and 1.75 seconds in some cases). This can be a major impediment to the scalability of pseudowire deployments. These issues have become increasingly relevant as providers of Multiservice Broadband Networks (MBNs) are rapidly replacing or augmenting their traditional Synchronous Optical Networking (SONET) equipment with cheaper Ethernet equipment in the evolution towards a 4G (i.e., 4th generation) network.” Taylor, 2:63-3:7.</p> <p>“One aspect of a solution to the problem of pseudowire failure is the deployment of redundant pseudowires. For example, redundant pseudowires have been used in the context of Multiprotocol Label Switching (MPLS) networks, which use a Label Distribution Protocol (LDP) to manage labels for forwarding traffic between routers. In this context, general requirements for redundancy schemes have been developed so that duplicate pseudowires are available when a given pseudowire fails (e.g., by using active/standby status indicators). In addition, more specific implementations for redundant pseudowires have also been developed.” Taylor, 3:8-18.</p>

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		<p>“FIG. 1 shows a reference network model 102 with applications to example embodiments disclosed herein. The reference network model 102 includes an aggregation network 104 of PE nodes and a distribution network 106 of PE nodes between a radio network controller (RNC) (or base station controller (BSC)) 108 on the core side of the model 102 and a radio tower 111 on the tower side of the model 102. Switching provider edge nodes S-PE1 and S-PE2 connect the two networks 104, 106. On the core side, two core terminating provider edges T-PE1 and T-PE2 connect to the RNC/BSC 108 through attachment circuits 110, 112. On the tower side, one tower terminating provider edge T-PE connects to the radio tower 111 through an attachment circuit 114.” Taylor, 3:19-31.</p> <p>“Additionally as noted in FIG. 1, peer-PE monitoring is carried out within each network 104, 106. That is, there is peer-PE monitoring between provider edges that share a segment, for example, by multi-hop bidirectional forwarding detection (BFD). Alternatively, peer monitoring can be accomplished by other means (e.g., MPLS-TP (Transport Protocol) keep-alives). This peer-PE monitoring is used to provide the mechanism for fast failure detection. Once a failure is detected, the network can react by “rerouting” the failed pseudowires to pre-provisioned backup paths and thus provide a minimal disruption in service to the end-user. This rerouting can be accomplished by LDP signaling between provider edges.” Taylor, 3:32-44.</p> <p>“The reference network model 102 may be considered as part of a larger hub-and-spoke model as shown in FIG. 2. A hub-and-spoke distribution model 202 includes a core network 204, distribution networks 206, and aggregation networks 208. Network elements including distribution nodes, aggregation nodes, and towers are also shown with nominal count values (e.g., 30 distribution nodes between the core network 204 and a distribution network 206). In this model 202, tower T-PEs are the spokes while core-PEs constitute the hub. Dozens to hundreds of tower T-PEs connect to a few S-PEs; these S-PEs are quite similar to ASBRs as they act as forwarders between the two distinct MPLS domains, providing isolation and, in the case of mobility, aggregation services. Typically, several aggregation networks 208 are connected to a single distribution network 206, eventually connecting the tower with the core router that connects the tower's ACs to the RNC/BSC. There are typically several distribution</p>

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		<p>networks in a Radio Access Network (RAN) connected to the service provider's core Internet Protocol (IP) network.” Taylor, 4:11-30.</p> <p>“With reference to FIG. 1, FIG. 3 shows a variety of failure modes encountered in the reference network model 102. Failure 302 of communications between tower T-PE and the S-PE can be detected via peer monitoring when both the S-PE and the T-PE are still active/alive. For example, this failure may be due to a loss of connectivity when the BFD session goes down. Failure 304 of S-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability (e.g., if the BFD-session hello timers cannot be serviced for the prescribed period of time). Failure 306 of communications between S-PE1 and core T-PE1 can be detected via peer monitoring when both S-PE1 and core T-PE1 are still active/alive. Failure 308 at core T-PE1 can be due to a hardware failure, power outage, or the lack of BFD-session maintenance capability.” Taylor, 4:46-60.</p> <p>“Pseudowire connectivity is further illustrated in FIG. 4 where the illustrated network includes four nodes: T-PE1 (10.1.1.1), S-PE2 (10.2.2.2), S-PE3 (10.3.3.3), and T-PEA (10.4.4.4). For the terminating nodes T-PE1 and T-PE4, specifications for VLANs (virtual Local Area Networks) connections (i.e., pseudowires) are shown using the Internet Operating System Command Line Interface (IOS CLI). The specification 402 for T-PE1 defines two VLANs as primary/backup combinations of virtual circuits for the network. The first three lines of the specification 402 define “VLAN 111” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 111” in the first line. The second line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE2 (10.12.2) with a virtual circuit Identification (VCID) set as VCID=1, and the third line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=101. The next three lines of the specification 402 define “VLAN 222” beginning with a specification of the hardware interface e1/0 of T-PE1 (10.1.1.1) and the label for “VLAN 222” in the fourth line. The fifth line uses the “xconnect” statement to specify a virtual circuit from that interface to S-PE3 (10.3.3.3) with VCID=2, and the sixth line uses the “backup peer” statement to specify another virtual circuit from that interface to S-PE2 (10.2.2.2) with VCID=102. These virtual circuits, VCID=1, VCID=2, VCID=101 and VCID=102 are shown in the figure between T-PE1 and the S-PEs with a solid line for the primary circuits VCID=1</p>

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		<p>and VCID=2 and a dashed line for the backup circuits VCID=101 and VCID=102.” Taylor, 4:61-5:23.</p> <p>“With respect to T-PE1 in FIG. 4, although “VLAN 111” and “VLAN 222” share the same hardware port, they do not share the same “control path disposition.” That is, “VLAN 111” is primary to S-PE2 (VCID=1) and standby to S-PE3 (VCID=101), while “VLAN 222” has an opposite configuration since it is primary to S-PE3 (VCID=2) and standby to S-PE2 (VCID=102). As discussed below, certain embodiments group pseudowires according to “control path disposition” (e.g., xconnect configuration as well as the hardware interface in order to improve failover performance. That is, to deal with both hardware port failures and switching path failures, the grouping criteria also considers the cross connects. In this case, on T-PE1 as well as T-PE4, there would exist two groups: one for active to S-PE2 and standby to S-PE3 and another for active to S-PE3 and standby to S-PE2 (i.e., the inverse configuration).” Taylor, 5:61-6:9.</p> <p>“First, local connectivity is characterized by local group identifications (Group-IDs), which depend on whether the allocation is done at a T-PE or S-PE. FIG. 5 shows an embodiment that illustrates an allocation of local group IDs in a network including terminating nodes T-PE1 (11.1.1.1), T-PE4 (14.1.1.1), and T-PE5 (15.1.1.1) and switching nodes S-PE2 (12.1.1.1) and S-PE3 (13.1.1.1). The specification 502 for T-PE1 determines corresponding local group IDs 504 based on the hardware interface and the control path. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, and the Group-ID=2 corresponds to VCID=5, VCID=6, VCID=7, and VCID=8.” Taylor, 6:10-21.</p> <p>“Local group IDs are maintained in a database so that pseudowire redundancy is also maintained. First, in a case without pseudowire redundancy, all the xconnect configurations from the same physical interface to the same peer are assigned the same local group ID. So, for example, in Ethernet cases all xconnect configurations under sub-interfaces of the same physical interface to the same peer will be assigned the same local group ID (e.g., e0/0 and e0/1 are sub-interfaces of e0). FIG. 6 shows a database representation for T-PE4 from FIG. 5. From the root node 602 for T-PE4, there is a first interface node 604 for e0/1 and a second interface node 606 for e1/0. The first interface node 604 is configured towards a single peer</p>

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		<p>node (12.1.1.1) 608 and is thus assigned a single local group ID (Group-ID=200) 610. Similarly, the second interface node 606 is configured towards a single peer node (12.1.1.1) 612 and is thus assigned a single local group ID (Group-ID=201) 614. In this case from the assignment of local group IDs 508 in FIG. 5, Group-ID=200 corresponds to VCID=1 and VCID=2, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). Both pseudowires (VCID=1 and VCID=2) are assigned the same local group ID (Group-ID=200) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly, from the assignment of local group IDs 508 in FIG. 5, T-PE4 has VCID=3 and VCID=4 under the physical interface e1/0 going to the same peer S-PE2 (12.1.1.1), and the local Group ID (Group-ID=201) is assigned to these VCs.” Taylor, 6:48-7:8.</p> <p>“For the pseudowire redundancy case, a separate redundancy-group database is maintained by the xconnect application. This redundancy-group database contains the peer IDs in the group and the local group IDs advertised to them. This is needed to maintain a 1:1 mapping between the primary pseudowires and their corresponding backup pseudowires. FIG. 7 shows a database representation for T-PE1 from FIG. 5. From the root node 702 for T-PE1, there is an interface node 704 for e0/0 and a redundancy group node 706 that shows connections for configurations to a first peer node (12.1.1.1) 708, which is assigned a local group ID (Group-ID=1) 710, and a second peer node (13.1.1.1) 712, which is assigned a local group ID (Group-ID=2) 714. In this case, Group-ID=1 corresponds to VCID=1, VCID=2, VCID=3, and VCID=4, which are under hardware interface e0/0 and configured towards the same peer, S-PE2 (12.1.1.1). These pseudowires are assigned the same group ID (Group-ID=1) in this case, and this is advertised in label mapping messages towards the remote provider edge, i.e., S-PE2. Similarly from the assignment of local group IDs 504 in FIG. 5, T-PE1 has VCID=5, VCID=6, VCID=7, and VCID=8 under the physical interface e0/0 going to another peer S-PE3 (13.1.1.1), and the local group ID (Group-ID=2) is assigned to these VCs. In this case these local group IDs are organized as a redundancy group 706.” Taylor, 7:9-35.</p>

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		<p>“Pseudowire grouping allows multiple pseudowires to be aggregated together when signaling either label withdrawals or status changes between segment end-point provider edges. This signaling can be carried out through LDP grouping TLV (Type Length Value). For example, when a PE node fails (e.g., failure 304 of S-PE1), aggregating the inter-segment PE signaling using the grouping TLV can provide significant scaling advantages. This allows all pseudowires sharing a physical port and PW configuration (e.g., xconnect configuration) to be signaled en masse between segment-adjacent provider edges.” Taylor, 7:36-46.</p> <p>“FIG. 8 shows an example based on FIG. 1 where VLAN ACs are shown as grouped by both port/HW-interface and pseudowire-class. The grouping criterion allows all “similar” pseudowires to be signaled together: All the grouped pseudowires share the same port and next-hop provider edge. Additionally, the figure contains many pseudowires, each grouped into a shaded tube. For example, the tube labeled “VLANs 100-549” contains 450 pseudowires grouped together. This figure depicts an incoming Ethernet comprised of 900 VLANs being segmented in two with 450 VLANs (100-549) active to S-PE1 while the other half of the VLANs (550-999) being active to S-PE2. This might be considered a type of manual load balancing. Furthermore, the aggregation network is only showing a single tower and the VLANs associated with it; other VLAN destinations are not shown in the figure.” Taylor, 7:47-62.</p> <p>“‘VLANs 110-112’ are active along a first pseudowire path 802 from Core T-PE1 to S-PE1 and a second pseudowire path 804 from S-PE1 to Tower T-PE. When a failure occurs at S-PE1 (e.g., as the switching node failure 304 shown in FIG. 3), then the standby pseudowires become active for ‘VLANs 110-112’ along a first pseudowire path 806 from Core T-PE1 to S-PE2 and a second pseudowire path 808 from S-PE2 to Tower T-PE.” Taylor, 7:63-8:3.</p> <p>“The standby pseudowires in FIG. 8 can be configured as HSPWs, a configuration that enables ACs to quickly failover to pre-provisioned pseudowires that are in active state but set to not-forwarding. Then when a failure occurs, switching over to these pre-provisioned HSPWs occurs quickly by switching from not-forwarding status to forwarding status.” Taylor, 8:4-9.</p>

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		<p>“FIG. 9 shows a UML (Unified Modeling Language) sequence diagram of signaling events related to the failover procedure illustrated in FIG. 8 for a switching node failure 304. The Tower T-PE and the Core T-PE independently detect a failure at S-PE1 (e.g., BFD forwarding with LDP signaling), and then mark down the status of the currently active pseudowires routed through S-PE1 and mark up the status of the standby pseudowires routed through S-PW2. Other failure modes shown in FIG. 3 can be handled similarly.” Taylor, 8:10-18.</p> <p>“In general, it is desirable for MPLS-based Ethernet networks to react quickly to failures, so proactive detection mechanisms are employed in order to pick up system failures quickly. All proactive monitoring is typically done between PE peers on a single MPLS network. These provider edges on the edges of the MPLS network act similarly to an Autonomous System Boundary Router (ASBR). As a result, related embodiments detect control path failures, which may not be the same as pseudowire data path failures. That is, the data packets and control packets may take different paths between provider edges in a MPLS network although typically these paths are coincident. Thus, when the control and data paths are not coincident, if the control path fails, then all pseudowires utilizing the control path are marked as failed. As a corollary, if the data path fails and the control path remains healthy, then failure will not be detected from monitoring the control path.” Taylor, 8:19-35.</p> <p>“A failure of a monitored provider edge initiates a switchover of all active pseudowires using the failing provider edge to their configured HSPWs (if they exist). Grouping can greatly reduce the number of messages needed between provider edges (Inter-PE Aggregation) and within a single provider edge (Intra-PE Aggregation). Furthermore, the MPLS network itself may be internally resilient deploying technologies such as, but not limited to, MPLS-TE (MPLS Traffic Engineering) and ERR (Fast Reroute). The paths across the MPLS network may recover quickly and might not trip the fault-monitoring systems.” Taylor, 8:36-46.</p> <p>“With reference to the above discussion, FIG. 10 shows a method 1002 of providing improved PW grouping according to an example embodiment. In a first operation 1004 of the method 1002, a physical interface is specified at a first PE node in a network. In a second operation 1006, a first control path is specified from the first PE node to a second PE node in the network. In a third operation 1008, a second control path is specified from the first PE node to a third</p>

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		<p>PE node in the network. These control paths related to a common physical interface can be used to characterize redundant pairs of pseudowires.” Taylor, 8:48-58.</p> <p>“In a fourth operation 1010, redundant combinations of pseudowires are specified, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node. Although a redundant combination may relate a single backup pseudowire to a given primary pseudowire, in some cases multiple backup pseudowires will be related to a given primary pseudowire for increased redundancy. In a fifth operation 1012, these pseudowires are grouped into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. In a sixth operation 1014, group identifiers for the primary PW group and the backup PW group are used to assign an active status to the primary pseudowires and a standby status to the backup pseudowires, where the active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 8:59-9:11.</p> <p>“In an optional seventh operation 1016, a failure may be detected on the first control path, and in an optional eighth operation 1018, in response to the detected failure, the group identifiers may be used to assign the active status to the backup pseudowires and the standby status to the primary pseudowires. For example, the failure on the first control path may be detected by using BED packet streams between PE nodes of the network. Then the detected failure can be signaled to PE nodes in the network by sending LDP status updates between PE nodes in the network. Then, after receiving the failure detection signals, the group identifiers can be used again to assign the active status to the backup pseudowires and the standby status to the primary pseudowires by sending LDP status updates between PE nodes in the network.” Taylor, 9:12-26.</p> <p>“Typically the network in is an MPLS network and the PE nodes are routers that provide network services to connected CE nodes of a customer network. In general, each control path is an Internet Protocol (IP) routing path between PE nodes in the network and each data path is a label switched path (LSP) between PE nodes in the network.” Taylor, 9:27-32.</p>

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		<p>“FIG. 11 shows a schematic representation of an apparatus 1102, in accordance with an example embodiment. For example, the apparatus 1102 may be used to implement the method 1002 of providing improved pseudowire grouping as described above with reference to FIG. 10. The apparatus 1102 is shown to include a processing system 1104 that may be implemented on a server, client, or other processing device that includes an operating system 1106 for executing software instructions.” Taylor, 10:2-10.</p> <p>“In accordance with an example embodiment, the apparatus 1102 includes a PW management module 1108 that includes a first specification module 1110, a second specification module 1112, third specification module 1114, a fourth specification module 1116, a grouping module 1118, and an assignment module 1120. The first specification module 1110 operates to specify a physical interface at a first PE node in a network. The second specification module 1112 operates to specify a first control path from the first PE node to a second PE node in the network. The third specification module 1114 operates to specify a second control path from the first PE node to a third PE node in the network. The fourth specification module 1116 operates to specify redundant combinations of pseudowires, where each redundant combination includes a primary pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the second PE node and a backup pseudowire that is configured as a virtual circuit between the physical interface of the first PE node and the third PE node.” Taylor, 10:11-29.</p> <p>“The grouping module 1118 operates to group the pseudowires into a primary PW group that includes the primary pseudowires and a backup PW group that includes the backup pseudowires. The assignment module 1120 operates to use group identifiers for the PW groups to assign an active status to the primary pseudowires and a standby status to the backup pseudowires. The active status enables data transfers along corresponding PW data paths and the standby status disables data transfers along corresponding PW data paths.” Taylor, 10:30-38.</p>

No.	'821 Patent Claim 20	The Reference
		<p>Vasseur '879 discloses:</p> <p>“A technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur '879, Abstract.</p> <p style="text-align: center;">FIG. 1</p> <p>Vasseur '879, FIG. 1 (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<p>SIGNALING MESSAGE 300 (PATH, RESV, OR ERROR)</p> <p>COMMON HEADER 310</p> <p>SOURCE ADDRESS 312 DESTINATION ADDRESS 314</p> <p>SIGNALING PROTOCOL (RSVP) SPECIFIC OBJECTS 320</p> <p>LSP-ATTRIBUTE OBJECT 330</p> <p>EXTENSION OBJECT(S) 400</p>

FIG. 3

Vasseur '879, FIG. 3 (annotated).

No.	'821 Patent Claim 20	The Reference
		<p style="text-align: center;">FIG. 5</p> <p>Vasseur '879, FIG. 5 (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<p>FIG. 6</p> <p>Vasseur '879, FIG. 6 (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<pre> graph TD START([START]) --> 705[HEAD-END NODE ESTABLISHES PRIMARY TUNNEL] 705 --> 710[HEAD-END NODE REQUESTS PROTECTION OF PRIMARY TUNNEL] 710 --> 715[POINTS OF LOCAL REPAIR (PLRs) CREATE BACKUP TUNNELS TO PROTECT PRIMARY TUNNEL] 715 --> 720[HEAD-END NODE REQUESTS BACKUP TUNNEL PATH QUALITY NOTIFICATION] 720 --> 725[PLRs OF PRIMARY TUNNEL COLLECT PATH QUALITY INFORMATION (e.g., METRICS/STATISTICS) FOR PRIMARY AND BACKUP TUNNELS (e.g., FROM PRIMARY AND/OR BACKUP NODES)] 725 --> 730[PLRs MAY SEND PATH QUALITY NOTIFICATION TO HEAD-END NODE] 730 --> 735[PLRs MONITOR PROTECTED SEGMENT OF PRIMARY TUNNEL FOR FAILURE] 735 --> 740{DETECT FAILURE ?} 740 -- N --> 705 740 -- Y --> A((A)) </pre> <p>FIG. 7A Vasseur '879, FIG. 7A (annotated).</p>

No.	'821 Patent Claim 20	The Reference
		<pre> graph TD A((A)) --> B[DETECTING PLR DIVERTS PRIMARY TUNNEL TRAFFIC TO BACKUP TUNNEL AND SENDS ERROR MESSAGE TO HEAD-END NODE] B --> C[PLR CONTINUES TO COLLECT PATH QUALITY INFORMATION FOR BACKUP TUNNEL] C --> D{PLR SEND NOTIFICATION TO HEAD-END NODE ?} D -- N --> E{TIMER EXPIRED ?} E -- Y --> F[HEAD-END NODE DETERMINES WHETHER TO REESTABLISH PRIMARY TUNNEL BASED ON BACKUP TUNNEL PATH QUALITY NOTIFICATION] F --> G{ACCEPTABLE PATH QUALITY ?} G -- Y --> H[HEAD-END NODE ATTEMPTS TO REESTABLISH PRIMARY TUNNEL] G -- N --> I(END) H --> I D -- Y --> F </pre> <p>FIG. 7B</p> <p>Vasseur '879, FIG. 7B.</p>

No.	'821 Patent Claim 20	The Reference
		<p>“The present invention relates to computer networks and more particularly to dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur '879, 1:10-14.</p> <p>“Since management of interconnected computer networks can prove burdensome, smaller groups of computer networks may be maintained as routing domains or autonomous systems. The networks within an autonomous system (AS) are typically coupled together by conventional “intradomain” routers configured to execute intradomain routing protocols, and are generally subject to a common authority. To improve routing scalability, a service provider (e.g., an ISP) may divide an AS into multiple “areas.” It may be desirable, however, to increase the number of nodes capable of exchanging data; in this case, interdomain routers executing interdomain routing protocols are used to interconnect nodes of the various ASes. Moreover, it may be desirable to interconnect various ASes that operate under different administrative domains. As used herein, an AS or an area is generally referred to as a “domain,” and a router that interconnects different domains together is generally referred to as a ‘border router.’” Vasseur '879, 1:40-56.</p> <p>“An example of an interdomain routing protocol is the Border Gateway Protocol version 4 (BGP), which performs routing between domains (ASes) by exchanging routing and reachability information among neighboring interdomain routers of the systems. An adjacency is a relationship formed between selected neighboring (peer) routers for the purpose of exchanging routing information messages and abstracting the network topology. The routing information exchanged by BGP peer routers typically includes destination address prefixes, i.e., the portions of destination addresses used by the routing protocol to render routing (“next hop”) decisions. Examples of such destination addresses include IP version 4 (IPv4) and version 6 (IPv6) addresses. BGP generally operates over a reliable transport protocol, such as TCP, to establish a TCP connection/session. The BGP protocol is well known and generally described in Request for Comments (RFC) 1771, entitled A Border Gateway Protocol 4 (BGP-4), published March 1995.” Vasseur '879, 1:57-2:7.</p>

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		<p>“Examples of an intradomain routing protocol, or an interior gateway protocol (IGP), are the Open Shortest Path First (OSPF) routing protocol and the Intermediate-System-to-Intermediate-System (IS-IS) routing protocol. The OSPF and IS-IS protocols are based on link-state technology and, therefore, are commonly referred to as link-state routing protocols. Link-state protocols define the manner with which routing information and network-topology information are exchanged and processed in a domain. This information is generally directed to an intradomain router's local state (e.g., the router's usable interfaces and reachable neighbors or adjacencies). The OSPF protocol is described in RFC 2328, entitled OSPF Version 2, dated April 1998 and the IS-IS protocol used in the context of IP is described in RFC 1195, entitled Use of OSI IS-IS for routing in TCP/IP and Dual Environments, dated December 1990, both of which are hereby incorporated by reference.” Vasseur '879, 2:8-24.</p> <p>“An intermediate network node often stores its routing information in a routing table maintained and managed by a routing information base (RIB). The routing table is a searchable data structure in which network addresses are mapped to their associated routing information. However, those skilled in the art will understand that the routing table need not be organized as a table, and alternatively may be another type of searchable data structure. Although the intermediate network node's routing table may be configured with a predetermined set of routing information, the node also may dynamically acquire (“learn”) network routing information as it sends and receives data packets. When a packet is received at the intermediate network node, the packet's destination address (e.g., stored in a header of the packet) may be used to identify a routing table entry containing routing information associated with the received packet. Among other things, the packet's routing information indicates the packet's next-hop address.” Vasseur '879, 2:25-41.</p> <p>“Multi-Protocol Label Switching (MPLS) Traffic Engineering has been developed to meet data networking requirements such as guaranteed available bandwidth or fast restoration. MPLS Traffic Engineering exploits modern label switching techniques to build guaranteed bandwidth end-to-end tunnels through an IP/MPLS network of label switched routers (LSRs). These tunnels are a type of label switched path (LSP) and thus are generally referred to as MPLS Traffic Engineering (TE) LSPs. Examples of MPLS TE can be found in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels dated December 2001, RFC 3784</p>

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		<p>entitled Intermediate-System-to-Intermediate-System (IS-IS) Extensions for Traffic Engineering (TE) dated June 2004, and RFC 3630, entitled Traffic Engineering (TE) Extensions to OSPF Version 2 dated September 2003, the contents of all of which are hereby incorporated by reference in their entirety.” Vasseur '879, 2:58-3:6.</p> <p>“Establishment of an MPLS TE-LSP from a head-end LSR to a tail-end LSR involves computation of a path through a network of LSRs. Optimally, the computed path is the “shortest” path, as measured in some metric, that satisfies all relevant LSP Traffic Engineering constraints such as e.g., required bandwidth, “affinities” (administrative constraints to avoid or include certain links), etc. Path computation can either be performed by the head-end LSR or by some other entity operating as a path computation element (PCE) not co-located on the head-end LSR. The head-end LSR (or a PCE) exploits its knowledge of network topology and resources available on each link to perform the path computation according to the LSP Traffic Engineering constraints. Various path computation methodologies are available including CSPF (constrained shortest path first). MPLS TE-LSPs can be configured within a single domain, e.g., area, level, or AS, or may also span multiple domains, e.g., areas, levels, or ASes.” Vasseur '879, 3:7-24.</p> <p>“The PCE is an entity having the capability to compute paths between any nodes of which the PCE is aware in an AS or area. PCEs are especially useful in that they are more cognizant of network traffic and path selection within their AS or area, and thus may be used for more optimal path computation. A head-end LSR may further operate as a path computation client (PCC) configured to send a path computation request to the PCE, and receive a response with the computed path, potentially taking into consideration other path computation requests from other PCCs. It is important to note that when one PCE sends a request to another PCE, it acts as a PCC.” Vasseur '879, 3:25-36.</p> <p>“Some applications may incorporate unidirectional data flows configured to transfer time-sensitive traffic from a source (sender) in a computer network to a destination (receiver) in the network in accordance with a certain “quality of service” (QoS). Here, network resources may be reserved for the unidirectional flow to ensure that the QoS associated with the data flow is maintained. The Resource ReSerVation Protocol (RSVP) is a network-control protocol that</p>

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		<p>enables applications to reserve resources in order to obtain special QoS for their data flows. RSVP works in conjunction with routing protocols to, e.g., reserve resources for a data flow in a computer network in order to establish a level of QoS required by the data flow. RSVP is defined in R. Braden, et al., Resource ReSerVation Protocol (RSVP), RFC 2205, the contents of which are hereby incorporated by reference in its entirety. In the case of traffic engineering applications, RSVP signaling (with Traffic Engineering extensions) is used to establish a TE-LSP and to convey various TE-LSP attributes to routers, such as border routers, along the TE-LSP obeying the set of required constraints whose path may have been computed by various means.” Vasseur ’879, 3:37-57.</p> <p>“Generally, a tunnel is a logical structure that encapsulates a packet (a header and data) of one protocol inside a data field of another protocol packet with a new header. In this manner, the encapsulated data may be transmitted through networks that it would otherwise not be capable of traversing. More importantly, a tunnel creates a transparent virtual network link between two network nodes that is generally unaffected by physical network links or devices (i.e., the physical network links or devices merely forward the encapsulated packet based on the new header). While one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur ’879, 3:58-4:3.</p> <p>“Occasionally, a network element (e.g., a node or link) will fail, causing redirection of the traffic that originally traversed the failed network element to other network elements that bypass the failure. Generally, notice of this failure is relayed to the nodes in the network through an advertisement of the new network topology, e.g., an IGP or BGP Advertisement, and routing tables are updated to avoid the failure accordingly. Reconfiguring a network in response to a network element failure using, e.g., pure IP rerouting, can be time consuming. Many recovery techniques, however, are available to provide fast recovery and/or network configuration in the event of a network element failure, including, inter alia, “Fast Reroute”, e.g., MPLS TE Fast Reroute. An example of MPLS TE Fast Reroute is described in Pan, et al., Fast Reroute Extensions to RSVP-TE for LSP Tunnels, RFC 4090, May 2005, which is hereby incorporated by reference as though fully set forth herein.” Vasseur ’879, 4:4-21.</p>

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		<p>“Fast Reroute (or FRR) has been widely deployed to protect against network element failures, where “backup tunnels” are created to bypass one or more protected network elements (e.g., links, shared risk link groups (SRLGs), and nodes). When the network element fails, traffic is quickly diverted (“Fast Rerouted”) over a backup tunnel to bypass the failed element, or more particularly, in the case of MPLS, a set of primary TE-LSPs (tunnels) is quickly diverted. Specifically, the point of local repair (PLR) node configured to reroute the traffic inserts (“pushes”) a new label for the backup tunnel, and the traffic is diverted accordingly. Once the failed element is bypassed, the backup tunnel label is removed (“popped”), and the traffic is routed along the original path according to the next label (e.g., that of the original TE-LSP). Notably, the backup tunnel, in addition to bypassing the failed element along a protected primary TE-LSP, also intersects the primary TE-LSP, i.e., it begins and ends at nodes along the protected primary TE-LSP.” Vasseur '879, 4:22-39.</p> <p>“To offer maximum protection, e.g., guaranteed bandwidth, during Fast Reroute, backup tunnels may reserve a configurable amount of bandwidth to ensure that Fast Rerouted traffic from the primary tunnel has a reserved path to follow. For example, the bandwidth reserved for the primary tunnel may also be reserved on the backup tunnel. While this approach provides maximum protection, it also requires a non-negligible amount of network resources (e.g., capacity/bandwidth) and may increase operational complexity.” Vasseur '879, 4:40-48.</p> <p>“Certain techniques are available to efficiently minimize the amount of resources required by the establishment and maintenance of the backup tunnels for Fast Reroute. One such technique is to create zero-bandwidth (“0-BW”) backup tunnels (i.e., tunnels that reserve no bandwidth) to protect non-0-BW primary tunnels. This “best effort” approach does not guarantee that the path followed by the backup tunnel will have enough bandwidth to support the diverted primary tunnel at the time of failure without QoS degradation, however in many situations the path quality of the backup tunnel is sufficient. For instance, if the network is not overly congested, or the backup tunnel follows a non-congested path, there may be enough available bandwidth along the backup tunnel to support the newly rerouted traffic. Also, because primary tunnels often reserve bandwidth in response to “peak” traffic utilization, the amount of traffic over the primary tunnel at the time of failure may be far less than the reserved bandwidth (e.g., at “off-peak” times). Notably, those skilled in the art will understand that in</p>

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		<p>the absence of the above exceptions, a 0-BW backup tunnel may have unacceptable bandwidth (e.g., affecting path quality) to support the diverted traffic.” Vasseur ’879, 4:49-5:2.</p> <p>“Currently, head-end nodes (LSRs) may be configured to systematically reroute the primary tunnels affected by the network element failure (e.g., diverted primary tunnels), especially in the case with 0-BW backup tunnels, such as, e.g., by reestablishing a new primary tunnel that follows a path excluding the failed network element. In particular, 0-BW backup tunnels represent a best effort attempt to allow the head-end node to more gracefully reestablish the primary tunnel in response to a failure, since the backup tunnels may not be able to support the diverted traffic without QoS degradation. The systematic reestablishing may potentially result in the reestablishment of a large number of primary tunnels (e.g., up to 3000 for a single network element failure in today’s networks). Notably, reestablishing diverted primary tunnels may be undesirable for the network, such as by creating traffic churn, jitter, control plane overloads, etc., as will be understood by those skilled in the art. However, as noted above, there are situations where the backup tunnel may provide acceptable bandwidth, at least, for example, for a period of time (e.g., possibly short) until the failed network element is restored. In these situations, then, it may have been unnecessary to reestablish the diverted primary tunnels. There remains a need, therefore, for a technique that dynamically determines whether to reestablish a diverted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network.” Vasseur ’879, 5:3-28.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 5:32-47.</p>

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		<p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as Resource ReSerVation Protocol (RSVP) Traffic Engineering (TE) signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or type/length/value (TLV) encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object of the RSVP messages to convey the path quality notification.” Vasseur '879, 5:48-58.</p> <p>“In accordance with one aspect of the present invention, the head-end node requests the establishment of the primary tunnel (e.g., a TE-Label Switched Path, TE-LSP), along with a request for Fast Reroute protection of one or more network elements (e.g., with zero-bandwidth, 0-BW backup tunnels) at a PLR node. In addition, the head-end node may include a request for backup tunnel path quality notification, such as, e.g., through the use of the novel Feedback sub-object. The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. Once failure is detected, the PLR node diverts the traffic onto the backup tunnel, and sends an error message (e.g., an RSVP PathErr) to the head end node indicating the “Fast Rerouting” of the primary tunnel.” Vasseur '879, 5:59-6:6.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, the PLR node may collect metrics/statistics (e.g., packet drops, path cost, jitter, etc.) of the primary and/or backup tunnels. Once the primary tunnel is Fast Rerouted, the PLR node continues to collect metrics of the backup tunnel, and may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. Notably, the PLR node may be configured to send path quality notifications to the head-end node once, continually, periodically, in response to configurable changes in path quality, etc. Also, as in the case where multiple primary tunnels are Fast Rerouted, the path quality notification may include an indication of which Fast Rerouted primary tunnels in particular have been effected by the changed path quality.” Vasseur '879, 6:7-23.</p>

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		<p>“In accordance with yet another aspect of the present invention, upon receiving the error message (PathErr), the head-end node may wait for the path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. Notably, in the event the head-end node does not receive a path quality notification for the backup tunnel (e.g., within a configurable time limit), the head-end node may attempt to reestablish the new primary tunnel. Moreover, where the head-end node has multiple primary tunnels being Fast Rerouted, a configurable subset of the primary tunnels may be reestablished, e.g., to reduce congestion of the backup tunnels, and/or to limit the number of reestablished primary tunnels within a given period of time.” Vasseur '879, 6:24-43.</p> <p>“Advantageously, the novel technique dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. By providing the head-end node of the primary tunnel with path quality feedback of the backup tunnel, the novel technique avoids reestablishing a potentially large number of tunnels over one or more alternate paths after a failure (and Fast Reroute) if the backup tunnels have acceptable path quality. In particular, the backup tunnels, e.g., 0-BW backup tunnels, may not be congested or subsequently burdened by the Fast Rerouted traffic of the primary tunnel. Also, the failed network element (thus the primary tunnel) may be quickly restored; therefore by not reestablishing the primary tunnel, network jitter, churn, etc., may be avoided. Further, the dynamic nature of the novel technique alleviates the need for cumbersome manual configuration.” Vasseur '879, 6:44-59.</p> <p>“FIG. 3 is schematic block diagram of an exemplary signaling (RSVP) message that may be advantageously used with the present invention.” Vasseur '879, 7:6-8.</p> <p>“FIG. 5 is a schematic block diagram of the computer network in FIG. 1 showing Fast Reroute protection of a primary tunnel using a backup tunnel in accordance with the present invention.” Vasseur '879, 7:12-15.</p>

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		<p>“FIG. 6 is a schematic block diagram of the computer network in FIG. 5 showing an unacceptable backup tunnel path quality and resultant reestablishing of the primary tunnel in accordance with the present invention.” Vasseur '879, 7:16-19.</p> <p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention.” Vasseur '879, 7:20-24.</p> <p>“FIG. 1 is a schematic block diagram of an exemplary computer network 100 comprising a plurality of nodes A-F, such as routers or other network devices, interconnected as shown. The nodes may be a part of one or more autonomous systems, routing domains, or other networks or subnetworks. For instance, routers A and E may be provider edge (PE) devices of a provider network, (e.g., a service provider network) that are interconnected to one or more customer networks through customer edge (CE) devices (not shown, while the remaining nodes B-D and F may be core provider (P) devices, as will be understood by those skilled in the art. Those skilled in the art will also understand that the nodes A-F may be any nodes within any arrangement of computer networks, and that the view shown herein is merely an example. For example, the nodes may be configured as connections to/from one or more virtual private networks (VPNs), as will be understood by those skilled in the art. These examples are merely representative. Those skilled in the art will understand that any number of routers, nodes, links, etc. may be used in the computer network 100 and connected in a variety of ways, and that the view shown herein is for simplicity.” Vasseur '879, 7:29-49.</p> <p>“Data packets may be exchanged among the computer network 100 using predefined network communication protocols such as the Transmission Control Protocol/Internet Protocol (TCP/IP), User Datagram Protocol (UDP), Asynchronous Transfer Mode (ATM) protocol, Frame Relay protocol, Internet Packet Exchange (IPX) protocol, etc. Routing information may be distributed among the routers of the computer network using predetermined Interior Gateway Protocols (IGPs), such as conventional distance-vector protocols or, illustratively, link-state protocols, through the use of IGP Advertisements.” Vasseur '879, 7:50-60.</p>

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		<p>“FIG. 2 is a schematic block diagram of an exemplary router 200 that may be advantageously used with the present invention, e.g., as an edge router or a core router. The router comprises a plurality of network interfaces 210, a processor 220, and a memory 240 interconnected by a system bus 250. The network interfaces 210 contain the mechanical, electrical and signaling circuitry for communicating data over physical links coupled to the network 100. The network interfaces may be configured to transmit and/or receive data using a variety of different communication protocols, including, inter alia, TCP/IP, UDP, ATM, synchronous optical networks (SONET), wireless protocols, Frame Relay, Ethernet, Fiber Distributed Data Interface (FDDI), etc.” Vasseur '879, 7:61-8:6.</p> <p>“The memory 240 comprises a plurality of storage locations that are addressable by the processor 220 and the network interfaces 210 for storing software programs and data structures associated with the present invention. The processor 220 may comprise necessary elements or logic adapted to execute the software programs and manipulate the data structures. A router operating system 242 (e.g., the Internetworking Operating System, or IOS™, of Cisco Systems, Inc.), portions of which is typically resident in memory 240 and executed by the processor, functionally organizes the router by, inter alia, invoking network operations in support of software processes and/or services executing on the router. These software processes and/or services may comprise routing services 247, Traffic Engineering (TE) services 244, and RSVP services 249. It will be apparent to those skilled in the art that other processor and memory means, including various computer-readable media, may be used to store and execute program instructions pertaining to the inventive technique described herein.” Vasseur '879, 8:7-26.</p> <p>“Routing services 247 contain computer executable instructions executed by processor 220 to perform functions provided by one or more routing protocols, such as IGP (e.g., OSPF and IS-IS), IP, BGP, etc. These functions may be configured to manage a forwarding information database (not shown) containing, e.g., data used to make forwarding decisions. Routing services 247 may also perform functions related to virtual routing protocols, such as maintaining VRF instances (not shown) as will be understood by those skilled in the art.” Vasseur '879, 8:27-36.</p>

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		<p>“RSVP services 249 contain computer executable instructions for implementing RSVP and processing RSVP messages in accordance with the present invention. RSVP is described in RFC 2205, entitled Resource ReSerVation Protocol (RSVP), and in RFC 3209, entitled RSVP-TE: Extensions to RSVP for LSP Tunnels, both as incorporated above.” Vasseur '879, 8:37-42.</p> <p>“TE services 244 contain computer executable instructions for operating TE functions in accordance with the present invention. Examples of Traffic Engineering are described in RFC 3209, RFC 3784, and RFC 3630 as incorporated above, and in RFC 3473, entitled, Generalized Multi-Protocol Label Switching (GMPLS) Signaling Resource ReSerVation Protocol-Traffic Engineering (RSVP-TE) Extensions dated January 2003, which is hereby incorporated by reference in its entirety. A TE database (TED, not shown) may be illustratively resident in memory 240 and used to store TE information provided by the routing protocols, such as IGP, BGP, and/or RSVP (with TE extensions, e.g., as described herein), including, inter alia, path quality information as described herein. The TED may be illustratively maintained and managed by TE services 244.” Vasseur '879, 8:43-57.</p> <p>“Changes in the network topology may be communicated among routers 200 using a link-state protocol, such as the conventional OSPF and IS-IS protocols. Suppose, for example, that a communication link fails or a cost value associated with a network node changes. Once the change in the network's state is detected by one of the routers, that router may flood an IGP Advertisement communicating the change to the other routers in the network. In this manner, each of the routers eventually “converges” to an identical view of the network topology.” Vasseur '879, 8:58-67.</p> <p>“In one embodiment, the routers described herein are IP routers that implement Multi-Protocol Label Switching (MPLS) and operate as label switched routers (LSRs). In one simple MPLS scenario, at an ingress to a network, a label is assigned to each incoming packet based on its forwarding equivalence class before forwarding the packet to a next-hop router. At each router, a forwarding selection and a new substitute label are determined by using the label found in the incoming packet as a reference to a label forwarding table that includes this information. At the network egress, a forwarding decision is made based on the incoming label</p>

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		<p>but optionally no label is included when the packet is sent on to the next hop. In some network configurations, one hop prior to the network egress, a penultimate hop popping (PHP) operation may be performed. Particularly, because the hop prior to the network egress (the penultimate hop) is attached to the network egress, the label is no longer needed to assure that the traffic follows a particular path to the network egress. As such, the PHP-enabled device “pops” the labels from the traffic before forwarding the traffic to the network egress, e.g., using conventional or native (IP) routing, thereby alleviating the task of removing the labels at the network egress.” Vasseur '879, 9:1-23.</p> <p>“The paths taken by packets that traverse the network in this manner are referred to as label switched paths (LSPs) or Traffic Engineering (TE)-LSPs. An example TE-LSP is shown as the thick line and arrow (T1) between a head-end node (router A) and a tailend node (router E) in FIG. 1. Establishment of a TE-LSP requires computation of a path, signaling along the path, and modification of forwarding tables along the path. MPLS TE establishes LSPs that have guaranteed bandwidth under certain conditions. Illustratively, the TE-LSPs may be signaled through the use of the RSVP protocol (with Traffic Engineering extensions), and in particular, RSVP TE signaling messages. Notably, when incorporating the use of PCEs (described below), the path computation request (and response) between PCC and PCE can be exchanged in accordance with a protocol specified in Vasseur, et al., Path Computation Element (PCE) Communication Protocol (PCEP)—Version 1—<draft-vasseur-pce-pcep-02.txt>, Internet Draft, September 2005, the contents of which are hereby incorporated by reference in its entirety. It should be understood that the use of RSVP or PCEP serves only as an example, and that other communication protocols may be used in accordance with the present invention.” Vasseur '879, 9:24-45.</p> <p>“In accordance with RSVP, to request a data flow (TE-LSP) between a sender and a receiver, the sender may send an RSVP path request (Path) message downstream to the receiver along a path (e.g., a unicast route) to identify the sender and indicate e.g., bandwidth needed to accommodate the data flow, along with other attributes of the TE-LSP. The Path message may contain various information about the data flow including, e.g., traffic characteristics of the data flow. Also in accordance with the RSVP, a receiver establishes the TE-LSP between the sender and receiver by responding to the sender's Path message with a reservation request</p>

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		<p>(Resv) message. The reservation request message travels upstream hop-by-hop along the flow from the receiver to the sender. The reservation request message contains information that is used by intermediate nodes along the flow to reserve resources for the data flow between the sender and the receiver, to confirm the attributes of the TE-LSP, and provide a TE-LSP label. If an intermediate node in the path between the sender and receiver acquires a Path message or Resv message for a new or established reservation (TE-LSP) and encounters an error (e.g., insufficient resources, failed network element, etc.), the intermediate node generates and forwards a path or reservation error (PathErr or ResvErr, hereinafter Error) message to the sender or receiver, respectively.” Vasseur '879, 9:46-10:2.</p> <p>“FIG. 3 is a schematic block diagram of portions of a signaling message 300 (e.g., RSVP message, such as Path, Resv or Error) that may be advantageously used with the present invention. Message 300 contains, inter alia, a common header 310 and one or more signaling protocol specific objects 320, such as an LSP-ATTRIBUTE object 330. The common header 310 may comprise a source address 312 and destination address 314, denoting the origination and requested termination of the message 300. Protocol specific objects 320 contain objects necessary for each type of message 300 (e.g., Path, Resv, Error, etc.). For instance, a Path message may have a sender template object, Tspec object, Previous-hop object, etc. The LSP-ATTRIBUTE object 330, for instance, may be used to signal attributes and/or information regarding an LSP (tunnel). To communicate this information, LSP-ATTRIBUTE object 330 (as well as specific objects 320) may include various type/length/value (TLV) encoding formats and/or flags, as will be understood by those skilled in the art. An example of an LSP-ATTRIBUTE object is further described in Farrel, et al., Encoding of Attributes for Multiprotocol Label Switching (MPLS) Label Switched Path (LSP) Establishment Using RSVP-TE <draft-ietf-mpls-rsvpte-attributes-05.txt>, Internet Draft, May 2005, which is hereby incorporated by reference as though fully set forth herein. A Resv message, on the other hand, may have specific objects 320 for a label object, session object, filter spec object, etc., in addition to the LSP-ATTRIBUTE object 330. Error messages 300 (e.g., PathErr or ResvErr), may also have specific objects 320, such as for defining the type of error, etc.” Vasseur '879, 10:3-31.</p>

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		<p>“It should be noted that in accordance with RSVP signaling, the state of the TE-LSP is refreshed on a timed interval, e.g., every thirty seconds, in which RSVP Path and Resv messages are exchanged. This timed interval is configurable by a system administrator. Moreover, various methods understood by those skilled in the art may be utilized to protect against route record objects (RROs) contained in signaling messages for a TE-LSP in the event security/privacy is desired. Such RRO filtering prevents a head-end node of the TE-LSP from learning of the nodes along the TE-LSP, i.e., nodes within the provider network.” Vasseur '879, 10:4-42.</p> <p>“Although the illustrative embodiment described herein is directed to MPLS, it should also be noted that the present invention may advantageously apply to Generalized MPLS (GMPLS), which pertains not only to packet and cell-based networks, but also to Time Division Multiplexed (TDM) and optical networks. GMPLS is well known and described in RFC 3945, entitled Generalized Multi-Protocol Label Switching (GMPLS) Architecture, dated October 2004, and RFC 3946, entitled Generalized Multi-Protocol Label Switching (GMPLS) Extensions for Synchronous Optical Network (SONET) and Synchronous Digital Hierarchy (SDH) Control, dated October 2004, the contents of both of which are hereby incorporated by reference in their entirety.” Vasseur '879, 10:43-55.</p> <p>“To obviate delays associated with updating routing tables when attempting to avoid a failed network element (i.e., during convergence), some networks have employed MPLS TE Fast Reroute (FRR). MPLS Fast Reroute is a technique that may be used to quickly divert (“Fast Reroute”) traffic around failed network elements in a TE-LSP. MPLS Fast Reroute is further described, for example, by Fast Reroute Extensions to RSVP-TE for LSP Tunnels, as incorporated by reference above. According to the technique, one or more network elements (e.g. links or nodes) in a network are protected by backup tunnels following an alternate path. If a failure occurs on a protected link or node, TE-LSPs (and consequently the traffic that they carry) are locally diverted onto an appropriate alternate path (e.g., a “backup tunnel”) by the node immediately upstream from the failure. The backup tunnel acts as a Fast Reroute path for the primary TE-LSP and obviates delays associated with other measures, such as tearing down the primary TE-LSP after having gracefully diverted the TE-LSPs affected by the failure, should an alternate path around the failed network element exist. In the event of a</p>

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		<p>failure of a protected element the head-end node of the backup tunnel (or a “point of local repair,” PLR node) may quickly begin diverting traffic over the backup tunnel with minimal disruption to traffic flow. Those skilled in the art will understand that MPLS Fast Reroute is one example of link or node failure protection, and that other known correction mechanisms may be used in accordance with the present invention. As mentioned above, however, the head-end node of the Fast Rerouted primary tunnel may attempt to reestablish the primary tunnel in response to learning of the protected element failure, particularly in the case where the backup tunnel is a zero-bandwidth (0-BW) tunnel. The attempt to reestablish the primary tunnel has conventionally been a systematic response to Fast Rerouting (diverting) of the primary tunnel, regardless of the path quality of the backup tunnel.” Vasseur ’879, 10:56-11:23.</p> <p>“The present invention is directed to a technique for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network. According to the novel technique, a head-end node establishes a primary tunnel to a destination, and a point of local repair (PLR) node along the primary tunnel establishes a backup tunnel around one or more protected network elements of the primary tunnel, e.g., for Fast Reroute protection. Once one of the protected network elements fail, the PLR node “Fast Reroutes,” i.e., diverts, the traffic received on the primary tunnel onto the backup tunnel, and sends notification of backup tunnel path quality (e.g., with one or more metrics) to the head-end node. The head-end node then analyzes the path quality metrics of the backup tunnel to determine whether to utilize the backup tunnel or reestablish a new primary tunnel.” Vasseur ’879, 11:24-39.</p> <p>“In the illustrative embodiment described herein, the notification of backup tunnel path quality may be embodied as extensions to a request/response signaling exchange, such as RSVP TE signaling messages. Notably, the RSVP extensions are, in turn, embodied as new RSVP objects, flags, and/or TLV encoded formats contained within the RSVP objects. For instance, a novel Fast Reroute Feedback (FFeed) sub-object may be included within an LSP-ATTRIBUTE object 330 of the RSVP messages 300 to convey the path quality notification.” Vasseur ’879, 11:40-49.</p>

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		<p>“FIG. 4 is a schematic block diagram illustrating the format of an extension object (TLV) 400, such as a Fast Reroute Feedback object, that may be advantageously used with the present invention. The extension object (or sub-object) 400 is illustratively embodied as a TLV contained in an LSP-ATTRIBUTE object 330 of an RSVP message 300 and is extended to carry backup (and/or primary) tunnel path quality information. To that end, the “Feedback” object 400 is organized to include a Type field 405 containing a predetermined type value signifying the specific content of the object 400. The Length field 410 is a variable length value. The TLV encoded format may also comprise one or more non-ordered sub-TLVs 450 carried within the TLV “payload” (e.g. Value field 415), each having a Type field 455, Length field 460, and Value field 465. The fields of the TLV 400 and sub-TLV(s) 450 are used in a variety of manners, including as described herein, according to the present invention.” Vasseur '879, 11:59-12:8.</p> <p>“In accordance with one aspect of the present invention, the head-end node (e.g., router A) requests the establishment of the primary tunnel (e.g., T1), such as a TE-LSP. Notably, the head-end node may be a head-end node for multiple primary tunnels, as will be understood by those skilled in the art. Along with the primary tunnel establishment, the head-end node may also request Fast Reroute protection of one or more network elements (e.g., all intermediate network elements) at a PLR node (e.g., router B as shown). Note that each intermediate node along the primary tunnel may act as a PLR node, and that router B is shown merely for simplicity. Illustratively, the Fast Reroute protection may be embodied as one or more zero-bandwidth (0-BW) backup tunnels at the PLR node (e.g., BT1). Those skilled in the art will also understand that the PLR node may protect more than one primary tunnel originating at more than one corresponding head-end node (not shown).” Vasseur '879, 12:9-25.</p> <p>“The primary and backup tunnels may then be established, and, in accordance with Fast Reroute, the PLR node may monitor the protected network elements for failure. For example, various connectivity verification protocols, such as, e.g., Bidirectional Forwarding Detection (BFD), IGP “Hello” packets, BGP KEEPALIVE messages, etc., may be used to detect a failure of a network element, as will be understood by those skilled in the art. Furthermore, other lower-layer failure detection mechanisms (e.g. optical or SONET/SDH alarms) may be used to detect a network element failure. Once failure is detected, the PLR node diverts the</p>

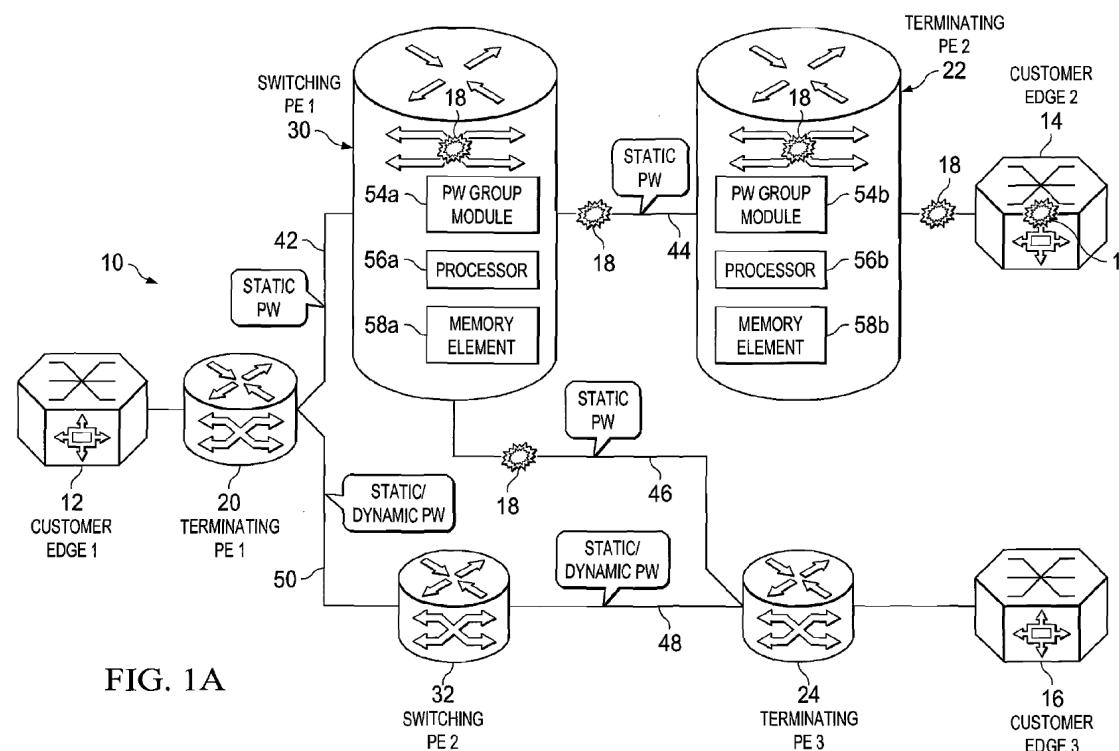
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		<p>traffic onto the backup tunnel, and may send an error message (e.g., an RSVP PathErr 300, such as a conventional “tunnel locally repaired” message) to the head end node indicating the “Fast Rerouting” of the primary tunnel. FIG. 5 is a schematic block diagram of the computer network 100 in FIG. 1 showing Fast Reroute protection of the primary tunnel T1 (e.g., in response to a protected network element, router C, failure, indicated with an overlaid “X”) using a backup tunnel BT1 in accordance with the present invention. Traffic originally received at the PLR node (router B) over the primary tunnel is now diverted over the backup tunnel to a remerge point (router D) of the primary tunnel, as will be understood by those skilled in the art.” Vasseur '879, 12:42-65.</p> <p>“In accordance with another aspect of the present invention, prior to Fast Rerouting, each PLR node may collect metrics/statistics of the primary and/or backup tunnels. For instance, example metrics may comprise, inter alia, packet drops, path cost, jitter, delay, bandwidth, etc. The PLR node may collect the metrics through traffic monitoring, probes, independent calculations, and/or through cooperation with protected nodes of the primary tunnel (primary nodes) and nodes of the backup tunnel (backup nodes), e.g., transmitting path quality notifications. Once the primary tunnel is Fast Rerouted (i.e., diverted after failure of a protected network element), the PLR node continues to collect metrics of the backup tunnel. (Alternatively, metrics of the backup tunnel may be collected only after Fast Reroute, and not prior to Fast Reroute.)” Vasseur '879, 12:66-13:12.</p> <p>“For example, a path cost increase from the primary tunnel to the backup tunnel may be computed (and transmitted, below) by the PLR node prior to the failure (or during the failure while the primary TE-LSP is diverted onto the backup tunnel) using its own routing tables. The path cost increase may be calculated as a difference between the entire length (head-end node to tail-end node) of the primary and backup tunnels, or just the difference between the protected segment of the primary tunnel and the backup tunnel (PLR node to remerge point). Also, a jitter increase of the primary and backup tunnels, which may be generally described as a difference between inter-arrival of packets, may be monitored using various known techniques, such as, e.g., sending probe packets (probes) from the PLR node to the remerge point. For instance, probes may determine that packets arrive at the remerge point along the primary tunnel from the PLR node consistently, e.g., every 10 milliseconds (ms) (e.g., an</p>

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		<p>average value). After Fast Reroute, however, probes may determine that packets do not arrive at the remerge point along the backup tunnel from the PLR node consistently, e.g., one may arrive in 10 ms, another in 100 ms, another in 50 ms, etc. The non-constant rate of received packets (jitter) may be undesirable, e.g., in particular for voice over IP (VoIP) traffic, as will be understood by those skilled in the art.” Vasseur '879, 13:13-36.</p> <p>“As a further example, packet dropping may be monitored for the primary and backup tunnels prior to and after Fast Reroute (respectively). For instance, based on the tunnel label of the dropped packet, primary nodes and/or backup nodes may be able to distinguish which tunnel corresponds to the dropped packets. Each of the primary and/or backup nodes collect packet drop data, and periodically inform the PLR node of the number of dropped packets (e.g., through a corresponding Feedback object 400). In the case of a backup node, the PLR node receiving the notification may interpret the association of the backup tunnel label and the primary tunnel label to reference an appropriate primary tunnel. Those skilled in the art will understand that the above path quality metrics are merely examples, and that any other metrics/statistics useful for determining path quality of the backup tunnel may be used in accordance with the present invention (e.g., delay, bandwidth, etc.). Further, the path quality information may be measured and compared in a variety of manners, such as, e.g., as a difference between primary and backup tunnels before and after Fast Reroute, or simply the difference between the backup tunnel before and after Fast Reroute, etc.” Vasseur '879, 13:37-58.</p> <p>“Also after the primary tunnel is Fast Rerouted, the PLR node may inform the head-end node of the primary tunnel of any configurable difference (e.g., decrease) in path quality associated with utilizing the backup tunnel, i.e., in a path quality notification. For instance, the novel Feedback object 400 may include one or more sub-TLVs 450 corresponding to metrics/statistics, as described above. Notably, the path quality information pertaining to a particular metric/statistic may be transmitted as total values for interpretation by the head-end node (e.g., to determine the difference), or as PLR-node-computed differences (e.g., between the primary and backup tunnels, or before and after Fast Reroute). For instance, if the delay of the primary tunnel (along the protected segment) prior to Fast Reroute were 2 ms, and after Fast Reroute the delay of the backup tunnel were 5 ms, the notification may be configured to</p>

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		<p>include both values 2 ms and 5 ms, or instead simply the difference, i.e., an increase of 3 ms.” Vasseur '879, 13:59-14:8.</p> <p>“In accordance with yet another aspect of the present invention, upon receiving the error message 300 (PathErr), the head-end node may wait for at least one path quality notification (i.e., if requested) prior to determining whether to reestablish the new primary tunnel. The determination may be made based on configurable boundaries, increases, combinations, etc., of the metrics. Also, any number of metrics may be used in the determination, e.g., as configured by a system administrator. For example, using the metrics described above, a head-end node may be configured to reestablish the primary tunnel in response to i) a certain number of packet drops, ii) a percent increase in packet drops, iii) a number of packet drops and a percent increase in path cost, iv) a percent increase in path cost and a percent increase in jitter, etc. Those skilled in the art will understand that these are merely examples of possible path quality comparisons and reestablishment determinations, and that any comparisons to any metrics at any configurable changes may be used in accordance with the present invention.” Vasseur '879, 14:41-59.</p> <p>“If the metrics are acceptable, the backup tunnel remains utilized and no primary tunnel reestablishment is performed. On the other hand, if the metrics are unacceptable, the head-end node may attempt to reestablish the new primary tunnel. FIG. 6 is a schematic block diagram of the computer network 100 in FIG. 5 showing an unacceptable backup tunnel path quality (dotted line and arrow) and resultant reestablishment of the primary tunnel in accordance with the present invention. Those skilled in the art will understand that the reestablished primary tunnel may traverse one or more primary nodes (not shown), and that it may be computed specifically to avoid the failed network element and any network elements of the unacceptable backup tunnel. Those skilled in the art will also understand that the attempt to reestablish the primary tunnel may not be able to find an acceptable path, in which case the head-end node may continue to use the unacceptable backup tunnel or other unacceptable rerouted path.” Vasseur '879, 14:60-15:9.</p>

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		<p>“FIGS. 7A and 7B are flowcharts illustrating a procedure for dynamically determining whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in accordance with the present invention. The procedure 700 starts at step 705, and continues to step 710, where a head-end node (e.g., router A) establishes a primary tunnel (e.g., T1) to a destination tail-end node (e.g., router E). During or after establishment, the head-end node requests protection of the primary tunnel at step 715, and in response, PLR nodes along the primary tunnel (e.g., router B) create backup tunnels (e.g., BT1) to protect the primary tunnel in step 720. (Those skilled in the art will understand that backup tunnels around the protected network elements may already exist at the PLR node, and that “creating backup tunnels” in step 720 implies an association with pre-existing backup tunnels.) As mentioned above, these backup tunnels may illustratively be embodied as 0-BW backup tunnels. In accordance with the present invention, the head-end node may additionally request backup tunnel path quality notification from the PLR nodes in step 725, such as, e.g., through the use of empty corresponding Feedback objects in RSVP (Path) messages 300, as described above.” Vasseur '879, 15:37-58.</p> <p>“The procedure 700 continues to FIG. 7B (step “A”), where in step 750 the PLR node detecting the failure diverts (“Fast Reroutes”) the primary tunnel traffic to the backup tunnel and sends an error message (e.g., an RSVP (Error) message 300) to the head end node, e.g., a “tunnel locally repaired” message. The detecting PLR node continues to collect path quality information for the backup tunnel in step 755 and at step 760 determines whether to send the path quality notification to the head-end node. For example, as mentioned above, the PLR node may be configured to continually send notifications, or periodically, or in response to a configurable change in path quality, etc. Also as mentioned above, the PLR node may be configured to send either the actual path quality information or the change (difference) in path quality. (As further mentioned above, metrics of the backup tunnel may alternatively be collected only after Fast Reroute, and not prior to Fast Reroute.).” Vasseur '879, 16:4-20.</p> <p>“If the PLR node decides to send the notification in step 760, then the head-end node determines whether to reestablish the primary tunnel based on the backup tunnel path quality notification in step 765, e.g., based on one or more configurable thresholds, percentages, etc., as described above. If the backup tunnel is currently maintaining an acceptable quality for the</p>

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		<p>traffic flow in step 770, the head-end node may continue to utilize the backup tunnel, and the PLR node continues to collect path quality information in step 755 to detect any change in quality. Otherwise, if the backup tunnel quality is not acceptable in step 770, the head-end node may attempt to reestablish the primary tunnel in step 775. Notably, as mentioned above, if the PLR node has not sent any notification (step 760) within a configurable period of time in step 780, e.g., due to a backup tunnel failure, over-congestion, etc., then the head-end node may also attempt to reestablish the primary tunnel in step 775 accordingly. Moreover, as described above, in the event more than one primary tunnel is Fast Rerouted for the head-end node, various techniques to reestablish one or more of the primary tunnels may be used (e.g., as many tunnels as necessary, a configurable subset of tunnels, all tunnels, the congested tunnels, etc.). The procedure 700 ends in step 785.” Vasseur '879, 16:21-43.</p> <p>“While there has been shown and described an illustrative embodiment that dynamically determines whether to reestablish a Fast Rerouted primary tunnel based on path quality feedback of a utilized backup tunnel in a computer network, it is to be understood that various other adaptations and modifications may be made within the spirit and scope of the present invention. For example, the invention has been shown and described herein using “Fast Reroute,” e.g., MPLS TE Fast Reroute (FRR). However, the invention in its broader sense is not so limited, and may, in fact, be used with other network element protection and failure correction mechanisms as will be understood by those skilled in the art. Moreover, while the above description describes performing the technique at the head-end node and PLR node, the invention may also be advantageously used with PCEs. In addition, while one example of a tunnel is an MPLS TE-LSP, other known tunneling methods include, inter alia, the Layer Two Tunnel Protocol (L2TP), the Point-to-Point Tunneling Protocol (PPTP), and IP tunnels.” Vasseur '879, 16:63-17:13.</p> <p>Rustogi discloses:</p> <p>“An example method includes identifying a fault condition in a network, and evaluating pseudowires affected by the fault condition in order to make a determination as to whether an aggregate failure occurred in the network for a group of pseudowires. The method also includes communicating a group message indicating that the group of pseudowires is associated with the fault condition. The group message includes a group identification (ID),</p>

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		<p>which identifies the group of pseudowires, and the group message includes a pseudowire group label identifying an in-band aggregate channel. More specifically, the pseudowire group label can be applicable to static pseudowires. In more detailed embodiments, the group ID identifies the group of pseudowires that are associated with an attachment circuit, a label switched path, or a port. Internal mappings can be maintained such that a plurality of pseudowires is mapped to individual interfaces of network elements in the network.” Rustogi, Abstract.</p>  <p>FIG. 1A illustrates a network architecture. It shows three Customer Edge nodes (12, 14, 16) connected to a network via terminating PE nodes (20, 22, 24). These nodes are connected to a switching PE node (30, 32). The switching PE nodes contain PW Group Modules (54a, 54b), Processors (56a, 56b), and Memory Elements (58a, 58b). Arrows indicate the flow of static and dynamic pseudowires (PW) between the nodes. The diagram also shows internal components of the PW Group Modules, including a central processing unit and memory elements.</p> <p>Rustogi, FIG. 1A.</p>

No.	'821 Patent Claim 20	The Reference
		<pre> graph TD START([START]) --> 100[100 ~ A GIVEN NETWORK ELEMENT CAN IDENTIFY A FAULT CONDITION IT RECEIVES] 100 --> 102[102 ~ THE NETWORK ELEMENT EVALUATES PSEUDOWIRES IN ORDER TO DETERMINE WHETHER A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED] 102 --> 104[104 ~ IF ONLY A FEW PSEUDOWIRES ARE AFFECTED BY THE FAULT CONDITION, THE GROUPING PROTOCOL MAY OPTIONALY NOT BE USED, WHERE A MORE ROUTINE MESSAGING PROTOCOL COULD BE EMPLOYED] 104 --> 106[106 ~ IF A SUFFICIENT NUMBER OF PSEUDOWIRES HAVE BEEN AFFECTED, THE GROUPING PROTOCOL IS EMPLOYED TO MINIMIZE THE MESSAGES THAT ARE SENT, RECEIVED, AND PROCESSED IN THE NETWORK] 106 --> 108[108 ~ IN THE CASE OF AN AGGREGATE FAILURE, AN AGGREGATE CHANNEL CAN BE USED TO OFFER APPROPRIATE GROUP MESSAGING. THE INDIVIDUAL MESSAGES THAT CONVEY GROUP IDENTIFICATIONS (IDS) CAN QUICKLY SIGNIFY THE FAULT CONDITION TO NETWORK PEERS] 108 --> END([END]) </pre> <p>FIG. 1B</p> <p>Rustogi, FIG. 1B.</p>

No.	'821 Patent Claim 20	The Reference
		<p>FIG. 2</p> <p>Rustogi, FIG. 2.</p>

No.	'821 Patent Claim 20	The Reference
		<p>The diagram illustrates a network topology with three PE nodes (PE1, PE2, PE3) connected via switching nodes (SW1, SW2) and terminating nodes (TERMINATING PE1, TERMINATING PE2, TERMINATING PE3). The connections are labeled with pseudowire group labels and group identifications:</p> <ul style="list-style-type: none"> SW1 connects PE1 to PE2 and PE1 to PE3. SW2 connects PE2 to PE3. TERMINATING PE1 has interfaces A and B. It receives traffic from PE1 and sends traffic to SW1 via interfaces A and B. A callout indicates "INTERFACE A AND B FAIL". TERMINATING PE2 has interface C. It receives traffic from SW1 via GROUP IDENTIFICATION C and sends traffic to PE2 via interface C. A callout indicates "GROUP ID A → C, D" and "GROUP ID B → E". TERMINATING PE3 has interface D. It receives traffic from SW2 via GROUP IDENTIFICATION D and sends traffic to PE3 via interface D. A callout indicates "PSEUDOWIRE GROUP LABEL C → 3". PE1 has interface 62. It receives traffic from TERMINATING PE1 via GROUP IDENTIFICATION A and sends traffic to SW1 via pseudowire group label A. A callout indicates "PSEUDOWIRE GROUP LABEL A → 2". PE1 also has interface 68. It receives traffic from SW1 via pseudowire group label B and sends traffic to SW2 via pseudowire group label B. A callout indicates "PSEUDOWIRE GROUP LABEL B → 2". PE1 has interface 70. It receives traffic from SW2 via pseudowire group label E and sends traffic to TERMINATING PE3 via pseudowire group label E. A callout indicates "PSEUDOWIRE GROUP LABEL E → 4". PE2 has interface 64. It receives traffic from SW1 via pseudowire group label C and sends traffic to TERMINATING PE2 via interface C. A callout indicates "PSEUDOWIRE GROUP LABEL C → 3". PE3 has interface 66. It receives traffic from SW2 via pseudowire group label D and sends traffic to TERMINATING PE3 via interface D. A callout indicates "PSEUDOWIRE GROUP LABEL D → 4".

Rustogi, FIG. 3.

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		<p>FIG. 4</p> <p>76</p> <p>Rustogi, FIG. 4.</p>

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		<p>FIG. 5</p> <p>80</p> <p>Rustogi, FIG. 5.</p>

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		<p style="text-align: center;">74</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>AC PORT</th><th>LSP</th><th>PW GROUP</th><th>PW GROUP LABEL IDENTIFICATION</th></tr> </thead> <tbody> <tr> <td>INTERFACE A</td><td>LSP 1</td><td>A</td><td>A</td></tr> <tr> <td>INTERFACE B</td><td>LSP 1</td><td>B</td><td>B</td></tr> <tr> <td>REMOTE INTERFACE C ON TERMINATING PE 2</td><td>LSP 2</td><td>F</td><td>F</td></tr> <tr> <td>REMOTE INTERFACE D ON TERMINATING PE 3</td><td>LSP 2</td><td>G, H</td><td>G, H</td></tr> </tbody> </table> <p style="text-align: center;">FIG. 6</p> <p>Rustogi, FIG. 6.</p> <p>“The field of communications has become increasingly important in today's society. In particular, the ability to quickly and to effectively provision connections presents a significant challenge to component manufacturers, system designers, and network operators. Multiprotocol Label Switching (MPLS) is a mechanism in telecommunications networks that carries data from one network node to the next. Layer 2 services (such as Frame Relay, Asynchronous Transfer Mode, and Ethernet) can be emulated over an MPLS backbone by encapsulating the Layer 2 Protocol Data Units (PDUs) and transmitting them over pseudowires. Protocols exist for establishing and maintaining the pseudowires. Certain issues have arisen in pseudowire scenarios, where faults are detected in the network.” Rustogi, ¶ [0002].</p> <p>“FIG. 1A is a simplified block diagram of a communication system for providing pseudowire group labels in a network environment in accordance with one embodiment of the present disclosure.” Rustogi, ¶ [0004].</p>	AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION	INTERFACE A	LSP 1	A	A	INTERFACE B	LSP 1	B	B	REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F	REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H
AC PORT	LSP	PW GROUP	PW GROUP LABEL IDENTIFICATION																			
INTERFACE A	LSP 1	A	A																			
INTERFACE B	LSP 1	B	B																			
REMOTE INTERFACE C ON TERMINATING PE 2	LSP 2	F	F																			
REMOTE INTERFACE D ON TERMINATING PE 3	LSP 2	G, H	G, H																			

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		<p>“FIG. 1B is a simplified flowchart depicting one possible, generic operational flow associated with the communication system.” Rustogi, ¶ [0005].</p> <p>“FIG. 2 is a simplified block diagram of an example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0006].</p> <p>“FIG. 3 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0007].</p> <p>“FIG. 4 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0008].</p> <p>“FIG. 5 is a simplified block diagram of another example group labeling operation in accordance with one embodiment.” Rustogi, ¶ [0009].</p> <p>“FIG. 6 is a simplified table of an example set of pseudowire group provisioning parameters in accordance with one embodiment.” Rustogi, ¶ [0010].</p> <p>“FIG. 1A is a simplified block diagram of a communication system 10 for providing pseudowire group labels in accordance with one example of the present disclosure. FIG. 1A includes a customer edge 1 (CE1) 12, a CE2 14, and a CE3 16, where a number of faults 18 are shown as propagating in the network. Typically, when an error or a failure occurs in the network (e.g., an interface failure, a pulled cable, a switch failure, hardware/software failures generally, etc.), messages are sent to various network devices in order to inform them of these fault conditions. Faults 18 of FIG. 1A are indicative of such messages, where the underlying fault condition (being signaled by the messages) can occur virtually anywhere in a network (e.g., in a customer edge, in provider equipment, etc.). FIG. 1A also includes a terminating provider equipment 1 (TPE1) 20, a TPE2 22, a TPE3 24, a switching provider edge 1 (SPE1) 30, and a SPE2 32. In one particular example implementation, the TPEs and SPEs of FIG. 1A are switches that are configured to exchange data in a network environment.” Rustogi, ¶ [0012].</p>

No.	'821 Patent Claim 20	The Reference
		<p>“SPE1 30 may include a pseudowire (PW) group module 54 a, a processor 56 a, and a memory element 58 a. In a similar fashion, TPE2 22 may include a pseudowire group module 54 b, a processor 56 b, and a memory element 58 b. FIG. 1A also includes a number of static pseudowires 42, 44, and 46. A set of static/dynamic pseudowires 48, 50 is also provided. Note that the group labeling protocol discussed herein can be executed between individual SPEs, TPEs, or between any combinations of these elements.” Rustogi, ¶ [0013].</p> <p>“In one particular arrangement, communication system 10 is provided in conjunction with a Layer-2 virtual private networks (L2VPN)/operation, administration, and maintenance (OAM) L2VPN/OAM framework. The OAM framework is intended to provide OAM layering across L2VPN services, pseudowires, and packet switched network (PSN) tunnels. Communication system 10 may include any suitable networking protocol or arrangement that provides a communicative platform for communication system 10. Thus, communication system 10 may include a configuration capable of transmission control protocol/internet protocol (TCP/IP) communications for the transmission and/or reception of packets in a network. Communication system 10 may also operate in conjunction with a user datagram protocol/IP (UDP/IP) or any other suitable protocol where appropriate and based on particular needs.” Rustogi, ¶ [0014].</p> <p>“Failure detection and failure notification for static pseudowires is inadequate, where sluggish signaling can result in poor scalability for failover performance. Typically, static pseudowires are manually configured at respective endpoints, where control channels are absent for providing group level signaling messages. Aggregate channels are significant tools for providing suitable scalability in the network, but no such aggregate channel exists for static pseudowires. For dynamic pseudowires, such an aggregate channel may be present in the form of a label distribution protocol (LDP) directed session. However, no such protocol exists for static pseudowire configurations such that an in-band aggregate channel would be available for static pseudowires.” Rustogi, ¶ [0016].</p>

No.	'821 Patent Claim 20	The Reference
		<p>“Communication system 10 can address the aforementioned issues (and others) by offering a pseudowire group label that can represent an aggregate channel for groups of static pseudowires. The aggregate channel of communication system 10 can allow for improved scalability of failover performance. In accordance with one potential configuration of communication system 10, a pseudowire group label is representative of a group of static pseudowires transported over a label switched path (LSP). The pseudowire group label can identify the aggregate channel, which captures the hierarchy relevant to OAM mechanisms. Additionally, the groups represented by the group identification (ID) can be mutually exclusive, where a pseudowire is part of only one group. In other embodiments, a pseudowire can be part of multiple groups, or be configured in any other suitable manner based on particular network arrangements.” Rustogi, ¶ [0017].</p> <p>“During operations, and with brief reference to FIG. 1B, a given network element can identify a fault condition it receives (at step 100) and, subsequently, evaluate pseudowires in order to determine whether a sufficient number of pseudowires have been affected. This is reflected by step 102. If only a few pseudowires are affected by the fault condition, the grouping protocol outlined herein may have only nominal value, where there could be an option to simply communicate the fault condition in a more routine manner, as outlined in step 104. However, if a sufficient number of pseudowires have been affected, the grouping protocol outlined herein can be employed to minimize the messages that are sent, received, and processed in the network. This is reflected as step 106. Note that the determination (as to whether a sufficient number of pseudowires have been impacted by the fault condition) can involve accessing internal tables such that a quick mapping can occur to determine if an aggregate failure has occurred. As used herein, the term ‘aggregate failure’ simply connotes that a sufficient number of pseudowires have experienced the fault condition such that an aggregate channel can be employed to offer appropriate group messaging. For the aggregate failure condition, the individual messages that convey Group identifications (IDs) can quickly signify the fault condition to network peers, as shown in step 108.” Rustogi, ¶ [0018].</p>

No.	'821 Patent Claim 20	The Reference
		<p>“In specific regards to OAM mechanisms, OAM messages typically result from common failures in the network. These fault conditions can be aggregated such that they are signaled as a single message, which could represent a group of failed pseudowires (as opposed to sending individual messages for each failed pseudowire). Hence, a single message could be sent to represent all the relevant OAM messages propagating in communication system 10. The group label that propagates in communication system 10 provides an architecture with a significant level of aggregation for failed pseudowires (i.e., pseudowires being affected by a given fault condition), particularly for OAM messaging. Moreover, the in-band aggregate channel of communication system 10 is based (at least in part) on the evolving trends of OAM mechanisms, which are required to be fast, responsive, and capable of being implemented in hardware or software. Additionally, in-band OAM protocols are a better measure of the path availability.” Rustogi, ¶ [0020].</p> <p>“In operation of one example implementation, a group label can represent the tuple <attachment circuit (AC) port level grouping, LSP>. This could signify that all pseudowires on an AC port (sought for aggregation) traverse a given LSP. Multiple pseudowire groups can exist within an LSP. Similarly, pseudowires on the same AC port (that traverse a different LSP) can use a different pseudowire group label. Alternatively, an administrator may seek to employ a one-to-one mapping between an LSP and a group label. If that were the case, then only one pseudowire group would exist within an LSP. In scenarios where there is no LSP label in the packet (e.g., due to penultimate hop popping), the pseudowire group label can provide the hierarchy that is appropriate.” Rustogi, ¶ [0021].</p> <p>“In one particular example, the group level pseudowire OAM message can be sent with the following label stack: Explicit/Implicit LSP Label+pseudowire group Label+GAL+ACH+pseudowire OAM with grouping TLV (where GAL=Generic Associated Channel Label, ACH=Associated Channel Header, TLV=Type-Length-Value). If there are multiple LSPs, then one group label can be provisioned for each LSP (for each pseudowire group), where per group messages can be sent on each LSP. The group label does not necessarily have a one-to-one mapping to the grouping of pseudowires implied by the Group ID in the grouping TLV. Note also that the group-based aggregate channel is applicable to</p>

No.	'821 Patent Claim 20	The Reference
		<p>static pseudowires, as well as for dynamic pseudowires in certain applications.” Rustogi, ¶ [0022].</p> <p>“As discussed herein, the aggregate channel of communication system 10 can be configured in various ways. For example, and with regards to a first option, a separate label may simply be used to identify a pseudowire group within an LSP. The association of an OAM message and a pseudowire group is straightforward. There could potentially be multiple pseudowire group labels per LSP. As a second option, one group label can be used to identify a common pseudowire group channel on the LSP. In this implementation, one pseudowire group label is provided per LSP. The OAM message association to a pseudowire group is not as simple as the first option. As a third option, one pseudowire is simply designated to convey grouping information (e.g., without using a group label). In this case, there is no need for a pseudowire group label. Again, the OAM message association to a pseudowire group is not as simple as the first option.” Rustogi, ¶ [0023].</p> <p>“Any combination of formatting (for the Group ID and the pseudowire group label) can be used in the group message to be communicated in the network. In one example, only one of these elements is communicated when an aggregate fault condition is detected, or these elements can be combined into a single unique identifier. In the most generic example, a group message would at least include the Group ID (identifying the pseudowires affected by the fault) and a pseudowire group label (identifying an aggregate channel for communicating the group message). In this generic sense, a pipe (the Group ID) within a pipe (the pseudowire group label) is being identified, where the group message is identifying both elements during an aggregate fault condition. Operational details of communication system 10 are described below with reference to FIGS. 2-6. Note that before turning to additional example flows and example embodiments of the present disclosure, a brief overview of the infrastructure of communication system 10 is provided.” Rustogi, ¶ [0024].</p> <p>“CE1 12, CE2 14, and CE3 16 represent devices, infrastructure, equipment, clients, or customers seeking to initiate a data session in communication system 10. These elements may comprise a digital subscriber line access multiplexer (DSLAM), a router, a personal computer, a server, a switch, and/or other devices associated with data propagation. Further,</p>

No.	'821 Patent Claim 20	The Reference
		<p>these elements may sit behind, or in front of, one or more of these identified devices. The term ‘CE’ may be inclusive of the devices identified above (e.g., a DSLAM, a switch, etc.), as well as devices used to initiate a communication, such as a console, a proprietary endpoint, a telephone, a cellular telephone, a bridge, a computer, a personal digital assistant (PDA), a laptop or an electronic notebook, or any other device, component, element, or object capable of initiating voice, audio, media, or data exchanges within communication system 10. The customer element may also include any device that seeks to initiate a communication on behalf of another entity or element, such as a program, a database, or any other component, device, element, or object capable of initiating a voice, a video, text, or a data exchange within communication system 10. Data, as used herein in this document, refers to any type of video, numeric, voice, media, or script data, or any type of source or object code, or any other suitable information in any appropriate format that may be communicated from one point to another.” Rustogi, ¶ [0025].</p> <p>“SPE1 30, SPE2 32, TPE1 20, TPE2 22, and TPE3 24 are network elements that facilitate communications in two directions in a network environment. In one particular example, each of these network elements is a switch configured to exchange data over static and/or dynamic pseudowire links. Further, the traffic exchanged between these components may be directed over an MPLS transport in certain embodiments. As used herein in this Specification, the term ‘network element’ is meant to encompass switches, routers, bridges, gateways, servers, processors, loadbalancers, firewalls, or any other suitable device, component, element, or object operable to exchange or process information in a network environment. Moreover, these network elements may include any suitable hardware, software, components, modules, interfaces, or objects that facilitate the operations thereof. This may be inclusive of appropriate algorithms and communication protocols that allow for the effective exchange of data or information. Along similar design alternatives, any of the internal modules and components of these network elements may be combined in various possible configurations.” Rustogi, ¶ [0029].</p>

No.	'821 Patent Claim 20	The Reference
		<p>“Turning to FIG. 2, FIG. 2 is a simplified block diagram of an example system 60 for providing an example use case using per-label switched path (LSP) pseudowire group labels. FIG. 2 includes a TPE1 62, a TPE2 64, a TPE3 66, a SPE1 68, and a SPE2 70. Each pseudowire group is identified, where a group identification (ID) for Group A and Group B is depicted at TPE1 62. Similarly, Groups C, D, and E have Group IDs at SPE1 68. TPE2 64 and TPE3 66 can couple to interfaces C and D, respectively.” Rustogi, ¶ [0031].</p> <p>“In this particular example, interfaces A and B have failed. Note that there is a multitude of attachment circuits (e.g., 1000 attachment circuits) that are being transported over these interfaces A and B, where the attachment circuits are being tunneled into a corresponding number of pseudowires. For example, there could be 500 attachment circuits on interface A (implicating 500 pseudowires) and 500 attachment circuits on interface B, where the fault condition for the pseudowires should be signaled. In other flawed systems, an architecture would individually signal this fault condition for each pseudowire (e.g., via signaling between TPE1 62 and SPE1 68). Instead of sending 500 messages, a single message can be sent, where a single label (and Group ID) can be used to identify the pseudowires. In this case, the Group ID A is used to signal the fault condition for 300 pseudowires and for 200 pseudowires (i.e., the top two links connecting TPE1 62 and SPE1 68) using a single message (that includes Group Label A and Group ID A). Thus, the status for Group A is quickly communicated to SPE1 68. Similarly, Group ID B can be used to signal the status of the other 500 pseudowires to appropriately convey the status for Group B. More specifically, the message can include Group Label A and Group ID B. Note that all 1000 pseudowires have effectively been accounted for using these Group IDs A and B.” Rustogi, ¶ [0032].</p> <p>“FIG. 3 is a simplified block diagram of an example system 72 for providing another use case for pseudowire group labels. Note that the grouping mechanism outlined herein is not limited to pseudowires that propagate over LSPs. Certain pseudowires can propagate over an LSP and represent one group, where two ports can be provisioned for two different groups (e.g., Group A and Group B). Hence, FIG. 3 is depicting a use case using pseudowire group labels for <port, LSP>mapping. In a general sense, such a configuration is showing how pseudowire mechanics can be used to offer different group signaling, which may be based on various possible implementations. Thus, there is a group level construct corresponding to the group</p>

No.	'821 Patent Claim 20	The Reference
		<p>labels that are created such that any OAM protocol can send the appropriate aggregate messages. In this particular example, the signaling for Group ID A, B, C, and D is similar to that of FIG. 2; however, the grouping mechanism has simply changed.” Rustogi, ¶ [0035].</p> <p>“FIG. 4 is a simplified block diagram of an example system 76 for providing another use case for pseudowire group labels. In this particular example, interface C fails (as shown at TPE2 64). Note that the same logical flow occurs in FIG. 4 in terms of the group signaling, as previously discussed. The group labels in two directions do not have to be the same, where the groupings for the messaging are not necessarily symmetrical. In this particular example, TPE2 64 sends a status for Group E with the corresponding group label (i.e., Group ID E for 300 pseudowires), where that message will have a Group Label E and a Group ID E. Hence, this particular signaling is indicative of 300 pseudowires failing in the network. SPE1 68 can send the status for Group F (where the Group ID F is associated with 300 pseudowires) to TPE1 62, where that message includes a Group Label F and a Group ID F.” Rustogi, ¶ [0036].</p> <p>“FIG. 5 is a simplified block diagram of an example system 80 for providing another use case for pseudowire group labels. In this particular example, interface D fails (as shown at TPE3 66), where all 700 pseudowires fail. In one implementation, TPE3 66 does not have a 700 pseudowire Group ID. Instead, the Group IDs can correspond to 200 and 500 pseudowires, when summed together account for the 700 pseudowires. In this particular example, TPE3 66 sends one message for Group I (representing 200 pseudowires) and another message for Group J (representing 500 pseudowires) to SPE1 68. In response, SPE1 68 sends a message for Group G (representing 200 pseudowires) and another message for Group H (representing 500 pseudowires). Again, the signaling being exchanged between these elements is minimal due to the effective grouping of pseudowires. SPE1 68 also sends a single message for Group I (associated with 200 pseudowires) and Group J (associated with 500 pseudowires) to TPE3 66, which is coupled to interface D. Group ID G is associated with 200 pseudowires, whereas Group ID H is representative of 500 pseudowires.” Rustogi, ¶ [0037].</p>

No.	'821 Patent Claim 20	The Reference
		<p>“FIG. 6 is a simplified table 74 illustrating an example set of pseudowire group provisioning parameters for TPE1 62, where these particular provisioning parameters could be relevant to the configuration of FIG. 3. At least in one generic sense, FIG. 2 can reflect one approach for mapping a PW group label to a PW Group ID, while FIGS. 3-5 can reflect a second approach for such mappings, where table 74 is associated with that second approach.” Rustogi, ¶ [0038].</p> <p>“In particular, table 74 illustrates the mapping between SPE1 68 and TPE1 62. The first column represents the attachment circuit port (e.g., interface A, interface B, remote interface C on TPE2 64, and remote interface D on TPE3 66). Additionally, table 74 depicts a number of LSPs, a set of pseudowire grouping labels, and a set of pseudowire Group IDs. Note that the Group IDs are provided inside the pseudowire group labels in this example such that these two columns match in table 74. Additionally, note that table 74 is merely representing some of the possible characteristics in a single direction, where different constructs could be used in the reverse direction. Note that the provisioning as discussed herein can significantly reduce messaging such that these presented concepts offer increased scalability. This is due in part to the nominal processing that occurs in the network, in contrast to the processing required to evaluate a prolific amount of signaling messages associated with particular pseudowires. Additionally, the paradigm discussed herein can afford service providers an adequate amount of downtime after a failure has occurred in the network.” Rustogi, ¶ [0039].</p>

EXHIBIT E-3
Defendant's First Amended Invalidity Contentions
Orckit Corporation v. Cisco Systems, Inc., 2:22-cv-00276-JRG-RSP

Chart for U.S. Patent 7,545,740 (“the ’740 Patent”)
35 U.S.C. §103

In this chart, “Reference” refers to each of the following references:

- U.S. Patent Publication No. 2006/0221974 to Hilla et al. (“Hilla”)
- U.S. Patent Publication No. 2003/0147387 to Devi et al. (“Devi”)
- Cisco EtherChannel Implementation In Cisco Products (“Cisco EtherChannel System”)
- IEEE Standard 802.3, 2002 (“IEEE 802.3”)
- Cisco EtherSwitch System (“Cisco EtherSwitch System”)
- U.S. Patent Publication No. 2004/0228278 to Bruckman et al. (“Bruckman”)
- U.S. Patent Publication No. 2003/0210688 to Basso et al. (“Basso”)
- U.S. Patent Publication No. 2006/0039366 to Ghosh et al. (“Ghosh”)
- U.S. Patent Publication No. 2004/0042448 to Lebizay et al. (“Lebizay”)
- U.S. Patent No. 6,081,530 to Wiher et al. (“Wiher ’530”)

The following additional references are identified individually:

- U.S. Patent Publication No. 2004/0037278 to Wong et al. (“Wong”)
- U.S. Patent Publication No. 2004/0208197 to Viswanathan (“Viswanathan”)
- U.S. Patent Publication No. 2006/0251074 to Solomon (“Solomon”)
- U.S. Patent Publication No. 7,221,652 to Singh et al. (“Singh”)
- U.S. Patent No. 8,990,430 to Smith et al. (“Smith ’430”)
- U.S. Patent No. 8,526,427 to Smith et al. (“Smith ’427”)
- U.S. Patent Publication No. 2006/0039384 to Dontu et al. (“Dontu”)
- U.S. Patent No. 6,553,029 to Alexander (“Alexander ’029”)
- U.S. Patent No. 6,473,424 to DeJager et al. (“DeJager ’424”)
- U.S. Patent No. 7,554,914 to Li et al. (“Li ’914”)

- U.S. Patent No. 8,155,125 to Borgione et al. (“Borgione ’125”)

As shown in the chart below, all Asserted Claims of the ’740 Patent are invalid under 35 U.S.C. § 103 because The Reference renders those claims obvious either alone, or in combination with the knowledge of a person having ordinary skill in the art, and in further combination with the references specifically identified below and in the following claim chart and/or one or more references identified in Defendant’s First Amended Invalidity Contentions.

Motivations to combine include at least the similarity in subject matter between the references to the extent they concern methods of data communication systems, and specifically to methods and systems for link aggregation in a data communication network. Insofar as the references cite other patents or publications, or suggest additional changes, one of ordinary skill in the art would look beyond a single reference to other references in the field.

These invalidity contentions are based on Defendant’s present understanding of the Asserted Claims, and Orckit’s apparent construction of the claims in its November 3, 2022 Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1, and Orckit’s January 19, 2023 First Amended Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1 (Orckit’s “Infringement Disclosures”), which is deficient at least insofar as it fails to cite any documents or identify accused structures, acts, or materials in the Accused Products with particularity. Defendant does not agree with Orckit’s application of the claims, or that the claims satisfy the requirements of 35 U.S.C. § 112. Defendant’s contentions herein are not, and should in no way be seen as, admissions or adoptions as to any particular claim scope or construction, or as any admission that any particular element is met by any accused product in any particular way. Defendant objects to any attempt to imply claim construction from this chart. Defendant’s prior art invalidity contentions are made in a variety of alternatives and do not represent Defendant’s agreement or view as to the meaning, definiteness, written description support for, or enablement of any claim contained therein.

The following contentions are subject to revision and amendment pursuant to Federal Rule of Civil Procedure 26(e), the Local Rules, and the Orders of record in this matter subject to further investigation and discovery regarding the prior art and the Court’s construction of the claims at issue.

No.	'740 Patent Claim 1	The Reference
1[preamble]	A method for communication, comprising:	<p>The Reference discloses a method for communication.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
1[a]	coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel,	<p>The Reference discloses coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Abstract ("Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the</p>

No.	'740 Patent Claim 1	The Reference
		<p>present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including I/OBase-T, I/OBase-T, and Gigabit Ethernet, and other net-work environments.”)</p> <p>DeJager '424 at 2:24-39 (“While static address distribution improves the efficiency of data transmission over a port group by distributing packet streams among the various ports of a port group, it does not account for the amount of traffic volume of different streams. Accordingly, static address distribution evenly (and thus most efficiently) distributes traffic over the ports of a port group of a switch only if there is the same amount of data being forwarded in each stream. If a given stream is trans-mitting much more than the average amount of data for streams being forwarded through the port group, then there may be inefficiencies in the data transmission in a static address distribution system. For example, this situation may result in there being a long queue for the port to which the heavily loaded stream is assigned, while other ports in the group, which are assigned to more lightly loaded streams, are available to transmit data.”)</p> <p>DeJager '424 at 1:10-28 (“A common computer network implementation includes a plurality of clients, such as personal computers or work stations, connected to each other and one or more servers via a switch or router by network cable. In the present application, the term “switch” is intended to mean any network device that forwards packets from a source to a destination, rather than broadcasting them (i.e., includes router, but excludes repeater). The network is configured to operate at one or more data transmission rates, typically 10 Mbit/sec (e.g., I/OBase-T Ethernet), or 100 Mbit/sec (e.g., I/OBase-T Fast Ethernet). More recently, Gigabit data trans-mission rates have become attainable. Data is forwarded on the network in packets which are typically received by a switch from a source network device and then</p>

No.	'740 Patent Claim 1	The Reference
		<p>directed to the appropriate destination device. The receipt and transmission of data packets by a switch occurs via ports on the switch. Packets travelling from the same source to the same destination are defined as members of the same stream.”)</p> <p>DeJager '424 at 2:47-65 (“The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 3:16-39 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of</p>

No.	'740 Patent Claim 1	The Reference
		<p>queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>DeJager '424 at 9:43-55 (“At intervals of time (load balancing time intervals) the time mark registers 408 and 410, respectively, are consulted to determine whether or not a stream may change queues (dynamic load balancing), for example as described previously. Each port in the switch's port group 416 preferably has a pair of queue mark and queue mark indicate registers associated with it in order to assist in determining that a port's queue is current and to determine whether the alter-nate time mark register should be cleared and the time mark registers switched, for example as described above. Each packet exits the switch through its assigned port in the port group 416 along one of outbound links 422 to its corresponding destination.”)</p> <p>DeJager '424 at Figure 1</p>

No.	'740 Patent Claim 1	The Reference
		<p>The diagram illustrates two network devices, X_2 and X_1. Network device X_2 (labeled 100) contains three components labeled 0, 102, and S_2. Network device X_1 (labeled 106) contains six components labeled 7, 8, 9, 1, 2, 3, 4, 5, 6, and S_1. Aggregated links connect components 0 and 102 in X_2 to components 7, 8, and 9 in X_1. Components 1 through 6 in X_1 are interconnected. Components 1 through 4 in X_1 are connected to components C_1, C_2, C_3, and C_4 respectively, which are further connected to S_1.</p>

FIG. 1

Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device

No.	'740 Patent Claim 1	The Reference
		<p>can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components).”)</p> <p>Dontu at [0034] (“Each network device component includes an inter-face (it is noted that each network device component can include several other interfaces as well). Network device component 110(1) includes interface 120(1), network device component 110(2) includes interface 120(2), and network device component 110(3) includes interface 120(3). Inter-faces 120(1)-120(3) are interfaces of network device 100(1). Network device component 110(4) includes interface 120(4), network device component 110(5) includes interface 120(5), and network device component 110(6) includes interface 120(6). Interfaces 120(4)-120(6) are interfaces of network device 100(2). Each interface 120(1)-120(6) can be a physical interface or logical interface.”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6).”)</p> <p>Dontu at [0104] (“In virtual network device sub-unit 1222(1), line card 1404(1) includes forwarding engine 1414(1) and inter-faces 1420(5), 1420(7), and 1420(9). Interface 1420(7) is coupled to network device 1220(3). Interface 1420(9) is also coupled to network device 1220(1). Interface 1420(5) is unused in this example. Line card 1404(3) includes forward-ing engine 1414(3), interfaces 1420(11) and 1420(13), and port 1420(15). Interfaces 1420(11) and 1420(13) are respec-tively coupled to network devices 1220(2) and 1220(1). Interface 1420(15) is coupled to server 1204(3). In embodi-ments in which network devices 1220(1)-1220(3) are adjunct network devices controlled by virtual network device 1302, interfaces 1420(7), 1420(9), 1420(11), and 1420(13) are operated as uplink interfaces, while interface 1420(15), which is not coupled to an adjunct network device, is operated as a normal port.”)</p> <p>Dontu at [0105] (“In virtual network device sub-unit 1222(2), line card 1404(2) includes forwarding engine 1414(2) and inter-faces 1420(6), 1420(8), and 1420(10). Interface 1420(8)</p>

No.	'740 Patent Claim 1	The Reference
		<p>is coupled to adjunct network device 1220(2), and interfaces 1420(6) and 1420(10) are unconnected. Line card 1404(4) includes forwarding engine 1414(4) and interfaces 1420(12), 1420(14), and 1420(16). Interfaces 1420(12) and 1420(16) are respectively coupled to adjunct network devices 1220(3) and 1220(1). Interface 1420(14) is unused. In embodiments in which network devices 1220(1)-1220(3) are adjunct network devices controlled by virtual network device 1302, interfaces 1420(8), 1420(12), and 1420(16) are operated as uplink interfaces.”)</p> <p>Dontu at Figure 14</p>

FIG. 14

No.	'740 Patent Claim 1	The Reference
		<p>Li '914 at Abstract ("A method and system for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. In one embodiment, a list is created of output ports that are coupled with the adjacent router, and the list is modified based on network traffic. A port is selected from the list of ports, and the packet is transmitted over the selected port. In one example, the list is modified continuously as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, a router can adaptively and evenly distribute the packet transmission traffic over the output ports.")</p> <p>Li '914 at 2:6-21 ("In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances 20 of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.")</p> <p>Li '914 at 2:44-55 ("According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creat-ing a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the</p>

No.	'740 Patent Claim 1	The Reference
		<p>destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 2:62-3:8 (“According to another embodiment, disclosed herein is a router having at least a first and second output port. The router includes at least one queue for the first output port, the at least one queue for storing packets to be transmitted along the first output port; at least one queue for the second output port, the at least one queue for storing packets to be transmitted along the second output port; a rate counter for measuring transmission rates along the first and second output ports; and means for determining upon which output port a packet should be transmitted. The means for determining may include means for creating a list of output ports that are coupled with the adjacent router, the list including the first and second output ports; and means for continuously modifying the list based on network traffic.”)</p> <p>Li '914 at 3:58-4:8 (“According to one broad aspect of the invention, disclosed herein is a system and method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. As will be discussed below, in one embodiment, a list is created of output ports that are coupled with the adjacent router, and the list is modified based on network traffic. A port is selected from the list of ports, and the packet is transmitted over the selected port. In one example, the list is modified continuously as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, by determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, a router can adaptively and evenly distribute the packet transmission traffic over the output ports. Various embodiments of the invention will now be discussed.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 1	The Reference
		<p>FIG. 1</p> <p>Borgione '125 at Abstract (“A method, system, and apparatus to transmit replicated multicast packets over a plurality of physical network links that are combined into one logical channel or link so that the replicated multicast packets are distributed over more than one network link is disclosed. It is further disclosed that distribution over the network links is accomplished, in part, through analyzing the multicast packet for information other than ethernet addresses. Such information can include a tag header including destination interface information.”)</p> <p>Borgione '125 at 3:60-4:2 (“The present invention presents a method, system, and apparatus to transmit replicated multicast packets over a plurality of physical network links that are combined into one logical channel or link so that the replicated multicast packets are</p>

No.	'740 Patent Claim 1	The Reference
		<p>distributed over more than one network link. This is accomplished, in part, through analyzing the multicast packet for information other than Ethernet addresses. Such information can include a tag header including destination interface information (for example, a VLAN identification field in an IEEE Std. 802.1Q packet header tag).”)</p> <p>Borgione '125 at 4:3-9 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 4:14-21 (“Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.”)</p> <p>Borgione '125 at 4:22-30 (“Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast packet thus forming replicated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select</p>

No.	'740 Patent Claim 1	The Reference
		<p>one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at Figure 1</p> <p style="text-align: center;">Figure 1</p>
1[b]	at least one of said first physical links being a bi-directional link operative to	The Reference discloses at least one of said first physical links being a bi-directional link operative to communicate in both an upstream direction and a downstream direction.

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	communicate in both an upstream direction and a downstream direction	To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
1[c]	coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel,	<p>The Reference discloses coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, Dontu, and Smith '427.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable</p>

No.	'740 Patent Claim 1	The Reference
		<p>of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 1	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70). An Ethernet interface for configuration (PCI) is also connected to the management card. Arrows indicate data flow between the line cards, backplane interconnects, and management card.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.")</p> <p>Singh at 5:54-58 ("A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

No.	'740 Patent Claim 1	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 1	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START]) -- 1700 --> TM[TRANSMITTING PACKETS FROM TM] TM -- 1702 --> INGRESS[ACCEPTING PACKET AT PORT CARD INGRESS PORT] INGRESS -- 1704 --> CONNECT[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] CONNECT -- 1706 --> 6TH[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 6TH -- 1706a --> iMS[STORING PACKETS IN iMS] iMS -- 1706b --> BACKPLANE[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] BACKPLANE -- 1706c --> CROSSBAR[SUPPLYING PACKETS TO CROSSBAR] CROSSBAR -- 1708 --> EGRESS[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] EGRESS -- 1710 --> FAULT[SENSING CONNECTION FAULT] FAULT -- 1712 --> RESELECT[RESELECTING CONNECTIONS] </pre>	<p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to</p>

No.	'740 Patent Claim 1	The Reference
		<p>virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p> <p>Further, Cisco innovated and patented the accused multi-chassis link aggregation features before Orckit, which involves a second group of physical links coupling line cards or interface modules of one apparatus to another.</p> <ul style="list-style-type: none"> • Smith '430 • Smith '427 <p>Smith '430 at Figure 3</p>

No.	'740 Patent Claim 1	The Reference
		<p>The diagram illustrates a Virtual Network Device 202 architecture. It consists of two Virtual Network Device Sub-Units: 122(1) and 122(2). Each sub-unit contains a Control Card (320(1) and 320(2)) with a Control Unit (310(1) and 310(2)) and a Forwarding Engine (312(1) and 312(2)). A Virtual Network Device Link (360) connects the two control cards. Each sub-unit also contains multiple Line Cards: 304(1), 304(3), and 304(4). Line Card 304(1) has Forwarding Engines 314(1) and 314(5), and Line Cards 304(3) and 304(4) each have Forwarding Engines 314(3) and 314(4). These line cards connect to external network devices: Server 104(3), Network Device 120(1), Network Device 120(2), and Network Device 120(3). The connections between the line cards and the external devices are grouped into three Virtual Link Bundles: 250(1), 250(2), and 250(3).</p> <p style="text-align: center;">FIG. 3</p> <p style="text-align: center;">Smith '430 at Figures 5A and 5B</p>

No.	'740 Patent Claim 1	The Reference
		<p style="text-align: center;">FIG. 5A</p> <p style="text-align: center;">FIG. 5B</p> <p>Smith '430 at 2:21-43 ('In such embodiments, the first virtual network device sub-unit can be configured to maintain consistent forwarding information with the second virtual network device sub-unit. For example, in one embodiment, the controller (in the first virtual network device sub-unit) is configured to perform control protocol processing for the first interface according to a routing protocol running on the interface bundle. The controller is configured to provide information generated when performing the control protocol processing to a</p>

No.	'740 Patent Claim 1	The Reference
		<p>secondary controller comprised in the second virtual network device sub-unit. The secondary controller is configured to use the information to manage the second interface. One embodiment of a method involves: assigning a first logical identifier to each interface included within an interface bundle, where the interface bundle includes a first interface of a first virtual network device sub-unit and a second interface of second virtual network device sub-unit; coupling a first end of a first link to the first interface, the first link included within a virtual link bundle; and coupling a first end of second link to the second interface, the second link also included within the virtual link bundle. The second end of each of the first link and the second link are coupled to a third network device.”)</p> <p>Smith '430 at 5:10-50 (“Multiple links can be implemented between devices in different network layers to provide additional redundancy. For example, as shown in FIG. 1, each network device 120 (1)-120(n) in access layer 110 can be coupled to distribution layer 112 by two (or more) different links. Similarly, each network device 122(1)-122(n) in distribution layer 112 can be coupled to core layer 114 by two (or more) different links. In one embodiment, each link is an Ethernet link. Within each network layer, multiple redundant network devices can be configured to collectively operate as a single virtual network device. For example, as shown in FIG. 1, two or more network devices in distribution layer 112 can operate as a virtual network device 202. Similarly, two or more of network devices 124(1)-124(n) can operate as a single virtual network device 204, and two or more of network devices 126(1)-126(n) can operate as a single virtual network device 206. More details of how two distribution-layer network devices can collectively operate as a distribution-layer virtual network device 202 are shown in FIGS. 2A, 2B, and 3. Virtual network devices can be coupled to other virtual network devices, to network devices, and/or to clients and/or servers by virtual link bundles, as described below. In general, any multi-ported device (whether a physical device, such as a network device, client, or server, or a virtual network device) can be coupled to a virtual network device by a virtual link bundle that includes several links, some of which terminate on different sub-units within the virtual network device. FIG. 2A shows an example of a network in which there are two network devices 120(1) and 120(2) in access layer 110. There are also two network devices 122(1) and 122(2) in distribution layer 112. These two network devices 122(1) and 122(2) operate as a single</p>

No.	'740 Patent Claim 1	The Reference
		<p>virtual network device 202 in this example. Each network device 120(1)-120(2) is coupled to distribution layer 112 by two links. In this example, each of those two links is coupled to a different one of network devices 122(1) and 122(2). This provides redundancy, allowing network devices 120(1) and 120(2) to continue to communicate with distribution layer 112 even if one of network devices 122(1) or 122(2) fails or if one of the links between a given access-layer network device and a given distribution-layer network device fails.”)</p> <p>Smith '430 at 6:25-45 (“In embodiments, such as the one shown in FIG. 2B, in which network devices 120(1) and 120(2) see themselves as being connected to a single network device, the use of a virtual link bundle is simplified. For example, if network device 120(1) is aware that virtual link bundle 250(1) terminates at two different network devices, network device 120(1) can select a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 120(1) simply views virtual network device 202 as a single entity. When viewing virtual network device 202 as a single entity, for example, network device 120(1) can simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 250(1) fails, there is no need for network device 120(1) to change how Spanning Tree Protocol is applied. Instead, network device 120(1) can simply continue to use the non-failed links within virtual link bundle 250(1).”)</p> <p>Smith '427 at Abstract (“A method may involve: receiving a packet (e.g., via a port or uplink interface in a satellite switch) and conveying the packet between one or more ports and one of several uplink interfaces. The one or more ports and the uplink interface are associated with each other. The association can be independent of VLAN (Virtual Local Area Network). As an example, such a method can involve: receiving a first packet via a first port; conveying the first packet to a distribution-layer via a first uplink interface; receiving a second packet via a second port; and conveying the second packet to the distribution-layer via a second uplink interface, where the first uplink interface is associated with the first port</p>

No.	'740 Patent Claim 1	The Reference
		<p>and the second uplink interface is associated with the second port. In some embodiments, ports and uplink interfaces are associated by being assigned to the same virtual linecard.”)</p> <p>Smith '427 at 2:28-55 (“A method may involve: receiving a packet (e.g., via a port or uplink interface in a satellite switch) and conveying the packet between one or more ports and one of several uplink interfaces. The one or more ports and the uplink interface are associated with each other. The association can be independent of VLAN (Virtual Local Area Network). As an example, in one embodiment, such a method can involve: receiving a first packet via a first port; conveying the first packet to the distribution-layer via a first uplink interface; receiving a second packet via a second port; and conveying the second packet to the distribution-layer via a second uplink interface, where the first uplink interface is associated with the first port and the second uplink interface is associated with the second port. The first port can be associated with the same VLAN as the second port. By communicating packets between only associated ports and uplink interfaces, undesirable bridging loops can be avoided. Several of the ports can be associated with the same uplink interface, and several uplink interfaces can be associated with the same port.</p> <p>In some embodiments, a system includes several ports, several uplink interfaces, and a local target agent configured to convey packets between the ports and uplink interfaces. The local target agent is configured to convey a packet between one of the ports and one of the uplink interfaces. The one of the ports and the one of the uplink interfaces are associated with each other (e.g., by being assigned to the same virtual linecard). Ports associated with different VLANs can be assigned to the same virtual linecard.”)</p> <p>Smith '427 at Figure 2</p>

No.	'740 Patent Claim 1	The Reference
		<p>FIG. 2</p>
1[d]	at least one of said second physical links being a bi-directional link operative to communicate in both an upstream direction and a downstream direction;	<p>The Reference discloses at least one of said second physical links being a bi-directional link operative to communicate in both an upstream direction and a downstream direction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, and Singh.</p>

No.	'740 Patent Claim 1	The Reference
		<p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 1	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70). An Ethernet interface for configuration (PCI) is also connected to the management card (60).</p> <p>FIG. 1</p>

Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)

Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208

No.	'740 Patent Claim 1	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 1	The Reference
		<p>Fig. 17</p> <pre> graph TD START([START]) -- 1700 --> TM[TRANSMITTING PACKETS FROM TM] TM -- 1702 --> INGRESS[ACCEPTING PACKET AT PORT CARD INGRESS PORT] INGRESS -- 1704 --> CONNECT[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] CONNECT -- 1706 --> 6PORTS[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 6PORTS -- 1706a --> iMS[STORING PACKETS IN iMS] iMS -- 1706b --> BACKPLANE[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] BACKPLANE -- 1706c --> CROSSBAR[SUPPLYING PACKETS TO CROSSBAR] CROSSBAR -- 1708 --> EGRESS[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] EGRESS -- 1710 --> FAULT[SENSING CONNECTION FAULT] FAULT -- 1712 --> RESELECT[RESELECTING CONNECTIONS] </pre>
1[e]	receiving a data frame having frame attributes sent between the communication network and the network node:	<p>The Reference discloses receiving a data frame having frame attributes sent between the communication network and the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

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1[f]:	<p>selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group; and</p>	<p>The Reference discloses selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more</p>

No.	'740 Patent Claim 1	The Reference
		<p>than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p> <p>The diagram illustrates a network architecture. At the top, four line cards are shown: LINE CARD 301, LINE CARD 302, LINE CARD 303, and LINE CARD 30n. Each line card has multiple ports: LINE CARD 301 has 1Gbps Ethernet (201), 2Gbps FC (202), 2Gbps FC (203), and 1Gbps Ethernet (204); LINE CARD 302 has 10Gbps FC (205); LINE CARD 303 has 10Gbps Ethernet (206); and LINE CARD 30n has IB (20n). Each line card is connected via a BACKPLANE INTERCONNECT (351, 352, 353, 35n) to a CROSSBAR FABRIC INTERCONNECT (501, 502, 503, 50n). The CROSSBAR FABRIC INTERCONNECT is represented by a large oval labeled 'CROSSBAR FABRIC INTERCONNECT'. Below the crossbar, a MANAGEMENT CARD (60) is connected via a BACKPLACE INTERCONNECT (70) to an ETHERNET INTERFACE FOR CONFIGURATION. A PCI logo is shown near the management card.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system</p>

No.	'740 Patent Claim 1	The Reference
		<p>(iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 11:28-38 (“FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 13:35-48 (“FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane charmel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane charmel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane channels).”)</p> <p>Singh at 18:44-53 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub-system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.”)</p> <p>Singh at 18:61-19:9 (“The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively</p>

No.	'740 Patent Claim 1	The Reference
		<p>connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port card backplane data links from crossbars. Step 1706f stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would</p>

No.	'740 Patent Claim 1	The Reference
		<p>communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), network device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including selecting physical links over which to send a packet. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu

No.	'740 Patent Claim 1	The Reference								
	<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>The diagram illustrates a system for determining a Stream Id. It starts with two input addresses: destinationAddress and sourceAddress. Each address is hashed (Hash). The resulting hash values are then XORed (XOR). The output of the XOR operation is a 16-bit value. This 16-bit value is then masked (Mask) to produce a 6-bit Stream Id. The Stream Id is used as an index to access a StreamStateTable. The StreamStateTable contains 64 entries, indexed from 0 to 63. Each entry consists of three fields: AssignedPortNumber[4:0], timeMark[0:1], and number of Streams - 1. The 'number of Streams - 1' field is explicitly labeled with an arrow pointing to the bottom of the table.</p>	<p>StreamStateTable</p> <table border="1"> <tr> <td>AssignedPortNumber[4:0]</td> <td>0</td> </tr> <tr> <td></td> <td>1</td> </tr> <tr> <td></td> <td>⋮</td> </tr> <tr> <td></td> <td>63</td> </tr> </table> <p>timeMark[0:1]</p> <p>number of Streams - 1</p>	AssignedPortNumber[4:0]	0		1		⋮		63
AssignedPortNumber[4:0]	0									
	1									
	⋮									
	63									

FIG. 2

DeJager '424 at Figure 3A

No.	'740 Patent Claim 1	The Reference
		<pre> graph TD START([START]) --> 302[Receive Packet] 302 --> 303[Assign Packet to Target Port Group] 303 --> 304[Hash and Mask Packet Address into a Stream ID] 304 --> 306{Is the Bit for that Stream ID set in the Current Time Mark Register?} 306 -- Yes --> 308[Set the Bit for that Stream ID in the Current Time Mark Register] 308 --> 310{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} 310 -- No --> 312 312 --> 313[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] 313 --> 314[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] 314 --> 316[LUQ PUSH Process] 316 --> 318[Forward (POP) Queued Packet] 318 --> 320[LUQ POP Process] 320 --> END([END]) </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

No.	'740 Patent Claim 1	The Reference
		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

No.	'740 Patent Claim 1	The Reference
		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

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		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-</p>

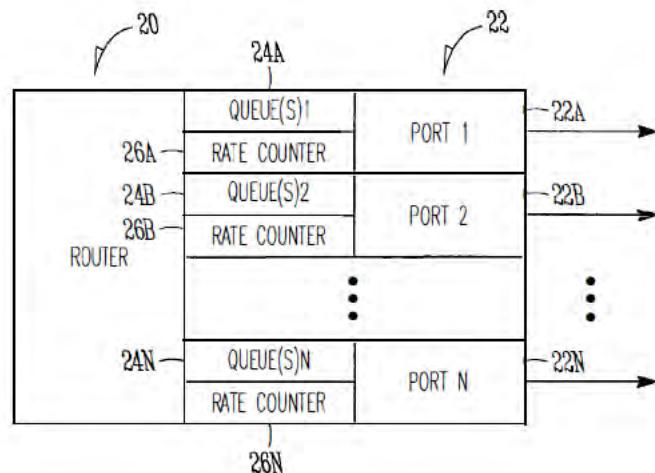
No.	'740 Patent Claim 1	The Reference
		<p>unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 1	The Reference
		<p>The diagram illustrates the architecture of Router A and its connections to other routers. Router A is represented by a central box labeled "ROUTER A". It has multiple ports labeled "PORT 1", "PORT 2", and "PORT N". A "PACKET IN" arrow enters Router A from the top. An "INTERFACE(10) TO ROUTER B" arrow exits Router A to the right, leading to Router B. Router B is shown as a separate box. Router A also has an "INTERFACE(16) TO ROUTER C" arrow exiting to the left, leading to Router C. Router C is another separate box. Arrows indicate bidirectional communication between Router A's ports and Router B, and between Router A's ports and Router C.</p>

FIG. 1

Li '914 at Figure 2

No.	'740 Patent Claim 1	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a box labeled "ROUTER" contains several components: "QUEUE(S)1", "RATE COUNTER", "QUEUE(S)2", "RATE COUNTER", and so on up to "QUEUE(S)N" and "RATE COUNTER". A bracket labeled "24A" spans across the first two rows, and another bracket labeled "24N" spans across all rows. A bracket labeled "26A" points to the first "QUEUE(S)" row, and a bracket labeled "26B" points to the second "QUEUE(S)" row. A bracket labeled "26N" points to the last "QUEUE(S)" row. On the right side, there are multiple output ports labeled "PORT 1", "PORT 2", and "PORT N". Arrows labeled "22A", "22B", and "22N" point from the router to each corresponding port. Ellipses between the ports indicate they represent multiple ports.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 1	The Reference
		<p style="text-align: center;"><u>HASH TABLE</u></p> <p style="text-align: right;">30</p> <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 1	The Reference
	<pre> RECEIVE PACKET TO TRANSMIT 40 ↓ COMPUTE HASH (DESTINATION ADDRESS) 42 ↓ USE RESULT AS INDEX INTO HASH TABLE 44 ↓ EXTRACT PORT NUMBER FROM HASH TABLE 46 ↓ TRANSMIT PACKET OVER PORT NUMBER 48 </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 1	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p style="text-align: center;">HASH TABLE 64</p> <p style="text-align: center;"><i>FIG.6</i></p> <p>Li '914 at Figure 7</p>

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		<pre> graph TD A[PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE] --> B[IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y] B --> C[EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES] C --> D[MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)] </pre> <p style="text-align: center;"><i>FIG. 7</i></p>

Li '914 at Figure 8

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	<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) each connected to a queue (QUEUE 1, QUEUE 2, and QUEUE 3). A hash operation, indicated by the formula $\text{HASH}(\text{DESTINATION}) = \text{INDEX } 82$, takes the destination address and generates an index (INDEX 82) into a hash table (TABLE 80). The table contains 16 entries, each mapping a port and a queue combination. The entries are as follows:</p> <table border="1"> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	<p>Li '914 at 1:30-43 ("For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.")</p> <p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router</p>
1	PORT 1, QUEUE 1																																					
2	PORT 2, QUEUE 1																																					
3	PORT 3, QUEUE 1																																					
1	PORT 1, QUEUE 2																																					
2	PORT 2, QUEUE 2																																					
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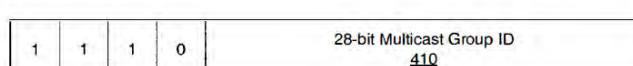
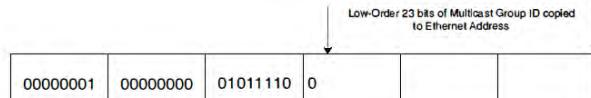
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		<p>A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

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		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

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		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

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		<p>Borgione '125 at 3:30-49 ("As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.")</p> <p>Borgione '125 at 4:3-30 ("Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

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		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

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		 <p style="text-align: center;">Figure 2</p>  <p style="text-align: center;">Figure 3</p>  <p style="text-align: center;">Figure 4</p>  <p style="text-align: center;">Figure 5</p>
1[g]	sending the data frame over the selected first and second physical links,	<p>The Reference discloses sending the data frame over the selected first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

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		<p>System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>StreamStateTable</p> <table border="1"> <thead> <tr> <th>AssignedPortNumber[4:0]</th> </tr> </thead> <tbody> <tr><td>0</td></tr> <tr><td>1</td></tr> <tr><td>⋮</td></tr> <tr><td>63</td></tr> </tbody> </table> <p>timeMark[0:1]</p> <p>number of Streams -1</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	AssignedPortNumber[4:0]	0	1	⋮	63
AssignedPortNumber[4:0]							
0							
1							
⋮							
63							

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		<pre> graph TD START([START]) --> 302[Receive Packet] 302 --> 303[Assign Packet to Target Port Group] 303 --> 304[Hash and Mask Packet Address into a Stream ID] 304 --> 306{Is the Bit for that Stream ID set in the Current Time Mark Register?} 306 -- Yes --> 308[Set the Bit for that Stream ID in the Current Time Mark Register] 308 --> 310{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} 310 -- No --> 312 312 --> 313[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] 313 --> 314[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] 314 --> 316[LUQ PUSH Process] 316 --> 318[Forward (POP) Queued Packet] 318 --> 320[LUQ POP Process] 320 --> END([END]) </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

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		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

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		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

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		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-</p>

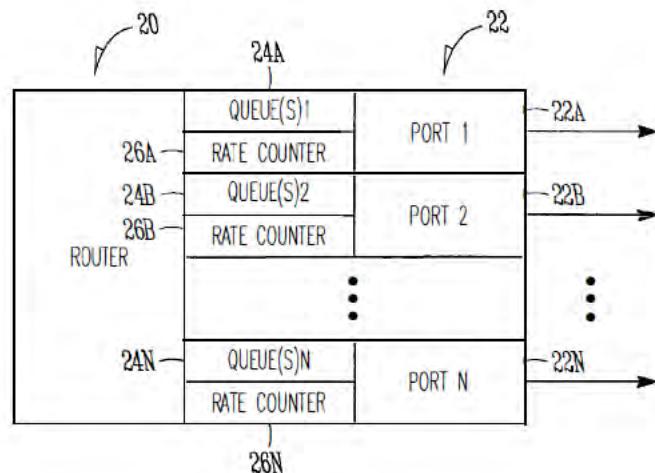
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		<p>unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 1	The Reference
		<p>The diagram illustrates the architecture of Router A and its connections to other routers. Router A is represented by a central box labeled "ROUTER A". It has multiple ports labeled "PORT 1", "PORT 2", and "PORT N". A "PACKET IN" arrow enters Router A from the top. An "INTERFACE(10) TO ROUTER B" arrow exits Router A to the right, leading to Router B. Router B is shown as a separate box. Router A also has an "INTERFACE(16) TO ROUTER C" arrow exiting to the left, leading to Router C. Router C is another separate box. Arrows indicate bidirectional communication between Router A's ports and Router B, and between Router A's ports and Router C.</p>

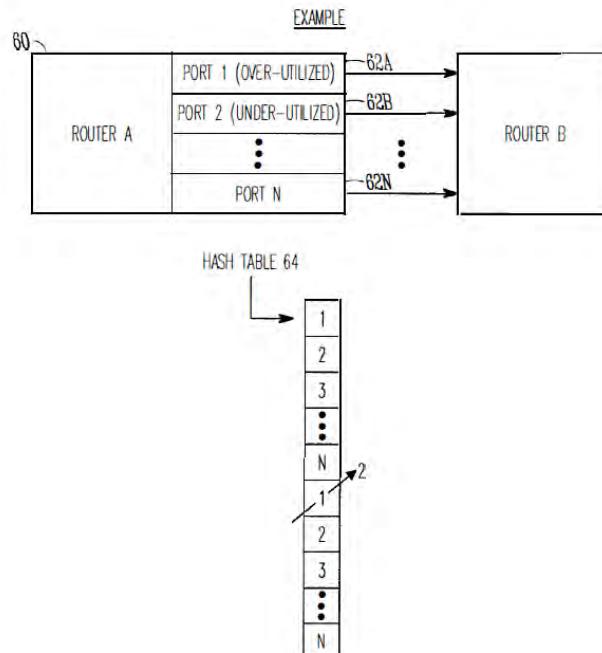
FIG. 1

Li '914 at Figure 2

No.	'740 Patent Claim 1	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a box labeled "ROUTER" contains several internal components. At the top is a section labeled "24A" containing "QUEUE(S)1" and a "RATE COUNTER". Below this are sections labeled "24B" and "24C", each containing "QUEUE(S)2" and a "RATE COUNTER". A vertical ellipsis indicates additional sections. At the bottom is a section labeled "24N" containing "QUEUE(S)N" and a "RATE COUNTER". To the right of these internal components are "PORT 1", "PORT 2", and "PORT N", each associated with a "RATE COUNTER". Arrows labeled "22A", "22B", and "22N" point from the router to the respective ports. The label "22" is positioned above the first port.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 1	The Reference
		<p style="text-align: center;"><u>HASH TABLE</u></p> <p style="text-align: right;">30</p> <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 1	The Reference
	<pre> RECEIVE PACKET TO TRANSMIT 40 ↓ COMPUTE HASH (DESTINATION ADDRESS) 42 ↓ USE RESULT AS INDEX INTO HASH TABLE 44 ↓ EXTRACT PORT NUMBER FROM HASH TABLE 46 ↓ TRANSMIT PACKET OVER PORT NUMBER 48 </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 1	The Reference										
		<p style="text-align: center;"><u>EXAMPLE</u></p>  <p style="text-align: center;">HASH TABLE 64</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>1</td></tr> <tr><td>2</td></tr> <tr><td>3</td></tr> <tr><td>⋮</td></tr> <tr><td>N</td></tr> <tr><td>1</td></tr> <tr><td>2</td></tr> <tr><td>3</td></tr> <tr><td>⋮</td></tr> <tr><td>N</td></tr> </table> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>	1	2	3	⋮	N	1	2	3	⋮	N
1												
2												
3												
⋮												
N												
1												
2												
3												
⋮												
N												

No.	'740 Patent Claim 1	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] </pre> <p style="text-align: center;"><i>FIG. 7</i></p>

Li '914 at Figure 8

No.	'740 Patent Claim 1	The Reference																		
	<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) each connected to a queue (QUEUE 1, QUEUE 2, and QUEUE 3). A hash operation, labeled <code>HASH(DESTINATION) = INDEX 82</code>, takes the destination address and generates an index (INDEX 82) into a hash table, labeled TABLE 80. The table contains 12 entries, indexed from 1 to 3, mapping combinations of port and queue to their respective values.</p> <table border="1"> <caption>TABLE 80</caption> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	<p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router</p>
1	PORT 1, QUEUE 1																			
2	PORT 2, QUEUE 1																			
3	PORT 3, QUEUE 1																			
1	PORT 1, QUEUE 2																			
2	PORT 2, QUEUE 2																			
3	PORT 3, QUEUE 2																			
1	PORT 1, QUEUE 3																			
2	PORT 2, QUEUE 3																			
3	PORT 3, QUEUE 3																			

No.	'740 Patent Claim 1	The Reference
		<p>A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

No.	'740 Patent Claim 1	The Reference
		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

No.	'740 Patent Claim 1	The Reference
		<p>Borgione '125 at 3:30-49 ("As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.")</p> <p>Borgione '125 at 4:3-30 ("Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

No.	'740 Patent Claim 1	The Reference
		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

No.	'740 Patent Claim 1	The Reference
		<p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

No.	'740 Patent Claim 1	The Reference
1[h]	said sending comprising communicating along at least one of said bi-directional links.	<p>The Reference discloses said sending comprising communicating along at least one of said bi-directional links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 2	The Reference
2[a]	The method according to claim 1, wherein the network node comprises a user node, and	<p>The Reference discloses the method according to claim 1, wherein the network node comprises a user node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
2[b]	wherein sending the data frame comprises establishing a communication service between the user node and the	<p>The Reference discloses wherein sending the data frame comprises establishing a communication service between the user node and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

No.	'740 Patent Claim 2	The Reference
	communication network.	the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.

No.	'740 Patent Claim 3	The Reference
3	The method according to claim 1, wherein the second physical links comprise backplane traces formed on a back plane to which the one or more interface modules are coupled.	<p>The Reference discloses the method according to claim 1, wherein the second physical links comprise backplane traces formed on a back plane to which the one or more interface modules are coupled.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be</p>

No.	'740 Patent Claim 3	The Reference
		<p>appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 3	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70). An Ethernet interface for configuration (labeled 'PCI') is also connected to the management card. Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect, and from the management card through its backplane interconnect to the crossbar fabric interconnect.</p> <p style="text-align: center;">FIG. 1</p>

Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)

Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208

No.	'740 Patent Claim 3	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 3	The Reference
		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 3	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 3	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1700[TRANSMITTING PACKETS FROM TM] 1700 --> 1702[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1702 --> 1704[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1704 --> 1706[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706 --> 1706a[STORING PACKETS IN iMS] 1706a --> 1706b[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706b --> 1706c[SUPPLYING PACKETS TO CROSSBAR] 1706c --> 1706d[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1706d --> 1708[SENSING CONNECTION FAULT] 1708 --> 1712[RESELECTING CONNECTIONS] </pre>	<p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>

No.	'740 Patent Claim 4	The Reference
4[preamble]	A method for communication, comprising:	<p>The Reference discloses a method for communication.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
4[a]	coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel;	<p>The Reference discloses coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
4[b]	coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel;	<p>The Reference discloses coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

No.	'740 Patent Claim 4	The Reference
		System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
4[c]	receiving a data frame having frame attributes sent between the communication network and the network node:	<p>The Reference discloses receiving a data frame having frame attributes sent between the communication network and the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
4[d]	selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group; and	<p>The Reference discloses selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified</p>

No.	'740 Patent Claim 4	The Reference
		<p>schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 4	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also connected to the crossbar fabric interconnect (40). The management card (60) is labeled with 'ETHERNET INTERFACE FOR CONFIGURATION' and has a 'PCI' label below it. Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect, and from the management card through its backplane interconnect to the crossbar fabric interconnect.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.")</p> <p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the</p>

No.	'740 Patent Claim 4	The Reference
		<p>ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 13:35-48 (“FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).”)</p> <p>Singh at 18:44-53 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.”)</p> <p>Singh at 18:61-19:9 (“The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port card backplane data links from crossbars. Step 1706f stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5</p>

No.	'740 Patent Claim 4	The Reference
		<p>ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> S5[SENSING CONNECTION FAULT 1710] S5 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the</p>

No.	'740 Patent Claim 4	The Reference
		<p>uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including selecting physical links over which to send a packet. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu • Li ’914 • Borgione ’125 <p>DeJager ’424 at Figure 2</p>

No.	'740 Patent Claim 4	The Reference
		<p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 4	The Reference
		<pre> graph TD START([START]) --> Receive[Receive Packet] Receive --> Assign[Assign Packet to Target Port Group] Assign --> Hash[Hash and Mask Packet Address into a Stream ID] Hash --> IsCurrent{Is the Bit for that Stream ID set in the Current Time Mark Register?} IsCurrent -- No --> SetCurrent[Set the Bit for that Stream ID in the Current Time Mark Register] SetCurrent --> IsAlternate{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} IsAlternate -- No --> AssignPUQ[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] AssignPUQ --> StoreLUQ[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] IsAlternate -- Yes --> CheckStream[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] CheckStream --> StoreLUQ StoreLUQ --> LUQPush[LUQ PUSH Process] LUQPush --> Forward[Forward (POP) Queued Packet] Forward --> LUQPop[LUQ POP Process] LUQPop --> END([END]) </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

No.	'740 Patent Claim 4	The Reference
		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

No.	'740 Patent Claim 4	The Reference
		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

No.	'740 Patent Claim 4	The Reference
		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet</p>

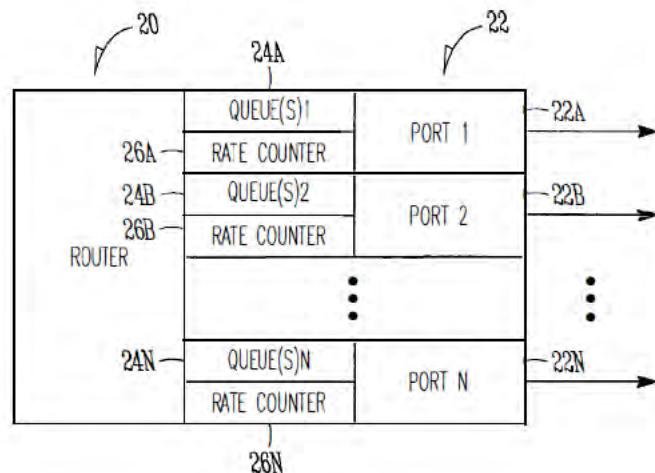
No.	'740 Patent Claim 4	The Reference
		<p>addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and</p>

No.	'740 Patent Claim 4	The Reference
		<p>1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 4	The Reference
		<p>Router A receives a PACKET IN and has an INTERFACE(10) TO ROUTER B. Router A also has an INTERFACE(16) TO ROUTER C. Router B has a port labeled 14. Router C has an output labeled PACKET OUT TO ROUTER B.</p>

FIG. 1

Li '914 at Figure 2

No.	'740 Patent Claim 4	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a large rectangular box is labeled "ROUTER". Inside this box, there are several horizontal rows of components. The first row contains "QUEUE(S)1" and "RATE COUNTER". The second row contains "QUEUE(S)2" and "RATE COUNTER". The third row contains "QUEUE(S)N" and "RATE COUNTER". Between the second and third rows, there are two vertical ellipses, indicating multiple intermediate queue and rate counter pairs. To the right of these rows, there are three vertical columns of labels: "PORT 1", "PORT 2", and "PORT N". Above each port column, there is a curved arrow pointing from the top of the row to the port label. Below each port label, there is a horizontal arrow pointing to the right, labeled "22A", "22B", and "22N" respectively. The entire diagram is enclosed in a rectangular border.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 4	The Reference
	<p>HASH(DESTINATION)=INDEX 38</p> <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>	<p><u>30</u></p>

No.	'740 Patent Claim 4	The Reference
	<pre> 40 RECEIVE PACKET TO TRANSMIT ↓ 42 COMPUTE HASH (DESTINATION ADDRESS) ↓ 44 USE RESULT AS INDEX INTO HASH TABLE ↓ 46 EXTRACT PORT NUMBER FROM HASH TABLE ↓ 48 TRANSMIT PACKET OVER PORT NUMBER </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 4	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p style="text-align: center;">HASH TABLE 64</p> <p style="text-align: center;"><i>FIG.6</i></p> <p>Li '914 at Figure 7</p>

No.	'740 Patent Claim 4	The Reference
		<pre> graph TD A[PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE] --> B[IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y] B --> C[EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES] C --> D[MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)] </pre>

FIG. 7

Li '914 at Figure 8

No.	'740 Patent Claim 4	The Reference																																				
	<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) and three queues (QUEUE 1, QUEUE 2, and QUEUE 3) per port. A hash operation, labeled $\text{HASH}(\text{DESTINATION}) = \text{INDEX } 82$, takes the destination address and generates an index into a hash table, labeled TABLE 80. The table contains 24 entries, each mapping a combination of port and queue to a value 1, 2, or 3. The table entries are as follows:</p> <table border="1"> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	<p>Li '914 at 1:30-43 ("For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.")</p> <p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of</p>
1	PORT 1, QUEUE 1																																					
2	PORT 2, QUEUE 1																																					
3	PORT 3, QUEUE 1																																					
1	PORT 1, QUEUE 2																																					
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		<p>Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

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		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

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		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

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		<p>Borgione '125 at 3:30-49 ("As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.")</p> <p>Borgione '125 at 4:3-30 ("Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

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		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

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		<p style="text-align: center;">Figure 2</p>
		<p style="text-align: center;">Figure 3</p>
4[e]	sending the data frame over the selected first and second physical links,	<p>The Reference discloses sending the data frame over the selected first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

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		<p>skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

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		<pre> graph TD START([START]) --> Receive[Receive Packet] Receive --> Assign[Assign Packet to Target Port Group] Assign --> Hash[Hash and Mask Packet Address into a Stream ID] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register?} Decision1 -- No --> SetCurrent[Set the Bit for that Stream ID in the Current Time Mark Register] SetCurrent --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} Decision2 -- No --> PushPUQ[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] PushPUQ --> StoreLUQ[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] PushPUQ --> PushProcess[LUQ PUSH Process] PushProcess --> Forward[Forward (POP) Queued Packet] Forward --> PopProcess[LUQ POP Process] PopProcess --> END([END]) Decision2 -- Yes --> CheckStream[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

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		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

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		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

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		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet</p>

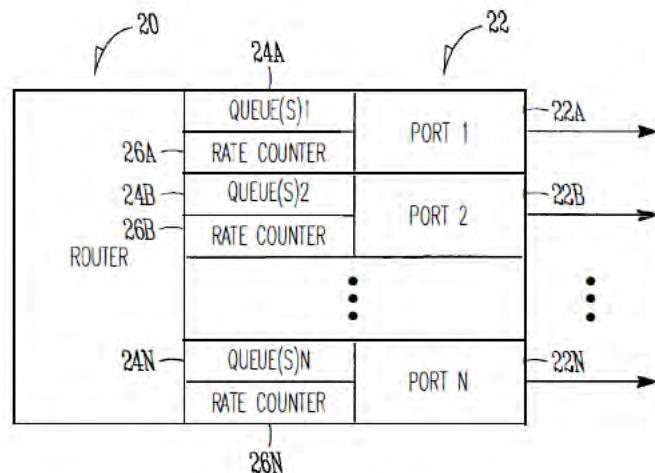
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		<p>addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and</p>

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		<p>1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

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		<p>Router A receives a PACKET IN and has an INTERFACE(10) TO ROUTER B. Router A also has an INTERFACE(16) TO ROUTER C. Router B has a port labeled 14. Router C has an output labeled PACKET OUT TO ROUTER B.</p>

FIG. 1

Li '914 at Figure 2

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		 <p>The diagram illustrates a router architecture. On the left, a large rectangular box is labeled "ROUTER". Inside this box, there are N horizontal rows, each representing a port. The first row is labeled "PORT 1", the second "PORT 2", and the last row is labeled "PORT N". Each row contains two main components: a "QUEUE(S)i" section and a "RATE COUNTER" section. The "QUEUE(S)i" section is divided into two parts: "24A" at the top and "26A" below it. The "RATE COUNTER" section is also divided into two parts: "24B" above and "26B" below. A bracket labeled "24A" spans the top of all rows, and another bracket labeled "26N" spans the bottom of all rows. Above the "ROUTER" box, a curved arrow labeled "20" points to the top edge. To the right of the "ROUTER" box, a curved arrow labeled "22" points to the right edge. From the right side of the "ROUTER" box, three arrows labeled "22A", "22B", and "22N" point outwards to the right, representing the output ports.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

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	<p>HASH(DESTINATION)=INDEX 38</p> <p><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>	<p><u>30</u></p>

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	<pre> 40 RECEIVE PACKET TO TRANSMIT ↓ 42 COMPUTE HASH (DESTINATION ADDRESS) ↓ 44 USE RESULT AS INDEX INTO HASH TABLE ↓ 46 EXTRACT PORT NUMBER FROM HASH TABLE ↓ 48 TRANSMIT PACKET OVER PORT NUMBER </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 4	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p style="text-align: center;">HASH TABLE 64</p> <p style="text-align: center;"><i>FIG.6</i></p> <p>Li '914 at Figure 7</p>

No.	'740 Patent Claim 4	The Reference
		<pre> graph TD A[PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE] --> B[IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y] B --> C[EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES] C --> D[MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)] </pre>

FIG. 7

Li '914 at Figure 8

No.	'740 Patent Claim 4	The Reference																																				
		<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) and three queues (QUEUE 1, QUEUE 2, and QUEUE 3) per port. A hash operation, labeled $\text{HASH}(\text{DESTINATION}) = \text{INDEX } 82$, takes the destination address and generates an index into a hash table, labeled TABLE 80. The table contains 24 entries, each mapping a destination to a specific port and queue combination. The entries are as follows:</p> <table border="1"> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3
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FIG. 8

Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)

Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of

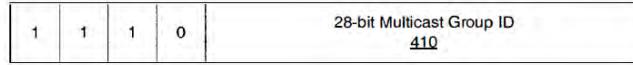
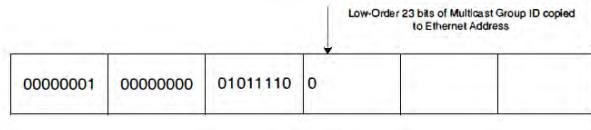
No.	'740 Patent Claim 4	The Reference
		<p>Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

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		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

No.	'740 Patent Claim 4	The Reference
		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

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		<p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

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		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

No.	'740 Patent Claim 4	The Reference
		 <p style="text-align: center;">Figure 2</p>  <p style="text-align: center;">Figure 3</p>  <p style="text-align: center;">Figure 4</p>  <p style="text-align: center;">Figure 5</p>
4[f]	at least one of the first and second groups of physical links comprising an Ethernet link aggregation (LAG) group.	<p>The Reference discloses at least one of the first and second groups of physical links comprising an Ethernet link aggregation (LAG) group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p>

No.	'740 Patent Claim 4	The Reference
		<p>Below is an example.</p> <p>Smith '430 at 5:51-64 ("The redundant links coupling each of network devices 120(1) and 120(2) to virtual network device 202 can be operated as a single logical link, referred to herein as a virtual link bundle. Network device 120(1) operates the two links coupling network device 120(1) to virtual network device 202 as 55 a virtual link bundle 250(1). In such an embodiment, each interface in network device 120(1) that is coupled to one of the links is included in an interface bundle, which corresponds to virtual link bundle 250(1). Network device 120(2) similarly operates the two links coupling network device 60 120(2) to virtual network device 202 as virtual link bundle 250(2). In some embodiments, virtual link bundles 250(1) and 250(2) are each operated as an EtherChannel™ or as an aggregated link (as described in IEEE 802.3).")</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the link aggregation technique. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Abstract ("Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to</p>

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		<p>transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other net-work environments.”)</p> <p>DeJager '424 at 1:38-50 (“One way to relieve this bottle-neck is to provide a logical grouping of multiple ports into a single port. The bandwidth of the new port is increased since it has multiple lines (cables) connecting a switch and another network device, each line capable of carrying data at the same rate as the line connecting data sources to the switch. This grouping of ports is sometimes referred to as a port aggregation or port group. One example of such a port aggregation implementation is Cisco Technology, Inc.'s Fast EtherChannel™ port group in a Fast Ethernet network. Further information regarding Fast EtherChannel™ may be found on Cisco Technology, Inc.'s World Wide Web site www.cisco.com. This information is incorporated by reference herein for all purposes.”)</p> <p>DeJager '424 at 2:47-65 (“The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 2:67-3:15 (“In one aspect, the present invention provides a method of distributing traffic over a network port group. The method involves receiving a packet of data to be forwarded, deter-mining a stream ID for the packet, and determining whether a prior packet having that stream ID has been distributed to a queue on a port in the group</p>

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		<p>during a predetermined time interval. Where a prior packet having that stream ID has not been distributed to a queue on a port of the group during the predetermined time interval, the method involves allocating the packet to a queue of a port having a lesser load in its queue than a queue of any other port of the group. The method may also involve, where a prior packet having that stream ID has been distributed to a queue on a port of the group during the predetermined time interval, allocating the packet to that queue. In addition, the method may involve monitoring the port group queues to maintain proper identification of the least utilized queue.”)</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 4:29-46 (“The present invention provides methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be</p>

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		<p>forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 4:47-58 (“FIG. 1 illustrates a block diagram of a simple network. The network 100 includes two servers S1. and S2, respectively, and two switches, X1 and X2, respectively, as well as four clients C1, C2, C3 and C4, respectively. Clients C , , and C4 are connected to switch X1 by, for example, Fast Ethernet links 102 via ports 1, 2, 3 and 4, respectively. Server S1 is connected to switch X1 via a port aggregation 104, which is a port group composed of ports 5 and 6 of switch X1 . Switch X1 is connected to switch X2 via a second port aggregation 106 which includes ports 7, 8 and 9. Switch X2 is connected to server S2 via port O and Fast Ethernet link 108.”)</p> <p>Dontu at Abstract (“Various methods and systems for preventing erroneous link aggregation due to component relocation are disclosed. Such methods include a method for changing the identifier used by a network device and communicating the identifier change to a peer network device without disrupting an aggregated link. In one embodiment, a method involves detecting an identifier change and sending a Port Aggregation Protocol (PAgP) protocol data unit (PDU) that includes a new identifier and information. The information indicates the identifier change. The new identifier identifies a network device subsequent to the identifier change. Another embodiment of a method involves detecting an identifier change and, subsequent to the identifier change, sending a link aggregation protocol PDU that includes an “old device identifier” field dedicated to conveying an old identifier. The old identifier identifies a network device prior to the identifier change.”)</p> <p>Dontu at Figure 2</p>

No.	'740 Patent Claim 4	The Reference
		<p style="text-align: center;">5</p> <p>Port Aggregation Protocol PDU 200 (sent from Interfaces 120(1), 120(2) and 120(3))</p> <p style="text-align: center;">FIG. 2</p> <p>Dontu at Figure 3</p>

No.	'740 Patent Claim 4	The Reference
		<p style="text-align: center;">Network Device 100(1)</p> <p style="text-align: center;">Network Device 100(2)</p> <p style="text-align: center;">Aggregated Link 105</p> <p style="text-align: center;">Network Device 100(2)</p>

FIG. 3

Dontu at Figure 14

No.	'740 Patent Claim 4	The Reference
	<p>The diagram illustrates a Virtual Network Device (1302) which is divided into two Virtual Network Device Sub-Units: 122(1) and 122(2). Each sub-unit contains a Control Card (e.g., 1402(1), 1402(2)) with a Control Unit (e.g., 1410(1), 1410(2)) and a Forwarding Engine (e.g., 1412(1), 1412(2)). These units are interconnected via a Virtual Network Device Link (1460). Each sub-unit also contains multiple Line Cards (e.g., 1404(1), 1404(2), 1404(3), 1404(4)), each with its own Forwarding Engine (e.g., 1414(1), 1414(2), 1414(3), 1414(4)). The Line Cards are connected to external devices: a Server (1204(3)), three Network Devices (1220(1), 1220(2), 1220(3)), and three Virtual Link Bundles (1350(1), 1350(2), 1350(3)).</p>	<p>FIG. 14</p> <p>Dontu at [0004] (“Link aggregation is used to logically combine two or more individual links into a single aggregated link. Link aggregation can provide improved performance and increased fault tolerance. Improved performance arises because the aggregated link appears to have a bandwidth equal to the combined bandwidth of the individual links. Traffic can be load-balanced among the individual links. Increased fault tolerance is provided since one or more individual links within an aggregated link can fail without disrupting communication between the devices coupled by the aggregated link. Link aggregation techniques include Link Aggregation Control Protocol (LACP), which is defined in IEEE 803.2ad, and Port</p>

No.	'740 Patent Claim 4	The Reference
		<p>Aggregation Protocol (PAgP), which is a standard promulgated by CISCO SYS-TEMS, INC.”)</p> <p>Dontu at [0012] (“The method can also involve detecting whether a partner interface is executing a compatible version of PAgP. If the partner interface is not executing the compatible version of PAgP, the compatible version of PAgP can be provided to the partner interface. Alternatively, if the partner interface is not executing the compatible version of PAgP, the partner interface can be inhibited from including a link in an aggregated link.”)</p> <p>Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components.”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6.”)</p> <p>Dontu at [0037] (“In this example, the network devices 100(1) and 100(2) use Port Aggregation Protocol (PAgP) to form aggregated links. Network devices 100(1) each send PAgP protocol data units (PDUs) to each other in order to determine whether any of the</p>

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		<p>links between the two network devices can be combined into an aggregated link. Each PAgP PDU includes an identifier that uniquely identifies the network device that sent that PAgP PDU. Within network device 100(1), identifier module 130(1) of network device component 110(1) supplies an identifier "X" to each of the interfaces 120(1)-120(3) within network device 100(1). Interfaces 120(1)-120(3) include identifier X in each PAgP PDU sent by those interfaces. Similarly, identifier module 130(2) of network device component 110(4) supplies an identifier "Y" to each interface 120(4)-120(6) of network device 100(2). Interfaces 120(4)-120(6) include identifier Y in each PAgP PDU sent by those interfaces.”)</p> <p>Dontu at [0040] (“FIG. 2 illustrates some of the fields that can be included in a PAgP PDU. As shown, PDU 200 includes Version field 202, My Device Identifier field 204 (“My” refers to the device sending the PAgP PDU), My Distribution Requirements field 206, My Port Priority field 208, My Port Identifier field 212, My Group Capability field 212, My Agport (Aggregated Port) Identifier field 214, Your Device Identifier field 216 (“Your” refers to the device to which the PAgP PDU is being sent), Your Distribution Requirements field 218, Your Port Priority field 220, Your Port Identifier field 222, Your Group Capability field 224, Your Agport Identifier field 226, and Partner Count field 228.”)</p> <p>Dontu at [0110] (“Interfaces 1420(13), 1420(9), and 1420(16), which are each coupled to network device 1220(1) by virtual link bundle 1350(1), form an interface bundle (e.g., an Ether-Channel (TM) port bundle). Similarly, interfaces 1420(11) and 1420(8) form another interface bundle that is coupled to network device 1220(2) by virtual link bundle 1350(2). Interfaces 1420(7) and 1420(12) form a third interface bundle that is coupled to network device 1220(3) by virtual link bundle 1350(3). Within virtual network device 1302, each interface in the same interface bundle is assigned the same logical identifier. For example, interfaces 1420(13), 1420(9), and 1420(16) are each assigned the same logical identifier. In some embodiments, packets received via one of these interfaces are tagged or otherwise associated with the logical identifier to indicate that those packets were received via the virtual link bundle coupling virtual network device 1302 to network device 1220(1). It is noted that similar interface bundles are implemented within each network device 1220(1)-1220(3), and that interfaces included in such bundles are also assigned the same logical identifier by each network device (or by virtual network device 1302, in embodiments in</p>

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		<p>which virtual network device 1302 controls the configuration of the network devices 1220(1)-1220(3)). For example, network device 1220(1) can assign the same logical identifier to each of the interfaces coupled to virtual link bundle 1350(1).”)</p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 2:6-22 (“In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.”)</p> <p>Li '914 at 4:9-25 (“Referring to FIG. 1, a Router A is shown having an inter-face 10 with a plurality of ports which connect Router A with Router B over a plurality of connections, lines, wires, links or bundled links 14. The ports 12 of Router A are configured to permit transmission of packets from Router A to Router B, and these ports 12 may be referred to as output ports, egress ports, links, or the like. As shown in FIG. 1, port 1 to port N may be</p>

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		<p>connected with Router B, and there may be additional interfaces 16 having ports 18 connected with other routers in the network. When a packet is received by Router A, Router A determines whether the received packet should be transmitted to Router B or to other routers connected to Router A, based in part upon the destination address of the packet. If a packet is to be transmitted from Router A to Router B, then Router A may transmit this packet over the one of the ports 12 shown in FIG. 1.”)</p> <p>Li '914 at Figure 1</p> <p>FIG. 1</p> <p>Borgione '125 at 1:55-65 (“Link nodes 110 and 120 can be in physically remote locations, thereby connecting their associated local area networks (LANs). The plurality of network links 150 between link nodes 110 and 120 can be aggregated as a single logical link over which all traffic between link nodes 110 and 120 is distributed. Such aggregation multiplies the available bandwidth for communications between link nodes 110 and 120, and therefore</p>

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		<p>between the two local area networks. When appropriately configured, such a connection can permit the two local area networks to interact as if they were one large local area network.”)</p> <p>Borgione '125 at 1:66-2:7 (“As stated above, the plurality of network links between 110 and 120 can be aggregated as a single logical link. In this manner, each link node 110 and 120 sees the plurality of network links between them as one logical interface. One type of such an aggregate of links is an EtherChannel, a protocol that allows up to eight Fast Ethernet or Gigabit Ethernet links to be aggregated. Routing protocols treat the aggregated links as a single, routed interface with a common IP address.”)</p> <p>Borgione '125 at 5:28-50 (“The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.”)</p>

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5[preamble]	A method for communication, comprising:	<p>The Reference discloses a method for communication.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
5[a]	coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel;	<p>The Reference discloses coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
5[b]	coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel;	<p>The Reference discloses coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

No.	'740 Patent Claim 5	The Reference
		System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
5[c]	receiving a data frame having frame attributes sent between the communication network and the network node:	<p>The Reference discloses receiving a data frame having frame attributes sent between the communication network and the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
5[d]	selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group; and	<p>The Reference discloses selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified</p>

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		<p>schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

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		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70). An Ethernet interface for configuration (labeled 'PCI') is also connected to the management card. Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect, and from the management card through its backplane interconnect to the crossbar fabric interconnect.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 11:28-38 (“FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the</p>

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		<p>ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 13:35-48 (“FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).”)</p> <p>Singh at 18:44-53 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.”)</p> <p>Singh at 18:61-19:9 (“The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port card backplane data links from crossbars. Step 1706f stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5</p>

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		<p>ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> S5[SENSING CONNECTION FAULT 1710] S5 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the</p>

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		<p>uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including selecting physical links over which to send a packet. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu • Li ’914 • Borgione ’125 <p>DeJager ’424 at Figure 2</p>

No.	'740 Patent Claim 5	The Reference
		<p>destinationAddress → Hash → XOR → Mask → Stream Id → StreamStateTable</p> <p>sourceAddress → Hash → XOR → Mask → Stream Id → StreamStateTable</p> <p>Stream Id (6 bits)</p> <p>StreamStateTable</p> <ul style="list-style-type: none"> AssignedPortNumber[4:0] timeMark[0:1] number of Streams - 1

FIG. 2

DeJager '424 at Figure 3A

No.	'740 Patent Claim 5	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> IsCurrent{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} IsCurrent -- No --> SetCurrent[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetCurrent --> IsAlternate{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} IsAlternate -- No --> AssignPUQ[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] AssignPUQ --> StoreLUQ[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] StoreLUQ --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) IsCurrent -- Yes --> CheckStream[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] CheckStream --> IsAlternate </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

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		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

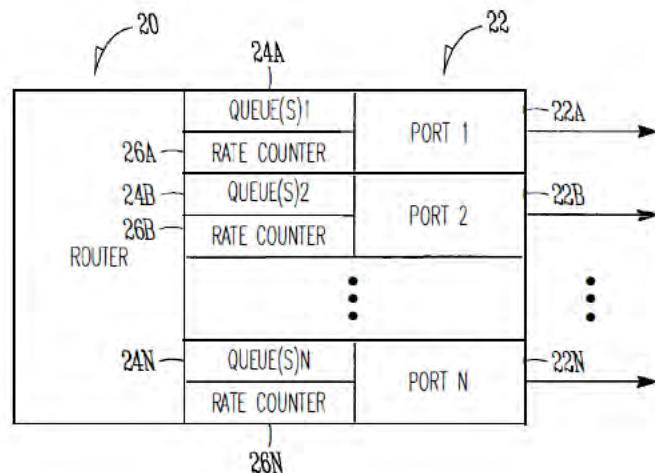
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		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

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		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-</p>

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		<p>unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p>

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		<p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 5	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 5	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a large rectangular box is labeled "ROUTER". Inside this box, there are several horizontal rows of components. The first row contains "QUEUE(S)1" and "RATE COUNTER". The second row contains "QUEUE(S)2" and "RATE COUNTER". The third row contains "QUEUE(S)N" and "RATE COUNTER". Between the second and third rows, there are two vertical ellipses, indicating multiple intermediate queue and rate counter pairs. To the right of these rows, there are three vertical columns of labels: "PORT 1", "PORT 2", and "PORT N". Above each port column, there is a bracket labeled "22A", "22B", and "22N" respectively. Arrows point from the "PORT 1", "PORT 2", and "PORT N" labels to the right, indicating the output paths for each port. The entire assembly is labeled "20" at the top and "26" at the bottom.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 5	The Reference
	<p style="text-align: center;">HASH(DESTINATION)=INDEX 38</p> <p style="text-align: right;"><u>30</u></p>	

FIG. 3

Li '914 at Figure 4

No.	'740 Patent Claim 5	The Reference
	<pre> RECEIVE PACKET TO TRANSMIT 40 ↓ COMPUTE HASH (DESTINATION ADDRESS) 42 ↓ USE RESULT AS INDEX INTO HASH TABLE 44 ↓ EXTRACT PORT NUMBER FROM HASH TABLE 46 ↓ TRANSMIT PACKET OVER PORT NUMBER 48 </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 5	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <pre> graph LR subgraph RouterA [Router A] direction TB P1["PORT 1 (OVER-UTILIZED)"] P2["PORT 2 (UNDER-UTILIZED)"] dots1["⋮"] PN["PORT N"] P1 --- b2A P2 --- b2B dots1 --- b2N PN --- b2N end subgraph RouterB [Router B] end subgraph HashTable [HASH TABLE 64] direction TB H1[1] H2[2] H3[3] dotsH["⋮"] HN[N] H1 --- 1 H2 --- 2 dotsH --- N HN --- 1 H1_2[H1] --- 2 H2_1[H2] --- 1 end </pre> <p style="text-align: center;"><i>FIG.6</i></p> <p>Li '914 at Figure 7</p>

No.	'740 Patent Claim 5	The Reference
		<pre> graph TD A[PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE] --> B[IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y] B --> C[EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES] C --> D[MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)] </pre>

FIG. 7

Li '914 at Figure 8

No.	'740 Patent Claim 5	The Reference																																				
	<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) each connected to a queue (QUEUE 1, QUEUE 2, and QUEUE 3). A hash operation, labeled $\text{HASH}(\text{DESTINATION}) = \text{INDEX } 82$, takes the destination address and generates an index (INDEX 82) into a hash table (TABLE 80). The table contains 16 entries, indexed from 1 to 3, mapping combinations of port and queue to their respective values.</p> <table border="1"> <caption>TABLE 80</caption> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	<p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router</p>
1	PORT 1, QUEUE 1																																					
2	PORT 2, QUEUE 1																																					
3	PORT 3, QUEUE 1																																					
1	PORT 1, QUEUE 2																																					
2	PORT 2, QUEUE 2																																					
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No.	'740 Patent Claim 5	The Reference
		<p>A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

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		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

No.	'740 Patent Claim 5	The Reference
		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

No.	'740 Patent Claim 5	The Reference
		<p>Borgione '125 at 3:30-49 ("As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.")</p> <p>Borgione '125 at 4:3-30 ("Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

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		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

No.	'740 Patent Claim 5	The Reference
		<p style="text-align: center;">Figure 2</p>
		<p style="text-align: center;">Figure 3</p>
5[e]	sending the data frame over the selected first and second physical links,	<p>The Reference discloses sending the data frame over the selected first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

No.	'740 Patent Claim 5	The Reference
		<p>System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 5	The Reference
		<pre> graph TD START([START]) --> Receive[Receive Packet] Receive --> Assign[Assign Packet to Target Port Group] Assign --> Hash[Hash and Mask Packet Address into a Stream ID] Hash --> IsCurrent{Is the Bit for that Stream ID set in the Current Time Mark Register?} IsCurrent -- No --> SetCurrent[Set the Bit for that Stream ID in the Current Time Mark Register] SetCurrent --> IsAlternate{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} IsAlternate -- No --> AssignPUQ[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] IsAlternate -- Yes --> CheckStream[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] AssignPUQ --> StoreLUQ[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] CheckStream --> StoreLUQ StoreLUQ --> LUQPUSH[LUQ PUSH Process] LUQPUSH --> Forward[Forward (POP) Queued Packet] Forward --> LUQPOP[LUQ POP Process] LUQPOP --> END([END]) </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

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		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

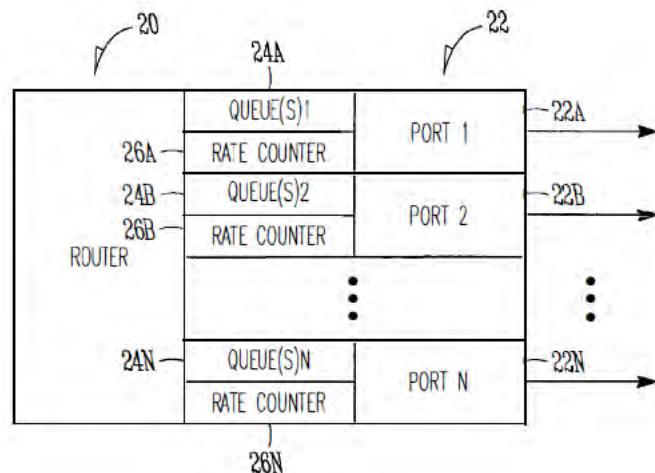
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		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

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		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-</p>

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		<p>unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p>

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		<p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

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		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 5	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a large rectangular box is labeled "ROUTER". Inside this box, there are several horizontal rows of components. The first row contains "QUEUE(S)1" and "RATE COUNTER". The second row contains "QUEUE(S)2" and "RATE COUNTER". The third row contains "QUEUE(S)N" and "RATE COUNTER". Between the second and third rows, there are two vertical ellipses, indicating multiple intermediate queue and rate counter pairs. To the right of these rows, there are three vertical columns. The first column is labeled "PORT 1", the second is "PORT 2", and the third is "PORT N". Above each port column, there is a curved arrow pointing to it from the top edge of the "ROUTER" box. Below each port column, there is a curved arrow pointing away from the bottom edge of the "ROUTER" box. To the right of each port column, there is an output arrow labeled "22A", "22B", or "22N" respectively, representing the connection to an external network or device.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 5	The Reference
	<p style="text-align: center;">HASH(DESTINATION)=INDEX 38</p> <p style="text-align: right;"><u>30</u></p>	

FIG. 3

Li '914 at Figure 4

No.	'740 Patent Claim 5	The Reference
	<pre> RECEIVE PACKET TO TRANSMIT 40 ↓ COMPUTE HASH (DESTINATION ADDRESS) 42 ↓ USE RESULT AS INDEX INTO HASH TABLE 44 ↓ EXTRACT PORT NUMBER FROM HASH TABLE 46 ↓ TRANSMIT PACKET OVER PORT NUMBER 48 </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 5	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p style="text-align: center;"><i>FIG.6</i></p> <p>Li '914 at Figure 7</p>

No.	'740 Patent Claim 5	The Reference
		<pre> graph TD A[PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE] --> B[IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y] B --> C[EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES] C --> D[MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)] </pre>

FIG. 7

Li '914 at Figure 8

No.	'740 Patent Claim 5	The Reference																		
	<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) and three corresponding queues (QUEUE 1, QUEUE 2, and QUEUE 3). An input line labeled '84' enters the Router. A table labeled 'TABLE 80' shows a mapping from a hash index to port and queue pairs. The table has 8 rows, indexed from 1 to 8. The entries are as follows:</p> <table border="1"> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table> <p>A note indicates 'HASH(DESTINATION) = INDEX 82'.</p>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	<p>Li '914 at 1:30-43 ("For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.")</p> <p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router</p>
1	PORT 1, QUEUE 1																			
2	PORT 2, QUEUE 1																			
3	PORT 3, QUEUE 1																			
1	PORT 1, QUEUE 2																			
2	PORT 2, QUEUE 2																			
3	PORT 3, QUEUE 2																			
1	PORT 1, QUEUE 3																			
2	PORT 2, QUEUE 3																			
3	PORT 3, QUEUE 3																			

No.	'740 Patent Claim 5	The Reference
		<p>A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

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		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

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		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

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		<p>Borgione '125 at 3:30-49 ("As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.")</p> <p>Borgione '125 at 4:3-30 ("Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

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		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

No.	'740 Patent Claim 5	The Reference
		<p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>
5[f]	coupling the network node to the one or more interface modules comprises aggregating two or more of the first physical links into an external Ethernet link aggregation (LAG) group so as to increase	<p>The Reference discloses coupling the network node to the one or more interface modules comprises aggregating two or more of the first physical links into an external Ethernet link aggregation (LAG) group so as to increase a data bandwidth provided to the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

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	<p>a data bandwidth provided to the network node.</p>	<p>System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below is an example.</p> <p>Smith '430 at 5:51-64 (“The redundant links coupling each of network devices 120(1) and 120(2) to virtual network device 202 can be operated as a single logical link, referred to herein as a virtual link bundle. Network device 120(1) operates the two links coupling network device 120(1) to virtual network device 202 as 55 a virtual link bundle 250(1). In such an embodiment, each interface in network device 120(1) that is coupled to one of the links is included in an interface bundle, which corresponds to virtual link bundle 250(1). Network device 120(2) similarly operates the two links coupling network device 60 120(2) to virtual network device 202 as virtual link bundle 250(2). In some embodiments, virtual link bundles 250(1) and 250(2) are each operated as an EtherChannel™ or as an aggregated link (as described in IEEE 802.3).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the link aggregation technique. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Abstract (“Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port’s current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This</p>

No.	'740 Patent Claim 5	The Reference
		<p>is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 1:38-50 (“One way to relieve this bottle-neck is to provide a logical grouping of multiple ports into a single port. The bandwidth of the new port is increased since it has multiple lines (cables) connecting a switch and another network device, each line capable of carrying data at the same rate as the line connecting data sources to the switch. This grouping of ports is sometimes referred to as a port aggregation or port group. One example of such a port aggregation implementation is Cisco Technology, Inc.'s Fast EtherChannel™ port group in a Fast Ethernet network. Further information regarding Fast EtherChannel™ may be found on Cisco Technology, Inc.'s World Wide Web site www.cisco.com. This information is incorporated by reference herein for all purposes.”)</p> <p>DeJager '424 at 2:47-65 (“The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 2:67-3:15 (“In one aspect, the present invention provides a method of distributing traffic over a network port group. The method involves receiving a packet of</p>

No.	'740 Patent Claim 5	The Reference
		<p>data to be forwarded, determining a stream ID for the packet, and determining whether a prior packet having that stream ID has been distributed to a queue on a port in the group during a predetermined time interval. Where a prior packet having that stream ID has not been distributed to a queue on a port of the group during the predetermined time interval, the method involves allocating the packet to a queue of a port having a lesser load in its queue than a queue of any other port of the group. The method may also involve, where a prior packet having that stream ID has been distributed to a queue on a port of the group during the predetermined time interval, allocating the packet to that queue. In addition, the method may involve monitoring the port group queues to maintain proper identification of the least utilized queue.”)</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 4:29-46 (“The present invention provides methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be</p>

No.	'740 Patent Claim 5	The Reference
		<p>forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 4:47-58 (“FIG. 1 illustrates a block diagram of a simple network. The network 100 includes two servers S1. and S2, respectively, and two switches, X1 and X2, respectively, as well as four clients C1, C2, C3 and C4, respectively. Clients C , , and C4 are connected to switch X1 by, for example, Fast Ethernet links 102 via ports 1, 2, 3 and 4, respectively. Server S1 is connected to switch X1 via a port aggregation 104, which is a port group composed of ports 5 and 6 of switch X1 . Switch X1 is connected to switch X2 via a second port aggregation 106 which includes ports 7, 8 and 9. Switch X2 is connected to server S2 via port O and Fast Ethernet link 108.”)</p> <p>Dontu at Abstract (“Various methods and systems for preventing erroneous link aggregation due to component relocation are disclosed. Such methods include a method for changing the identifier used by a network device and communicating the identifier change to a peer network device without disrupting an aggregated link. In one embodiment, a method involves detecting an identifier change and sending a Port Aggregation Protocol (PAgP) protocol data unit (PDU) that includes a new identifier and information. The information indicates the identifier change. The new identifier identifies a network device subsequent to the identifier change. Another embodiment of a method involves detecting an identifier change and, subsequent to the identifier change, sending a link aggregation protocol PDU that includes an “old device identifier” field dedicated to conveying an old identifier. The old identifier identifies a network device prior to the identifier change.”)</p> <p>Dontu at Figure 2</p>

No.	'740 Patent Claim 5	The Reference
		<p style="text-align: center;">5</p> <p style="text-align: center;">FIG. 2</p> <p>Dontu at Figure 3</p>

No.	'740 Patent Claim 5	The Reference

FIG. 3

Dontu at Figure 14

No.	'740 Patent Claim 5	The Reference
	<p>The diagram illustrates a Virtual Network Device (1302) which is divided into two Virtual Network Device Sub-Units: 122(1) and 122(2). Each sub-unit contains a Control Card (e.g., 1402(1), 1402(2)) with a Control Unit (e.g., 1410(1), 1410(2)) and a Forwarding Engine (e.g., 1412(1), 1412(2)). These units are interconnected via a Virtual Network Device Link (1460). Each sub-unit also contains multiple Line Cards (e.g., 1404(1), 1404(2), 1404(3), 1404(4)), each with its own Forwarding Engine (e.g., 1414(1), 1414(2), 1414(3), 1414(4)). The line cards are connected to external devices: a Server (1204(3)), three Network Devices (1220(1), 1220(2), 1220(3)), and three Virtual Link Bundles (1350(1), 1350(2), 1350(3)).</p>	<p>Dontu at [0004] (“Link aggregation is used to logically combine two or more individual links into a single aggregated link. Link aggregation can provide improved performance and increased fault tolerance. Improved performance arises because the aggregated link appears to have a bandwidth equal to the combined bandwidth of the individual links. Traffic can be load-balanced among the individual links. Increased fault tolerance is provided since one or more individual links within an aggregated link can fail without disrupting communication between the devices coupled by the aggregated link. Link aggregation techniques include Link Aggregation Control Protocol (LACP), which is defined in IEEE 803.2ad, and Port</p>

FIG. 14

No.	'740 Patent Claim 5	The Reference
		<p>Aggregation Protocol (PAgP), which is a standard promulgated by CISCO SYS-TEMS, INC.”)</p> <p>Dontu at [0012] (“The method can also involve detecting whether a partner interface is executing a compatible version of PAgP. If the partner interface is not executing the compatible version of PAgP, the compatible version of PAgP can be provided to the partner interface. Alternatively, if the partner interface is not executing the compatible version of PAgP, the partner interface can be inhibited from including a link in an aggregated link.”)</p> <p>Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components.”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6).”)</p> <p>Dontu at [0037] (“In this example, the network devices 100(1) and 100(2) use Port Aggregation Protocol (PAgP) to form aggregated links. Network devices 100(1) each send PAgP protocol data units (PDUs) to each other in order to determine whether any of the</p>

No.	'740 Patent Claim 5	The Reference
		<p>links between the two network devices can be combined into an aggregated link. Each PAgP PDU includes an identifier that uniquely identifies the network device that sent that PAgP PDU. Within network device 100(1), identifier module 130(1) of network device component 110(1) supplies an identifier "X" to each of the interfaces 120(1)-120(3) within network device 100(1). Interfaces 120(1)-120(3) include identifier X in each PAgP PDU sent by those interfaces. Similarly, identifier module 130(2) of network device component 110(4) supplies an identifier "Y" to each interface 120(4)-120(6) of network device 100(2). Interfaces 120(4)-120(6) include identifier Y in each PAgP PDU sent by those interfaces.”)</p> <p>Dontu at [0040] (“FIG. 2 illustrates some of the fields that can be included in a PAgP PDU. As shown, PDU 200 includes Version field 202, My Device Identifier field 204 (“My” refers to the device sending the PAgP PDU), My Distribution Requirements field 206, My Port Priority field 208, My Port Identifier field 212, My Group Capability field 212, My Agport (Aggregated Port) Identifier field 214, Your Device Identifier field 216 (“Your” refers to the device to which the PAgP PDU is being sent), Your Distribution Requirements field 218, Your Port Priority field 220, Your Port Identifier field 222, Your Group Capability field 224, Your Agport Identifier field 226, and Partner Count field 228.”)</p> <p>Dontu at [0110] (“Interfaces 1420(13), 1420(9), and 1420(16), which are each coupled to network device 1220(1) by virtual link bundle 1350(1), form an interface bundle (e.g., an Ether-Channel (TM) port bundle). Similarly, interfaces 1420(11) and 1420(8) form another interface bundle that is coupled to network device 1220(2) by virtual link bundle 1350(2). Interfaces 1420(7) and 1420(12) form a third interface bundle that is coupled to network device 1220(3) by virtual link bundle 1350(3). Within virtual network device 1302, each interface in the same interface bundle is assigned the same logical identifier. For example, interfaces 1420(13), 1420(9), and 1420(16) are each assigned the same logical identifier. In some embodiments, packets received via one of these interfaces are tagged or otherwise associated with the logical identifier to indicate that those packets were received via the virtual link bundle coupling virtual network device 1302 to network device 1220(1). It is noted that similar interface bundles are implemented within each network device 1220(1)-1220(3), and that interfaces included in such bundles are also assigned the same logical identifier by each network device (or by virtual network device 1302, in embodiments in</p>

No.	'740 Patent Claim 5	The Reference
		<p>which virtual network device 1302 controls the configuration of the network devices 1220(1)-1220(3)). For example, network device 1220(1) can assign the same logical identifier to each of the interfaces coupled to virtual link bundle 1350(1).”)</p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 2:6-22 (“In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.”)</p> <p>Li '914 at 4:9-25 (“Referring to FIG. 1, a Router A is shown having an inter-face 10 with a plurality of ports which connect Router A with Router B over a plurality of connections, lines, wires, links or bundled links 14. The ports 12 of Router A are configured to permit transmission of packets from Router A to Router B, and these ports 12 may be referred to as output ports, egress ports, links, or the like. As shown in FIG. 1, port 1 to port N may be</p>

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		<p>connected with Router B, and there may be additional interfaces 16 having ports 18 connected with other routers in the network. When a packet is received by Router A, Router A determines whether the received packet should be transmitted to Router B or to other routers connected to Router A, based in part upon the destination address of the packet. If a packet is to be transmitted from Router A to Router B, then Router A may transmit this packet over the one of the ports 12 shown in FIG. 1.”)</p> <p>Li '914 at Figure 1</p> <p>FIG. 1</p> <p>Borgione '125 at 1:55-65 (“Link nodes 110 and 120 can be in physically remote locations, thereby connecting their associated local area networks (LANs). The plurality of network links 150 between link nodes 110 and 120 can be aggregated as a single logical link over which all traffic between link nodes 110 and 120 is distributed. Such aggregation multiplies the available bandwidth for communications between link nodes 110 and 120, and therefore</p>

No.	'740 Patent Claim 5	The Reference
		<p>between the two local area networks. When appropriately configured, such a connection can permit the two local area networks to interact as if they were one large local area network.”)</p> <p>Borgione '125 at 1:66-2:7 (“As stated above, the plurality of network links between 110 and 120 can be aggregated as a single logical link. In this manner, each link node 110 and 120 sees the plurality of network links between them as one logical interface. One type of such an aggregate of links is an EtherChannel, a protocol that allows up to eight Fast Ethernet or Gigabit Ethernet links to be aggregated. Routing protocols treat the aggregated links as a single, routed interface with a common IP address.”)</p> <p>Borgione '125 at 5:28-50 (“The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.”)</p>

No.	'740 Patent Claim 6	The Reference
6	The method according to claim 1, wherein	The Reference discloses the method according to claim 1, wherein coupling each of the one or more interface modules to the communication network comprises at least one of

No.	'740 Patent Claim 6	The Reference
	<p>coupling each of the one or more interface modules to the communication network comprises at least one of multiplexing upstream data frames sent from the network node to the communication network, and demultiplexing downstream data frames sent from the communication network to the network node.</p>	<p>multiplexing upstream data frames sent from the network node to the communication network, and demultiplexing downstream data frames sent from the communication network to the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, and Wong.</p> <p>Below is an example.</p> <p>Wong at [0072] ("The address resolution circuit 136 comprises a port based load balancing circuit 258 including: a source port selection multiplexer 260 having a plurality of (2M=8) inputs 262 each coupled to receive a corresponding one of the port mapping values from output 256 of a corresponding one of the configuration registers 252 of unit 116, an output 264, and a control port 268 coupled to receive a three-bit source port ID value carried by a source port signal received from the input queuing control logic unit 121 (FIG. 3A) of the particular switching device; a destination trunk port register 272 having an input 274 coupled to receive selected ones of the port mapping values from output 264 of multiplexer 260, and a plurality of (2M=8) outputs 276; and a trunk port selection multiplexer 280 having a plurality of (2M=8) inputs 282 each coupled to receive a two-bit value from a corresponding one of the outputs 276 of register 272, an output 284, and a control port 286 coupled to receive a trunked port ID signal carrying a three bit trunked port ID value (x,x,t) from the packet routing table 134 (FIGS. 3A and 3B) as further explained below.")</p> <p>Wong at [0073] ("In the depicted embodiment, the register bank 251 of unit 116 includes eight of the trunk port configuration registers 252, designated TP 0-TP 7, each being associated with a corresponding one of the eight network ports 14 of the particular switching device 12 (FIG. 3A). The source port selection multiplexer 260 selects from the outputs 256</p>

No.	'740 Patent Claim 6	The Reference
		of registers 252 in response to the source port value received at its control port 268 from the input queuing control logic 121 (FIG. 3A). The source port value indicates the source port at which a particular packet has been received. Therefore, the multiplexer 260 selects one of the registers 252 which corresponds with the source port associated with the received packet. As further described below, the 16-bit port mapping value stored in the selected one of the registers 252 includes eight separate 2-bit port values select each indicating a destination port associated with the particular packet received at the corresponding source port.”)

No.	'740 Patent Claim 7	The Reference
7	The method according to claim 1, wherein selecting the first and second physical links comprises balancing a frame data rate among at least some of the first and second physical links.	<p>The Reference discloses the method according to claim 1, wherein selecting the first and second physical links comprises balancing a frame data rate among at least some of the first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 8	The Reference
8	The method according to claim 1, wherein selecting the first and second physical links comprises applying a	The Reference discloses the method according to claim 1, wherein selecting the first and second physical links comprises applying a mapping function to the at least one of the frame attributes.

No.	'740 Patent Claim 8	The Reference
	<p>mapping function to the at least one of the frame attributes.</p>	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Solomon, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Solomon at [0024] ("In another embodiment, switching the data packets includes mapping the data packets to the selected port responsively to the label. Additionally or alternatively, mapping the data packets includes applying a hashing function to the label so as to determine a number of the selected port, and choosing the label includes applying an inverse of the hashing function to the number of the selected port.")</p> <p>Solomon at [0048] ("The mapping function typically uses MPLS label 52 for mapping, since the MPLS label uniquely identifies MPLS tunnel 28, and it is required that all MPLS packets belonging to the same tunnel be switched through the same physical port 24. Additionally or alternatively, the mapping function uses a "PW" label (pseudo wire label, formerly known as a virtual connection, or VC label), which is optionally added to MPLS header 50. The PW label com-prises information that the egress node requires for deliver-ing the packet to its destination, and is optionally added during the encapsulation of MPLS packets. Additional details regarding the VC label can be found in an IETF draft by Martini et al. entitled "Encapsulation Methods for Trans-port of Ethernet Frames Over IP/MPLS Networks" (IETF draft-ietf-pwe3-ethernet-encap-07.txt, May, 2004), which is incorporated herein by reference. In some embodiments, mapper 34 applies a hashing function to the MPLS and/or PW label, as will be described below.")</p> <p>Solomon at [0059] ("In this method, the mapping function used by mapper 34 of switch A is a hashing function. Various hashing functions are known in the art, and any suitable hashing</p>

No.	'740 Patent Claim 8	The Reference
		<p>function may be used in mapper 34. Since the hashing operation is performed for each packet, it is desirable to have a hashing function that is computationally simple.”)</p> <p>Solomon at [0060] (“As mentioned above, the hashing function typically hashes the value of MPLS label 52 to determine the selected physical port, as the MPLS label uniquely identifies tunnel 28. For example, the following hashing function may be used by mapper 34: Selected port number=1+((MPLS label) mod N), wherein N denotes the number of physical Ethernet ports in LAG group 25, and “mod” denotes the modulus operator. Assuming the values of MPLS labels are distributed uniformly over a certain range, this function achieves a uniform distribution of port allocations for the different MPLS labels. It can also be seen that all packets carrying the same MPLS label (in other words belonging to the same MPLS tunnel) will be mapped to the same physical port.”)</p> <p>Solomon at [0065] (“Mapper 34 of switch A maps each received packet to the selected physical port of LAG group 25 using the hashing function, at a hashing step 90. Mapper 34 extracts the MPLS label from each received packet and uses the hashing function to calculate the serial number of the selected physical port, which was selected by the CAC processor at step 82. Following the numerical example given above, the mapper extracts MPLS label=65647 from the packet. Substituting this value and N=3 into the hashing function gives: Selected port number=1+(65647 mod 3)=2, which is indeed the port number selected in the example above.”)</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, “backplane interconnects 30”). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable</p>

No.	'740 Patent Claim 8	The Reference
		<p>of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 8	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to the crossbar fabric interconnect (40) via a backplane interconnect (70). The management card (60) is also connected to an Ethernet interface for configuration (PCI). The line cards (301, 302, 303, 30n) receive various types of traffic: Line Card 301 receives 1Gbps Ethernet (251), 2Gbps FC (202), 2Gbps FC (203), and 1Gbps Ethernet (204); Line Card 302 receives 10Gbps FC (252); Line Card 303 receives 10Gbps Ethernet (253); and Line Card 30n receives IB (25n).</p> <p style="text-align: right;">FIG. 1</p>

Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)

Singh at 11:28-38 (“FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the

No.	'740 Patent Claim 8	The Reference
		<p>ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 13:35-48 (“FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).”)</p> <p>Singh at 18:44-53 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.”)</p> <p>Singh at 18:61-19:9 (“The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port card backplane data links from crossbars. Step 1706f stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5</p>

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		<p>ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the</p>

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		<p>uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p>

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9	The method according to claim 8, wherein applying the mapping function comprises applying a hashing function.	<p>The Reference discloses the method according to claim 8, wherein applying the mapping function comprises applying a hashing function.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

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		<p>the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Solomon, Smith '430, Alexander, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Solomon at [0024] ("In another embodiment, switching the data packets includes mapping the data packets to the selected port responsively to the label. Additionally or alternatively, mapping the data packets includes applying a hashing function to the label so as to determine a number of the selected port, and choosing the label includes applying an inverse of the hashing function to the number of the selected port.")</p> <p>Solomon at [0048] ("The mapping function typically uses MPLS label 52 for mapping, since the MPLS label uniquely identifies MPLS tunnel 28, and it is required that all MPLS packets belonging to the same tunnel be switched through the same physical port 24. Additionally or alternatively, the mapping function uses a "PW" label (pseudo wire label, formerly known as a virtual connection, or VC label), which is optionally added to MPLS header 50. The PW label comprises information that the egress node requires for delivering the packet to its destination, and is optionally added during the encapsulation of MPLS packets. Additional details regarding the VC label can be found in an IETF draft by Martini et al. entitled "Encapsulation Methods for Trans-port of Ethernet Frames Over IP/MPLS Networks" (IETF draft-ietf-pwe3-ethernet-encap-07.txt, May, 2004), which is incorporated herein by reference. In some embodiments, mapper 34 applies a hashing function to the MPLS and/or PW label, as will be described below.")</p> <p>Solomon at [0059] ("In this method, the mapping function used by mapper 34 of switch A is a hashing function. Various hashing functions are known in the art, and any suitable hashing function may be used in mapper 34. Since the hashing operation is performed for each packet, it is desirable to have a hashing function that is computationally simple.")</p>

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		<p>Solomon at [0060] ("As mentioned above, the hashing function typically hashes the value of MPLS label 52 to determine the selected physical port, as the MPLS label uniquely identifies tunnel 28. For example, the following hashing function may be used by mapper 34: Selected port number=1+((MPLS label) mod N), wherein N denotes the number of physical Ethernet ports in LAG group 25, and "mod" denotes the modulus operator. Assuming the values of MPLS labels are distributed uniformly over a certain range, this function achieves a uniform distribution of port allocations for the different MPLS labels. It can also be seen that all packets carrying the same MPLS label (in other words-belonging to the same MPLS tunnel) will be mapped to the same physical port.")</p> <p>Solomon at [0065] ("Mapper 34 of switch A maps each received packet to the selected physical port of LAG group 25 using the hashing function, at a hashing step 90. Mapper 34 extracts the MPLS label from each received packet and uses the hashing function to calculate the serial number of the selected physical port, which was selected by the CAC processor at step 82. Following the numerical example given above, the mapper extracts MPLS label=65647 from the packet. Substituting this value and N=3 into the hashing function gives: Selected port number=1+(65647 mod 3)=2, which is indeed the port number selected in the example above.")</p> <p>Smith '430 at 10:21-39 ("The same logical identifiers can be used to identify uplink interface bundles by each of virtual network device sub-units 122(1) and 122(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 122(1) and 122(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 122(1) is forwarding a packet via the uplink interface bundle that includes interfaces 320(9), 320(13), and 320(16), the hash value generated by virtual network device sub-unit will identify one of its interfaces 320(9) or 320(13).")</p>

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		<p>Alexander at 1:36-55 (“A typical prior art Ethernet link aggregation implementation utilizes a hardware means for distributing packets across multiple physical links, and re-aggregating them at the receiving end. This is typically due to the high speeds involved (100 Mb/s or even 1000 Mb/s per link) in the packet transfer. The use of such hardware is expensive in terms of the silicon resources required to perform the distribution and collection functions, and is also inflexible in terms of the algorithms used to determine how packets may be distributed across links. Additionally, the complexity of the distribution function when accounting for the various packet ordering and sequencing requirements of the Ethernet protocol renders a hardware-only approach difficult to design and debug. A well-partitioned, mixed hardware/ firmware approach is preferable when implementing link aggregation at high speeds. This approach, permits high speeds to be attained while at the same time preserving flexibility in implementation, which is necessary for tracking changing standards or implementing different distribution algorithms.”)</p> <p>Alexander at 2:55-67 (“Advantageously, the source context corresponding to an extracted source address is derived by producing a hash key through application of a hash function to the extracted source address. The incoming port on which the packet containing the extracted source address was received is identified. If the identified incoming port is within an aggregated grouping of incoming ports, then a port identifier representative that aggregated grouping is derived. If the identified incoming port is not within an aggregated grouping of incoming ports, then a port identifier representative of the identified incoming port is derived. The hash key and the port identifier are then combined to form the source context corresponding to the extracted source address.”)</p> <p>Alexander at 3:1-40 (“The hash function is preferably selected such that successive application of the hash function to all source and destination addresses expected to be seen by the Ethernet switch will produce a lowest value hash key, a highest value hash key, and a group of hash keys having intermediate values distributed evenly between the lowest and highest values.</p>

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		<p>The distribution table contains a separate port identifier look-up table for each aggregated grouping of outgoing ports. Advantageously, the hash key is an N bit hash key; and, each port identifier look-up table contains 2^N entries occupying 2^N consecutive locations, with each entry being an identifier of a particular one of the physical outgoing ports.</p> <p>Identifiers for particular outgoing ports are retrieved from the distribution table by extracting first and second N bit hash keys which form part of the retrieved destination and source address contexts respectively. The hash keys are combined to form an N bit connection identifier. The port identifier look-up table corresponding to the aggregated grouping represented by the retrieved destination address is selected, and the entry at the table location corresponding to the value of the N bit connection identifier is retrieved. If the address look-up table does not contain a destination address corresponding to the extracted destination address then first and second hash keys are produced by applying a hash function to the extracted source and destination addresses respectively. The hash keys are combined to form an N bit connection identifier. The incoming port on which the packet containing the extracted source address was received is identified. All of the aggregated groupings are scanned to identify all outgoing ports to which packets may be directed from the incoming port on which the packet was received. For each one of those outgoing ports, the port identifier look-up table corresponding to the aggregated grouping containing that outgoing port is selected, the entry at the table location corresponding to the value of the N bit connection identifier is retrieved, and the received packet is queued for outgoing transmission on the outgoing port corresponding to the retrieved entry.”)</p> <p>Alexander at 6:49-65 (“If the context information for the destination address indicates, however, that the target is an aggregate group (i.e. if processing branches along the “Yes” exit from FIG. 2, block 42) then the logical identifier assigned to the aggregate group is retrieved and is used to select the proper look-up table contained within the distribution table data structure. The hash keys (partial connection identifiers) stored into the contexts for the source and destination MAC addresses are obtained from address resolution unit 10 and combined to generate a “connection identifier” with the same number of bits (FIG. 2, block 44). (In the EXACT™ Ethernet switch, a Boolean exclusive-OR operation is used to combine the hash keys without increasing the number of bits.) This connection identifier is</p>

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		<p>then used to index into the selected look-up table, and finally retrieve an actual physical port index on which the packet must be transmitted (FIG. 2, block 46).")</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <pre> graph LR DA[destinationAddress] --> H1[Hash] SA[sourceAddress] --> H2[Hash] H1 --> XOR[XOR] H2 --> XOR XOR --> C[configuration] C --> S[StreamStateTable] S --> APN[AssignedPortNumber[4:0]] S --> TM[timeMark[0:1]] S --> NS[number of Streams -1] </pre> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 9	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> IsCurrent{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} IsCurrent -- No --> SetCurrent[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetCurrent --> IsAlternate{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} IsAlternate -- No --> AssignPUQ[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] IsAlternate -- Yes --> CheckStream[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] AssignPUQ --> StoreLUQ[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] CheckStream --> StoreLUQ StoreLUQ --> LUQPush[LUQ PUSH Process 313, 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and</p>

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		<p>assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-</p>

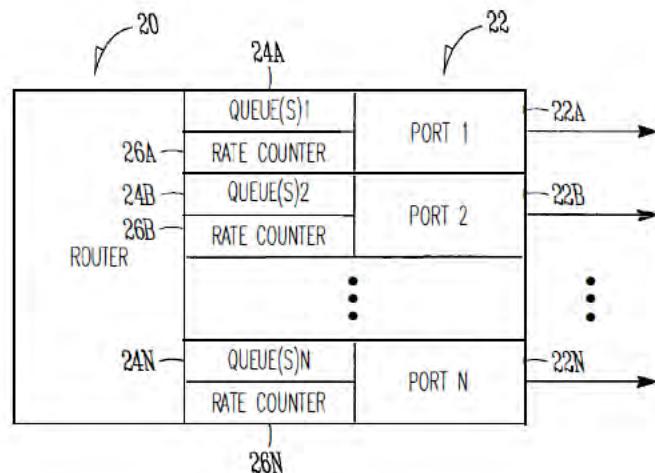
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		<p>bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more</p>

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		<p>restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual network device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet</p>

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		<p>addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and</p>

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		<p>1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 9	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 9	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, the stack includes: a header labeled "20", followed by a row labeled "24A" containing "QUEUE(S)1" and "RATE COUNTER"; another row labeled "26A" containing "QUEUE(S)2" and "RATE COUNTER"; a third row labeled "26B" containing "QUEUE(S)3" and "RATE COUNTER"; a vertical ellipsis "⋮"; and finally a row labeled "2AN" containing "QUEUE(S)N" and "RATE COUNTER". To the right of the stack, there are "PORT 1", "PORT 2", and "PORT N", each associated with a "RATE COUNTER" and a "QUEUE(S)" component. Arrows labeled "22A", "22B", and "22N" point from the router to the respective ports. The label "26N" is positioned below the "PORT N" row.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 9	The Reference
	<p style="text-align: center;">HASH(DESTINATION)=INDEX 38</p> <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>	<p><u>30</u></p>

No.	'740 Patent Claim 9	The Reference
	<pre> 40 RECEIVE PACKET TO TRANSMIT ↓ 42 COMPUTE HASH (DESTINATION ADDRESS) ↓ 44 USE RESULT AS INDEX INTO HASH TABLE ↓ 46 EXTRACT PORT NUMBER FROM HASH TABLE ↓ 48 TRANSMIT PACKET OVER PORT NUMBER </pre> <p><i>FIG. 4</i></p>	Li '914 at Figure 6

No.	'740 Patent Claim 9	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p style="text-align: center;">HASH TABLE 64</p> <p style="text-align: center;"><i>FIG.6</i></p> <p>Li '914 at Figure 7</p>

No.	'740 Patent Claim 9	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] </pre> <p style="text-align: center;"><i>FIG. 7</i></p>

Li '914 at Figure 8

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	<p>The diagram illustrates a Router with three ports (PORT 1, PORT 2, and PORT 3) and three queues (QUEUE 1, QUEUE 2, and QUEUE 3) per port. A hash operation, labeled $\text{HASH}(\text{DESTINATION}) = \text{INDEX } 82$, takes the destination address and generates an index into a hash table, labeled TABLE 80. The table contains 24 entries, each mapping a combination of port and queue to a value 1, 2, or 3. The table entries are as follows:</p> <table border="1"> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 1</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 1</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 1</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 2</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 2</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 2</td></tr> <tr><td>1</td><td>PORT 1, QUEUE 3</td></tr> <tr><td>2</td><td>PORT 2, QUEUE 3</td></tr> <tr><td>3</td><td>PORT 3, QUEUE 3</td></tr> </table>	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	1	PORT 1, QUEUE 1	2	PORT 2, QUEUE 1	3	PORT 3, QUEUE 1	1	PORT 1, QUEUE 2	2	PORT 2, QUEUE 2	3	PORT 3, QUEUE 2	1	PORT 1, QUEUE 3	2	PORT 2, QUEUE 3	3	PORT 3, QUEUE 3	<p>Li '914 at 1:30-43 ("For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.")</p> <p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of</p>
1	PORT 1, QUEUE 1																																					
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		<p>Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an</p>

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		<p>index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p>

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		<p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li '914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione '125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione '125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p>

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		<p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast</p>

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		<p>packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p> <p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>

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		<p>Figure 2</p> <p>Figure 3</p> <p>Figure 4</p> <p>Figure 5</p>

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10[a]	The method according to claim 9, wherein applying the hashing function comprises	The Reference discloses the method according to claim 9, wherein applying the hashing function comprises determining a hashing size responsively to a number of at least some of the first and second physical links.

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	determining a hashing size responsively to a number of at least some of the first and second physical links,	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Singh, Solomon, and Alexander.</p> <p>Below are examples of such references.</p> <p>Singh at 15:15-39 ("The number of crossbars that are required in a system is dependent on how many links are being used to create the backplane channels. There should be an even number of crossbars and they would be divided evenly across the switch cards. The following equation, for most cases, provides the correct number of crossbars:</p> $\# \text{ of Crossbars} = (\# \text{ links per ingress channel} \times \# \text{ of ingress channels per port} \times \# \text{ of port cards} \times \text{speedup}) / 32.$ <p>For the 8x8 configuration, the # of crossbars should be multiplied by $(4 \times \# \text{ of iMS}) / (\# \text{ backplane channels per port card})$. The number of port cards should be rounded up to the nearest supported configuration, i.e. 8, 16, or 32. The speedup in the case of crossbars should be the fractional speedup that is desired.</p> <p>Example to determine the number of arbiters and cross-bars for the following system:</p> <p>4 channel port cards (40 Gbps) 8 links per channel 16 port cards Speedup=1.5 $\# \text{ of arbiters} = (4 \times 2 \times 2) / 2 = 8$</p>

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		<p># of crossbars=(8x4x16x1.5)/32=24. This would give 3crossbars per arbiter.”)</p> <p>Solomon at [0060] (“As mentioned above, the hashing function typically hashes the value of MPLS label 52 to determine the selected physical port, as the MPLS label uniquely identifies tunnel 28. For example, the following hashing function may be used by mapper 34: Selected port number=l+((MPLS label) mod N), wherein N denotes the number of physical Ethernet ports in LAG group 25, and "mod" denotes the modulus operator. Assuming the values of MPLS labels are distributed uniformly over a certain range, this function achieves a uniform distribution of port allocations for the different MPLS labels. It can also be seen that all packets carrying the same MPLS label (in other words belonging to the same MPLS tunnel) will be mapped to the same physical port.”)</p> <p>Alexander at 3:1-40 (“The hash function is preferably selected such that successive application of the hash function to all source and destination addresses expected to be seen by the Ethernet switch will produce a lowest value hash key, a highest value hash key, and a group of hash keys having intermediate values distributed evenly between the lowest and highest values.</p> <p>The distribution table contains a separate port identifier look-up table for each aggregated grouping of outgoing ports. Advantageously, the hash key is an N bit hash key; and, each port identifier look-up table contains 2^N entries occupying 2^N consecutive locations, with each entry being an identifier of a particular one of the physical outgoing ports.</p> <p>Identifiers for particular outgoing ports are retrieved from the distribution table by extracting first and second N bit hash keys which form part of the retrieved destination and source address contexts respectively. The hash keys are combined to form an N bit connection identifier. The port identifier look-up table corresponding to the aggregated grouping represented by the retrieved destination address is selected, and the entry at the table location corresponding to the value of the N bit connection identifier is retrieved. If the address look-up table does not contain a destination address corresponding to the extracted destination</p>

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		<p>address then first and second hash keys are produced by applying a hash function to the extracted source and destination addresses respectively. The hash keys are combined to form an N bit connection identifier. The incoming port on which the packet containing the extracted source address was received is identified. All of the aggregated groupings are scanned to identify all outgoing ports to which packets may be directed from the incoming port on which the packet was received. For each one of those outgoing ports, the port identifier look-up table corresponding to the aggregated grouping containing that outgoing port is selected, the entry at the table location corresponding to the value of the N bit connection identifier is retrieved, and the received packet is queued for outgoing transmission on the outgoing port corresponding to the retrieved entry.”)</p>
10[b]	applying the hashing function to the at least one of the frame attributes to produce a hashing key,	<p>The Reference discloses applying the hashing function to the at least one of the frame attributes to produce a hashing key.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, and Alexander.</p> <p>Below is an example.</p> <p>Alexander at 3:1-40 (“The hash function is preferably selected such that successive application of the hash function to all source and destination addresses expected to be seen by the Ethernet switch will produce a lowest value hash key, a highest value hash key, and a group of hash keys having intermediate values distributed evenly between the lowest and highest</p>

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		<p>values.</p> <p>The distribution table contains a separate port identifier look-up table for each aggregated grouping of outgoing ports. Advantageously, the hash key is an N bit hash key; and, each port identifier look-up table contains 2^N entries occupying 2^N consecutive locations, with each entry being an identifier of a particular one of the physical outgoing ports.</p> <p>Identifiers for particular outgoing ports are retrieved from the distribution table by extracting first and second N bit hash keys which form part of the retrieved destination and source address contexts respectively. The hash keys are combined to form an N bit connection identifier. The port identifier look-up table corresponding to the aggregated grouping represented by the retrieved destination address is selected, and the entry at the table location corresponding to the value of the N bit connection identifier is retrieved. If the address look-up table does not contain a destination address corresponding to the extracted destination address then first and second hash keys are produced by applying a hash function to the extracted source and destination addresses respectively. The hash keys are combined to form an N bit connection identifier. The incoming port on which the packet containing the extracted source address was received is identified. All of the aggregated groupings are scanned to identify all outgoing ports to which packets may be directed from the incoming port on which the packet was received. For each one of those outgoing ports, the port identifier look-up table corresponding to the aggregated grouping containing that outgoing port is selected, the entry at the table location corresponding to the value of the N bit connection identifier is retrieved, and the received packet is queued for outgoing transmission on the outgoing port corresponding to the retrieved entry.”)</p> <p>Alexander at 5:10-35 (“If a packet arrives bearing a source Ethernet MAC address that was not found in look-up table 12 by address resolution unit 10, learning function 16 is invoked to update look-up table 12 with the new address (i.e. processing branches along the “No” exit from FIG. 2, block 36). Learning function 16 first computes a hash function on the source Ethernet MAC address, generating an N-bit hash key (“partial connection identifier”) from the 48-bit MAC address, where N is some small integer in the range of 3 to 8 (FIG. 2, block</p>

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		<p>38). The physical port on which the packet arrived is then determined. If the physical port is found to be associated with an aggregate group (i.e., it is one of a set of ports that have been bound into a single logical port), then the logical identifier assigned to the aggregate group is also determined. The hash key is then stored into address look-up table 12 in conjunction with the actual Ethernet MAC address and the port identifier (FIG. 2, block 40). The physical port identifier is used if the port is not part of an aggregate group (i.e. if processing branched along the "No" exit from block 30 and through block 32), while the logical identifier is used for ports that have been aggregated (i.e. if processing branched along the "Yes" exit from block 30 and through block 34). The hash key and port identifier are considered to form the "context" for the given MAC address.”)</p> <p>Alexander at 5:36-46 (“The hash function should be selected to ensure an even distribution of hash key values over the range of MAC addresses that are expected to be seen by the Ethernet switch. As a specific example, the EXACT™ Ethernet switch system employs an exclusive-OR based hash function, wherein the 48-bit MAC address is divided into 16-bit blocks, which are then exclusive-ORed together to form a single 16-bit number; the 3 least significant bits (LSBs) of this number are taken to produce a 3-bit hash key. Other schemes such as CRC-based or checksum-based hashes may also be used.”)</p> <p>Alexander at 6:49-65 (“If the context information for the destination address indicates, however, that the target is an aggregate group (i.e. if processing branches along the “Yes” exit from FIG. 2, block 42) then the logical identifier assigned to the aggregate group is retrieved and is used to select the proper look-up table contained within the distribution table data structure. The hash keys (partial connection identifiers) stored into the contexts for the source and destination MAC addresses are obtained from address resolution unit 10 and combined to generate a “connection identifier” with the same number of bits (FIG. 2, block 44). (In the EXACT™ Ethernet switch, a Boolean exclusive-OR operation is used to combine the hash keys without increasing the number of bits.) This connection identifier is then used to index into the selected look-up table, and finally retrieve an actual physical port index on which the packet must be transmitted (FIG. 2, block 46).”)</p>

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10[c]	calculating a modulo of a division operation of the hashing key by the hashing size, and	<p>The Reference discloses calculating a modulo of a division operation of the hashing key by the hashing size.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Singh, and Alexander.</p> <p>Below are examples of such references.</p> <p>Singh at 9:30-43 ("The ratio between the number of line ingress links and the number of links carrying data to the backplane gives the backplane speedup for the system. In this example, there are 10 ingress links into the MS and 20 links (2 backplane channels) carrying that data to the backplane. This gives a backplane speedup of 2x. As another example, with 8 ingress links and 12 backplane links, there is a speedup of 1.5x. It should be noted that in addition to the backplane speedup, there is also an ingress/egress speedup. With 10 ingress links capable of carrying 2 Gbps each of raw data, this presents a 20 Gbps interface to the MS. An OC-192 only has approximately 10 Gbps worth of data. Taking into account cell overhead and cell quantization inefficiencies, there still remains excess capacity in the links.")</p> <p>Singh at 11:29-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a channel, whether it is a backplane channel or an ingress/egress channel.</p>

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		<p>There is no logical relationship between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 15:15-39 (“The number of crossbars that are required in a system is dependent on how many links are being used to create the backplane channels. There should be an even number of crossbars and they would be divided evenly across the switch cards. The following equation, for most cases, provides the correct number of crossbars:</p> $\text{# of Crossbars} = (\text{\# links per ingress channel} \times \text{\# of ingress channels per port} \times \text{\# of port cards} \times \text{speedup}) / 32.$ <p>For the 8x8 configuration, the # of crossbars should be multiplied by $(4 \times \text{\# of iMS}) / (\text{\# backplane channels per port card})$. The number of port cards should be rounded up to the nearest supported configuration, i.e. 8, 16, or 32. The speedup in the case of crossbars should be the fractional speedup that is desired.</p> <p>Example to determine the number of arbiters and cross-bars for the following system:</p> <p>4 channel port cards (40 Gbps) 8 links per channel 16 port cards Speedup=1.5 $\text{\# of arbiters} = (4 \times 2 \times 2) / 2 = 8$ $\text{\# of crossbars} = (8 \times 4 \times 16 \times 1.5) / 32 = 24$. This would give 3 crossbars per arbiter.”)</p> <p>Singh at 16:28-44 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 16x16 and 32x32 is the organization of the switchplane. The port card remains the same. Backplane channels 1 and 2 are used for the backplane connectivity. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the</p>

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		<p>ingress and egress to the traffic manager. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 16, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p> <p>Singh at 17:31-49 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 8x8 and 16x16 is the organization of the switchplane. The port card remains the same. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Links 0-7 and 24-31 on the arbiters would not be used and would be powered off. Links 0-7 and 24-31 on the crossbars would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Backplane channels 1 and 2 are used for the backplane connectivity. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 8, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p> <p>Alexander at 5:36-46 (“The hash function should be selected to ensure an even distribution of hash key values over the range of MAC addresses that are expected to be seen by the Ethernet switch. As a specific example, the EXACT™ Ethernet switch system employs an exclusive-OR based hash function, wherein the 48-bit MAC address is divided into 16-bit blocks, which are then exclusive-ORed together to form a single 16-bit number; the 3 least significant bits (LSBs) of this number are taken to produce a 3-bit hash key. Other schemes such as CRC-based or checksum-based hashes may also be used.”)</p>
10[d]	selecting the first and second physical links	The Reference discloses selecting the first and second physical links responsively to the modulo.

No.	'740 Patent Claim 10	The Reference
	responsively to the modulo.	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Singh, and Alexander.</p> <p>Below are examples of such references.</p> <p>Singh at 9:30-43 ("The ratio between the number of line ingress links and the number of links carrying data to the backplane gives the backplane speedup for the system. In this example, there are 10 ingress links into the MS and 20 links (2 backplane channels) carrying that data to the backplane. This gives a backplane speedup of 2x. As another example, with 8 ingress links and 12 backplane links, there is a speedup of 1.5x. It should be noted that in addition to the backplane speedup, there is also an ingress/egress speedup. With 10 ingress links capable of carrying 2 Gbps each of raw data, this presents a 20 Gbps interface to the MS. An OC-192 only has approximately 10 Gbps worth of data. Taking into account cell overhead and cell quantization inefficiencies, there still remains excess capacity in the links.")</p> <p>Singh at 11:29-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a channel, whether it is a backplane channel or an ingress/egress channel. There is no logical relationship between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p>

No.	'740 Patent Claim 10	The Reference
		<p>Singh at 15:15-39 ("The number of crossbars that are required in a system is dependent on how many links are being used to create the backplane channels. There should be an even number of crossbars and they would be divided evenly across the switch cards. The following equation, for most cases, provides the correct number of crossbars:</p> $\text{# of Crossbars} = (\text{\# links per ingress channel} \times \text{\# of ingress channels per port} \times \text{\# of port cards} \times \text{speedup}) / 32.$ <p>For the 8x8 configuration, the # of crossbars should be multiplied by $(4 \times \text{\# of iMS}) / (\text{\# backplane channels per port card})$. The number of port cards should be rounded up to the nearest supported configuration, i.e. 8, 16, or 32. The speedup in the case of crossbars should be the fractional speedup that is desired.</p> <p>Example to determine the number of arbiters and cross-bars for the following system:</p> <p>4 channel port cards (40 Gbps) 8 links per channel 16 port cards Speedup=1.5 $\text{\# of arbiters} = (4 \times 2 \times 2) / 2 = 8$ $\text{\# of crossbars} = (8 \times 4 \times 16 \times 1.5) / 32 = 24$. This would give 3crossbars per arbiter.”)</p> <p>Singh at 16:28-44 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 16x16 and 32x32 is the organization of the switchplane. The port card remains the same. Backplane channels 1 and 2 are used for the backplane connectivity. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 16, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo</p>

No.	'740 Patent Claim 10	The Reference
		<p>10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p> <p>Singh at 17:31-49 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 8x8 and 16x16 is the organization of the switchplane. The port card remains the same. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Links 0-7 and 24-31 on the arbiters would not be used and would be powered off. Links 0-7 and 24-31 on the crossbars would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Backplane channels 1 and 2 are used for the backplane connectivity. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 8, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p> <p>Alexander at 5:36-46 (“The hash function should be selected to ensure an even distribution of hash key values over the range of MAC addresses that are expected to be seen by the Ethernet switch. As a specific example, the EXACT™ Ethernet switch system employs an exclusive-OR based hash function, wherein the 48-bit MAC address is divided into 16-bit blocks, which are then exclusive-ORed together to form a single 16-bit number; the 3 least significant bits (LSBs) of this number are taken to produce a 3-bit hash key. Other schemes such as CRC-based or checksum-based hashes may also be used.”)</p>

No.	'740 Patent Claim 11	The Reference
11	The method according to claim 10, wherein selecting	The Reference discloses the method according to claim 10, wherein selecting the first and second physical links responsively to the modulo comprises selecting the first and second

No.	'740 Patent Claim 11	The Reference
	<p>the first and second physical links responsively to the modulo comprises selecting the first and second physical links responsively to respective first and second subsets of bits in a binary representation of the modulo.</p>	<p>physical links responsively to respective first and second subsets of bits in a binary representation of the modulo.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Solomon, and Alexander.</p> <p>Below are examples of such references.</p> <p>Solomon at [0054] ("Having selected a physical port, RSVP-TE processor 30 of switch A now generates a suitable MPLS label, at a label generation step 64. The preceding node upstream of switch A will subsequently attach this MPLS label to all MPLS packets transmitted through tunnel 28 to switch A. The label is assigned, in conjunction with the mapping function of mapper 34, so as to ensure that all MPLS packets carrying this label are switched through the physical port that was selected for this tunnel at step 62. For this purpose, RSVP-TE processor 30 of switch A dedicates a sub-set of the bits of MPLS label 52 to encode the serial number of the selected physical port. For example, the four least-significant bits of MPLS label 52 may be used for encoding the selected port number. This configuration is suitable for representing LAG groups having up to 16 physical ports ($N < 16$). The remaining bits of MPLS label 52 may be chosen at random or using any suitable method known in the art.")</p> <p>Solomon at [0056] ("Mapper 34 of switch A maps the received packets belonging to tunnel 28 to the selected physical Ethernet port at a mapping step 70. For this purpose, mapper 34 extracts the MPLS label from each received packet and decodes the selected physical port number from the dedicated sub-set of bits, such as the four LSB, as described in step 64 above. The decoded value is used for mapping the packet to the selected physical port, which</p>

No.	'740 Patent Claim 11	The Reference
		<p>was allocated by the CAC processor at step 62 above. In the four-bit example described above, the mapping function may be written explicitly as: Selected port number=((MPLS label) and (0x0000F)), wherein "and" denotes the "bitwise and" operator.”)</p> <p>Alexander at 5:36-46 (“The hash function should be selected to ensure an even distribution of hash key values over the range of MAC addresses that are expected to be seen by the Ethernet switch. As a specific example, the EXACT™ Ethernet switch system employs an exclusive-OR based hash function, wherein the 48-bit MAC address is divided into 16-bit blocks, which are then exclusive-ORED together to form a single 16-bit number; the 3 least significant bits (LSBs) of this number are taken to produce a 3-bit hash key. Other schemes such as CRC-based or checksum-based hashes may also be used.”)</p>

No.	'740 Patent Claim 12	The Reference
12	The method according to claim 1, wherein the at least one of the frame attributes comprises at least one of a layer 2 header field, a layer 3 header field, a layer 4 header field, a source Internet Protocol (IP) address, a destination IP address, a source medium access control (MAC) address, a destination MAC address, a source Transmission Control Protocol (TCP) port and a destination TCP port.	<p>The Reference discloses the method according to claim 1, wherein the at least one of the frame attributes comprises at least one of a layer 2 header field, a layer 3 header field, a layer 4 header field, a source Internet Protocol (IP) address, a destination IP address, a source medium access control (MAC) address, a destination MAC address, a source Transmission Control Protocol (TCP) port and a destination TCP port.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 13	The Reference
13[preamble]	A method for communication, comprising:	<p>The Reference discloses a method for communication.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

No.	'740 Patent Claim 13	The Reference
		System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
13[a]	coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel;	<p>The Reference discloses coupling a network node to one or more interface modules using a first group of first physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
13[b]	coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel;	<p>The Reference discloses coupling each of the one or more interface modules to a communication network using a second group of second physical links arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified</p>

No.	'740 Patent Claim 13	The Reference
		<p>schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 13	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also part of the crossbar fabric interconnect (40). The management card (60) is labeled as being connected via a PCI interface. Arrows indicate data flow between the line cards and their respective backplane interconnects, and between the backplane interconnects and the crossbar fabric interconnect.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

No.	'740 Patent Claim 13	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 13	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal structure of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected sequentially: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections to the LIC include two ports on the left and a connection to the "AMCC CYCLONE SWITCH FABRIC" on the right. The switch fabric is part of the "S8005 SERIES". On the left side, there is a vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. On the right side, there is another vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. Red boxes highlight the connection points between the LIC and the switch fabric, specifically around the AMCC CYCLONE SWITCH FABRIC block.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 13	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 13	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual")</p>	

No.	'740 Patent Claim 13	The Reference
		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
13[c]	receiving a data frame having frame attributes sent between the communication network and the network node:	<p>The Reference discloses receiving a data frame having frame attributes sent between the communication network and the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
13[d]	selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group	<p>The Reference discloses selecting, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

No.	'740 Patent Claim 13	The Reference
	link out of the second group; and	<p>the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 13	The Reference
		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate various data paths: 1Gbps Ethernet (201, 204), 2Gbps FC (202, 203), 10Gbps FC (205, 206), 10Gbps Ethernet (20n), and InfiniBand (IB) (20n). Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (70) via a PCI bus. The management card is also connected to the backplane interconnect.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

No.	'740 Patent Claim 13	The Reference
		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane charmel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane charmel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane channels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub-system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

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		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including selecting physical links over which to send a packet. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu

No.	'740 Patent Claim 13	The Reference
	<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

No.	'740 Patent Claim 13	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

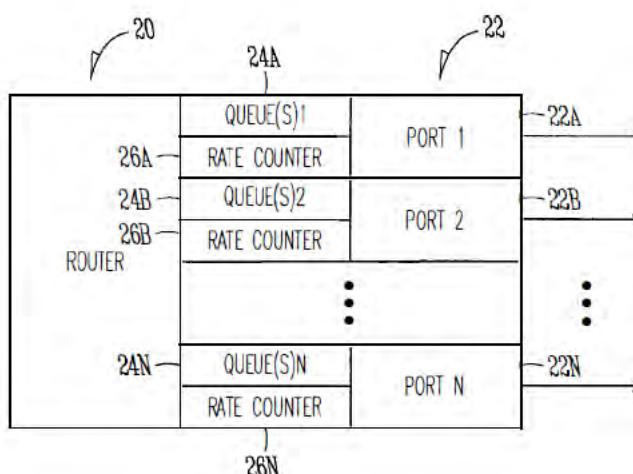
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

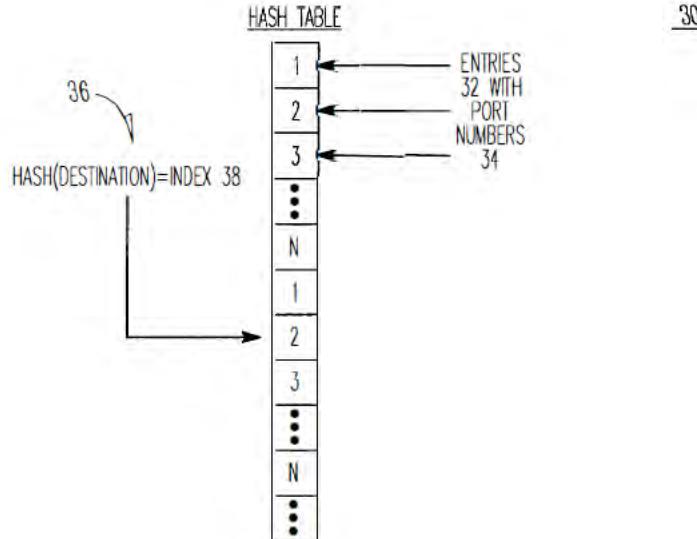
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 13	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. At the top, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, these components are: "QUEUE(S)1", "RATE COUNTER", "QUEUE(S)2", "RATE COUNTER", a vertical ellipsis "⋮", "QUEUE(S)N", and "RATE COUNTER". To the left of the first component is a reference numeral "26A", and to the left of the second component is "26B". To the left of the Nth component is "26N". Above the first component is "24A", above the second is "24B", and above the Nth is "24N". Above the entire stack is "20". To the right of the stack are three output ports labeled "PORT 1", "PORT 2", and "PORT N". Arrows labeled "22A", "22B", and "22N" point from the stack to each respective port. Ellipses between the ports indicate additional ports.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 13	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 13	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 13	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports: PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to specific output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 13	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 13	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 13	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

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13[e]	sending the data frame over the selected first and second physical links,	<p>The Reference discloses sending the data frame over the selected first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p>

No.	'740 Patent Claim 13	The Reference
		<p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 13	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

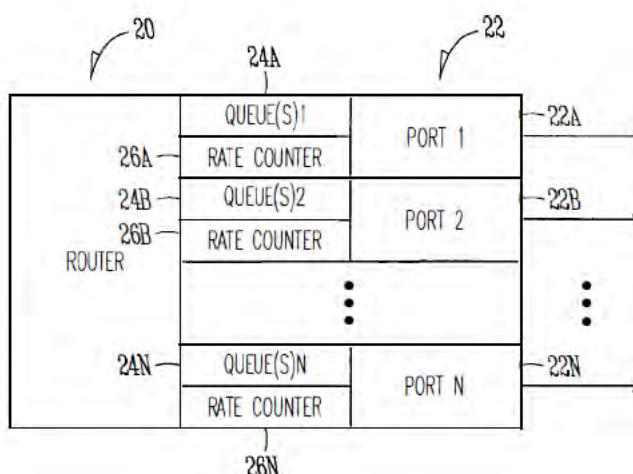
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

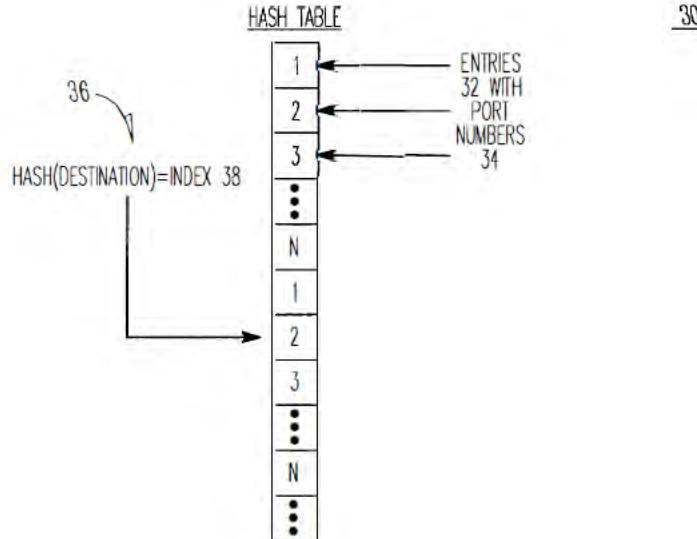
No.	'740 Patent Claim 13	The Reference
		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 13	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 13	The Reference
		 <p>The diagram illustrates a router architecture. At the top, a curved arrow labeled 20 points to a box labeled 'ROUTER'. Inside the 'ROUTER' box, there are two columns of components. The left column contains 'QUEUE(S)1', 'RATE COUNTER', 'QUEUE(S)2', 'RATE COUNTER', and 'QUEUE(S)N', each associated with a reference number 24A, 24B, 26A, 26B, and 2AN respectively. The right column contains 'PORT 1', 'PORT 2', and 'PORT N', each associated with a reference number 22A, 22B, and 22N respectively. Arrows indicate connections from the queues and counters to their corresponding ports. Below the router box, a curved arrow labeled 26N points to the bottom edge of the box.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 13	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 13	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 13	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), ..., PORT N. Arrows labeled 62A, 62B, ..., 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed 1 through N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to different output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 13	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 13	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

No.	'740 Patent Claim 13	The Reference
		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

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13[f]	coupling the network node to the one or more interface modules and	<p>The Reference discloses coupling the network node to the one or more interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
13[g]	coupling each of the one or more interface modules to the communication network comprising	<p>The Reference discloses coupling each of the one or more interface modules to the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
13[h]	specifying bandwidth requirements comprising at least one of a committed information rate (CIR), a peak information rate (PIR) and an excess	<p>The Reference discloses specifying bandwidth requirements comprising at least one of a committed information rate (CIR), a peak information rate (PIR) and an excess information rate (EIR) of a communication service provided by the communication network to the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

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	information rate (EIR) of a communication service provided by the communication network to the network node, and	<p>the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, and Solomon.</p> <p>Below is an example.</p> <p>Solomon at [0023] ("In another embodiment, establishing the path includes receiving an indication of a requested service property of the flow, and selecting the port includes assigning the port to the flow so as to comply with the requested service property. In a disclosed embodiment, the requested service property includes at least one of a guaranteed bandwidth, a peak bandwidth and a class-of-service. Additionally or alternatively, assigning the port includes selecting the port having a maximum available bandwidth out of the plurality of aggregated ports. Further additionally or alternatively, assigning the port includes selecting the port having a minimum available bandwidth out of the plurality of aggregated ports, which is still greater than or equal to the guaranteed bandwidth.")</p> <p>Solomon at [0050] ("The method of FIG. 3 begins when the preceding node asks to establish a part of tunnel 28 (comprising one or more hops) for sending MPLS packets to MPLS/LAG switch 26 A. The preceding node requests and then receives the MPLS label, which it will subsequently attach to all packets that are sent to MPLS/LAG switch 26 labeledA. The preceding node sends downstream an RSVP-TE PATH message augmented with a LABEL_REQUEST object, as defined by RSVP-TE, to MPLS/LAG switch A, at a label requesting step 60. The PATH message typically comprises information regarding service properties that are requested for tunnel 28. The service properties may comprise a guaranteed bandwidth (sometimes denoted CIR-Committed Information Rate) and a peak bandwidth (sometimes denoted PIR-Peak Information Rate), as well as a requested CoS (Class of Service-a measure of packet priority).")</p>

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13[i]	allocating a bandwidth for the communication service over the first and second physical links responsively to the bandwidth requirements.	<p>The Reference discloses allocating a bandwidth for the communication service over the first and second physical links responsively to the bandwidth requirements.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

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14[preamble]	A method for connecting user ports to a communication network, comprising:	<p>The Reference discloses a method for connecting user ports to a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
14[a]	coupling the user ports to one or more user interface modules;	<p>The Reference discloses coupling the user ports to one or more user interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

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		the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
14[b]	coupling each user interface module to the communication network via a backplane using two or more backplane traces arranged in parallel,	<p>The Reference discloses coupling each user interface module to the communication network via a backplane using two or more backplane traces arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric</p>

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		<p>topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p> <p>The diagram illustrates a network interface architecture. At the top, four line cards (301, 302, 303, 30n) are shown. Line card 301 has four ports: 201 (1Gbps Ethernet), 202 (2Gbps FC), 203 (2Gbps FC), and 204 (1Gbps Ethernet). Line card 302 has two ports: 205 (10Gbps FC) and 206 (10Gbps Ethernet). Line card 303 has one port: 207 (IB). Line card 30n has one port: 20n (IB). Each line card is connected to a corresponding backplane interconnect (351, 352, 353, 35n). These backplane interconnects are connected to a central crossbar fabric interconnect (40). A management card (60) is also connected to the crossbar fabric interconnect (40) via a backplane interconnect (70). An Ethernet interface for configuration (PCI) is shown at the bottom left, connected to the management card (60).</p> <p style="text-align: right;">FIG. 1</p>

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		<p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTm); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208 through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

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		<p>Fig. 3</p> <p>The diagram illustrates the internal architecture of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections from the LIC include two fiber optic ports on the left and a connection to the "AMCC CYCLONE SWITCH FABRIC" on the right. The switch fabric is part of the "S8005 SERIES". On the left side, there is a vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. On the right side, there is another vertical stack of line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. Red boxes highlight the connection points between the LIC and the switch fabric, specifically around the AMCC CYCLONE SWITCH FABRIC and the S8005 SERIES labels.</p> <p>Singh at Figure 4 (annotations added)</p>

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		<p>Fig. 4</p> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
14[c]	at least one of said backplane traces being bi-directional and operative to communicate in both an upstream direction and a downstream direction;	<p>The Reference discloses at least one of said backplane traces being bi-directional and operative to communicate in both an upstream direction and a downstream direction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces</p>

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		<p>35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

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		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also part of the crossbar fabric interconnect (40). The management card (60) is labeled as being connected via a PCI bus. Arrows indicate data flow between the line cards and the management card, and between the backplane interconnects.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

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		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 14	The Reference
		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 14	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual")</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
14[d]	receiving data frames sent between the user ports and the communication network, the data frames having respective frame attributes;	<p>The Reference discloses receiving data frames sent between the user ports and the communication network, the data frames having respective frame attributes.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
14[e]	for each data frame, selecting responsively to at least one of the respective frame attributes a backplane trace from the two or	<p>The Reference discloses for each data frame, selecting responsively to at least one of the respective frame attributes a backplane trace from the two or more backplane traces.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

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	more backplane traces; and	<p>skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar interconnect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

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		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also part of the crossbar fabric interconnect (40). The management card (60) is labeled as being connected via a PCI interface. Arrows indicate data flow between the line cards and their respective backplane interconnects, and between the backplane interconnects and the crossbar fabric interconnect.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

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		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

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		<p>Fig. 3</p> <p>The diagram illustrates the internal architecture of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections to the LIC include two ports on the left and a connection to the "AMCC CYCLONE SWITCH FABRIC" on the right. The switch fabric is part of the "S8005 SERIES". On the left side, there is a vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. On the right side, there is another vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. Red boxes highlight the connection points between the LIC and the switch fabric, specifically around the AMCC CYCLONE SWITCH FABRIC block.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 14	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 14	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu • Li ’914 • Borgione ’125 <p>DeJager ’424 at Figure 2</p>

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		<p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 14	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

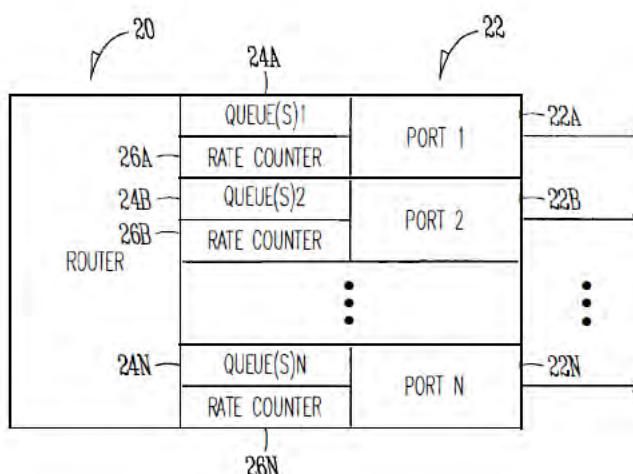
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

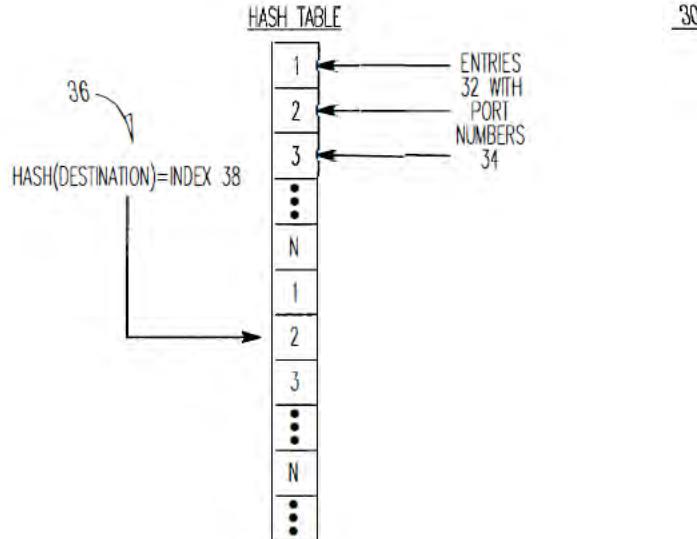
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 14	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 14	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, it includes: a header labeled "20", followed by a section labeled "24A" containing "QUEUE(S)1" and a "RATE COUNTER"; another section labeled "24B" containing "QUEUE(S)2" and a "RATE COUNTER"; a vertical ellipsis "⋮"; and finally a section labeled "24N" containing "QUEUE(S)N" and a "RATE COUNTER". To the right of these sections are two output ports labeled "PORT 1" and "PORT 2", which are connected to arrows labeled "22A" and "22B" respectively. Below the router box is a label "26N".</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 14	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 14	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 14	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports: PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to specific output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 14	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p style="text-align: center;"><i>FIG. 7</i></p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 14	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

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14[f]	sending the data frame over the selected backplane trace;	<p>The Reference discloses sending the data frame over the selected backplane trace.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may</p>

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		<p>be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p> <p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (351, 352, 353, 35n). Line card 301 has four ports: 201 (1Gbps Ethernet), 202 (2Gbps FC), 203 (2Gbps FC), and 204 (1Gbps Ethernet). Line card 302 has one port 205 (10Gbps FC). Line card 303 has one port 206 (10Gbps Ethernet). Line card 30n has one port 20n (IB). Arrows indicate bidirectional communication between each line card and its corresponding backplane interconnect. The backplane interconnects are interconnected via a crossbar fabric interconnect (40). A management card (60) is connected to a backplane interconnect (70) via a PCI bus. An Ethernet interface for configuration (labeled 10) is also connected to the management card (60).</p> <p>FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system</p>

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		<p>(iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 11:28-38 (“FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 13:35-48 (“FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).”)</p> <p>Singh at 18:44-53 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.”)</p> <p>Singh at 18:61-19:9 (“The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below,</p>

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		<p>they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port card backplane data links from crossbars. Step 1706f stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> TPT[TRANSMITTING PACKETS FROM TM 1702] TPT --> API[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] API --> SCIE[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] SCIE --> APIP[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] SCIE --> SIPS[STORING PACKETS IN IMS 1706b] SCIE --> APBL[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] SCIE --> SPB[SUPPLYING PACKETS TO CROSSBAR 1706d] SCIE --> SPPF[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] SCIE --> SCF[SENSING CONNECTION FAULT 1710] SCIE --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to</p>

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		<p>operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p>

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		<p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

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		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

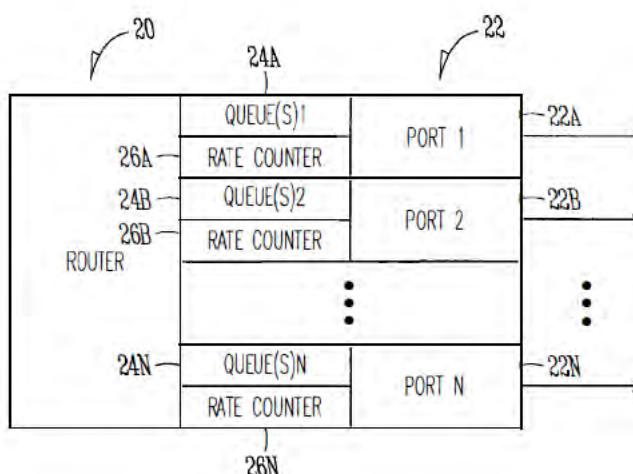
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

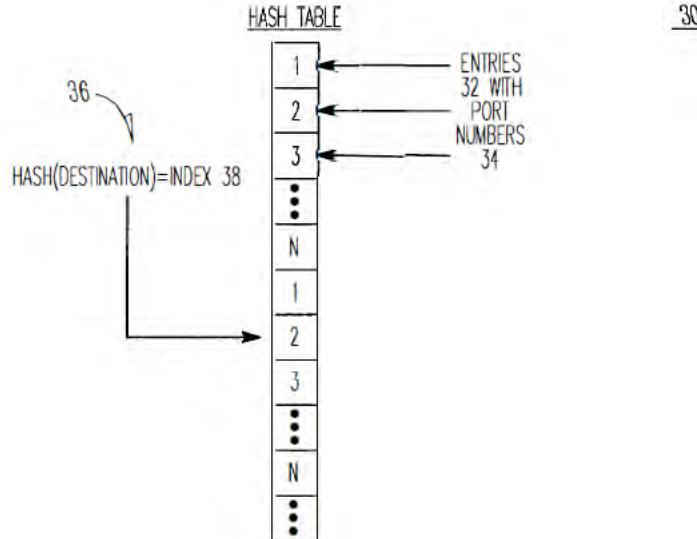
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 14	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 14	The Reference
		 <p>The diagram illustrates a router architecture. At the top, a box labeled 'ROUTER' contains a vertical stack of components. From top to bottom, these components are: 'QUEUE(S)1', 'RATE COUNTER', 'QUEUE(S)2', 'RATE COUNTER', a vertical ellipsis '...', 'QUEUE(S)N', and 'RATE COUNTER'. To the left of the first component is a reference numeral '26A'. To the left of the second component is '24A'. To the left of the third component is '26B'. To the left of the fourth component is '24B'. To the left of the fifth component is '2AN'. Above the first component is '20'. Above the second component is '24A'. Above the third component is '22'. To the right of the stack, there are three horizontal arrows pointing to the right, labeled 'PORT 1', 'PORT 2', and 'PORT N' respectively. Below the stack, a bracket labeled '26N' spans the width of the stack. Arrows point from 'PORT 1', 'PORT 2', and 'PORT N' to the right side of the stack.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 14	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 14	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 14	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 14	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p style="text-align: center;"><i>FIG. 7</i></p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 14	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

No.	'740 Patent Claim 14	The Reference
		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

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14[g]	said sending comprising communicating along said at least one of said backplane traces.	<p>The Reference discloses said sending comprising communicating along said at least one of said backplane traces.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 15	The Reference
15[preamble]	A method for connecting user ports to a communication network, comprising:	<p>The Reference discloses a method for connecting user ports to a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
15[a]	coupling the user ports to one or more user interface modules;	<p>The Reference discloses coupling the user ports to one or more user interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

No.	'740 Patent Claim 15	The Reference
		the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
15[b]	coupling each user interface module to the communication network via a backplane using two or more backplane traces arranged in parallel;	<p>The Reference discloses coupling each user interface module to the communication network via a backplane using two or more backplane traces arranged in parallel.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric</p>

No.	'740 Patent Claim 15	The Reference
		<p>topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p> <p>The diagram illustrates a network interface architecture. At the top, four line cards (301, 302, 303, 30n) are shown. Line card 301 has four ports: 201 (1Gbps Ethernet), 202 (2Gbps FC), 203 (2Gbps FC), and 204 (1Gbps Ethernet). Line card 302 has two ports: 205 (10Gbps FC) and 206 (10Gbps Ethernet). Line card 303 has one port: 207 (IB). Line card 30n has one port: 20n (IB). Each line card is connected to a corresponding backplane interconnect (351, 352, 353, 35n). These backplane interconnects are connected to a central crossbar fabric interconnect (40). A management card (60) is also connected to the crossbar fabric interconnect (40) via a backplane interconnect (70). An Ethernet interface for configuration (PCI) is shown at the bottom left, connected to the management card (60).</p> <p style="text-align: right;">FIG. 1</p>

No.	'740 Patent Claim 15	The Reference
		<p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208 through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 15	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal architecture of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections from the LIC include two ports on the left and a connection to the "AMCC CYCLONE SWITCH FABRIC". The switch fabric is part of the "S8005 SERIES". On the left side, there is a vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. On the right side, there is another vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. Red boxes highlight the connection points between the LIC and the switch fabric, and between the switch fabric and the line interfaces.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 15	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] A4 --> S4[SENSING CONNECTION FAULT 1710] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), network device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
15[c]	receiving data frames sent between the user ports and the communication network, the data frames having respective frame attributes;	<p>The Reference discloses receiving data frames sent between the user ports and the communication network, the data frames having respective frame attributes.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
15[d]	for each data frame, selecting responsively to at least one of the respective frame attributes a backplane trace from the two or	<p>The Reference discloses for each data frame, selecting responsively to at least one of the respective frame attributes a backplane trace from the two or more backplane traces.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

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	more backplane traces; and	<p>skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <pre> graph LR DA[destinationAddress] --> H1[Hash] SA[sourceAddress] --> H2[Hash] H1 --> XOR[XOR] H2 --> XOR XOR --> C[configuration] C --> SId[Stream Id] SId --> SST[StreamStateTable] SST --> APPN[AssignedPortNumber[4:0]] SST --> TM[timeMark[0:1]] SST --> NS[number of Streams - 1] </pre>

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		<p>DeJager '424 at Figure 3A</p> <pre> graph TD START((START)) --> 302[Receive Packet] 302 --> 303[Assign Packet to Target Port Group] 303 --> 304[Hash and Mask Packet Address into a Stream ID] 304 --> 306{Is the Bit for that Stream ID set in the Current Time Mark Register?} 306 -- Yes --> 308[Set the Bit for that Stream ID in the Current Time Mark Register] 308 --> 310{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} 310 -- No --> 312[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] 312 --> 313[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] 310 -- Yes --> 314[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] 313 --> 316[LUQ PUSH Process] 316 --> 318[Forward (POP) Queued Packet] 318 --> 320[LUQ POP Process] 320 --> END((END)) </pre> <p>FIG. 3A</p>

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		<p>DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.")</p> <p>DeJager '424 at 5:19-30 ("Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).")</p> <p>DeJager '424 at 5:42-45 ("Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.")</p>

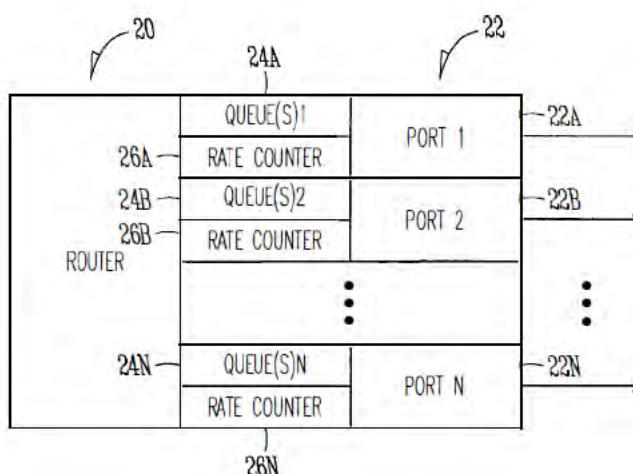
No.	'740 Patent Claim 15	The Reference
		<p>DeJager '424 at 7:59-8:10 ("FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.")</p> <p>DeJager '424 at 9:17-26 ("FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a "switch" 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.")</p> <p>Dontu at [0095] ("In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.")</p>

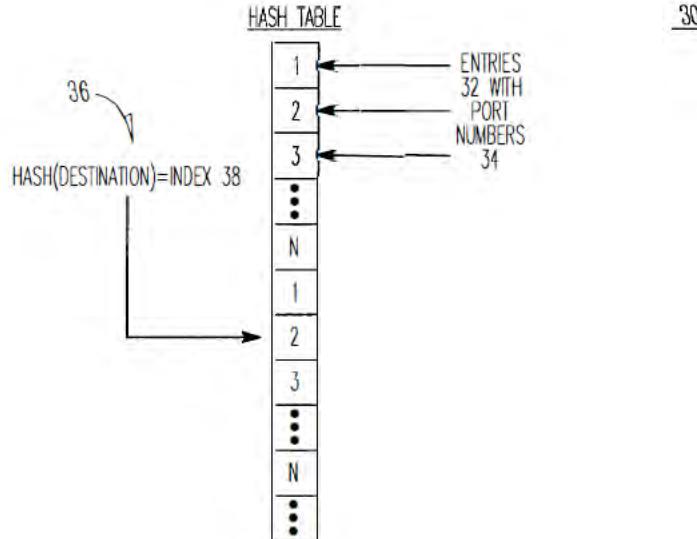
No.	'740 Patent Claim 15	The Reference
		<p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to</p>

No.	'740 Patent Claim 15	The Reference
		<p>send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-</p>

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		<p>unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 15	The Reference
		 <p>The diagram illustrates a router architecture. At the top, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, these components are: "QUEUE(S)1", "RATE COUNTER", "QUEUE(S)2", "RATE COUNTER", a vertical ellipsis "⋮", "QUEUE(S)N", and "RATE COUNTER". To the left of the first component is a label "26A", and to the left of the second component is a label "26B". To the left of the Nth component is a label "26N". Above the first component is a label "24A", and above the second component is a label "24B". Above the Nth component is a label "24N". To the right of the stack, there are three horizontal arrows pointing to the right, labeled "PORT 1", "PORT 2", and "PORT N" respectively. Above each of these arrows is a label "22": "22A" above PORT 1, "22B" above PORT 2, and "22N" below PORT N. Curved arrows at the top point from "24A" to "PORT 1", from "24B" to "PORT 2", and from "24N" to "PORT N".</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 15	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 15	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to specific output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 15	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 15	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

No.	'740 Patent Claim 15	The Reference
		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>
15[e]	sending the data frame over the	The Reference discloses sending the data frame over the selected backplane trace.

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	selected backplane trace,	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p>

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		<p>Viswanathan at Figure 1</p> <p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown. Line card 301 has four ports: 201 (1Gbps Ethernet), 202 (2Gbps FC), 203 (2Gbps FC), and 204 (1Gbps Ethernet). Line card 302 has two ports: 205 (10Gbps FC) and 206 (10Gbps Ethernet). Line card 303 has one port: 20n (IB). Each line card is connected to a corresponding backplane interconnect (501, 502, 503, 50n). These backplane interconnects are interconnected by a crossbar fabric interconnect (40). A management card (60) is also connected to the crossbar fabric interconnect (40) via a backplane interconnect (70). The management card (60) is connected to an Ethernet interface for configuration (PCI). The diagram is labeled FIG. 1.</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

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		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional</p>

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		<p>substeps. Step 1706e includes each port card accepting packets on a second plurality of port card backplane data links from crossbars. Step 1706f stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T[TRANSMITTING PACKETS FROM TM 1702] T --> A[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A --> S[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S --> A6[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A6 --> S1[STORING PACKETS IN iMS 1706b] S1 --> A2[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A2 --> S2[SUPPLYING PACKETS TO CROSSBAR 1706d] S2 --> S3[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S3 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would</p>

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		<p>communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), network device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424

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	<ul style="list-style-type: none"> • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>The diagram illustrates a Stream State Table (FIG. 2). It starts with two input paths: 'destinationAddress' and 'sourceAddress'. Each path goes through a 'Hash' block, then an 'XOR' block, and finally a multiplexer. The multiplexer's output is labeled 'Stream Id' and has a width of 16 bits. This Stream Id is then used to index into a 'StreamStateTable'. The table has 64 entries, indexed from 0 to 63. Each entry contains the following fields: <ul style="list-style-type: none"> AssignedPortNumber[4:0]: A 5-bit field with values 0, 1, ..., 63. timeMark[0:1]: A 2-bit field. number of Streams - 1: A 1-bit field with value 0. An arrow labeled 'configuration' points from the Stream Id to the table, indicating how the table is populated or updated based on the Stream Id. </p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

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		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

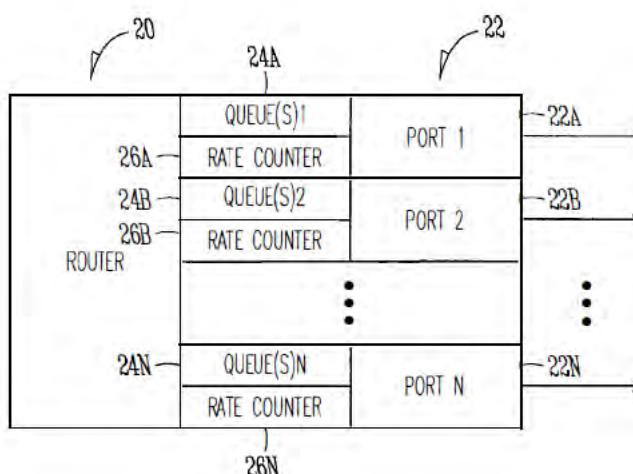
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

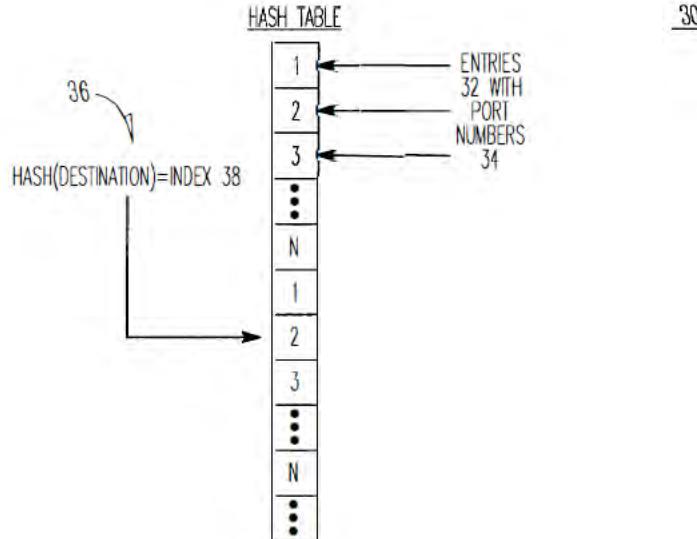
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. At the top, a curved arrow labeled 20 points to a box labeled 'ROUTER'. Inside the 'ROUTER' box, there are three horizontal rows. The first row contains 'QUEUE(S)1' and 'RATE COUNTER' for PORT 1. The second row contains 'QUEUE(S)2' and 'RATE COUNTER' for PORT 2. The third row contains 'QUEUE(S)N' and 'RATE COUNTER' for PORT N. Ellipses between the second and third rows indicate intermediate ports. Arrows labeled 24A, 24B, and 2AN point to the 'QUEUE(S)1', 'QUEUE(S)2', and 'QUEUE(S)N' sections respectively. Arrows labeled 26A, 26B, and 26N point to the 'RATE COUNTER' sections. Arrows labeled 22A, 22B, and 22N point from the 'PORT 1', 'PORT 2', and 'PORT N' sections to the right, representing output ports.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 15	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 15	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to specific output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 15	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

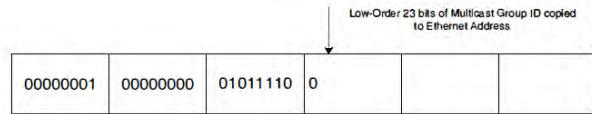
No.	'740 Patent Claim 15	The Reference
		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 15	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

No.	'740 Patent Claim 15	The Reference
		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>  <p style="text-align: center;">Figure 2</p>  <p style="text-align: center;">Figure 3</p>  <p style="text-align: center;">Figure 4</p>  <p style="text-align: center;">Figure 5</p>

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15[f]	<p>at least some of the backplane traces being aggregated into an Ethernet link aggregation (LAG) group.</p>	<p>The Reference discloses at least some of the backplane traces being aggregated into an Ethernet link aggregation (LAG) group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below is an example.</p> <p>Smith '430 at 5:51-64 (“The redundant links coupling each of network devices 120(1) and 120(2) to virtual network device 202 can be operated as a single logical link, referred to herein as a virtual link bundle. Network device 120(1) operates the two links coupling network device 120(1) to virtual network device 202 as 55 a virtual link bundle 250(1). In such an embodiment, each interface in network device 120(1) that is coupled to one of the links is included in an interface bundle, which corresponds to virtual link bundle 250(1). Network device 120(2) similarly operates the two links coupling network device 60 120(2) to virtual network device 202 as virtual link bundle 250(2). In some embodiments, virtual link bundles 250(1) and 250(2) are each operated as an EtherChannel™ or as an aggregated link (as described in IEEE 802.3).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the link aggregation technique. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914

No.	'740 Patent Claim 15	The Reference
		<ul style="list-style-type: none"> • Borgjone '125 <p>DeJager '424 at Abstract ("Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.")</p> <p>DeJager '424 at 1:38-50 ("One way to relieve this bottle-neck is to provide a logical grouping of multiple ports into a single port. The bandwidth of the new port is increased since it has multiple lines (cables) connecting a switch and another network device, each line capable of carrying data at the same rate as the line connecting data sources to the switch. This grouping of ports is sometimes referred to as a port aggregation or port group. One example of such a port aggregation implementation is Cisco Technology, Inc.'s Fast EtherChannel™ port group in a Fast Ethernet network. Further information regarding Fast EtherChannel™ may be found on Cisco Technology, Inc.'s World Wide Web site www.cisco.com. This information is incorporated by reference herein for all purposes.")</p> <p>DeJager '424 at 2:47-65 ("The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current</p>

No.	'740 Patent Claim 15	The Reference
		<p>utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 2:67-3:15 (“In one aspect, the present invention provides a method of distributing traffic over a network port group. The method involves receiving a packet of data to be forwarded, determining a stream ID for the packet, and determining whether a prior packet having that stream ID has been distributed to a queue on a port in the group during a predetermined time interval. Where a prior packet having that stream ID has not been distributed to a queue on a port of the group during the predetermined time interval, the method involves allocating the packet to a queue of a port having a lesser load in its queue than a queue of any other port of the group. The method may also involve, where a prior packet having that stream ID has been distributed to a queue on a port of the group during the predetermined time interval, allocating the packet to that queue. In addition, the method may involve monitoring the port group queues to maintain proper identification of the least utilized queue.”)</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for</p>

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		<p>a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 4:29-46 (“The present invention provides methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present 432ubsti-tion is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port’s current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including I0Base-T, I00Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 4:47-58 (“FIG. 1 illustrates a block diagram of a simple network. The network 100 includes two servers S1. And S2, respectively, and two switches, X1 and X2, respectively, as well as four clients C1, C2, C3 and C4, respectively. Clients C , , and C4 are connected to switch X1 by, for example, Fast Ethernet links 102 via ports 1, 2, 3 and 4, respectively. Server S1 is connected to switch X1 via a port aggregation 104, which is a port group composed of ports 5 and 6 of switch X1 . Switch X1 is connected to switch X2 via a second port aggregation 106 which includes ports 7, 8 and 9. Switch X2 is connected to server S2 via port O and Fast Ethernet link 108.”)</p> <p>Dontu at Abstract (“Various methods and systems for preventing erroneous link aggregation due to component relocation are disclosed. Such methods include a method for changing the identifier used by a network device and communicating the identifier change to a peer</p>

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		<p>network device without disrupting an aggregated link. In one embodiment, a method involves detecting an identifier change and sending a Port Aggregation Protocol (PapP) protocol data unit (PDU) that includes a new identifier and information. The information indicates the identifier change. Another embodiment of a method involves detecting an identifier change and, subsequent to the identifier change, sending a link aggregation protocol PDU that includes an “old device identifier” field dedicated to conveying an old identifier. The old identifier identifies a network device prior to the identifier change.”)</p> <p>Dontu at Figure 2</p>

No.	'740 Patent Claim 15	The Reference														
		<p style="text-align: center;">5</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Version <u>202</u></td></tr> <tr><td>My Device Identifier <u>204</u> (ID = X)</td></tr> <tr><td>My Distribution Requirements <u>206</u></td></tr> <tr><td>My Port Priority <u>208</u></td></tr> <tr><td>My Port Identifier <u>210</u></td></tr> <tr><td>My Group Capability <u>212</u></td></tr> <tr><td>My Agport Identifier <u>214</u></td></tr> <tr><td>Your Device Identifier <u>216</u> (ID = Y)</td></tr> <tr><td>Your Distribution Requirements <u>218</u></td></tr> <tr><td>Your Port Priority <u>220</u></td></tr> <tr><td>Your Port Identifier <u>222</u></td></tr> <tr><td>Your Group Capability <u>224</u></td></tr> <tr><td>Your Agport Identifier <u>226</u></td></tr> <tr><td>Partner Count <u>228</u></td></tr> </table> <p style="text-align: center;">↗</p> <p>Port Aggregation Protocol PDU 200 (sent from Interfaces 120(1), 120(2) and 120(3))</p> <p style="text-align: center;">FIG. 2</p> <p>Dontu at Figure 3</p>	Version <u>202</u>	My Device Identifier <u>204</u> (ID = X)	My Distribution Requirements <u>206</u>	My Port Priority <u>208</u>	My Port Identifier <u>210</u>	My Group Capability <u>212</u>	My Agport Identifier <u>214</u>	Your Device Identifier <u>216</u> (ID = Y)	Your Distribution Requirements <u>218</u>	Your Port Priority <u>220</u>	Your Port Identifier <u>222</u>	Your Group Capability <u>224</u>	Your Agport Identifier <u>226</u>	Partner Count <u>228</u>
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Your Group Capability <u>224</u>																
Your Agport Identifier <u>226</u>																
Partner Count <u>228</u>																

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		<p style="text-align: center;">Network Device 100(1)</p> <p style="text-align: center;">Network Device 100(2)</p> <p style="text-align: center;">Aggregated Link 105</p> <p style="text-align: center;">Network Device 100(2)</p>

FIG. 3

Dontu at Figure 14

No.	'740 Patent Claim 15	The Reference
		<p style="text-align: center;">FIG. 14</p> <p>Dontu at [0004] (“Link aggregation is used to logically combine two or more individual links into a single aggregated link. Link aggregation can provide improved performance and increased fault tolerance. Improved performance arises because the aggregated link appears to have a bandwidth equal to the combined bandwidth of the individual links. Traffic can be load-balanced among the individual links. Increased fault tolerance is provided since one or more individual links within an aggregated link can fail without disrupting communication between the devices coupled by the aggregated link. Link aggregation techniques include</p>

No.	'740 Patent Claim 15	The Reference
		<p>Link Aggregation Control Protocol (LACP), which is defined in IEEE 803.2ad, and Port Aggregation Protocol (PagP), which is a standard promulgated by CISCO SYS-TEMS, INC.”)</p> <p>Dontu at [0012] (“The method can also involve detecting whether a partner interface is executing a compatible version of PagP. If the partner interface is not executing the compatible version of PagP, the compatible version of PagP can be provided to the partner interface. Alternatively, if the partner interface is not executing the compatible version of PagP, the partner interface can be inhibited from including a link in an aggregated link.”)</p> <p>Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components).”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6).”)</p>

No.	'740 Patent Claim 15	The Reference
		<p>Dontu at [0037] ("In this example, the network devices 100(1) and 100(2) use Port Aggregation Protocol (PagP) to form aggregated links. Network devices 100(1) each send PagP protocol data units (PDUs) to each other in order to determine whether any of the links between the two network devices can be combined into an aggregated link. Each PagP PDU includes an identifier that uniquely identifies the network device that sent that PagP PDU. Within network device 100(1), identifier module 130(1) of network device component 110(1) supplies an identifier "X" to each of the interfaces 120(1)-120(3) within network device 100(1). Interfaces 120(1)-120(3) include identifier X in each PagP PDU sent by those interfaces. Similarly, identifier module 130(2) of network device component 110(4) supplies an identifier "Y" to each interface 120(4)-120(6) of network device 100(2). Interfaces 120(4)-120(6) include identifier Y in each PagP PDU sent by those interfaces.")</p> <p>Dontu at [0040] ("FIG. 2 illustrates some of the fields that can be included in a PagP PDU. As shown, PDU 200 includes Version field 202, My Device Identifier field 204 ("My" refers to the device sending the PagP PDU), My Distribution Requirements field 206, My Port Priority field 208, My Port Identifier field 212, My Group Capability field 212, My Agport (Aggregated Port) Identifier field 214, Your Device Identifier field 216 ("Your" refers to the device to which the PagP PDU is being sent), Your Distribution Requirements field 218, Your Port Priority field 220, Your Port Identifier field 222, Your Group C"pability field 224, Your Agport Identifier field 226, and Partner Count field 228.")</p> <p>Dontu at [0110] ("Interfaces 1420(13), 1420(9), and 1420(16), which are each coupled to network device 1220(1) by virtual link bundle 1350(1), form an interface bundle (e.g., an Ether-Channel I port bundle). Similarly, interfaces 1420(11) and 1420(8) form another interface bundle that is coupled to network device 1220(2) by virtual link bundle 1350(2). Interfaces 1420(7) and 1420(12) form a third interface bundle that is coupled to network device 1220(3) by virtual link bundle 1350(3). Within virtual network device 1302, each interface in the same interface bundle is assigned the same logical identifier. For example, interfaces 1420(13), 1420(9), and 1420(16) are each assigned the same logical identifier. In some embodiments, packets received via one of these interfaces are tagged or otherwise associated with the logical identifier to indicate that those packets were received via the</p>

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		<p>virtual link bundle coupling virtual network device 1302 to network device 1220(1). It is noted that similar interface bundles are implemented within each network device 1220(1)-1220(3), and that interfaces included in such bundles are also assigned the same logical identifier by each network device (or by virtual network device 1302, in embodiments in which virtual network device 1302 controls the configuration of the network devices 1220(1)-1220(3)). For example, network device 1220(1) can assign the same logical identifier to each of the interfaces coupled to virtual link bundle 1350(1).”)</p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 2:6-22 (“In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.”)</p>

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		<p>Li '914 at 4:9-25 ("Referring to FIG. 1, a Router A is shown having an interface 10 with a plurality of ports which connect Router A with Router B over a plurality of connections, lines, wires, links or bundled links 14. The ports 12 of Router A are configured to permit transmission of packets from Router A to Router B, and these ports 12 may be referred to as output ports, egress ports, links, or the like. As shown in FIG. 1, port 1 to port N may be connected with Router B, and there may be additional interfaces 16 having ports 18 connected with other routers in the network. When a packet is received by Router A, Router A determines whether the received packet should be transmitted to Router B or to other routers connected to Router A, based in part upon the destination address of the packet. If a packet is to be transmitted from Router A to Router B, then Router A may transmit this packet over the one of the ports 12 shown in FIG. 1.")</p> <p>Li '914 at Figure 1</p> <p>The diagram illustrates the architecture of Router A. It features a central rectangular box labeled "ROUTER A". On the left side, an arrow labeled "PACKET IN" points into the router. On the right side, there are two main connection paths. The top path connects Router A to Router B. It starts with an arrow labeled "INTERFACE(10) TO ROUTER B" pointing to a vertical stack of ports labeled "PORT 1", "PORT 2", "PORT N", and "PORT N". From each of these ports, an arrow labeled "14" points to Router B. The bottom path connects Router A to Router C. It starts with an arrow labeled "INTERFACE(16) TO ROUTER C" pointing to a horizontal row of ports labeled "PORT 1", "PORT 2", "...", and "PORT N". Arrows labeled "18" point from these ports to Router C. Additionally, an arrow labeled "12" points from the "PORT N" port of Router A to Router B, indicating a direct connection between the two routers at that specific port level.</p> <p style="text-align: center;"><i>FIG. 1</i></p>

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		<p>Borgione '125 at 1:55-65 ("Link nodes 110 and 120 can be in physically remote locations, thereby connecting their associated local area networks (LANs). The plurality of network links 150 between link nodes 110 and 120 can be aggregated as a single logical link over which all traffic between link nodes 110 and 120 is distributed. Such aggregation multiplies the available bandwidth for communications between link nodes 110 and 120, and therefore between the two local area networks. When appropriately configured, such a connection can permit the two local area networks to interact as if they were one large local area network.")</p> <p>Borgione '125 at 1:66-2:7 ("As stated above, the plurality of network links between 110 and 120 can be aggregated as a single logical link. In this manner, each link node 110 and 120 sees the plurality of network links between them as one logical interface. One type of such an aggregate of links is an EtherChannel, a protocol that allows up to eight Fast Ethernet or Gigabit Ethernet links to be aggregated. Routing protocols treat the aggregated links as a single, routed interface with a common IP address.")</p> <p>Borgione '125 at 5:28-50 ("The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network environment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even</p>

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		distribution of multicast packet transmission across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.”)

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16	The method according to claim 14, wherein selecting the backplane trace comprises applying a hashing function to the at least one of the frame attributes.	<p>The Reference discloses the method according to claim 14, wherein selecting the backplane trace comprises applying a hashing function to the at least one of the frame attributes.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar interconnect 40 via backplane interconnects 501-50n (collectively, “backplane interconnects 30”). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric</p>

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		<p>topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 16	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Arrows indicate various data paths: 1Gbps Ethernet (251), 2Gbps FC (202, 203), 10Gbps FC (205), 10Gbps Ethernet (253, 206), and InfiniBand (IB) (25n). Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (70), which is also connected to the crossbar fabric interconnect (40).</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress</p>

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		<p>data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iT M); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208 through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-chamial port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

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		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 16	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 16	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] A4 --> S4[SENSING CONNECTION FAULT 1710] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), network device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu • Li ’914 • Borgione ’125 <p>DeJager ’424 at Figure 2</p>

No.	'740 Patent Claim 16	The Reference
		<p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 16	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

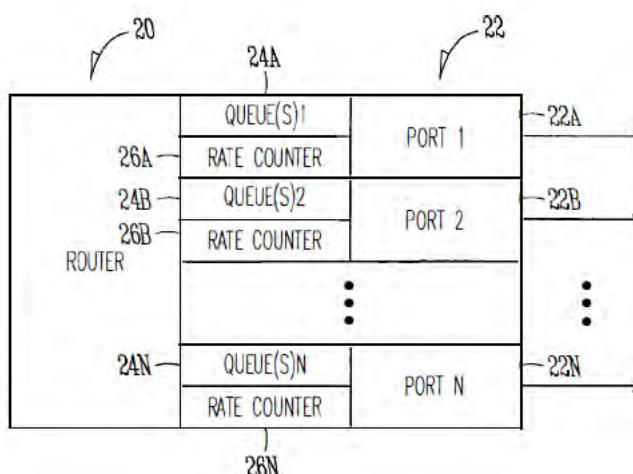
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

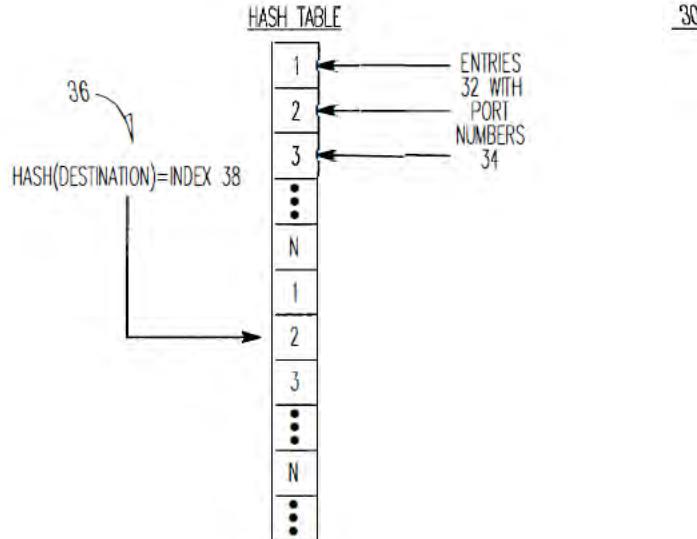
No.	'740 Patent Claim 16	The Reference
		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet’s destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 16	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 16	The Reference
		 <p>The diagram illustrates a router architecture. At the top, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, these components are: "QUEUE(S)1", "RATE COUNTER", "QUEUE(S)2", "RATE COUNTER", a vertical ellipsis "⋮", "QUEUE(S)N", and "RATE COUNTER". To the left of the first component is a label "26A", and to the left of the second component is a label "26B". To the left of the Nth component is a label "26N". Above the first component is a label "24A", above the second component is a label "24B", and above the Nth component is a label "24N". Above the entire stack is a label "20". To the right of the stack, there are three horizontal arrows pointing to the right, labeled "PORT 1", "PORT 2", and "PORT N" respectively. Above each of these arrows is a label "22": "22A" above PORT 1, "22B" above PORT 2, and "22N" above PORT N. Below the stack of components is a label "26N".</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 16	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 16	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 16	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports: PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This indicates that traffic from PORT 1 is mapped to slot 1 and traffic from PORT 2 is mapped to slot 2.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 16	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 16	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

No.	'740 Patent Claim 16	The Reference
		<p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 16	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p> <p>Li ’914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by 466substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li ’914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione ’125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione ’125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

No.	'740 Patent Claim 17	The Reference
17[preamble]	Apparatus for connecting a network node with a communication network, comprising:	<p>The Reference discloses apparatus for connecting a network node with a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
17[a]	one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network,	<p>The Reference discloses one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
17[b]	at least one of said interface modules being operative to communicate in both an upstream direction	<p>The Reference discloses at least one of said interface modules being operative to communicate in both an upstream direction and a downstream direction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was</p>

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	and a downstream direction;	known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
17[c]	a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules;	<p>The Reference discloses a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
17[d]	a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network; and	<p>The Reference discloses a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p>

No.	'740 Patent Claim 17	The Reference
		<p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, “backplane interconnects 30”). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 17	The Reference
		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect. Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (70), which is also connected to the backplane interconnect. A PCI logo is shown near the management card.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

No.	'740 Patent Claim 17	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 17	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal architecture of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections to the LIC include two ports on the left and a connection to the "AMCC CYCLONE SWITCH FABRIC" on the right. The switch fabric is part of the "S8005 SERIES". On the left side, there is a vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. On the right side, there is another vertical stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. Red boxes highlight the connection points between the LIC and the switch fabric, specifically around the AMCC CYCLONE SWITCH FABRIC block.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 17	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 17	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), network device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

No.	'740 Patent Claim 17	The Reference
		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
17[e]	<p>a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame;</p>	<p>The Reference discloses a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces</p>

No.	'740 Patent Claim 17	The Reference
		<p>251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

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		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate various data paths: 1Gb/s Ethernet (201, 204), 2Gb/s FC (202, 203), 10Gb/s FC (205, 206), 10Gb/s Ethernet (20n), and InfiniBand (IB) (20n). Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (70) via a PCI bus. The management card is also connected to the backplane interconnect.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

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		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

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		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu

No.	'740 Patent Claim 17	The Reference
	<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

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		<pre> graph TD START([START]) --> Receive[Receive Packet] Receive --> Assign[Assign Packet to Target Port Group] Assign --> Hash[Hash and Mask Packet Address into a Stream ID] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register?} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register?} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID] Push --> LUQ_PUSH[LUQ PUSH Process] LUQ_PUSH --> Forward[Forward (POP) Queued Packet] Forward --> LUQ_POP[LUQ POP Process] LUQ_POP --> END([END]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

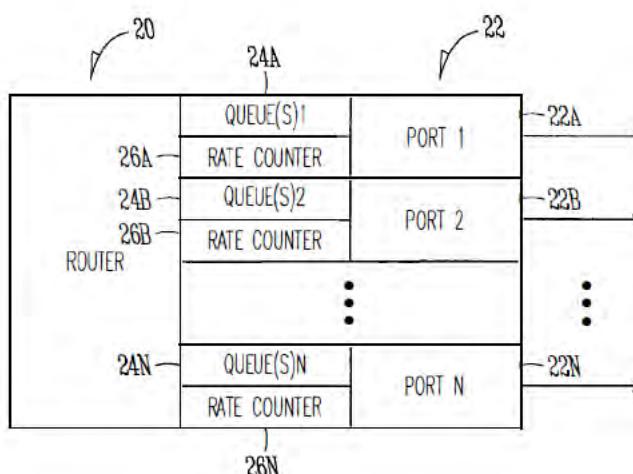
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

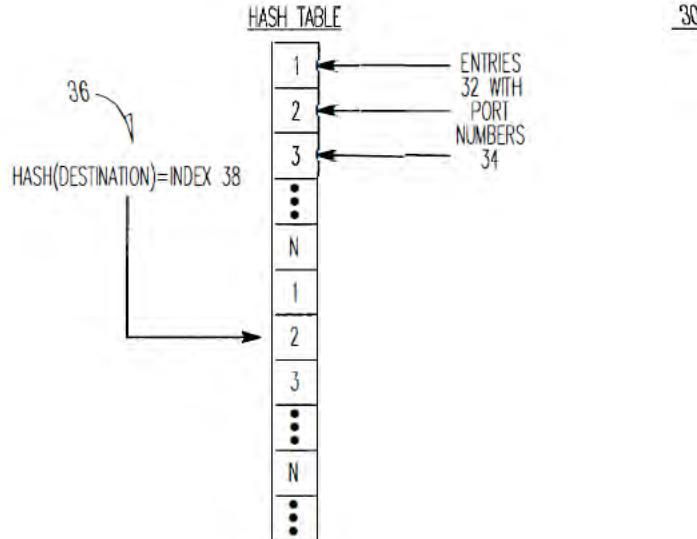
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet’s destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 17	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. On the left, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, it includes: a header labeled "20", followed by a section labeled "24A" which contains "QUEUE(S)1" and a "RATE COUNTER"; another section labeled "24B" containing "QUEUE(S)2" and a "RATE COUNTER"; a vertical ellipsis "⋮"; another section labeled "24N" containing "QUEUE(S)N" and a "RATE COUNTER". To the right of these sections are two columns of labels: "PORT 1" and "PORT N". Arrows point from each section to its corresponding port label. Specifically, "QUEUE(S)1" points to "PORT 1", "QUEUE(S)2" points to "PORT 2", and so on down to "QUEUE(S)N" pointing to "PORT N". Each port is connected to an output line labeled "22A", "22B", or "22N" respectively. The entire stack of components is labeled "26A", "26B", and "26N" on the far left.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 17	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 17	The Reference
		<pre> graph TD A[RECEIVE PACKET TO TRANSMIT] --> B[COMPUTE HASH (DESTINATION ADDRESS)] B --> C[USE RESULT AS INDEX INTO HASH TABLE] C --> D[EXTRACT PORT NUMBER FROM HASH TABLE] D --> E[TRANSMIT PACKET OVER PORT NUMBER] </pre> <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 17	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports: PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to specific output slots based on utilization levels.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 17	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 17	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 17	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p> <p>Li ’914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by 500ubstitute- ing an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li ’914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corre-sponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione ’125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione ’125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

No.	'740 Patent Claim 17	The Reference
		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

No.	'740 Patent Claim 17	The Reference
		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

No.	'740 Patent Claim 17	The Reference
17[f]	at least one of said first physical links and at least one of said second links being bi-directional links operative to communicate in both said upstream direction and said downstream direction.	<p>The Reference discloses at least one of said first physical links and at least one of said second links being bi-directional links operative to communicate in both said upstream direction and said downstream direction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 18	The Reference
18[a]	The apparatus according to claim 17, and comprising a backplane to which the one or more interface modules are coupled,	<p>The Reference discloses the apparatus according to claim 17, and comprising a backplane to which the one or more interface modules are coupled.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p>

No.	'740 Patent Claim 18	The Reference
		<p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, “backplane interconnects 30”). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 18	The Reference
		<p>FIG. 1</p> <p>Singh at 4:5-13 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTm); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.")</p> <p>Singh at 5:54-58 ("A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

No.	'740 Patent Claim 18	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 18	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal structure of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections to the LIC include two fiber optic ports on the left and a connection to the "AMCC CYCLONE SWITCH FABRIC" on the right. The switch fabric is part of the "S8005 SERIES". On the left side, there is a stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. On the right side, there is a stack of 32 line interfaces, numbered LINE INTERFACE 0 at the top and LINE INTERFACE 31 at the bottom. Red boxes highlight the connection points between the LIC and the switch fabric, specifically around the AMCC CYCLONE SWITCH FABRIC block.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 18	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 18	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> TM[TRANSMITTING PACKETS FROM TM 1702] TM --> INGRESS[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] INGRESS --> CONNECT[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] CONNECT --> 6PORTS[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] 6PORTS --> iMS[STORING PACKETS IN iMS 1706b] iMS --> ASSIGN[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] ASSIGN --> CROSSBAR[SUPPLYING PACKETS TO CROSSBAR 1708] CROSSBAR --> EGRESS[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] EGRESS --> FAULT[SENSING CONNECTION FAULT 1710] FAULT --> RESELECT[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

No.	'740 Patent Claim 18	The Reference
		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
18[b]	wherein the second physical links comprise back plane traces formed on the backplane.	<p>The Reference discloses wherein the second physical links comprise back plane traces formed on the backplane.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces</p>

No.	'740 Patent Claim 18	The Reference
		<p>35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 18	The Reference
		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect. Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (70), which is also connected to the backplane interconnect. A PCI logo is shown near the management card.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

No.	'740 Patent Claim 18	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 18	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal structure of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections from the LIC include two ports labeled "DDCVID" and "DDVBI" on the left, and a "PARALLEL FIBER OPTICS" port on the right. Below the LIC, a dashed line separates it from an "AMCC CYCLONE SWITCH FABRIC". This fabric is part of the "S8005 SERIES". On the left side, there is an "INGRESS" section containing 32 line interfaces numbered 0 through 31. On the right side, there is an "EGRESS" section containing 32 line interfaces numbered 0 through 31. The ingress and egress line interfaces are connected to the switch fabric via bidirectional arrows. A red rectangular box highlights the connection area between the LIC and the switch fabric.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 18	The Reference
		<p>Fig. 4</p> <pre> graph TD TX["TX AND RX OPTICS AND PHY."] --> Framer[FRAMER OR MAC] Framer --> IPQ[S8565 PRIORITY QUEUE (PQ)] Framer --> N1[NETWORK PROCESSING] N1 --> IEM[INGRESS & EGRESS TRAFFIC MANAGEMENT] IEM --> SSS[S8905 MEMORY SUBSYSTEM] IEM --> OPQ[S8805 EDFQ OR S8505 PQ (OPQ)] SSS <--> BSL[BACKPLANE SERIAL LINKS] SSS <--> Arbiters[ARBITERS] Arbiters -- BIDS TO ARBITERS --> IPQ Arbiters -- GRANTS FROM ARBITERS --> SSS IPQ --> SSS OPQ --> SSS </pre> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 18	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] A4 --> S4[SENSING CONNECTION FAULT 1710] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

No.	'740 Patent Claim 18	The Reference
		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>

No.	'740 Patent Claim 19	The Reference
19[preamble]	Apparatus for connecting a network node with a communication network, comprising:	<p>The Reference discloses apparatus for connecting a network node with a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
19[a]	one or more interface modules, which are arranged to process data frames having frame attributes sent	The Reference discloses one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network.

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	between the network node and the communication network;	To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
19[b]	a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules;	<p>The Reference discloses a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
19[c]	a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network; and	<p>The Reference discloses a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p>

No.	'740 Patent Claim 19	The Reference
		<p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, “backplane interconnects 30”). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 19	The Reference
		<p>FIG. 1</p> <p>Singh at 4:5-13 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTm); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.")</p> <p>Singh at 5:54-58 ("A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

No.	'740 Patent Claim 19	The Reference
		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 19	The Reference
		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 19	The Reference
		<p>Fig. 4</p> <pre> graph TD TX["TX AND RX OPTICS AND PHY."] --> Framer[FRAMER OR MAC] Framer --> Network1[NETWORK PROCESSING] Network1 --> Ingress[INGRESS & EGRESS TRAFFIC MANAGEMENT] Ingress --> SerialLinks[SERIAL LINKS] Ingress --> Memory[SS905 MEMORY SUBSYSTEM] Memory --> SerialLinks Memory --> PQ[S8565 PRIORITY QUEUE (PQ)] PQ --> Arbiters[ARBITERS] Arbiters --> Grants[GRANTS FROM ARBITERS] Grants --> Backplane[BACKPLANE SERIAL LINKS] Backplane --> PQ Backplane --> Memory PQ --> Network2[NETWORK PROCESSING] Network2 --> Framer </pre> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 19	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> TM[TRANSMITTING PACKETS FROM TM 1702] TM --> INGRESS[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] INGRESS --> CONNECT[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] CONNECT --> 6PORTS[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] 6PORTS --> iMS[STORING PACKETS IN iMS 1706b] iMS --> ASSIGN[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] ASSIGN --> CROSSBAR[SUPPLYING PACKETS TO CROSSBAR 1706d] CROSSBAR --> EGRESS[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] EGRESS --> FAULT[SENSING CONNECTION FAULT 1710] FAULT --> RESELECT[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual")</p>	

No.	'740 Patent Claim 19	The Reference
		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
19[d]	a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame,	<p>The Reference discloses a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown</p>

No.	'740 Patent Claim 19	The Reference
		<p>in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, “backplane interconnects 30”). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 19	The Reference
		<p>The diagram illustrates a network switching architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also part of the crossbar fabric interconnect (40). Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect. Specific port labels include 201, 202, 203, 204, 205, 206, 20n, 251, 252, 253, 25n, and 10Gbps Ethernet, 2Gbps FC, 1Gbps Ethernet, IB.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

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		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

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		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu

No.	'740 Patent Claim 19	The Reference
	<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <pre> graph LR DA[destinationAddress] --> H1[Hash] SA[sourceAddress] --> H2[Hash] H1 --> XOR[XOR] H2 --> XOR XOR --> Mask[Mask] Mask --> StreamId[Stream Id] StreamId --> SST[StreamStateTable] SST --> APN[AssignedPortNumber[4:0]] SST --> TM[timeMark[0:1]] SST --> NS[number of Streams - 1] </pre> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

No.	'740 Patent Claim 19	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

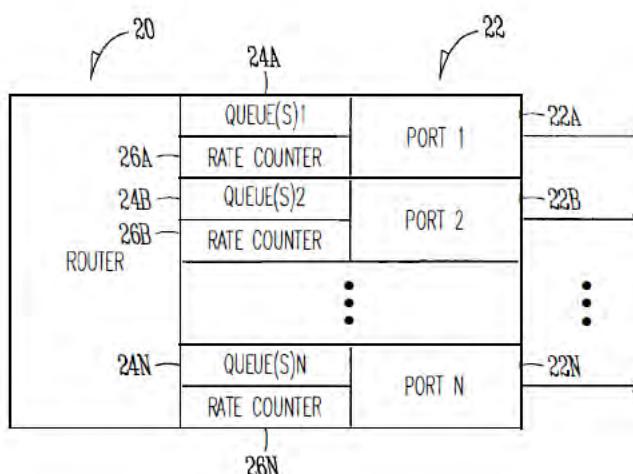
No.	'740 Patent Claim 19	The Reference
		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

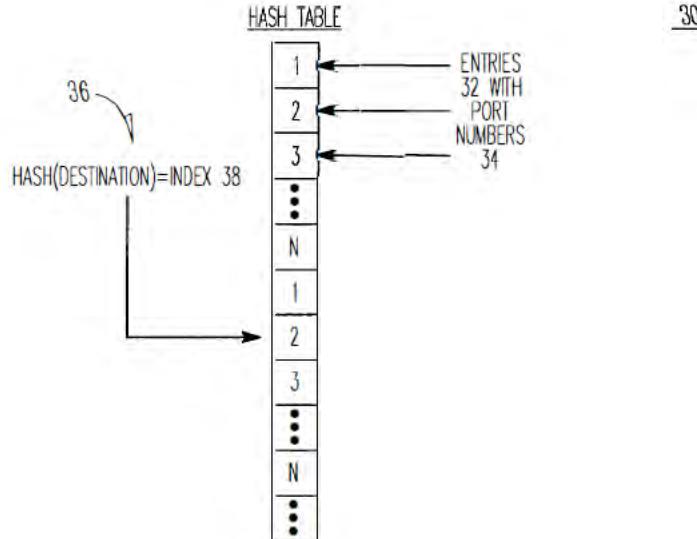
No.	'740 Patent Claim 19	The Reference
		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet’s destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

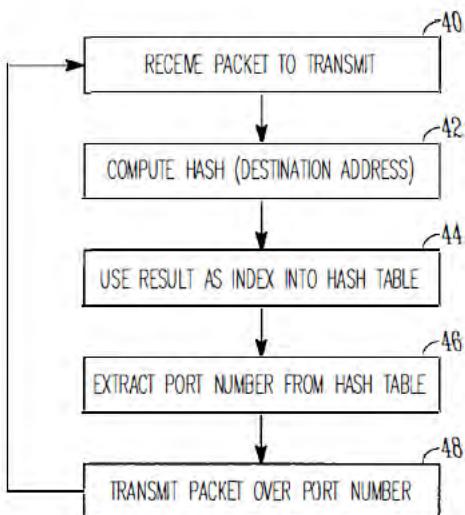
No.	'740 Patent Claim 19	The Reference
		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 19	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 19	The Reference
		 <p>The diagram illustrates a router architecture. On the left, a box labeled "ROUTER" contains a vertical stack of components. From top to bottom, it includes: a header section labeled "20" with a curved arrow pointing to it; a section labeled "24A" containing "QUEUE(S)1" and a "RATE COUNTER"; a section labeled "24B" containing "QUEUE(S)2" and a "RATE COUNTER"; a section labeled "26A" containing three vertical dots; a section labeled "2AN" containing "QUEUE(S)N" and a "RATE COUNTER". To the right of these sections are two columns of labels: "PORT 1" and "PORT N". Arrows point from each section to its corresponding port. The "PORT 1" column has an arrow labeled "22A" pointing to it. The "PORT N" column has an arrow labeled "22N" pointing to it. Below the "ROUTER" box, a bracket labeled "26N" spans the width of the internal sections. Three curved arrows at the top point to the "20", "24A", and "22" labels.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 19	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 19	The Reference
		 <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 19	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), ..., PORT N. Arrows labeled 62A, 62B, ..., 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed 1 through N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to different output paths based on the hash table.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 19	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 19	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

No.	'740 Patent Claim 19	The Reference
		<p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 19	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p> <p>Li ’914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by 548substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li ’914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione ’125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione ’125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

No.	'740 Patent Claim 19	The Reference
19[e]	<p>at least one of the first and second groups of physical links comprising an Ethernet link aggregation (LAG) group.</p>	<p>The Reference discloses at least one of the first and second groups of physical links comprising an Ethernet link aggregation (LAG) group.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below is an example.</p> <p>Smith '430 at 5:51-64 (“The redundant links coupling each of network devices 120(1) and 120(2) to virtual network device 202 can be operated as a single logical link, referred to herein as a virtual link bundle. Network device 120(1) operates the two links coupling network device 120(1) to virtual network device 202 as 55 a virtual link bundle 250(1). In such an embodiment, each interface in network device 120(1) that is coupled to one of the links is included in an interface bundle, which corresponds to virtual link bundle 250(1). Network device 120(2) similarly operates the two links coupling network device 60 120(2) to virtual network device 202 as virtual link bundle 250(2). In some embodiments, virtual link bundles 250(1) and 250(2) are each operated as an EtherChannel™ or as an aggregated link (as described in IEEE 802.3).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the link aggregation technique. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914

No.	'740 Patent Claim 19	The Reference
		<ul style="list-style-type: none"> • Borgjone '125 <p>DeJager '424 at Abstract ("Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including I/OBase-T, I/O0Base-T, and Gigabit Ethernet, and other network environments.")</p> <p>DeJager '424 at 1:38-50 ("One way to relieve this bottle-neck is to provide a logical grouping of multiple ports into a single port. The bandwidth of the new port is increased since it has multiple lines (cables) connecting a switch and another network device, each line capable of carrying data at the same rate as the line connecting data sources to the switch. This grouping of ports is sometimes referred to as a port aggregation or port group. One example of such a port aggregation implementation is Cisco Technology, Inc.'s Fast EtherChannel™ port group in a Fast Ethernet network. Further information regarding Fast EtherChannel™ may be found on Cisco Technology, Inc.'s World Wide Web site www.cisco.com. This information is incorporated by reference herein for all purposes.")</p> <p>DeJager '424 at 2:47-65 ("The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current</p>

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		<p>utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 2:67-3:15 (“In one aspect, the present invention provides a method of distributing traffic over a network port group. The method involves receiving a packet of data to be forwarded, determining a stream ID for the packet, and determining whether a prior packet having that stream ID has been distributed to a queue on a port in the group during a predetermined time interval. Where a prior packet having that stream ID has not been distributed to a queue on a port of the group during the predetermined time interval, the method involves allocating the packet to a queue of a port having a lesser load in its queue than a queue of any other port of the group. The method may also involve, where a prior packet having that stream ID has been distributed to a queue on a port of the group during the predetermined time interval, allocating the packet to that queue. In addition, the method may involve monitoring the port group queues to maintain proper identification of the least utilized queue.”)</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for</p>

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		<p>a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 4:29-46 (“The present invention provides methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 4:47-58 (“FIG. 1 illustrates a block diagram of a simple network. The network 100 includes two servers S1. and S2, respectively, and two switches, X1 and X2, respectively, as well as four clients C1, C2, C3 and C4, respectively. Clients C1, C2, C3 and C4 are connected to switch X1 by, for example, Fast Ethernet links 102 via ports 1, 2, 3 and 4, respectively. Server S1 is connected to switch X1 via a port aggregation 104, which is a port group composed of ports 5 and 6 of switch X1. Switch X1 is connected to switch X2 via a second port aggregation 106 which includes ports 7, 8 and 9. Switch X2 is connected to server S2 via port O and Fast Ethernet link 108.”)</p> <p>Dontu at Abstract (“Various methods and systems for preventing erroneous link aggregation due to component relocation are disclosed. Such methods include a method for changing the identifier used by a network device and communicating the identifier change to a peer</p>

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		<p>network device without disrupting an aggregated link. In one embodiment, a method involves detecting an identifier change and sending a Port Aggregation Protocol (PAgP) protocol data unit (PDU) that includes a new identifier and information. The information indicates the identifier change. The new identifier identifies a network device subsequent to the identifier change. Another embodiment of a method involves detecting an identifier change and, subsequent to the identifier change, sending a link aggregation protocol PDU that includes an "old device identifier" field dedicated to conveying an old identifier. The old identifier identifies a network device prior to the identifier change.")</p> <p>Dontu at Figure 2</p>

No.	'740 Patent Claim 19	The Reference														
		<p style="text-align: center;">5</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Version <u>202</u></td></tr> <tr><td>My Device Identifier <u>204</u> (ID = X)</td></tr> <tr><td>My Distribution Requirements <u>206</u></td></tr> <tr><td>My Port Priority <u>208</u></td></tr> <tr><td>My Port Identifier <u>210</u></td></tr> <tr><td>My Group Capability <u>212</u></td></tr> <tr><td>My Agport Identifier <u>214</u></td></tr> <tr><td>Your Device Identifier <u>216</u> (ID = Y)</td></tr> <tr><td>Your Distribution Requirements <u>218</u></td></tr> <tr><td>Your Port Priority <u>220</u></td></tr> <tr><td>Your Port Identifier <u>222</u></td></tr> <tr><td>Your Group Capability <u>224</u></td></tr> <tr><td>Your Agport Identifier <u>226</u></td></tr> <tr><td>Partner Count <u>228</u></td></tr> </table> <p style="text-align: center;">↗</p> <p>Port Aggregation Protocol PDU 200 (sent from Interfaces 120(1), 120(2) and 120(3))</p> <p style="text-align: center;">FIG. 2</p> <p>Dontu at Figure 3</p>	Version <u>202</u>	My Device Identifier <u>204</u> (ID = X)	My Distribution Requirements <u>206</u>	My Port Priority <u>208</u>	My Port Identifier <u>210</u>	My Group Capability <u>212</u>	My Agport Identifier <u>214</u>	Your Device Identifier <u>216</u> (ID = Y)	Your Distribution Requirements <u>218</u>	Your Port Priority <u>220</u>	Your Port Identifier <u>222</u>	Your Group Capability <u>224</u>	Your Agport Identifier <u>226</u>	Partner Count <u>228</u>
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Partner Count <u>228</u>																

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		<p style="text-align: center;">Network Device 100(1)</p> <p style="text-align: center;">Network Device 100(2)</p> <p style="text-align: center;">Aggregated Link 105</p> <p style="text-align: center;">FIG. 3</p> <p>Dontu at Figure 14</p>

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		<p style="text-align: center;">FIG. 14</p> <p>Dontu at [0004] (“Link aggregation is used to logically combine two or more individual links into a single aggregated link. Link aggregation can provide improved performance and increased fault tolerance. Improved performance arises because the aggregated link appears to have a bandwidth equal to the combined bandwidth of the individual links. Traffic can be load-balanced among the individual links. Increased fault tolerance is provided since one or more individual links within an aggregated link can fail without disrupting communication between the devices coupled by the aggregated link. Link aggregation techniques include</p>

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		<p>Link Aggregation Control Protocol (LACP), which is defined in IEEE 803.2ad, and Port Aggregation Protocol (PAgP), which is a standard promulgated by CISCO SYS-TEMS, INC.”)</p> <p>Dontu at [0012] (“The method can also involve detecting whether a partner interface is executing a compatible version of PAgP. If the partner interface is not executing the compatible version of PAgP, the compatible version of PAgP can be provided to the partner interface. Alternatively, if the partner interface is not executing the compatible version of PAgP, the partner interface can be inhibited from including a link in an aggregated link.”)</p> <p>Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components).”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6).”)</p>

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		<p>Dontu at [0037] ("In this example, the network devices 100(1) and 100(2) use Port Aggregation Protocol (PAgP) to form aggregated links. Network devices 100(1) each send PAgP protocol data units (PDUs) to each other in order to determine whether any of the links between the two network devices can be combined into an aggregated link. Each PAgP PDU includes an identifier that uniquely identifies the network device that sent that PAgP PDU. Within network device 100(1), identifier module 130(1) of network device component 110(1) supplies an identifier "X" to each of the interfaces 120(1)-120(3) within network device 100(1). Interfaces 120(1)-120(3) include identifier X in each PAgP PDU sent by those interfaces. Similarly, identifier module 130(2) of network device component 110(4) supplies an identifier "Y" to each interface 120(4)-120(6) of network device 100(2). Interfaces 120(4)-120(6) include identifier Y in each PAgP PDU sent by those interfaces.")</p> <p>Dontu at [0040] ("FIG. 2 illustrates some of the fields that can be included in a PAgP PDU. As shown, PDU 200 includes Version field 202, My Device Identifier field 204 ("My" refers to the device sending the PAgP PDU), My Distribution Requirements field 206, My Port Priority field 208, My Port Identifier field 212, My Group Capability field 212, My Agport (Aggregated Port) Identifier field 214, Your Device Identifier field 216 ("Your" refers to the device to which the PAgP PDU is being sent), Your Distribution Requirements field 218, Your Port Priority field 220, Your Port Identifier field 222, Your Group Capability field 224, Your Agport Identifier field 226, and Partner Count field 228.")</p> <p>Dontu at [0110] ("Interfaces 1420(13), 1420(9), and 1420(16), which are each coupled to network device 1220(1) by virtual link bundle 1350(1), form an interface bundle (e.g., an Ether-Channel (TM) port bundle). Similarly, interfaces 1420(11) and 1420(8) form another interface bundle that is coupled to network device 1220(2) by virtual link bundle 1350(2). Interfaces 1420(7) and 1420(12) form a third interface bundle that is coupled to network device 1220(3) by virtual link bundle 1350(3). Within virtual network device 1302, each interface in the same interface bundle is assigned the same logical identifier. For example, interfaces 1420(13), 1420(9), and 1420(16) are each assigned the same logical identifier. In some embodiments, packets received via one of these interfaces are tagged or otherwise associated with the logical identifier to indicate that those packets were received via the</p>

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		<p>virtual link bundle coupling virtual network device 1302 to network device 1220(1). It is noted that similar interface bundles are implemented within each network device 1220(1)-1220(3), and that interfaces included in such bundles are also assigned the same logical identifier by each network device (or by virtual network device 1302, in embodiments in which virtual network device 1302 controls the configuration of the network devices 1220(1)-1220(3)). For example, network device 1220(1) can assign the same logical identifier to each of the interfaces coupled to virtual link bundle 1350(1).”)</p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 2:6-22 (“In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.”)</p>

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		<p>Li '914 at 4:9-25 ("Referring to FIG. 1, a Router A is shown having an interface 10 with a plurality of ports which connect Router A with Router B over a plurality of connections, lines, wires, links or bundled links 14. The ports 12 of Router A are configured to permit transmission of packets from Router A to Router B, and these ports 12 may be referred to as output ports, egress ports, links, or the like. As shown in FIG. 1, port 1 to port N may be connected with Router B, and there may be additional interfaces 16 having ports 18 connected with other routers in the network. When a packet is received by Router A, Router A determines whether the received packet should be transmitted to Router B or to other routers connected to Router A, based in part upon the destination address of the packet. If a packet is to be transmitted from Router A to Router B, then Router A may transmit this packet over the one of the ports 12 shown in FIG. 1.")</p> <p>Li '914 at Figure 1</p> <p>The diagram illustrates the architecture of Router A. It features a central rectangular box labeled "ROUTER A". On the left side, an arrow labeled "PACKET IN" points into the router. On the right side, there are two main connection paths. The top path consists of a vertical stack of ports labeled "PORT 1", "PORT 2", and "PORT N". Above this stack is a label "INTERFACE(10) TO ROUTER B". Arrows from each port in the stack point to a common connection line labeled "14", which then connects to a separate rectangular box labeled "ROUTER B". The bottom path involves a horizontal stack of ports labeled "PORT 1", "PORT 2", and "PORT N". An arrow labeled "18" points from this stack to another horizontal stack of ports labeled "PORT 1", "PORT 2", and "PORT N". This second stack is connected to a third rectangular box labeled "ROUTER C". Arrows from each port in the stack point to a common connection line labeled "12", which then connects to a connection line labeled "PACKET OUT TO ROUTER B". Ellipses between the port stacks indicate multiple ports between the ones explicitly labeled.</p> <p><i>FIG. 1</i></p>

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		<p>Borgione '125 at 1:55-65 ("Link nodes 110 and 120 can be in physically remote locations, thereby connecting their associated local area networks (LANs). The plurality of network links 150 between link nodes 110 and 120 can be aggregated as a single logical link over which all traffic between link nodes 110 and 120 is distributed. Such aggregation multiplies the available bandwidth for communications between link nodes 110 and 120, and therefore between the two local area networks. When appropriately configured, such a connection can permit the two local area networks to interact as if they were one large local area network.")</p> <p>Borgione '125 at 1:66-2:7 ("As stated above, the plurality of network links between 110 and 120 can be aggregated as a single logical link. In this manner, each link node 110 and 120 sees the plurality of network links between them as one logical interface. One type of such an aggregate of links is an EtherChannel, a protocol that allows up to eight Fast Ethernet or Gigabit Ethernet links to be aggregated. Routing protocols treat the aggregated links as a single, routed interface with a common IP address.")</p> <p>Borgione '125 at 5:28-50 ("The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network environment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even</p>

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		distribution of multicast packet transmission across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.”)

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20[preamble]	Apparatus for connecting a network node with a communication network, comprising:	<p>The Reference discloses apparatus for connecting a network node with a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
20[a]	one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network;	<p>The Reference discloses one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

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20[b]	a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules;	<p>The Reference discloses a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
20[c]	a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network; and	<p>The Reference discloses a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards</p>

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		<p>30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

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		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also part of the crossbar fabric interconnect (40). An Ethernet interface for configuration (labeled PCI) is connected to the management card (60).</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

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		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 20	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal structure of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected sequentially: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections to the LIC include two ports on the left and a connection to the right side. Below the LIC, a dashed line separates it from the "AMCC CYCLONE SWITCH FABRIC". This fabric is part of the "S8005 SERIES". On the left, there is an "INGRESS" section containing 32 line interfaces numbered 0 through 31. On the right, there is an "EGRESS" section containing 32 line interfaces numbered 0 through 31. Arrows indicate the flow of traffic from the ingress interfaces through the switch fabric to the egress interfaces.</p> <p>Singh at Figure 4 (annotations added)</p>

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		<p>Fig. 4</p> <pre> graph TD TX["TX AND RX OPTICS AND PHY."] --> Framer[FRAMER OR MAC] Framer --> IPQ[S8565 PRIORITY QUEUE (PQ)] Framer --> N1[NETWORK PROCESSING] N1 --> IEM[INGRESS & EGRESS TRAFFIC MANAGEMENT] IEM --> SLL[SERIAL LINKS] IEM --> N2[NETWORK PROCESSING] N2 --> SLL SLL --> BSS[S8905 MEMORY SUBSYSTEM] SLL --> EDFQ[S8905 EDFQ OR S8505 PQ (PQ)] BSS --> ARBITERS[ARBITERS] ARBITERS --> BIDS[BIDS TO ARBITERS] ARBITERS --> GRANTS[GRANTS FROM ARBITERS] BIDS --> BSS GRANTS --> BSS BSS --> BACKPLANE[BACKPLANE SERIAL LINKS] BACKPLANE --> SLL BACKPLANE --> EDFQ </pre> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> TM[TRANSMITTING PACKETS FROM TM 1702] TM --> INGRESS[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] INGRESS --> CONNECT[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] CONNECT --> 6PORTS[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] 6PORTS --> iMS[STORING PACKETS IN iMS 1706b] iMS --> ASSIGN[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] ASSIGN --> CROSSBAR[SUPPLYING PACKETS TO CROSSBAR 1706d] CROSSBAR --> EGRESS[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] EGRESS --> FAULT[SENSING CONNECTION FAULT 1710] FAULT --> RESELECT[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
20[d]	a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame,	<p>The Reference discloses a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces</p>

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		<p>251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 20	The Reference
		<p>The diagram illustrates a network switching system architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (60), which is also connected to the backplane interconnect (70). The management card is labeled as being PCI-based. Arrows indicate data flow from various ports (e.g., 201, 202, 203, 204, 205, 206, 20n) through the line cards and backplane interconnects to the crossbar fabric interconnect.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

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		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

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		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu

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	<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

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		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

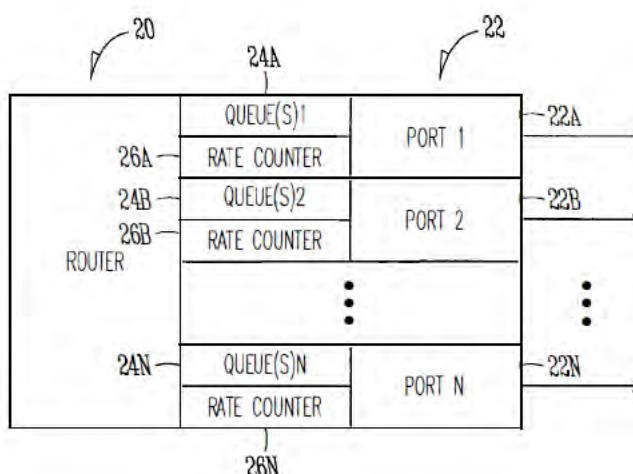
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

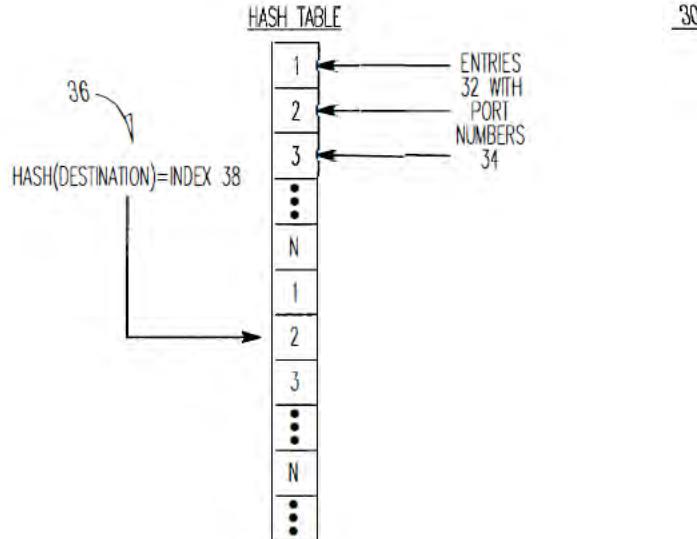
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

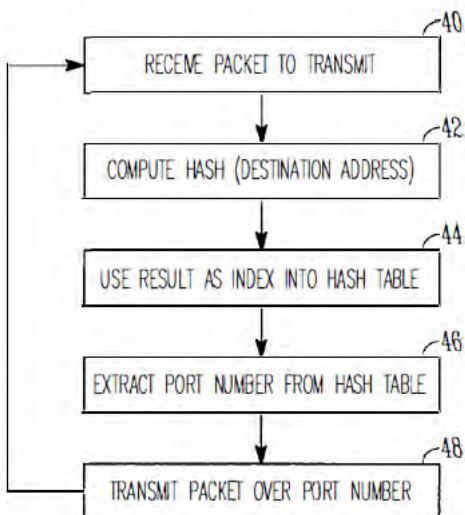
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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

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		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. On the left, a large rectangular box is labeled "ROUTER". Inside this box, there are N horizontal rows, each representing a port. The first row is labeled "PORT 1", the second "PORT 2", and the last one "PORT N". Each row contains two main components: "QUEUE(S)1" and "RATE COUNTER". The "QUEUE(S)1" component is connected to the "RATE COUNTER" via a double-headed arrow. The "RATE COUNTER" is then connected to the corresponding "PORT". Above the first row, there is a label "24A" with a curved arrow pointing to the "QUEUE(S)1" component. Below the first row, there is a label "26A" with a curved arrow pointing to the "RATE COUNTER". Above the second row, there is a label "24B" with a curved arrow pointing to the "QUEUE(S)1" component. Below the second row, there is a label "26B" with a curved arrow pointing to the "RATE COUNTER". This pattern continues for all N ports. Arrows labeled "22A", "22B", and "22N" point from the "PORT 1", "PORT 2", and "PORT N" respectively, indicating the output paths. Ellipses between the second and third rows indicate intermediate ports.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 20	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 20	The Reference
		 <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

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		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), ..., PORT N. Arrows labeled 62A, 62B, ..., 62N point from each port to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed 1 through N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This indicates that the first two ports of Router A (PORT 1 and PORT 2) are mapped to the first two slots of the hash table.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 20	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p style="text-align: center;"><i>FIG. 7</i></p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 20	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

No.	'740 Patent Claim 20	The Reference
		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>

No.	'740 Patent Claim 20	The Reference
20[e]	<p>two or more of the first physical links being aggregated into an external Ethernet link aggregation (LAG) group so as to increase a data bandwidth provided to the network node.</p>	<p>The Reference discloses two or more of the first physical links being aggregated into an external Ethernet link aggregation (LAG) group so as to increase a data bandwidth provided to the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below is an example.</p> <p>Smith '430 at 5:51-64 (“The redundant links coupling each of network devices 120(1) and 120(2) to virtual network device 202 can be operated as a single logical link, referred to herein as a virtual link bundle. Network device 120(1) operates the two links coupling network device 120(1) to virtual network device 202 as 55 a virtual link bundle 250(1). In such an embodiment, each interface in network device 120(1) that is coupled to one of the links is included in an interface bundle, which corresponds to virtual link bundle 250(1). Network device 120(2) similarly operates the two links coupling network device 60 120(2) to virtual network device 202 as virtual link bundle 250(2). In some embodiments, virtual link bundles 250(1) and 250(2) are each operated as an EtherChannel™ or as an aggregated link (as described in IEEE 802.3).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the link aggregation technique. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu

No.	'740 Patent Claim 20	The Reference
		<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Abstract (“Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 1:38-50 (“One way to relieve this bottle-neck is to provide a logical grouping of multiple ports into a single port. The bandwidth of the new port is increased since it has multiple lines (cables) connecting a switch and another network device, each line capable of carrying data at the same rate as the line connecting data sources to the switch. This grouping of ports is sometimes referred to as a port aggregation or port group. One example of such a port aggregation implementation is Cisco Technology, Inc.'s Fast EtherChannel™ port group in a Fast Ethernet network. Further information regarding Fast EtherChannel™ may be found on Cisco Technology, Inc.'s World Wide Web site www.cisco.com. This information is incorporated by reference herein for all purposes.”)</p> <p>DeJager '424 at 2:47-65 (“The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets</p>

No.	'740 Patent Claim 20	The Reference
		<p>from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 2:67-3:15 (“In one aspect, the present invention provides a method of distributing traffic over a network port group. The method involves receiving a packet of data to be forwarded, determining a stream ID for the packet, and determining whether a prior packet having that stream ID has been distributed to a queue on a port in the group during a predetermined time interval. Where a prior packet having that stream ID has not been distributed to a queue on a port of the group during the predetermined time interval, the method involves allocating the packet to a queue of a port having a lesser load in its queue than a queue of any other port of the group. The method may also involve, where a prior packet having that stream ID has been distributed to a queue on a port of the group during the predetermined time interval, allocating the packet to that queue. In addition, the method may involve monitoring the port group queues to maintain proper identification of the least utilized queue.”)</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for</p>

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		<p>determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 4:29-46 (“The present invention provides methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 4:47-58 (“FIG. 1 illustrates a block diagram of a simple network. The network 100 includes two servers S1. and S2, respectively, and two switches, X1 and X2, respectively, as well as four clients C1, C2, C3 and C4, respectively. Clients C , , and C4 are connected to switch X1 by, for example, Fast Ethernet links 102 via ports 1, 2, 3 and 4, respectively. Server S1 is connected to switch X1 via a port aggregation 104, which is a port group composed of ports 5 and 6 of switch X1 . Switch X1 is connected to switch X2 via a second port aggregation 106 which includes ports 7, 8 and 9. Switch X2 is connected to server S2 via port O and Fast Ethernet link 108.”)</p> <p>Dontu at Abstract (“Various methods and systems for preventing erroneous link aggregation due to component relocation are disclosed. Such methods include a method for changing the</p>

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		<p>identifier used by a network device and communicating the identifier change to a peer network device without disrupting an aggregated link. In one embodiment, a method involves detecting an identifier change and sending a Port Aggregation Protocol (PAgP) protocol data unit (PDU) that includes a new identifier and information. The information indicates the identifier change. The new identifier identifies a network device subsequent to the identifier change. Another embodiment of a method involves detecting an identifier change and, subsequent to the identifier change, sending a link aggregation protocol PDU that includes an "old device identifier" field dedicated to conveying an old identifier. The old identifier identifies a network device prior to the identifier change.”)</p> <p>Dontu at Figure 2</p>

No.	'740 Patent Claim 20	The Reference														
		<p style="text-align: center;">5</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Version <u>202</u></td></tr> <tr><td>My Device Identifier <u>204</u> (ID = X)</td></tr> <tr><td>My Distribution Requirements <u>206</u></td></tr> <tr><td>My Port Priority <u>208</u></td></tr> <tr><td>My Port Identifier <u>210</u></td></tr> <tr><td>My Group Capability <u>212</u></td></tr> <tr><td>My Agport Identifier <u>214</u></td></tr> <tr><td>Your Device Identifier <u>216</u> (ID = Y)</td></tr> <tr><td>Your Distribution Requirements <u>218</u></td></tr> <tr><td>Your Port Priority <u>220</u></td></tr> <tr><td>Your Port Identifier <u>222</u></td></tr> <tr><td>Your Group Capability <u>224</u></td></tr> <tr><td>Your Agport Identifier <u>226</u></td></tr> <tr><td>Partner Count <u>228</u></td></tr> </table> <p style="text-align: center;">↗</p> <p>Port Aggregation Protocol PDU 200 (sent from Interfaces 120(1), 120(2) and 120(3))</p> <p style="text-align: center;">FIG. 2</p> <p>Dontu at Figure 3</p>	Version <u>202</u>	My Device Identifier <u>204</u> (ID = X)	My Distribution Requirements <u>206</u>	My Port Priority <u>208</u>	My Port Identifier <u>210</u>	My Group Capability <u>212</u>	My Agport Identifier <u>214</u>	Your Device Identifier <u>216</u> (ID = Y)	Your Distribution Requirements <u>218</u>	Your Port Priority <u>220</u>	Your Port Identifier <u>222</u>	Your Group Capability <u>224</u>	Your Agport Identifier <u>226</u>	Partner Count <u>228</u>
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Partner Count <u>228</u>																

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		<p style="text-align: center;">Network Device 100(1)</p> <p style="text-align: center;">Network Device 100(2)</p> <p style="text-align: center;">Aggregated Link 105</p> <p style="text-align: center;">FIG. 3</p> <p>Dontu at Figure 14</p>

No.	'740 Patent Claim 20	The Reference
		<p style="text-align: center;">FIG. 14</p> <p>Dontu at [0004] (“Link aggregation is used to logically combine two or more individual links into a single aggregated link. Link aggregation can provide improved performance and increased fault tolerance. Improved performance arises because the aggregated link appears to have a bandwidth equal to the combined bandwidth of the individual links. Traffic can be load-balanced among the individual links. Increased fault tolerance is provided since one or more individual links within an aggregated link can fail without disrupting communication between the devices coupled by the aggregated link. Link aggregation techniques include</p>

No.	'740 Patent Claim 20	The Reference
		<p>Link Aggregation Control Protocol (LACP), which is defined in IEEE 803.2ad, and Port Aggregation Protocol (PAgP), which is a standard promulgated by CISCO SYS-TEMS, INC.”)</p> <p>Dontu at [0012] (“The method can also involve detecting whether a partner interface is executing a compatible version of PAgP. If the partner interface is not executing the compatible version of PAgP, the compatible version of PAgP can be provided to the partner interface. Alternatively, if the partner interface is not executing the compatible version of PAgP, the partner interface can be inhibited from including a link in an aggregated link.”)</p> <p>Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components).”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6).”)</p>

No.	'740 Patent Claim 20	The Reference
		<p>Dontu at [0037] ("In this example, the network devices 100(1) and 100(2) use Port Aggregation Protocol (PAgP) to form aggregated links. Network devices 100(1) each send PAgP protocol data units (PDUs) to each other in order to determine whether any of the links between the two network devices can be combined into an aggregated link. Each PAgP PDU includes an identifier that uniquely identifies the network device that sent that PAgP PDU. Within network device 100(1), identifier module 130(1) of network device component 110(1) supplies an identifier "X" to each of the interfaces 120(1)-120(3) within network device 100(1). Interfaces 120(1)-120(3) include identifier X in each PAgP PDU sent by those interfaces. Similarly, identifier module 130(2) of network device component 110(4) supplies an identifier "Y" to each interface 120(4)-120(6) of network device 100(2). Interfaces 120(4)-120(6) include identifier Y in each PAgP PDU sent by those interfaces.")</p> <p>Dontu at [0040] ("FIG. 2 illustrates some of the fields that can be included in a PAgP PDU. As shown, PDU 200 includes Version field 202, My Device Identifier field 204 ("My" refers to the device sending the PAgP PDU), My Distribution Requirements field 206, My Port Priority field 208, My Port Identifier field 212, My Group Capability field 212, My Agport (Aggregated Port) Identifier field 214, Your Device Identifier field 216 ("Your" refers to the device to which the PAgP PDU is being sent), Your Distribution Requirements field 218, Your Port Priority field 220, Your Port Identifier field 222, Your Group Capability field 224, Your Agport Identifier field 226, and Partner Count field 228.")</p> <p>Dontu at [0110] ("Interfaces 1420(13), 1420(9), and 1420(16), which are each coupled to network device 1220(1) by virtual link bundle 1350(1), form an interface bundle (e.g., an Ether-Channel (TM) port bundle). Similarly, interfaces 1420(11) and 1420(8) form another interface bundle that is coupled to network device 1220(2) by virtual link bundle 1350(2). Interfaces 1420(7) and 1420(12) form a third interface bundle that is coupled to network device 1220(3) by virtual link bundle 1350(3). Within virtual network device 1302, each interface in the same interface bundle is assigned the same logical identifier. For example, interfaces 1420(13), 1420(9), and 1420(16) are each assigned the same logical identifier. In some embodiments, packets received via one of these interfaces are tagged or otherwise associated with the logical identifier to indicate that those packets were received via the</p>

No.	'740 Patent Claim 20	The Reference
		<p>virtual link bundle coupling virtual network device 1302 to network device 1220(1). It is noted that similar interface bundles are implemented within each network device 1220(1)-1220(3), and that interfaces included in such bundles are also assigned the same logical identifier by each network device (or by virtual network device 1302, in embodiments in which virtual network device 1302 controls the configuration of the network devices 1220(1)-1220(3)). For example, network device 1220(1) can assign the same logical identifier to each of the interfaces coupled to virtual link bundle 1350(1).”)</p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 2:6-22 (“In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.”)</p>

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		<p>Li '914 at 4:9-25 ("Referring to FIG. 1, a Router A is shown having an interface 10 with a plurality of ports which connect Router A with Router B over a plurality of connections, lines, wires, links or bundled links 14. The ports 12 of Router A are configured to permit transmission of packets from Router A to Router B, and these ports 12 may be referred to as output ports, egress ports, links, or the like. As shown in FIG. 1, port 1 to port N may be connected with Router B, and there may be additional interfaces 16 having ports 18 connected with other routers in the network. When a packet is received by Router A, Router A determines whether the received packet should be transmitted to Router B or to other routers connected to Router A, based in part upon the destination address of the packet. If a packet is to be transmitted from Router A to Router B, then Router A may transmit this packet over the one of the ports 12 shown in FIG. 1.")</p> <p>Li '914 at Figure 1</p> <p>The diagram illustrates the architecture of Router A. It features a central rectangular box labeled "ROUTER A". On the left side, an arrow labeled "PACKET IN" points into the router. On the right side, there are two main connection paths. The top path consists of a vertical stack of ports labeled "PORT 1", "PORT 2", and "PORT N". Above this stack is a label "INTERFACE(10) TO ROUTER B". Arrows from each port in the stack point to a single horizontal line labeled "14", which then connects to a second rectangular box labeled "ROUTER B". The bottom path involves a horizontal line labeled "18" connecting to a stack of ports labeled "PORT 1", "PORT 2", and "PORT N". Arrows from these ports point to another horizontal line labeled "12", which then connects to a third rectangular box labeled "ROUTER C". Finally, an arrow labeled "PACKET OUT TO ROUTER B" points from Router A towards Router B.</p> <p><i>FIG. 1</i></p>

No.	'740 Patent Claim 20	The Reference
		<p>Borgione '125 at 1:55-65 ("Link nodes 110 and 120 can be in physically remote locations, thereby connecting their associated local area networks (LANs). The plurality of network links 150 between link nodes 110 and 120 can be aggregated as a single logical link over which all traffic between link nodes 110 and 120 is distributed. Such aggregation multiplies the available bandwidth for communications between link nodes 110 and 120, and therefore between the two local area networks. When appropriately configured, such a connection can permit the two local area networks to interact as if they were one large local area network.")</p> <p>Borgione '125 at 1:66-2:7 ("As stated above, the plurality of network links between 110 and 120 can be aggregated as a single logical link. In this manner, each link node 110 and 120 sees the plurality of network links between them as one logical interface. One type of such an aggregate of links is an EtherChannel, a protocol that allows up to eight Fast Ethernet or Gigabit Ethernet links to be aggregated. Routing protocols treat the aggregated links as a single, routed interface with a common IP address.")</p> <p>Borgione '125 at 5:28-50 ("The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network environment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even</p>

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		distribution of multicast packet transmission across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.”)

No.	'740 Patent Claim 21	The Reference
21	The apparatus according to claim 17, and comprising a multiplexer, which is arranged to perform at least one of multiplexing upstream data frames sent from the network node to the communication network, and demultiplexing downstream data frames sent from the communication network to the network node.	<p>The Reference discloses the apparatus according to claim 17, and comprising a multiplexer, which is arranged to perform at least one of multiplexing upstream data frames sent from the network node to the communication network, and demultiplexing downstream data frames sent from the communication network to the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 22	The Reference
22	<p>The apparatus according to claim 17, wherein the control module is arranged to balance a frame data rate among at least some of the first and second physical links.</p>	<p>The Reference discloses the apparatus according to claim 17, wherein the control module is arranged to balance a frame data rate among at least some of the first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim	The Reference
	23	

23	<p>The apparatus according to claim 17, wherein the control module is arranged to apply a mapping function to the at least one of the frame attributes so as to select the first and second physical links.</p>	<p>The Reference discloses the apparatus according to claim 17, wherein the control module is arranged to apply a mapping function to the at least one of the frame attributes so as to select the first and second physical links.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p>
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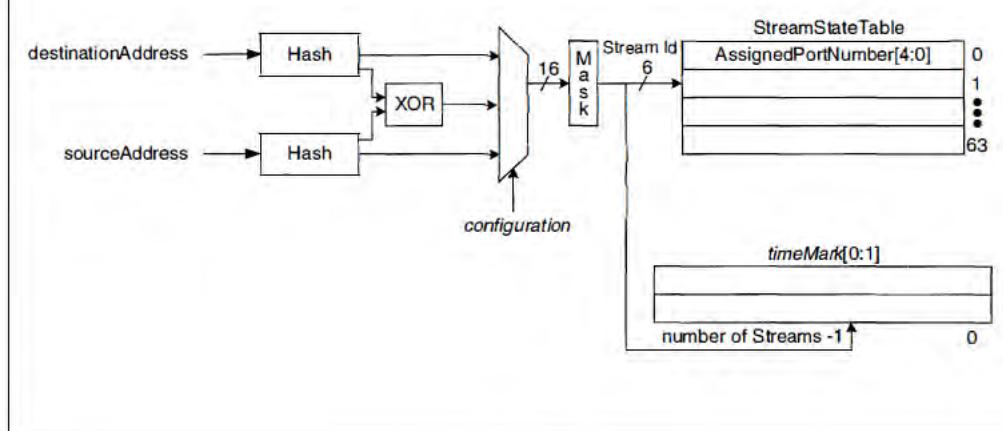


FIG. 2

DeJager '424 at Figure 3A

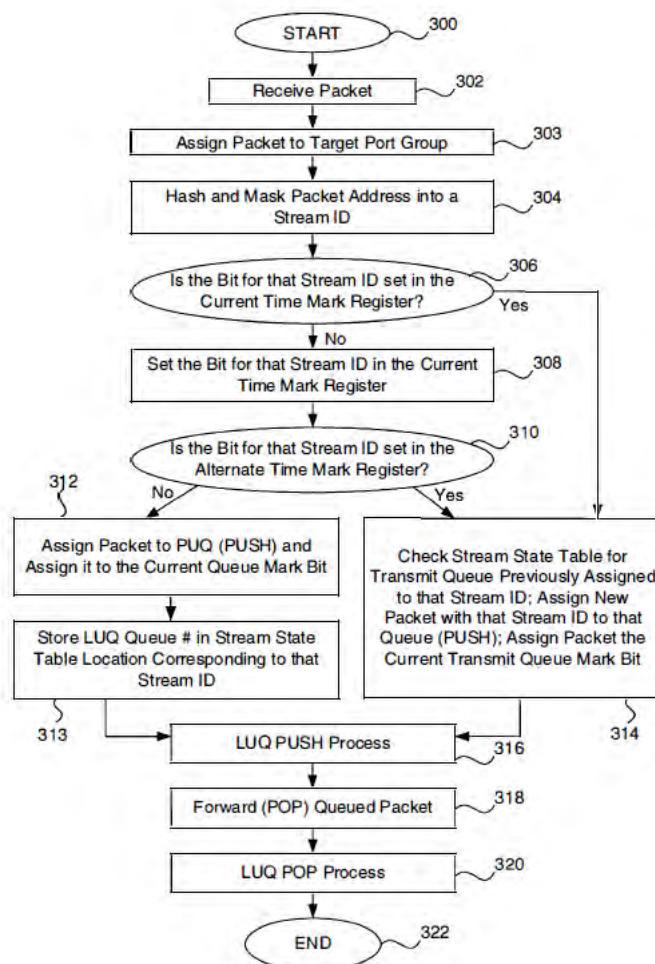


FIG. 3A

DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a

	<p>mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the</p>
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	<p>negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity.</p>
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	<p>When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p>
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	<p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates</p>
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	<p>information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>
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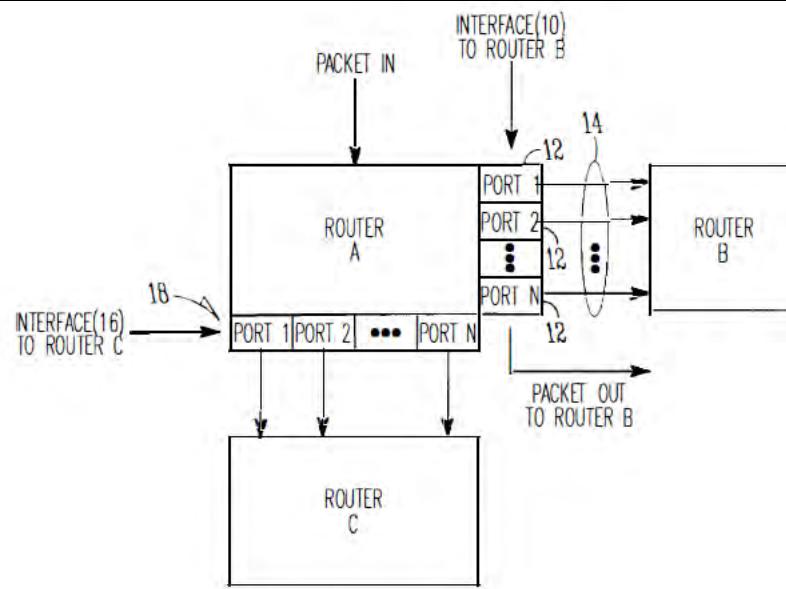


FIG. 1

Li '914 at Figure 2

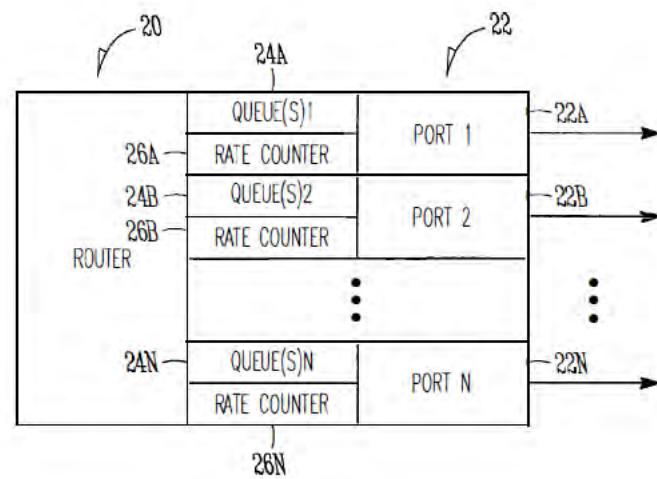


FIG. 2

Li '914 at Figure 3

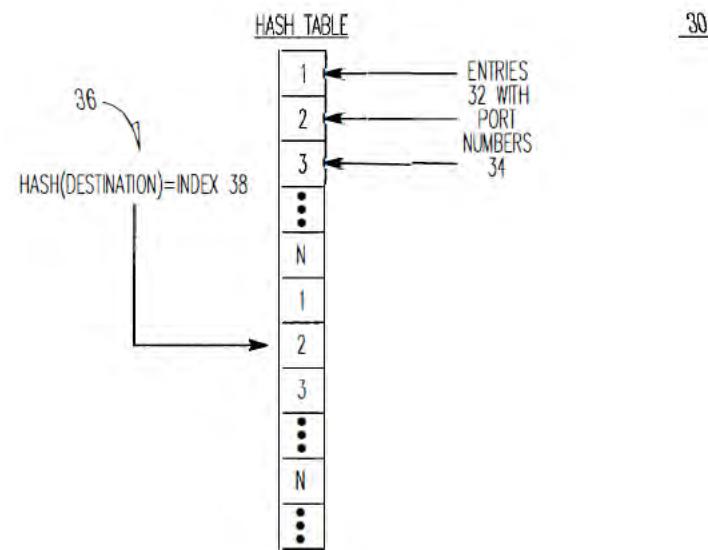


FIG. 3

Li '914 at Figure 4

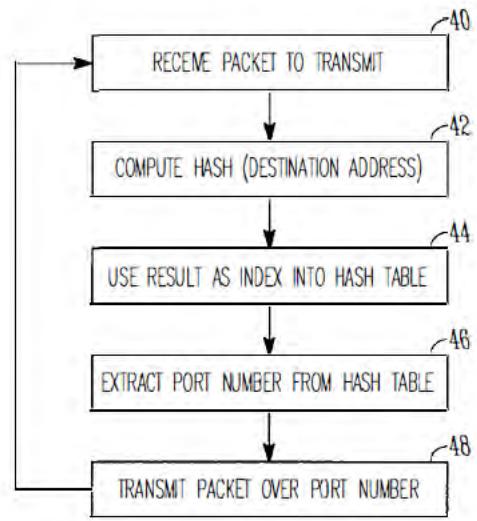


FIG. 4

Li '914 at Figure 6

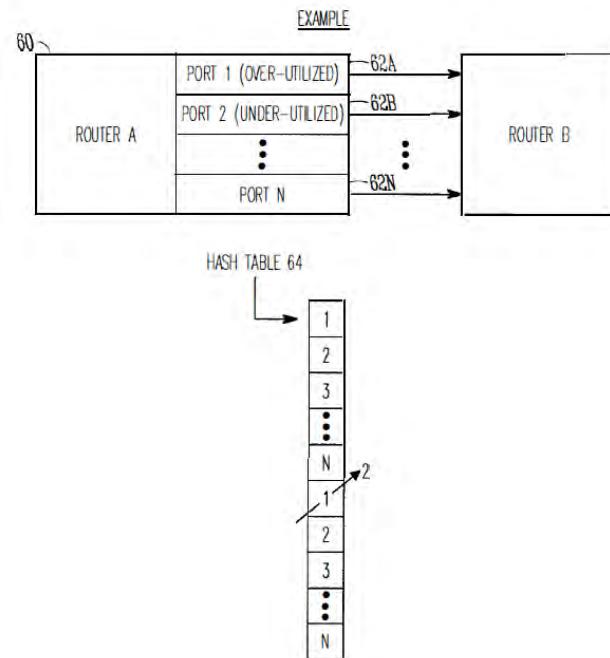


FIG.6

Li '914 at Figure 7

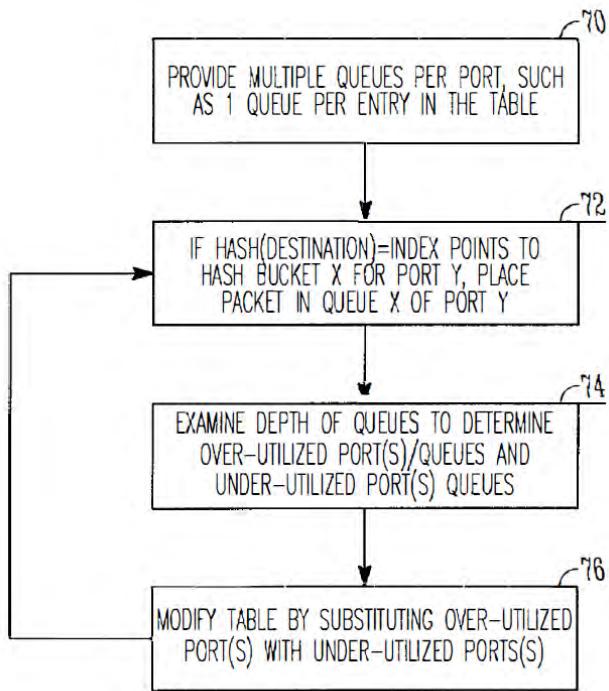


FIG. 7

Li '914 at Figure 8

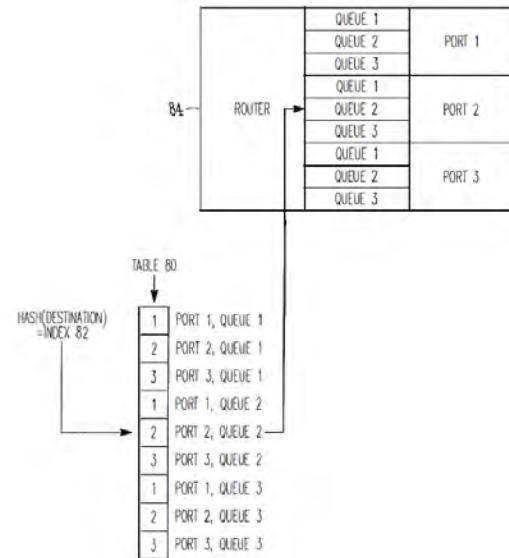


FIG. 8

Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)

Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the

	<p>above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash</p>
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	<p>operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p> <p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed</p>
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	<p>multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li ’914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione ’125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione ’125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione ’125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link</p>
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	<p>identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast packet thus forming replicated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p>
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	<p>Borgione '125 at 5:28-54 ("The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network environment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmission across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.")</p> <p>Borgione '125 at 7:1-5 ("A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID.)")</p> <p>Borgione '125 at Figure 2-5</p>
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Figure 2

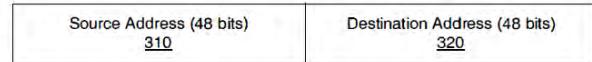


Figure 3

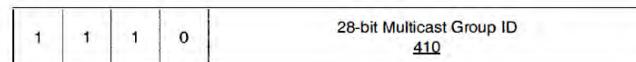


Figure 4

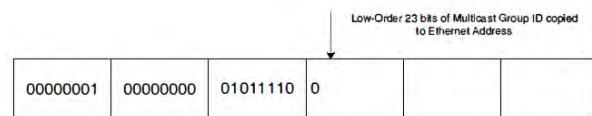


Figure 5

No.	'740 Patent Claim 24	The Reference

24	<p>The apparatus according to claim 23, wherein the mapping function comprises a hashing function.</p>	<p>The Reference discloses the apparatus according to claim 23, wherein the mapping function comprises a hashing function.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p>
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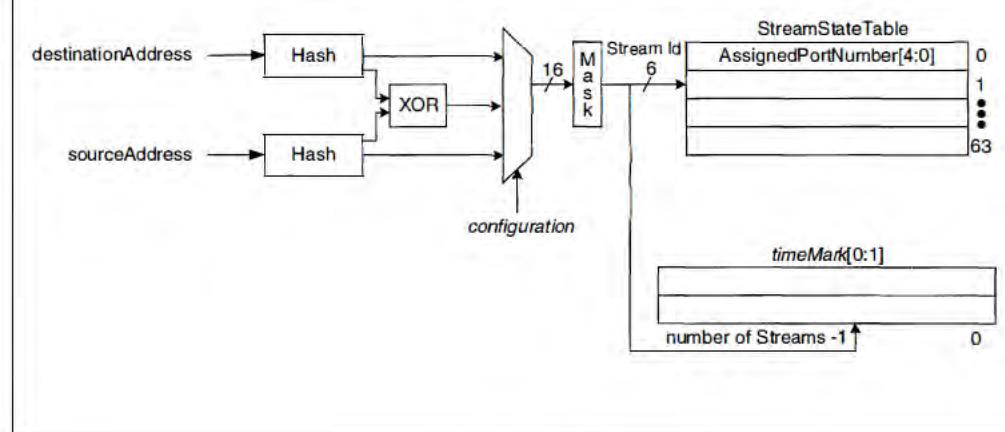


FIG. 2

DeJager '424 at Figure 3A

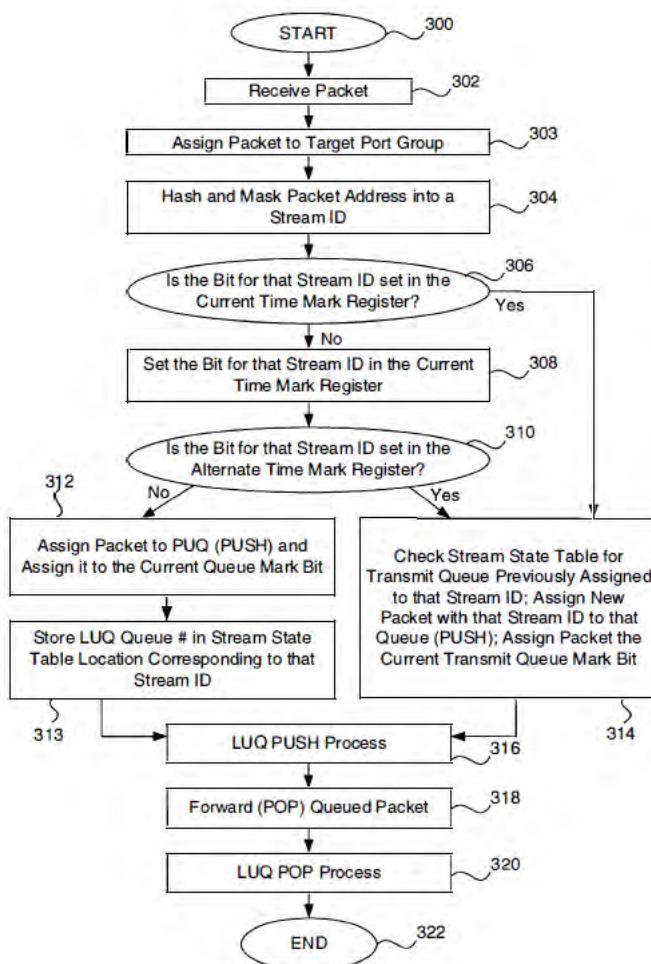


FIG. 3A

DeJager '424 at 3:16-38 ("In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a

	<p>mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager ’424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager ’424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager ’424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data is received for forwarding. At a step 303 the packet’s address is analyzed to determine the packet’s target port group. At a step 304, the packet’s address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the</p>
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	<p>negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity.</p>
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	<p>When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices. Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p>
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	<p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates</p>
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	<p>information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>
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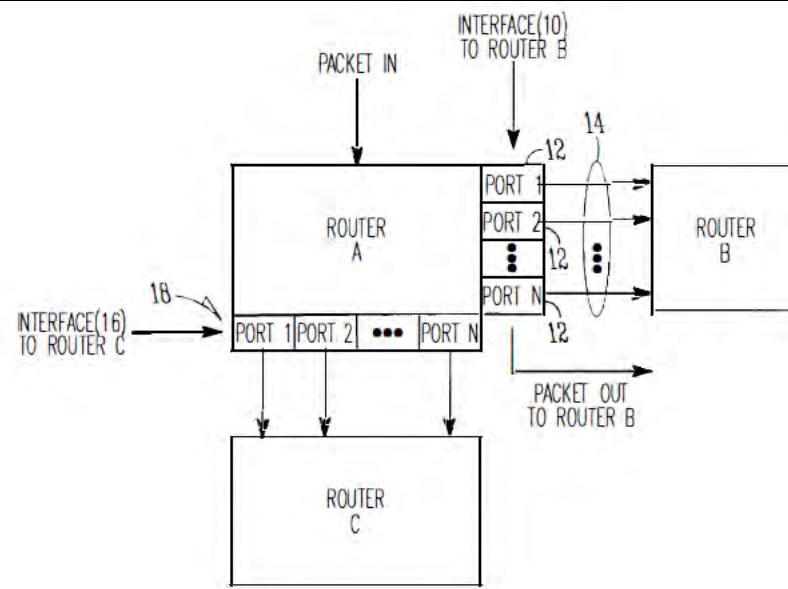


FIG. 1

Li '914 at Figure 2

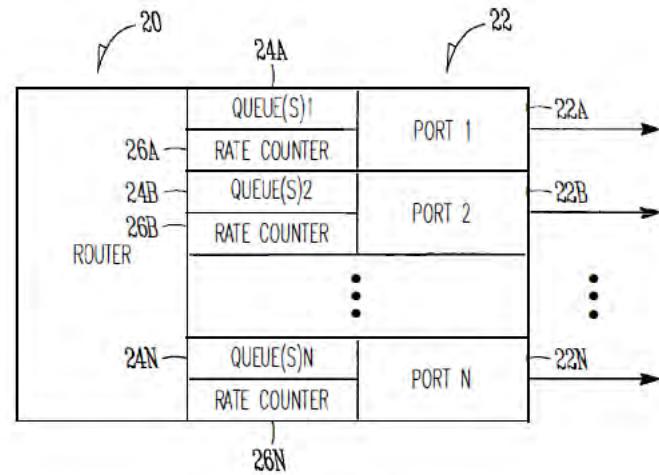


FIG. 2

Li '914 at Figure 3

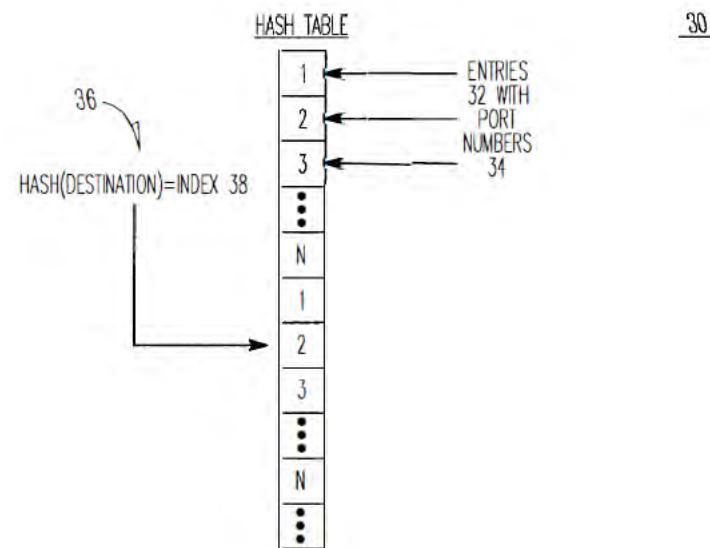


FIG. 3

Li '914 at Figure 4

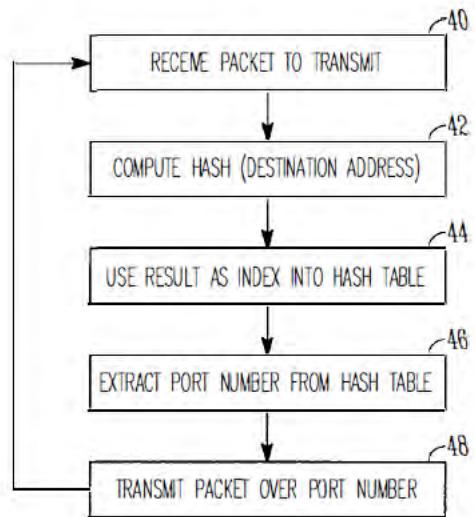


FIG. 4

Li '914 at Figure 6

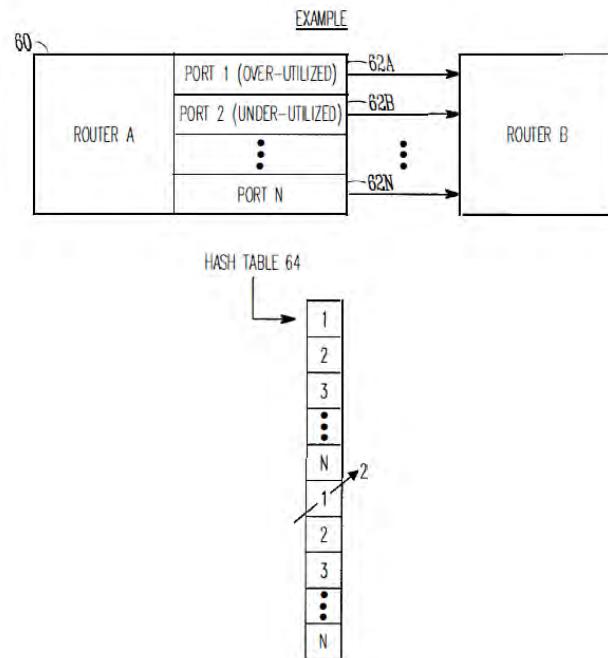


FIG.6

Li '914 at Figure 7

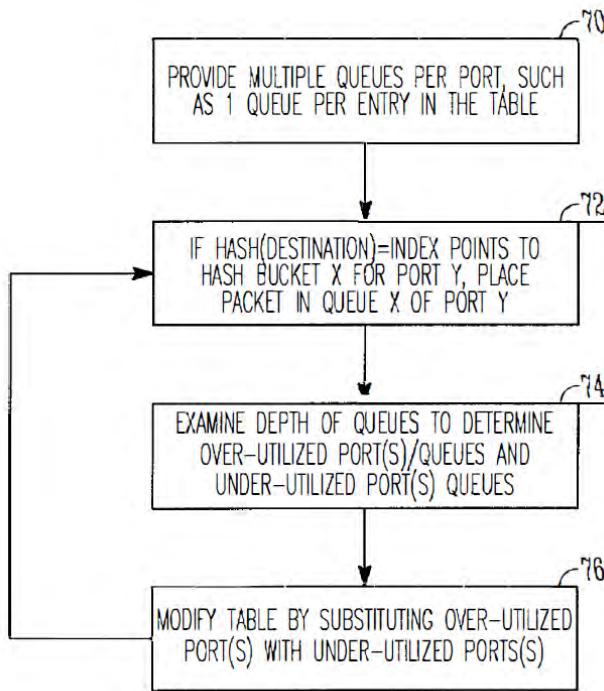


FIG. 7

Li '914 at Figure 8

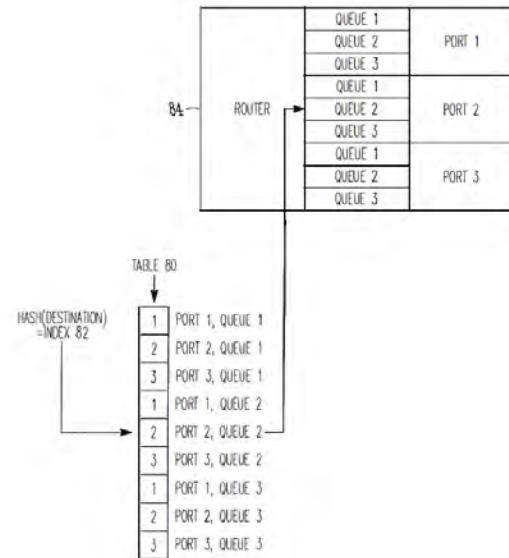


FIG. 8

Li '914 at 1:30-43 ("For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.")

Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the

	<p>above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p> <p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash</p>
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	<p>operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table 30, then in this example the port number “1” is extracted from the table and the packet is transmitted to appropriate adjacent router using port number “1.” These examples are provided for purposes of illustration only.”)</p> <p>Li '914 at 5:66-6:8 (“Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed</p>
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	<p>multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.”)</p> <p>Li ’914 at 6:65-7:7 (“At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.”)</p> <p>Borgione ’125 at 2:8-18 (“Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.”)</p> <p>Borgione ’125 at 3:14-23 (“A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione ’125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link</p>
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	<p>identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link.</p> <p>Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is configured to replicate the multicast packet thus forming replicated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p>
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	<p>Borgione '125 at 5:28-54 ("The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network environment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmission across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.")</p> <p>Borgione '125 at 7:1-5 ("A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID.)")</p> <p>Borgione '125 at Figure 2-5</p>
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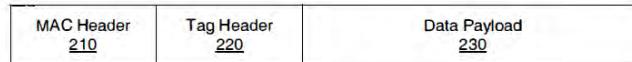


Figure 2

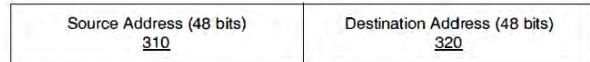


Figure 3

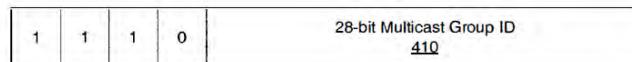


Figure 4

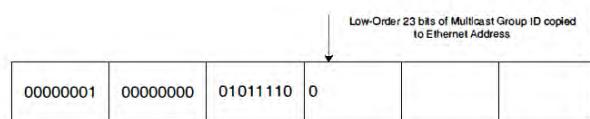


Figure 5

No.	'740 Patent Claim 25	The Reference
25[a]	The apparatus according to claim 24, wherein the control module is arranged to determine a hashing size responsively to a number of at least some of the first and second physical links. To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of	The Reference discloses the apparatus according to claim 24, wherein the control module is arranged to determine a hashing size responsively to a number of at least some of the first and second physical links. To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of

No.	'740 Patent Claim 25	The Reference
	number of at least some of the first and second physical links,	the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
25[b]	to apply the hashing function to the at least one of the frame attributes to produce a hashing key,	<p>The Reference discloses to apply the hashing function to the at least one of the frame attributes to produce a hashing key.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
25[c]	to calculate a modulo of a division operation of the hashing key by the hashing size, and	<p>The Reference discloses to calculate a modulo of a division operation of the hashing key by the hashing size.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, and Singh.</p>

No.	'740 Patent Claim 25	The Reference
		<p>Singh at 9:30-43 (“The ratio between the number of line ingress links and the number of links carrying data to the backplane gives the backplane speedup for the system. In this example, there are 10 ingress links into the MS and 20 links (2 backplane channels) carrying that data to the backplane. This gives a backplane speedup of 2x. As another example, with 8 ingress links and 12 backplane links, there is a speedup of 1.5x. It should be noted that in addition to the backplane speedup, there is also an ingress/egress speedup. With 10 ingress links capable of carrying 2 Gbps each of raw data, this presents a 20 Gbps interface to the MS. An OC-192 only has approximately 10 Gbps worth of data. Taking into account cell overhead and cell quantization inefficiencies, there still remains excess capacity in the links.”)</p> <p>Singh at 11:29-38 (“FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a channel, whether it is a backplane channel or an ingress/egress channel. There is no logical relationship between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 16:28-44 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 16x16 and 32x32 is the organization of the switchplane. The port card remains the same. Backplane channels 1 and 2 are used for the backplane connectivity. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 16, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p>

No.	'740 Patent Claim 25	The Reference
		<p>Singh at 17:31-49 ("In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 8x8 and 16x16 is the organization of the switchplane. The port card remains the same. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Links 0-7 and 24-31 on the arbiters would not be used and would be powered off. Links 0-7 and 24-31 on the crossbars would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Backplane channels 1 and 2 are used for the backplane connectivity. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 8, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.")</p>
25[d]	to select the first and second physical links responsively to the modulo.	<p>The Reference discloses to select the first and second physical links responsively to the modulo.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, and Singh.</p> <p>Singh at 9:30-43 ("The ratio between the number of line ingress links and the number of links carrying data to the backplane gives the backplane speedup for the system. In this example, there are 10 ingress links into the MS and 20 links (2 backplane channels) carrying that data to the backplane. This gives a backplane speedup of 2x. As another example, with 8</p>

No.	'740 Patent Claim 25	The Reference
		<p>ingress links and 12 backplane links, there is a speedup of 1.5x. It should be noted that in addition to the backplane speedup, there is also an ingress/egress speedup. With 10 ingress links capable of carrying 2 Gbps each of raw data, this presents a 20 Gbps interface to the MS. An OC-192 only has approximately 10 Gbps worth of data. Taking into account cell overhead and cell quantization inefficiencies, there still remains excess capacity in the links.”)</p> <p>Singh at 11:29-38 (“FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a channel, whether it is a backplane channel or an ingress/egress channel. There is no logical relationship between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.”)</p> <p>Singh at 16:28-44 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 16x16 and 32x32 is the organization of the switchplane. The port card remains the same. Backplane channels 1 and 2 are used for the backplane connectivity. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 16, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p> <p>Singh at 17:31-49 (“In the single channel configuration, the egress MS is the same as the ingress MS. As far as the port card is concerned, the only difference between 8x8 and 16x16 is the organization of the switchplane. The port card remains the same. Ingress and egress links 30-39 on the MS would not be used and would be powered off. Links 0-7 and 24-31 on</p>

No.	'740 Patent Claim 25	The Reference
		<p>the arbiters would not be used and would be powered off. Links 0-7 and 24-31 on the crossbars would not be used and would be powered off. Arbiter interfaces O.A, O.B, 3.A and 3.B on the PQ are unused and would be powered off. MS links 0-7 are used for both the ingress and egress to the traffic manager. Backplane channels 1 and 2 are used for the backplane connectivity. Each crossbar always handles the same numbered link within a backplane channel from each port card. Link numbers on the crossbars, modulo 8, correspond to the port card numbers. Link numbers on the MSs to the backplane, modulo 10, correspond to the backplane channel's link number. If it were desired to run IO-links per channel, a 5th crossbar would be added to each switch card.”)</p>

No.	'740 Patent Claim 26	The Reference
26	<p>The apparatus according to claim 25, wherein the control module is arranged to select the first and second physical links responsively to respective first and second subsets of bits in a binary representation of the modulo.</p>	<p>The Reference discloses the apparatus according to claim 25, wherein the control module is arranged to select the first and second physical links responsively to respective first and second subsets of bits in a binary representation of the modulo.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, and Solomon.</p> <p>Solomon at [0054] ("Having selected a physical port, RSVP-TE processor 30 of switch A now generates a suitable MPLS label, at a label generation step 64. The preceding node upstream of switch A will subsequently attach this MPLS label to all MPLS packets transmitted through tunnel 28 to switch A. The label is assigned, in conjunction with the mapping function of mapper 34, so as to ensure that all MPLS packets carrying this label are switched through the physical port that was selected for this tunnel at step 62. For this purpose, RSVP-TE processor 30 of switch A dedicates a sub-set of the bits of MPLS label 52 to encode the serial number of the selected physical port. For example, the four least-significant bits of MPLS label 52 may be used for encoding the selected port number. This configuration is suitable for representing LAG groups having up to 16 physical ports ($N < 16$). The remaining bits of MPLS label 52 may be chosen at random or using any suitable method known in the art.")</p> <p>Solomon at [0056] ("Mapper 34 of switch A maps the received packets belonging to tunnel 28 to the selected physical Ethernet port at a mapping step 70. For this purpose, mapper 34 extracts the MPLS label from each received packet and decodes the selected physical port number from the dedicated sub-set of bits, such as the four LSB, as described in step 64 above. The decoded value is used for mapping the packet to the selected physical port, which</p>

		was allocated by the CAC processor at step 62 above. In the four-bit example described above, the mapping function may be written explicitly as: Selected port number=((MPLS label) and (0x0000F)), wherein "and" denotes the "bitwise and" operator."
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No.	'740 Patent Claim 27	The Reference
27	The apparatus according to claim 17, wherein the at least one of the frame attributes comprises at least one of a layer 2 header field, a layer 3 header field, a layer 4 header field, a source Internet Protocol (IP) address, a destination IP address, a source medium access control (MAC) address, a destination MAC address, a source Transmission Control Protocol (TCP) port and a destination TCP port.	<p>The Reference discloses the apparatus according to claim 17, wherein the at least one of the frame attributes comprises at least one of a layer 2 header field, a layer 3 header field, a layer 4 header field, a source Internet Protocol (IP) address, a destination IP address, a source medium access control (MAC) address, a destination MAC address, a source Transmission Control Protocol (TCP) port and a destination TCP port.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

No.	'740 Patent Claim 28	The Reference
28[preamble]	Apparatus for connecting a network	The Reference discloses apparatus for connecting a network node with a communication network.

No.	'740 Patent Claim 28	The Reference
	node with a communication network, comprising:	To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
28[a]	one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network;	<p>The Reference discloses one or more interface modules, which are arranged to process data frames having frame attributes sent between the network node and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
28[b]	a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules;	<p>The Reference discloses a first group of first physical links arranged in parallel so as to couple the network node to the one or more interface modules.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel</p>

No.	'740 Patent Claim 28	The Reference
		System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
28[c]	a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network; and	<p>The Reference discloses a second group of second physical links arranged in parallel so as to couple the one or more interface modules to the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p>

No.	'740 Patent Claim 28	The Reference
		<p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p> <p style="text-align: center;">FIG. 1</p>

No.	'740 Patent Claim 28	The Reference
		<p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iT M); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208 through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 28	The Reference
		<p>Fig. 3</p> <p>The diagram illustrates the internal architecture of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections from the LIC include two ports labeled "DDCVID" and "DDVBI" on the left, and a "PARALLEL FIBER OPTICS" port on the right. Below the LIC, a dashed line separates it from an "AMCC CYCLONE SWITCH FABRIC". This fabric is part of the "S8005 SERIES". On the left side, there is an "INGRESS" section containing 32 line interfaces numbered 0 through 31. On the right side, there is an "EGRESS" section containing 32 line interfaces numbered 0 through 31. The ingress and egress line interfaces are connected to the switch fabric via bidirectional arrows. A red rectangular box highlights the connection area between the LIC and the switch fabric.</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 28	The Reference
		<p>Fig. 4</p> <p>Singh at Figure 17</p>

No.	'740 Patent Claim 28	The Reference
	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] A4 --> S4[SENSING CONNECTION FAULT 1710] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

No.	'740 Patent Claim 28	The Reference
		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
28[d]	a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame,	<p>The Reference discloses a control module, which is arranged to select for each data frame sent between the communication network and the network node, in a single computation based on at least one of the frame attributes, a first physical link out of the first group and a second physical link out of the second group over which to send the data frame.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces</p>

No.	'740 Patent Claim 28	The Reference
		<p>251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 28	The Reference
		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate various data paths: 1Gbps Ethernet (201, 204), 2Gbps FC (202, 203), 10Gbps FC (205, 206), 10Gbps Ethernet (20n), and InfiniBand (IB) (20n). Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (60), which is also connected to the backplane interconnect (70). A PCI logo is shown near the management card.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

No.	'740 Patent Claim 28	The Reference
		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane charmel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane charmel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane channels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub-system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

No.	'740 Patent Claim 28	The Reference
		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1708] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1710] A4 --> S4[SENSING CONNECTION FAULT 1712] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu

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	<ul style="list-style-type: none"> • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

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		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

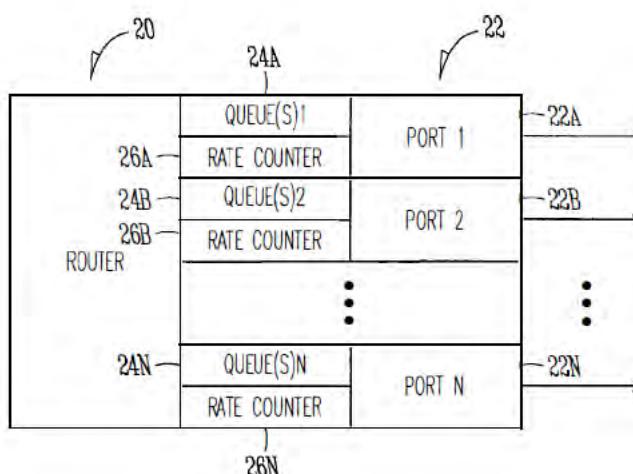
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

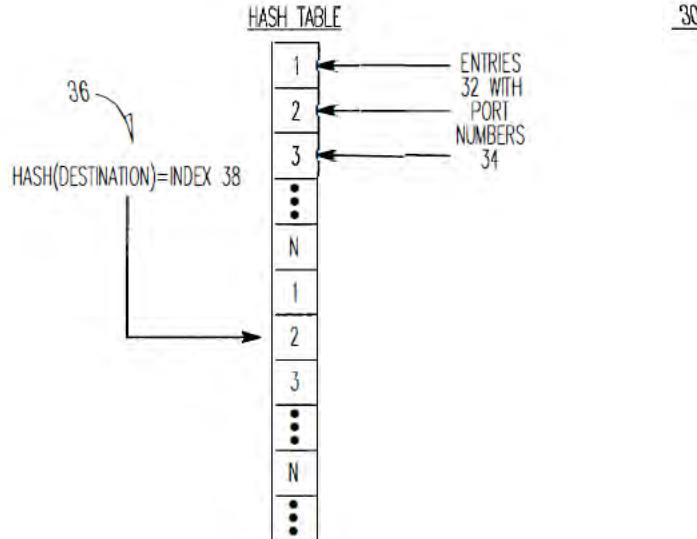
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

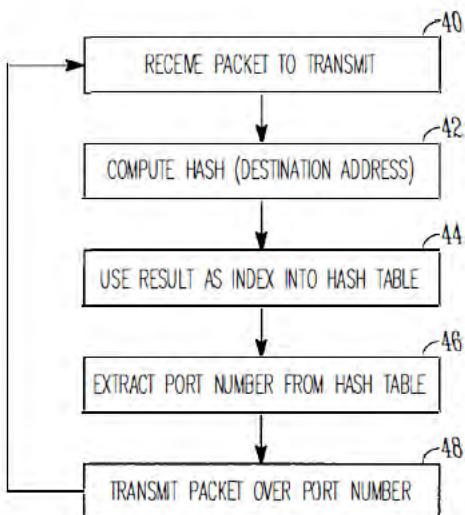
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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

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		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. At the top, a curved arrow labeled 20 points to a box labeled 'ROUTER'. Inside the 'ROUTER' box, there are two columns of components. The left column contains 'QUEUE(S)1', 'RATE COUNTER', 'QUEUE(S)2', 'RATE COUNTER', and 'QUEUE(S)N', each associated with a reference number 24A, 24B, 26A, 26B, and 2AN respectively. The right column contains 'PORT 1', 'PORT 2', and 'PORT N', each associated with a reference number 22A, 22B, and 22N respectively. Arrows indicate connections from the queues and rate counters to their corresponding ports. Below the router box, a curved arrow labeled 26N points to the bottom edge of the box.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

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		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

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		 <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

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		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), ..., PORT N. Arrows labeled 62A, 62B, ..., 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed 1 through N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to different output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

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		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 28	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>
28[e]	the communication network being	The Reference discloses the communication network being arranged to provide a communication service to the network node.

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	arranged to provide a communication service to the network node,	To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.
28[f]	the service having specified bandwidth requirements comprising at least one of a committed information rate (CR), a peak information rate (PIR) and an excess information rate (EIR), and	<p>The Reference discloses the service having specified bandwidth requirements comprising at least one of a committed information rate (CR), a peak information rate (PIR) and an excess information rate (EIR).</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
28[g]	the first and second groups of physical links being dimensioned to provide an allocated bandwidth for the communication service responsively to the bandwidth requirements.	<p>The Reference discloses the first and second groups of physical links being dimensioned to provide an allocated bandwidth for the communication service responsively to the bandwidth requirements.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

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	to the band width requirements.	skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.

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29[preamble]	Apparatus for connecting user ports to a communication network, comprising:	<p>The Reference discloses apparatus for connecting user ports to a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
29[a]	one or more user interface modules coupled to the user ports, which are arranged to process data frames having frame attributes sent between the user ports and the communication network,	<p>The Reference discloses one or more user interface modules coupled to the user ports, which are arranged to process data frames having frame attributes sent between the user ports and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>

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29[b]	at least one of said user interface modules being bi-directional and operative to communicate in both an upstream direction and a downstream direction;	<p>The Reference discloses at least one of said user interface modules being bi-directional and operative to communicate in both an upstream direction and a downstream direction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
29[c]	a backplane having the one or more user interface comprising a plurality of backplane traces arranged in parallel so as to transfer the data frames between the one or more user interface modules and the communication network,	<p>The Reference discloses a backplane having the one or more user interface comprising a plurality of backplane traces arranged in parallel so as to transfer the data frames between the one or more user interface modules and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces</p>

No.	'740 Patent Claim 29	The Reference
		<p>251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 29	The Reference
		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate data flow from the line cards through the backplane interconnects to the crossbar fabric interconnect. Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (70), which is also connected to the backplane interconnect. A PCI logo is shown near the management card.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

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		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

No.	'740 Patent Claim 29	The Reference
		<p>Fig. 3</p> <p>Singh at Figure 4 (annotations added)</p>

No.	'740 Patent Claim 29	The Reference
		<p>Fig. 4</p> <pre> graph TD TX["TX AND RX OPTICS AND PHY."] --> Framer[FRAMER OR MAC] Framer --> Network1[NETWORK PROCESSING] Network1 --> Ingress[INGRESS & EGRESS TRAFFIC MANAGEMENT] Ingress --> Network2[NETWORK PROCESSING] Network2 --> Framer Framer -- TDM --> Ingress Ingress --> SerialLinks[SERIAL LINKS] SerialLinks --> Memory[SS905 MEMORY SUBSYSTEM] Memory --> SerialLinks Memory --> PQ[S8565 PRIORITY QUEUE (PQ)] PQ --> Memory PQ --> Arbiters[ARBITERS] Arbiters -- BIDS TO ARBITERS --> PQ Arbiters -- GRANTS FROM ARBITERS --> Memory Memory --> PQ PQ --> Memory PQ --> EDFQ[S8805 EDFQ OR S8505 PQ (PQ)] EDFQ --> PQ </pre> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] A4 --> S4[SENSING CONNECTION FAULT 1710] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
29[d]	at least one of said backplane traces being bi-directional and operative to communicate in both said upstream direction and said downstream direction; and	<p>The Reference discloses at least one of said backplane traces being bi-directional and operative to communicate in both said upstream direction and said downstream direction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
29[e]	a control module, which is arranged to select, for each data frame, responsively to at least one of the frame attributes, a backplane trace from the plurality of backplane traces over which to send the data frame.	<p>The Reference discloses a control module, which is arranged to select, for each data frame, responsively to at least one of the frame attributes, a backplane trace from the plurality of backplane traces over which to send the data frame.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary</p>

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	backplane traces over which to send the data frame.	<p>skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 29	The Reference
		<p>The diagram illustrates a network switch architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Arrows indicate various data paths: 1Gbps Ethernet (201, 204), 2Gbps FC (202, 203), 10Gbps FC (205, 206), 10Gbps Ethernet (20n), and InfiniBand (IB) (20n). Below the line cards, an Ethernet interface for configuration (60) is connected to a management card (60). The management card is also connected to the crossbar fabric interconnect (40) via a backplane interconnect (70). A PCI logo is shown near the management card.</p> <p style="text-align: center;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

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		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

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		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424

No.	'740 Patent Claim 29	The Reference
	<ul style="list-style-type: none"> • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>The diagram illustrates a Stream State Table (FIG. 2). It starts with two input paths: 'destinationAddress' and 'sourceAddress'. Each path goes through a 'Hash' block, then an 'XOR' block, and finally a multiplexer. The multiplexer's output is labeled 'Stream Id' and has a width of 16 bits. This Stream Id is then used to index into a 'StreamStateTable'. The table has 64 entries, indexed from 0 to 63. Each entry contains the following fields: <ul style="list-style-type: none"> AssignedPortNumber[4:0]: A 5-bit field with values 0, 1, ..., 63. timeMark[0:1]: A 2-bit field. number of Streams - 1: A 1-bit field with value 0. An arrow labeled 'configuration' points from the Stream Id to the table, indicating how the table is populated or updated based on the Stream Id. </p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>	

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		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

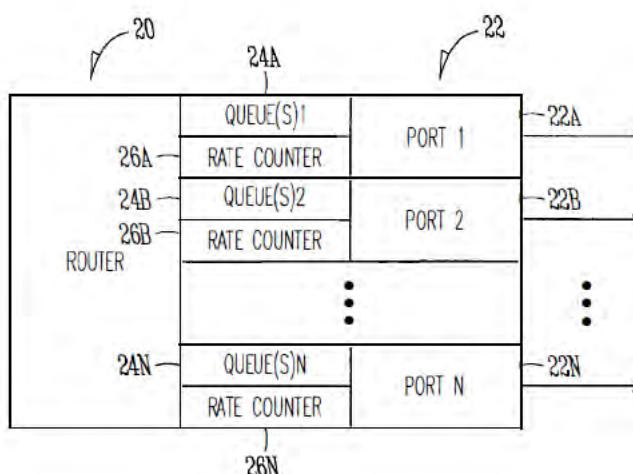
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

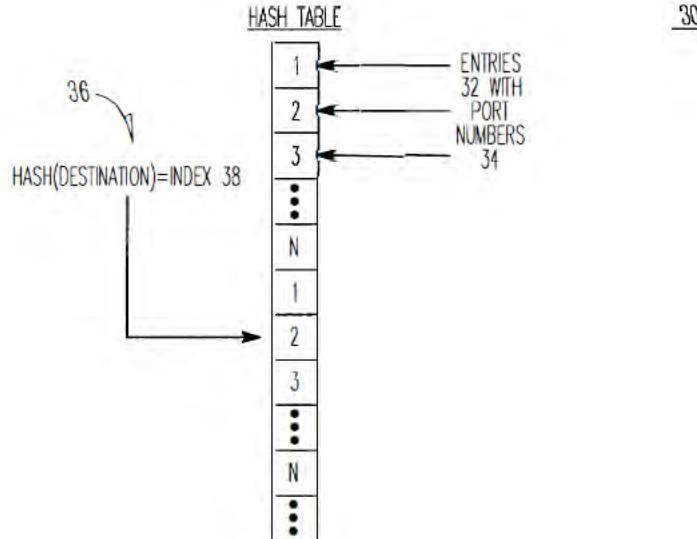
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

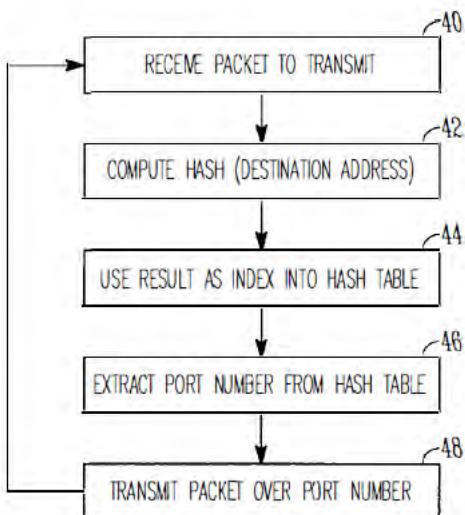
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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 29	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. At the top, a box labeled 'ROUTER' contains a vertical stack of components. From top to bottom, these components are: 'QUEUE(S)1', 'RATE COUNTER', 'QUEUE(S)2', 'RATE COUNTER', a vertical ellipsis '...', 'QUEUE(S)N', and 'RATE COUNTER'. To the left of the first component is a label '26A', and to the left of the second component is '26B'. To the left of the Nth component is '26N'. Above the first component is a label '24A', and above the second component is '24B'. Above the Nth component is '26N'. Above the entire stack is a label '20'. To the right of the stack, three horizontal arrows point to the right, labeled 'PORT 1', 'PORT 2', and 'PORT N' respectively. Above each of these arrows is a label '22'. Below the stack, there is a label '26N'.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 29	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 29	The Reference
		 <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

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		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), ..., PORT N. Arrows labeled 62A, 62B, ..., 62N point from these ports to Router B. Below this, a vertical stack of boxes is labeled "HASH TABLE 64". The top half of the table contains entries 1, 2, 3, ..., N. The bottom half contains entries 1, 2, 3, ..., N. An arrow points from the label "HASH TABLE 64" to the top of the table. Another arrow points from entry 2 in the top half to entry 2 in the bottom half, indicating a mapping or selection process.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 29	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 29	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

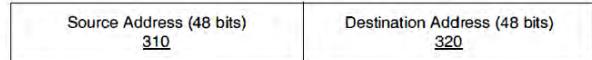
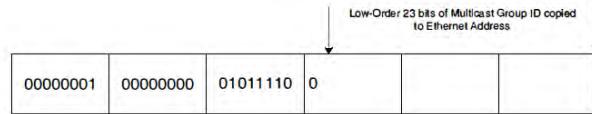
No.	'740 Patent Claim 29	The Reference
		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>  <p style="text-align: center;">Figure 2</p>  <p style="text-align: center;">Figure 3</p>  <p style="text-align: center;">Figure 4</p>  <p style="text-align: center;">Figure 5</p>

No.	'740 Patent Claim 30	The Reference
30[preamble]	Apparatus for connecting user ports to a communication network, comprising:	<p>The Reference discloses apparatus for connecting user ports to a communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
30[a]	one or more user interface modules coupled to the user ports, which are arranged to process data frames having frame attributes sent between the user ports and the communication network;	<p>The Reference discloses one or more user interface modules coupled to the user ports, which are arranged to process data frames having frame attributes sent between the user ports and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530.</p>
30[b]	a backplane having the one or more user interface modules coupled thereto and comprising a plurality of backplane traces arranged in parallel so as to	<p>The Reference discloses a backplane having the one or more user interface modules coupled thereto and comprising a plurality of backplane traces arranged in parallel so as to transfer the data frames between the one or more user interface modules and the communication network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of</p>

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	<p>transfer the data frames between the one or more user interface modules and the communication network;</p>	<p>the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, and Dontu.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] ("In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, "networks 20") are coupled to line interfaces 251-25n (collectively, "line interfaces 25") of line cards 301 -30n (collectively, "line cards 30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.")</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 30	The Reference
		<p>The diagram illustrates a network architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a crossbar fabric interconnect (40). Below the line cards, a management card (60) is connected to a backplane interconnect (70), which is also part of the crossbar fabric interconnect (40). The management card (60) is labeled as being connected via a PCI interface. Arrows indicate data flow between the line cards and their respective backplane interconnects, and between the backplane interconnects and the crossbar fabric interconnect.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTM); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208</p>

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		<p>through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-channel port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

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		<p>Fig. 3</p> <p>The diagram illustrates the internal structure of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are connected in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections from the LIC include two ports on the left and a port on the right. Below the LIC, a dashed line separates it from the "AMCC CYCLONE SWITCH FABRIC". This fabric is part of the "S8005 SERIES". On the left side, there is an ingress section with 32 line interfaces numbered 0 through 31. On the right side, there is an egress section with 32 line interfaces numbered 0 through 31. The ingress line interfaces connect to the switch fabric, which then connects to the egress line interfaces. Red boxes highlight the connection points between the ingress/egress line interfaces and the switch fabric.</p> <p>Singh at Figure 4 (annotations added)</p>

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		<p>Fig. 4</p> <pre> graph LR TX["TX AND RX OPTICS AND PHY."] --> Framer[FRAMER OR MAC] Framer --> Network1[NETWORK PROCESSING] Network1 --> Ingress[INGRESS & EGRESS TRAFFIC MANAGEMENT] Ingress --> SerialLinks[SERIAL LINKS] Ingress --> Memory[8895 MEMORY SUBSYSTEM] Memory --> SerialLinks Memory --> PQ[S8565 PRIORITY QUEUE (PQ)] PQ --> Arbiters[ARBITERS] Arbiters --> Grants[GRANTS FROM ARBITERS] Grants --> Backplane[BACKPLANE SERIAL LINKS] Backplane --> Memory Backplane --> PQ2[S8805 EDFQ OR S850E PQ (PQ)] PQ2 --> Network2[NETWORK PROCESSING] Network2 --> Framer </pre> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START]) --> 1702[TRANSMITTING PACKETS FROM TM] 1702 --> 1704[ACCEPTING PACKET AT PORT CARD INGRESS PORT] 1704 --> 1706[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS] 1706 --> 1706a[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS] 1706a --> 1706b[STORING PACKETS IN iMS] 1706b --> 1706c[ASSIGNING PACKETS TO BACKPLANE DATA LINKS] 1706c --> 1706d[SUPPLYING PACKETS TO CROSSBAR] 1706d --> 1708[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS] 1708 --> 1710[SENSING CONNECTION FAULT] 1710 --> 1712[RESELECTING CONNECTIONS] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual")</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p>
30[c]	<p>a control module, which is arranged to select, for each data frame, responsively to at least one of the frame attributes, a backplane trace from the plurality of backplane traces over which to send the data frame;</p>	<p>The Reference discloses a control module, which is arranged to select, for each data frame, responsively to at least one of the frame attributes, a backplane trace from the plurality of backplane traces over which to send the data frame.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards</p>

No.	'740 Patent Claim 30	The Reference
		<p>30"). Line cards 30 further include fabric interfaces 351-35n (collectively, "fabric interfaces 35") which serve to couple line cards 30 to crossbar inter-connect 40 via backplane interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used."</p> <p>Viswanathan at [0031] ("Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.")</p> <p>Viswanathan at Figure 1</p>

No.	'740 Patent Claim 30	The Reference
		<p>The diagram illustrates a network switching architecture. At the top, four line cards (301, 302, 303, 30n) are shown, each connected to a respective backplane interconnect (501, 502, 503, 50n). The backplane interconnects are interconnected by a central crossbar fabric interconnect (40). Arrows indicate various data paths: <ul style="list-style-type: none"> Line Card 301 receives 1Gbps Ethernet (201), 2Gbps FC (202), 2Gbps FC (203), and 1Gbps Ethernet (204) and transmits 251. Line Card 302 receives 10Gbps FC (205) and transmits 252. Line Card 303 receives 10Gbps Ethernet (206) and transmits 253. Line Card 30n receives IB (20n) and transmits 25n. Below the line cards, a management card (60) is connected to a backplane interconnect (70) via a PCI interface. An Ethernet interface for configuration is also connected to the management card.</p> <p style="text-align: right;">FIG. 1</p> <p>Singh at 4:5-13 (“In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iTMs); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p>

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		<p>Singh at 11:28-38 ("FIG. 9 is a diagram illustrating link to channel assignments. The MS provides the interface between the line side and the fabric. As mentioned previously, the ratio between the number of backplane links used and the number of ingress/egress links used sets the speedup of the fabric. Each MS has 40 input/output data links which can be used. Every 10 links create a charmel, whether it is a backplane channel or an ingress/egress charmel. There is no logical relationship 35 between backplane and ingress/egress channels. A packet that arrives on one link can, in general, leave on any other link.")</p> <p>Singh at 13:35-48 ("FIG. 10 is a diagram depicting iPQ arbiter interface to switchplane and backplane chamiel mapping. The arbiter interfaces on the iPQ directly correspond to the backplane channels of the MS, as shown. In other words, arbiter interfaces 0.A and 0.B handles the bids and grants for backplane chamiel 0. The two arbiters attached to interfaces 0.A and 0.B form switchplane O (as shown in FIG. 20) that controls the crossbars attached to the links of backplane channel 0. An iPQ has 8 arbiter interfaces and can handle the bids and grants to 4 switch planes, thus servicing all the 4 backplane channels possible in an MS. A 4-ingress channel configuration, shown in FIG. 6, requires two iPQs and two MSs to support a 2x speedup (generates 8 backplane chan-nels).")</p> <p>Singh at 18:44-53 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes substeps. Step 1706a includes each port card accepting packets on a sixth plurality of ingress data links, through a corresponding sixth plurality of port card ingress ports, from at least one ingress TM (iTMs). Step 1706b stores the accepted packets in a port card ingress memory sub- system (iMS). Step 1706c assigns packets to a second plurality of port card backplane data links. Step 1706d supplies assigned packets to a crossbar.")</p> <p>Singh at 18:61-19:9 ("The egress function of the switch fabric works analogously to the ingress function. Although the substeps associated with the egress function are listed below, they are not included in the figure in the interest of clarity. In some aspects, selectively connecting port card ingress ports to port card egress ports in Step 1706 includes additional substeps. Step 1706e includes each port card accepting packets on a second plurality of port</p>

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		<p>card backplane data links from crossbars. Step 1706/ stores the accepted packets in a port card egress memory subsystem (eMS). Step 1706g assigns packets to a sixth plurality of port card egress ports. Step 1706h supplies assigned packets to selected port card egress 5 ports from the eMS. Step 1706i includes each port card supplying packets on a sixth plurality of egress data links, through the corresponding sixth plurality of port card ports, to at least one egress TM (eTM).”)</p> <p>Singh at Figure 17</p> <p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> S4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] S4 --> SF[SENSING CONNECTION FAULT 1710] SF --> RC[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 (“Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is</p>

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		<p>handling a packet addressed to server 104(3), net-work device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in tum provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical iden-tifier to each uplink interface within the same uplink inter-face bundle. When forwarding packets via an uplink inter-face bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) gen-erates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424

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		<ul style="list-style-type: none"> • Dontu • Li '914 • Borgione '125 <p>DeJager '424 at Figure 2</p> <p>The diagram illustrates a Stream State Table (FIG. 2). It shows two input paths: 'destinationAddress' and 'sourceAddress'. Each path goes through a 'Hash' block, then an 'XOR' block, and finally a multiplexer. The multiplexer's output is labeled 'Stream Id' and has a width of 16 bits. This output is also connected to a 'Mask' block. The 'Mask' block has a width of 6 bits and its output is connected to the Stream State Table. The Stream State Table is a memory structure with 64 entries, indexed by Stream Id. The table contains fields: 'AssignedPortNumber[4:0]' (values 0 to 63), 'timeMark[0:1]', and 'number of Streams - 1' (value 0). A 'configuration' block is shown influencing the multiplexer and the Mask block.</p> <p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

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		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

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		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

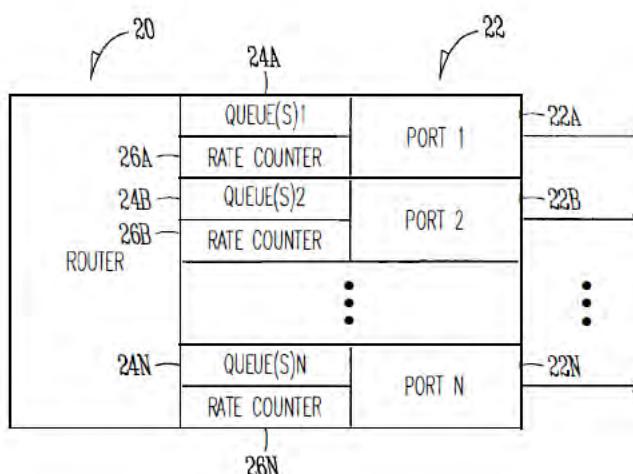
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		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

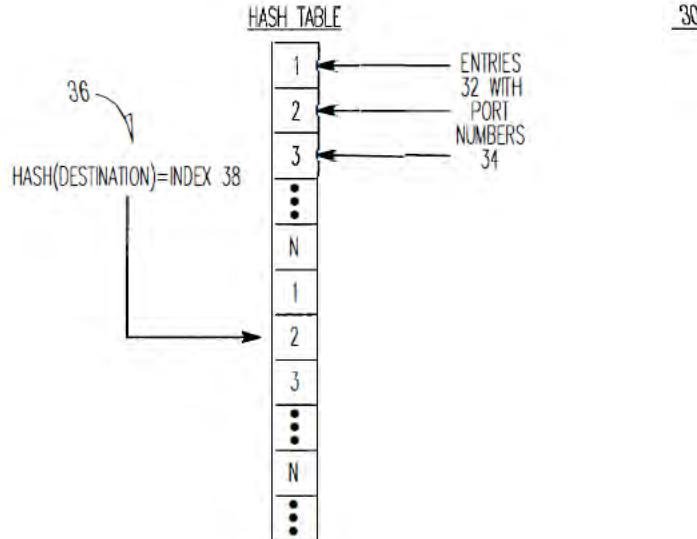
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		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

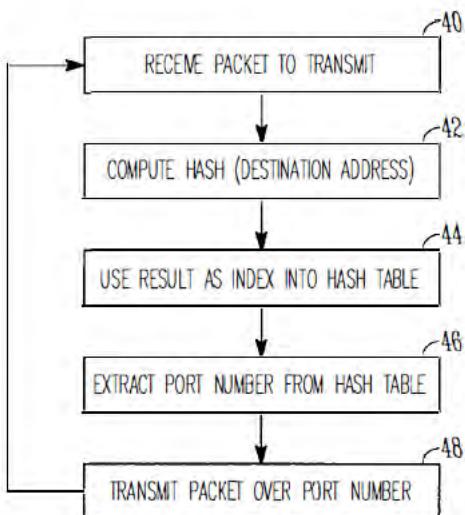
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		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

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		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

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		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

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		 <p>The diagram illustrates a router architecture. At the top, a box labeled 'ROUTER' contains a vertical stack of components. From top to bottom, the stack includes: a header labeled '20', followed by 'QUEUE(S)1' and 'RATE COUNTER' (labeled 24A), another 'QUEUE(S)2' and 'RATE COUNTER' (labeled 24B), a vertical ellipsis '...', and finally 'QUEUE(S)N' and 'RATE COUNTER' (labeled 24N). To the right of this stack are two columns of labels: 'PORT 1' and 'PORT N'. Arrows point from each component row to its corresponding port label. Specifically, 'QUEUE(S)1' points to 'PORT 1', 'QUEUE(S)2' points to 'PORT 2', and so on down to 'QUEUE(S)N' pointing to 'PORT N'. Each port is connected to an output line labeled '22'. The first port is labeled '22A', the second '22B', and the last one shown is '22N'. There are also three vertical ellipses between the '22B' and '22N' labels, indicating additional ports.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

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		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

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		 <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

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		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports: PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), and PORT N. Arrows labeled 62A, 62B, and 62N point from these ports to Router B. Below this, a 'HASH TABLE 64' is shown as a vertical column of 64 slots, indexed from 1 to N. An arrow points from the top of the table to the first slot. Another arrow points from the bottom of the table to the last slot. Within the table, two specific entries are highlighted: slot 1 contains '1' and slot 2 contains '2'. This illustrates how a hash function maps traffic from different ports to specific output slots based on utilization.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

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		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p style="text-align: center;"><i>FIG. 7</i></p> <p>Li '914 at Figure 8</p>

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		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

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		<p>Li '914 at 1:44-57 (“As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.”)</p> <p>Li '914 at 2:39-55 (“In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or “next-hop” router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.”)</p> <p>Li '914 at 4:41-67 (“In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

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		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

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		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

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		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

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		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

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		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p> <p style="text-align: center;">Figure 2</p> <p style="text-align: center;">Figure 3</p> <p style="text-align: center;">Figure 4</p> <p style="text-align: center;">Figure 5</p>
30[d]	at least some of the backplane traces are	The Reference discloses at least some of the backplane traces are aggregated into an Ethernet link aggregation (LAG) group.

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	<p>aggregated into an Ethernet link aggregation (LAG) group.</p>	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, and Wiher '530, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below is an example.</p> <p>Smith '430 at 5:51-64 (“The redundant links coupling each of network devices 120(1) and 120(2) to virtual network device 202 can be operated as a single logical link, referred to herein as a virtual link bundle. Network device 120(1) operates the two links coupling network device 120(1) to virtual network device 202 as a virtual link bundle 250(1). In such an embodiment, each interface in network device 120(1) that is coupled to one of the links is included in an interface bundle, which corresponds to virtual link bundle 250(1). Network device 120(2) similarly operates the two links coupling network device 120(2) to virtual network device 202 as virtual link bundle 250(2). In some embodiments, virtual link bundles 250(1) and 250(2) are each operated as an EtherChannel™ or as an aggregated link (as described in IEEE 802.3).”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the link aggregation technique. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager '424 • Dontu • Li '914 • Borgione '125

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		<p>DeJager '424 at Abstract ("Provided are methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.")</p> <p>DeJager '424 at 1:38-50 ("One way to relieve this bottle-neck is to provide a logical grouping of multiple ports into a single port. The bandwidth of the new port is increased since it has multiple lines (cables) connecting a switch and another network device, each line capable of carrying data at the same rate as the line connecting data sources to the switch. This grouping of ports is sometimes referred to as a port aggregation or port group. One example of such a port aggregation implementation is Cisco Technology, Inc.'s Fast EtherChannel™ port group in a Fast Ethernet network. Further information regarding Fast EtherChannel™ may be found on Cisco Technology, Inc.'s World Wide Web site www.cisco.com. This information is incorporated by reference herein for all purposes.")</p> <p>DeJager '424 at 2:47-65 ("The present invention meets this need by providing methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods, apparatuses and systems of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by</p>

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		<p>ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 2:67-3:15 (“In one aspect, the present invention provides a method of distributing traffic over a network port group. The method involves receiving a packet of data to be forwarded, determining a stream ID for the packet, and determining whether a prior packet having that stream ID has been distributed to a queue on a port in the group during a predetermined time interval. Where a prior packet having that stream ID has not been distributed to a queue on a port of the group during the predetermined time interval, the method involves allocating the packet to a queue of a port having a lesser load in its queue than a queue of any other port of the group. The method may also involve, where a prior packet having that stream ID has been distributed to a queue on a port of the group during the predetermined time interval, allocating the packet to that queue. In addition, the method may involve monitoring the port group queues to maintain proper identification of the least utilized queue.”)</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized</p>

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		<p>queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 4:29-46 (“The present invention provides methods, apparatuses and systems for balancing the load of data transmissions through a port aggregation. The methods and apparatuses of the present invention allocate port assignments based on load, that is, the amount of data being forwarded through each port in the group. The load balancing of the present invention is preferably dynamic, that is, packets from a given stream may be forwarded on different ports depending upon each port's current utilization. When a new port is selected to transmit a particular packet stream, it is done so that the packets cannot be forwarded out of order. This is preferably accomplished by ensuring passage of a period of time sufficient to allow all packets of a given stream to be forwarded by a port before a different port is allocated to transmit packets of the same stream. The invention may be used in a variety of different network environments and speeds, including 10Base-T, 100Base-T, and Gigabit Ethernet, and other network environments.”)</p> <p>DeJager '424 at 4:47-58 (“FIG. 1 illustrates a block diagram of a simple network. The network 100 includes two servers S1. and S2, respectively, and two switches, X1 and X2, respectively, as well as four clients C1, C2, C3 and C4, respectively. Clients C1, C2, C3 and C4 are connected to switch X1 by, for example, Fast Ethernet links 102 via ports 1, 2, 3 and 4, respectively. Server S1 is connected to switch X1 via a port aggregation 104, which is a port group composed of ports 5 and 6 of switch X1. Switch X1 is connected to switch X2 via a second port aggregation 106 which includes ports 7, 8 and 9. Switch X2 is connected to server S2 via port O and Fast Ethernet link 108.”)</p> <p>Dontu at Abstract (“Various methods and systems for preventing erroneous link aggregation due to component relocation are disclosed. Such methods include a method for changing the identifier used by a network device and communicating the identifier change to a peer network device without disrupting an aggregated link. In one embodiment, a method involves detecting an identifier change and sending a Port Aggregation Protocol (PAgP) protocol data</p>

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		<p>unit (PDU) that includes a new identifier and information. The information indicates the identifier change. The new identifier identifies a network device subsequent to the identifier change. Another embodiment of a method involves detecting an identifier change and, subsequent to the identifier change, sending a link aggregation protocol PDU that includes an "old device identifier" field dedicated to conveying an old identifier. The old identifier identifies a network device prior to the identifier change.”)</p> <p>Dontu at Figure 2</p>

No.	'740 Patent Claim 30	The Reference														
		<p style="text-align: center;">5</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>Version <u>202</u></td></tr> <tr><td>My Device Identifier <u>204</u> (ID = X)</td></tr> <tr><td>My Distribution Requirements <u>206</u></td></tr> <tr><td>My Port Priority <u>208</u></td></tr> <tr><td>My Port Identifier <u>210</u></td></tr> <tr><td>My Group Capability <u>212</u></td></tr> <tr><td>My Agport Identifier <u>214</u></td></tr> <tr><td>Your Device Identifier <u>216</u> (ID = Y)</td></tr> <tr><td>Your Distribution Requirements <u>218</u></td></tr> <tr><td>Your Port Priority <u>220</u></td></tr> <tr><td>Your Port Identifier <u>222</u></td></tr> <tr><td>Your Group Capability <u>224</u></td></tr> <tr><td>Your Agport Identifier <u>226</u></td></tr> <tr><td>Partner Count <u>228</u></td></tr> </table> <p style="text-align: center;">↗</p> <p>Port Aggregation Protocol PDU 200 (sent from Interfaces 120(1), 120(2) and 120(3))</p> <p style="text-align: center;">FIG. 2</p> <p>Dontu at Figure 3</p>	Version <u>202</u>	My Device Identifier <u>204</u> (ID = X)	My Distribution Requirements <u>206</u>	My Port Priority <u>208</u>	My Port Identifier <u>210</u>	My Group Capability <u>212</u>	My Agport Identifier <u>214</u>	Your Device Identifier <u>216</u> (ID = Y)	Your Distribution Requirements <u>218</u>	Your Port Priority <u>220</u>	Your Port Identifier <u>222</u>	Your Group Capability <u>224</u>	Your Agport Identifier <u>226</u>	Partner Count <u>228</u>
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Your Group Capability <u>224</u>																
Your Agport Identifier <u>226</u>																
Partner Count <u>228</u>																

No.	'740 Patent Claim 30	The Reference
		<p style="text-align: center;">Network Device 100(1)</p> <p style="text-align: center;">Network Device 100(2)</p> <p style="text-align: center;">Aggregated Link 105</p> <p style="text-align: center;">FIG. 3</p> <p>Dontu at Figure 14</p>

No.	'740 Patent Claim 30	The Reference
		<p>Virtual Network Device 1302</p> <p>Virtual Network Device Sub-Unit 1222(1)</p> <p>Control Card 1402(1)</p> <p>Control Unit 1410(1)</p> <p>Forwarding Engine 1412(1)</p> <p>1420(1)</p> <p>1420(3)</p> <p>Line Card 1404(1)</p> <p>1420(5)</p> <p>1420(7)</p> <p>1420(9)</p> <p>Forwarding Engine 1414(1)</p> <p>Line Card 1404(3)</p> <p>1420(11)</p> <p>1420(13)</p> <p>1420(15)</p> <p>Forwarding Engine 1414(3)</p> <p>Virtual Network Device Link 1460</p> <p>Virtual Network Device Sub-Unit 1222(2)</p> <p>Control Card 1402(2)</p> <p>Control Unit 1410(2)</p> <p>Forwarding Engine 1412(2)</p> <p>1420(2)</p> <p>1420(4)</p> <p>Line Card 1404(2)</p> <p>1420(6)</p> <p>1420(8)</p> <p>1420(10)</p> <p>Forwarding Engine 1414(2)</p> <p>Line Card 1404(4)</p> <p>1420(12)</p> <p>1420(14)</p> <p>1420(16)</p> <p>Forwarding Engine 1414(4)</p> <p>1420(1)</p> <p>1420(3)</p> <p>1420(5)</p> <p>1420(7)</p> <p>1420(9)</p> <p>1420(11)</p> <p>1420(13)</p> <p>1420(15)</p> <p>1420(17)</p> <p>1420(19)</p> <p>1420(21)</p> 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<p>1420(1179)</p> <p>1420(1181)</p> <p>1420(1183)</p> <p>1420(1185)</p> <p>1420(1187)</p> <p>1420(1189)</p> <p>1420(1191)</p> <p>1420(1193)</p> <p>1420(1195)</p> <p>1420(1197)</p> <p>1420(1199)</p> <p>1420(1201)</p> <p>1420(1203)</p> <p>1420(1205)</p> <p>1420(1207)</p> <p>1420(1209)</p> <p>1420(1211)</p> <p>1420(1213)</p> <p>1420(1215)</p> <p>1420(1217)</p> <p>1420(1219)</p> <p>1420(1221)</p> <p>1420(1223)</p> <p>1420(1225)</p> <p>1420(1227)</p> <p>1420(1229)</p> <p>1420(1231)</p> <p>1420(1233)</p> <p>1420(1235)</p> <p>1420(1237)</p> <p>1420(1239)</p> <p>1420(1241)</p> <p>1420(1243)</p> <p>1420(1245)</p> <p>1420(1247)</p> <p>1420(1249)</p> <p>1420(1251)</p> <p>1420(1253)</p> <p>1420(1255)</p> <p>1420(1257)</p> <p>1420(1259)</p> <p>1420(1261)</p> <p>1420(1263)</p> <p>1420(1265)</p> <p>1420(1267)</p> <p>1420(1269)</p> <p>1420(1271)</p> <p>1420(1273)</p> <p>1420(1275)</p> <p>1420(1277)</p> <p>1420(1279)</p> <p>1420(1281)</p> <p>1420(1283)</p> <p>1420(1285)</p> <p>1420(1287)</p> <p>1420(1289)</p> <p>1420(1291)</p> <p>1420(1293)</p> <p>1420(1295)</p> <p>1420(1297)</p> <p>1420(1299)</p> <p>1420(1301)</p> <p>1420(1303)</p> <p>1420(1305)</p> <p>1420(1307)</p> <p>1420(1309)</p> <p>1420(1311)</p> <p>1420(1313)</p> <p>1420(1315)</p> <p>1420(1317)</p> <p>1420(1319)</p> <p>1420(1321)</p> <p>1420(1323)</p> <p>1420(1325)</p> <p>1420(1327)</p> <p>1420(1329)</p> <p>1420(1331)</p> <p>1420(1333)</p> <p>1420(1335)</p> <p>1420(1337)</p> <p>1420(1339)</p> <p>1420(1341)</p> <p>1420(1343)</p> <p>1420(1345)</p> <p>1420(1347)</p> <p>1420(1349)</p> <p>1420(1351)</p> <p>1420(1353)</p> <p>1420(1355)</p> <p>1420(1357)</p> <p>1420(1359)</p> <p>1420(1361)</p> <p>1420(1363)</p> <p>1420(1365)</p> <p>1420(1367)</p> <p>1420(1369)</p> <p>1420(1371)</p> <p>1420(1373)</p> <p>1420(1375)</p> <p>1420(1377)</p> <p>1420(1379)</p> <p>1420(1381)</p> <p>1420(1383)</p> <p>1420(1385)</p> <p>1420(1387)</p> <p>1420(1389)</p> <p>1420(1391)</p> <p>1420(1393)</p> <p>1420(1395)</p> <p>1420(1397)</p> <p>1420(1399)</p> <p>1420(1401)</p> <p>1420(1403)</p> <p>1420(1405)</p> <p>1420(1407)</p> <p>1420(1409)</p> <p>1420(1411)</p> <p>1420(1413)</p> <p>1420(1415)</p> <p>1420(1417)</p> <p>1420(1419)</p> <p>1420(1421)</p> <p>1420(1423)</p> <p>1420(1425)</p> <p>1420(1427)</p> <p>1420(1429)</p> <p>1420(1431)</p> <p>1420(1433)</p> <p>1420(1435)</p> <p>1420(1437)</p> <p>1420(1439)</p> <p>1420(1441)</p> <p>1420(1443)</p> <p>1420(1445)</p> <p>1420(1447)</p> <p>1420(1449)</p> <p>1420(1451)</p> <p>1420(1453)</p> <p>1420(1455)</p> <p>1420(1457)</p> <p>1420(1459)</p> <p>1420(1461)</p> <p>1420(1463)</p> <p>1420(1465)</p> <p>1420(1467)</p> <p>1420(1469)</p> <p>1420(1471)</p> <p>1420(1473)</p> <p>1420(1475)</p> <p>1420(1477)</p> <p>1420(1479)</p> <p>1420(1481)</p> <p>1420(1483)</p> <p>1420(1485)</p> <p>1420(1487)</p> <p>1420(1489)</p> <p>1420(1491)</p> <p>1420(1493)</p> <p>1420(1495)</p> <p>1420(1497)</p> <p>1420(1499)</p> <p>1420(1501)</p> <p>1420(1503)</p> <p>1420(1505)</p> <p>1420(1507)</p> <p>1420(1509)</p> <p>1420(1511)</p> <p>1420(1513)</p> <p>1420(1515)</p> <p>1420(1517)</p> <p>1420(1519)</p> <p>1420(1521)</p> <p>1420(1523)</p> <p>1420(1525)</p> <p>1420(1527)</p> <p>1420(1529)</p> <p>1420(1531)</p> <p>1420(1533)</p> <p>1420(1535)</p> <p>1420(1537)</p> <p>1420(1539)</p> <p>1420(1541)</p> <p>1420(1543)</p> <p>1420(1545)</p> <p>1420(1547)</p> <p>1420(1549)</p> <p>1420(1551)</p> <p>1420(1553)</p> <p>1420(1555)</p> <p>1420(1557)</p> <p>1420(1559)</p> <p>1420(1561)</p> <p>1420(1563)</p> <p>1420(1565)</p> <p>1420(1567)</p> <p>1420(1569)</p> <p>1420(1571)</p> <p>1420(1573)</p> <p>1420(1575)</p> <p>1420(1577)</p> <p>14</p>

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		<p>Link Aggregation Control Protocol (LACP), which is defined in IEEE 803.2ad, and Port Aggregation Protocol (PAgP), which is a standard promulgated by CISCO SYS-TEMS, INC.”)</p> <p>Dontu at [0012] (“The method can also involve detecting whether a partner interface is executing a compatible version of PAgP. If the partner interface is not executing the compatible version of PAgP, the compatible version of PAgP can be provided to the partner interface. Alternatively, if the partner interface is not executing the compatible version of PAgP, the partner interface can be inhibited from including a link in an aggregated link.”)</p> <p>Dontu at [0033] (“Network device 100(1) includes three network device components 110(1)-110(3). Similarly, network device 100(2) includes three network device components 110(4)-110(6). Each network device component 110(1)-110(6) is a component (e.g., a line card, a virtual network device sub-unit (as described below), a chassis useable within a stackable switch, or the like) that can be removed and/or replaced independently of the other network device components. For example, if network device component 110(2) experiences a failure, network device component 110(2) can be removed from network device 100(1) for repair or replacement. The removal of network device component 110(2) does not necessitate the removal of network device components 110(1) and 110(3) from network device 100(1). It is noted that in other embodiments, each network device coupled by an aggregated link can include fewer or additional network device components than the network devices shown in FIG. 1. Additionally, the number of network device components within each network device can vary among network devices (e.g., one network device can include eight network device components, while another network device includes four network device components).”)</p> <p>Dontu at [0035] (“Aggregated link 105 link includes three links (these links can be physical or logical links). One link couples interface 120(1) to interface 120(4). Another link couples interface 120(2) to interface 120(5). The third link couples interface 120(3) to interface 120(6).”)</p>

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		<p>Dontu at [0037] ("In this example, the network devices 100(1) and 100(2) use Port Aggregation Protocol (PAgP) to form aggregated links. Network devices 100(1) each send PAgP protocol data units (PDUs) to each other in order to determine whether any of the links between the two network devices can be combined into an aggregated link. Each PAgP PDU includes an identifier that uniquely identifies the network device that sent that PAgP PDU. Within network device 100(1), identifier module 130(1) of network device component 110(1) supplies an identifier "X" to each of the interfaces 120(1)-120(3) within network device 100(1). Interfaces 120(1)-120(3) include identifier X in each PAgP PDU sent by those interfaces. Similarly, identifier module 130(2) of network device component 110(4) supplies an identifier "Y" to each interface 120(4)-120(6) of network device 100(2). Interfaces 120(4)-120(6) include identifier Y in each PAgP PDU sent by those interfaces.")</p> <p>Dontu at [0040] ("FIG. 2 illustrates some of the fields that can be included in a PAgP PDU. As shown, PDU 200 includes Version field 202, My Device Identifier field 204 ("My" refers to the device sending the PAgP PDU), My Distribution Requirements field 206, My Port Priority field 208, My Port Identifier field 212, My Group Capability field 212, My Agport (Aggregated Port) Identifier field 214, Your Device Identifier field 216 ("Your" refers to the device to which the PAgP PDU is being sent), Your Distribution Requirements field 218, Your Port Priority field 220, Your Port Identifier field 222, Your Group Capability field 224, Your Agport Identifier field 226, and Partner Count field 228.")</p> <p>Dontu at [0110] ("Interfaces 1420(13), 1420(9), and 1420(16), which are each coupled to network device 1220(1) by virtual link bundle 1350(1), form an interface bundle (e.g., an Ether-Channel (TM) port bundle). Similarly, interfaces 1420(11) and 1420(8) form another interface bundle that is coupled to network device 1220(2) by virtual link bundle 1350(2). Interfaces 1420(7) and 1420(12) form a third interface bundle that is coupled to network device 1220(3) by virtual link bundle 1350(3). Within virtual network device 1302, each interface in the same interface bundle is assigned the same logical identifier. For example, interfaces 1420(13), 1420(9), and 1420(16) are each assigned the same logical identifier. In some embodiments, packets received via one of these interfaces are tagged or otherwise associated with the logical identifier to indicate that those packets were received via the</p>

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		<p>virtual link bundle coupling virtual network device 1302 to network device 1220(1). It is noted that similar interface bundles are implemented within each network device 1220(1)-1220(3), and that interfaces included in such bundles are also assigned the same logical identifier by each network device (or by virtual network device 1302, in embodiments in which virtual network device 1302 controls the configuration of the network devices 1220(1)-1220(3)). For example, network device 1220(1) can assign the same logical identifier to each of the interfaces coupled to virtual link bundle 1350(1).”)</p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p> <p>Li '914 at 2:6-22 (“In light of the above and according to one broad aspect of one embodiment of the present invention, disclosed herein is a method for determining an output port upon which to trans-mit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent router. The method includes creating a list of output ports that are coupled with the adjacent router, modifying the list based on network traf-fic, selecting a port from the list of ports, and transmitting the packet over the selected port. In one example, the list is continuously modified as a background process based on network traffic. The list may be modified by determining a port which is under-utilized, determining a port which is over-utilized, and substituting in the list one or more instances of the port which is over-utilized with one or more instances of the port which is under-utilized. In this manner, the router can adaptively and evenly distribute the packet transmission traffic over the output ports of an interface.”)</p>

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		<p>Li '914 at 4:9-25 ("Referring to FIG. 1, a Router A is shown having an interface 10 with a plurality of ports which connect Router A with Router B over a plurality of connections, lines, wires, links or bundled links 14. The ports 12 of Router A are configured to permit transmission of packets from Router A to Router B, and these ports 12 may be referred to as output ports, egress ports, links, or the like. As shown in FIG. 1, port 1 to port N may be connected with Router B, and there may be additional interfaces 16 having ports 18 connected with other routers in the network. When a packet is received by Router A, Router A determines whether the received packet should be transmitted to Router B or to other routers connected to Router A, based in part upon the destination address of the packet. If a packet is to be transmitted from Router A to Router B, then Router A may transmit this packet over the one of the ports 12 shown in FIG. 1.")</p> <p>Li '914 at Figure 1</p> <p>The diagram illustrates the architecture of Router A. It features a central rectangular box labeled "ROUTER A". On the left side, an arrow labeled "PACKET IN" points into the router. On the right side, there are two main connection paths. The top path consists of a vertical stack of ports labeled "PORT 1", "PORT 2", and "PORT N". Above this stack is a label "INTERFACE(10) TO ROUTER B". Arrows from each port in the stack point to a common connection line labeled "14", which then connects to a separate rectangular box labeled "ROUTER B". The bottom path involves a horizontal row of ports labeled "PORT 1", "PORT 2", "...", and "PORT N". An arrow labeled "18" points from this row to another horizontal line, which then connects to a separate rectangular box labeled "ROUTER C". Additionally, an arrow labeled "INTERFACE(16) TO ROUTER C" points from the bottom row of ports towards Router C.</p> <p style="text-align: center;"><i>FIG. 1</i></p>

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		<p>Borgione '125 at 1:55-65 ("Link nodes 110 and 120 can be in physically remote locations, thereby connecting their associated local area networks (LANs). The plurality of network links 150 between link nodes 110 and 120 can be aggregated as a single logical link over which all traffic between link nodes 110 and 120 is distributed. Such aggregation multiplies the available bandwidth for communications between link nodes 110 and 120, and therefore between the two local area networks. When appropriately configured, such a connection can permit the two local area networks to interact as if they were one large local area network.")</p> <p>Borgione '125 at 1:66-2:7 ("As stated above, the plurality of network links between 110 and 120 can be aggregated as a single logical link. In this manner, each link node 110 and 120 sees the plurality of network links between them as one logical interface. One type of such an aggregate of links is an EtherChannel, a protocol that allows up to eight Fast Ethernet or Gigabit Ethernet links to be aggregated. Routing protocols treat the aggregated links as a single, routed interface with a common IP address.")</p> <p>Borgione '125 at 5:28-50 ("The present invention balances the transmission of replicated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network environment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even</p>

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		<p>distribution of multicast packet transmission across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.”)</p> <p>node are distributed over all communication links in the bundle by the load balancing process. Many load-balancing processes are designed so that all data packets in the same data flow are sent through the same port.”)</p>

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31	The apparatus according to claim 29, wherein the control module is arranged to apply a hashing function to the at least one of the frame attributes so as to select the backplane trace.	<p>The Reference discloses the apparatus according to claim 29, wherein the control module is arranged to apply a hashing function to the at least one of the frame attributes so as to select the backplane trace.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Hilla, Devi, Cisco EtherChannel System, IEEE 802.3, Cisco EtherSwitch System, Bruckman, Basso, Ghosh, Lebizay, Wiher '530, Viswanathan, Singh, Smith '430, DeJager '424, Dontu, Li '914, and Borgione '125.</p> <p>Below are examples of such references.</p> <p>Viswanathan at [0028] (“In one embodiment, the invention may be implemented as one or more line cards in a networked environment. To that end, FIG. 1 depicts a simplified schematic of a network interface 10 consistent with the principles of the invention. As shown in FIG. 1, networks 201 -20n (collectively, “networks 20”) are coupled to line interfaces 251-25n (collectively, “line interfaces 25”) of line cards 301 -30n (collectively, “line cards 30”). Line cards 30 further include fabric interfaces 351-35n (collectively, “fabric interfaces 35”) which serve to couple line cards 30 to crossbar interconnect 40 via backplane</p>

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		<p>interconnects 501-50n (collectively, "backplane interconnects 30"). It should be appreciated that the backplane interconnects 50 may be any switch/ gateway/router capable of connecting line cards 30 to crossbar interconnect 40. Moreover, crossbar interconnect 40 may be used to provide non-arbitrated open communication across all connected systems using a fabric topology (e.g., line cards 30, management card 60, etc.). However, it should equally be appreciated that an arbitrated bus architecture may similarly be used.”)</p> <p>Viswanathan at [0031] (“Certain management functions for the network interface 10 may be carried out using the management line card 60, which in the embodiment of FIG. 1 is coupled to the crossbar interconnect 40 using backplane interconnect 70. While FIG. 1 depicts only a single Management Line Card 60, it should similarly be appreciated that more than one may be used. In any event, Management Card 60 may execute software for setting up the routing tables for line cards 30, according to one embodiment.”)</p> <p>Viswanathan at Figure 1</p>

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		<p>FIG. 1</p> <p>Singh at 4:5-13 ("In some aspects of the method, selectively connecting port card ingress ports to port card egress ports includes: each port card accepting packets on a plurality of ingress</p>

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		<p>data links, through a corresponding plurality of port card ingress ports, from at least one ingress TM (iT M); storing the accepted packets in a port card ingress memory Sub system (iMS); assigning packets to a plurality of port card backplane data links; and, Supplying assigned packets to a crossbar.”)</p> <p>Singh at 5:54-58 (“A plurality of backplane data links transfers packets between port cards. Shown are a second plurality of ingress 55 backplane data links 1 through k on lines 208 through 210, respectively. Egress backplane data links 1 through t are associated with lines 212 through 214, respectively.)</p> <p>Singh at 6:20-29 (“The iPQ 216 has a control link on line 228 operatively connected to a corresponding crossbar 220 controlling the inter-port card transfer of packets on the ingress backplane data links. More specifically, the crossbar is part of a backplane that includes switchplane banks, switchplanes, and switch parts (not shown). Each switch card typically includes a plurality of crossbars controlled by an arbiter that maintains a control link with the iPQ. Additional details of the backplane switching mechanism are provided in Functional Description Section, below.”)</p> <p>Singh at 13:15-24 (“Two switch cards, that together service a backplane channel, form a switch plane. A backplane channel, as defined in the previous subsection, consists of a group of backplane data links from the MS that carry traffic to the same switch plane. The timing of the links in a backplane channel is such that one link is serviced in the channel every 32 ns with all the links in that channel getting serviced in one cell time. In a fully provisioned 32x32 port card system, there would be 32 4-chamail port cards and 16 switch cards forming 2 banks of 4 switchplanes as shown in FIG. 11.”)</p> <p>Singh at Figure 3 (annotations added)</p>

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		<p>Fig. 3</p> <p>The diagram illustrates the internal architecture of a Line Interface Card (LIC) and its connection to an external switch fabric. The LIC is labeled "LINE INTERFACE CARD (UP TO OC-768)". Inside the LIC, the following components are shown in sequence: AMCC TRANSMITTERS RECEIVERS, AMCC FEC AND SONET DEVICES, AMCC NETWORK PROCESSORS AND TRAFFIC MANAGERS, AMCC SERDES, and PARALLEL FIBER OPTICS AND AMCC PMD ICS. External connections to the LIC include two fiber optic ports on the left and a parallel fiber optic port on the right. Below the LIC, a dashed line separates it from an "AMCC CYCLONE SWITCH FABRIC". This fabric is part of the "S8005 SERIES". On the left side, there is an "INGRESS" section containing 32 line interfaces numbered 0 through 31. On the right side, there is an "EGRESS" section containing 32 line interfaces numbered 0 through 31. Arrows indicate the flow of traffic from the ingress line interfaces through the switch fabric to the egress line interfaces.</p> <p>Singh at Figure 4 (annotations added)</p>

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		<p>Fig. 4</p> <p>Singh at Figure 17</p>

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	<p>Fig. 17</p> <pre> graph TD START([START 1700]) --> T1[TRANSMITTING PACKETS FROM TM 1702] T1 --> A1[ACCEPTING PACKET AT PORT CARD INGRESS PORT 1704] A1 --> S1[SELECTIVELY CONNECTING INGRESS AND EGRESS PORTS 1706] S1 --> A2[ACCEPTING PACKETS ON 6TH PLURALITY OF INGRESS PORTS 1706a] A2 --> S2[STORING PACKETS IN iMS 1706b] S2 --> A3[ASSIGNING PACKETS TO BACKPLANE DATA LINKS 1706c] A3 --> S3[SUPPLYING PACKETS TO CROSSBAR 1706d] S3 --> A4[SUPPLYING PACKETS FROM PORT CARD EGRESS PORTS 1708] A4 --> S4[SENSING CONNECTION FAULT 1710] S4 --> R1[RESELECTING CONNECTIONS 1712] </pre> <p>Smith '430 at 9:6-29 ("Thus, providing interconnections between virtual network device sub-units 122(1) and 122(2) can allow virtual network device sub-units 122(1) and 122(2) to operate as a single virtual network device 202. Network devices 120(1)-120(3) communicate with virtual network device 202 in the same way that network devices 120(1)-120(3) would communicate with a single physical device. For example, if network device 120(2) is handling a packet addressed to server 104(3), network device 120(2) can select one of the two uplinks in network device bundle 250(2) on which to send the packet. This selection can be based on load-sharing criteria. In such a situation, since virtual network device 202 appears to be a single network device, network device 120(2) is just as likely to select the uplink to virtual network device sub-unit 122(2) as the uplink to virtual network device sub-unit 122(1), despite the fact that only virtual network device sub-unit 122(1) has a direct connection to server 104(3). If the packet is sent to virtual network device sub-unit 122(2), network device 122(2) can then use one of the uplinks included in virtual network device link 360 between virtual network device sub-units 122(1) and 122(2) to send the packet to virtual</p>	

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		<p>network device sub-unit 122(1), and virtual network device sub-unit 122(1) can in turn provide the packet to its destination, server 104(3).”)</p> <p>Dontu at [0039] (“Each identifier module 130(1)-130(3) is a part of a network device component that is capable of being the source of a unique identifier. In one embodiment, identifier modules supply media access control (MAC) addresses for use as identifiers. If the network device components are each line cards, the identifier modules can be read-only memories (ROMs) on each of the line cards. The ROMs store the MAC address of each line card. Alternatively, if each network device component is a virtual network device sub-unit, each identifier module can be a backplane. It is noted that other alternatives can be used to supply identifiers such as MAC addresses.”)</p> <p>Cisco has innovated and patented other improvements to EtherChannel technology, including the use of physical links connecting interface modules to a network node. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • DeJager ’424 • Dontu • Li ’914 • Borgione ’125 <p>DeJager ’424 at Figure 2</p>

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		<p>FIG. 2</p> <p>DeJager '424 at Figure 3A</p>

No.	'740 Patent Claim 31	The Reference
		<pre> graph TD START([START 300]) --> Receive[Receive Packet 302] Receive --> Assign[Assign Packet to Target Port Group 303] Assign --> Hash[Hash and Mask Packet Address into a Stream ID 304] Hash --> Decision1{Is the Bit for that Stream ID set in the Current Time Mark Register? 306} Decision1 -- No --> SetC[Set the Bit for that Stream ID in the Current Time Mark Register 308] SetC --> Decision2{Is the Bit for that Stream ID set in the Alternate Time Mark Register? 310} Decision2 -- No --> Push[Assign Packet to PUQ (PUSH) and Assign it to the Current Queue Mark Bit 312] Push --> Store[Store LUQ Queue # in Stream State Table Location Corresponding to that Stream ID 313] Push --> LUQPush[LUQ PUSH Process 316] LUQPush --> Forward[Forward (POP) Queued Packet 318] Forward --> LUQPop[LUQ POP Process 320] LUQPop --> END([END 322]) Decision2 -- Yes --> Check[Check Stream State Table for Transmit Queue Previously Assigned to that Stream ID; Assign New Packet with that Stream ID to that Queue (PUSH); Assign Packet the Current Transmit Queue Mark Bit] Check --> Store </pre> <p>FIG. 3A</p> <p>DeJager '424 at 3:16-38 (“In another aspect, the invention provides a network switch. The switch includes a port group and a system for distributing network traffic among ports of the</p>

No.	'740 Patent Claim 31	The Reference
		<p>port group. The system includes a mechanism for determining a stream ID for the packet and assigning the packet having the stream ID to a queue of a port in the port group, and a mechanism for adjusting a queue assignment of a prior packet having the stream ID to a queue of a different port of the port group based on load in the queues of the ports of the group. The mechanism for determining a stream ID and assigning the packet having the stream ID to a queue of a port in the port group may include a hashing and masking mechanism for determining a stream ID for the packet, a pair of time mark registers for determining whether another packet having the stream ID has been distributed to a queue for a port in the group during a time interval, and a stream state table for storing stream IDs with corresponding queue assignments. The adjusting mechanism may include a least utilized queue register for maintaining proper identification of a least utilized queue, and a pair of queue mark registers for determining whether a queue for a port in the port group is current. In addition, the switch may include a clock for timing a load balance time interval.”)</p> <p>DeJager '424 at 5:19-30 (“Ethernet addresses have 48 bits. Therefore, the number of possible streams identified by such an address may be 248 or, where the stream address is defined by both the source and the destination address, 296. In order to reduce the number of possible stream addresses and thereby permit a more economical system, both addresses may be hashed and then either an XOR (exclusive OR logical operation) of the two hashes or one of the hashes independently may be masked down to a n-bit index, where n is much less than 96, for example 6, as shown in FIG. 2. Conventional hashing and masking techniques and mechanisms known to those of skill in the art may be used. This results in a table depth of 64 (26).”)</p> <p>DeJager '424 at 5:42-45 (“Once it has been hashed and masked, the 6-bit stream identification (stream ID) is used to address the stream state table. This table stores the port number currently assigned to a stream.”)</p> <p>DeJager '424 at 7:59-8:10 (“FIG. 3A is primarily addressed to the basic load balancing feature of the present invention, that is, assignment of packets to ports in a port aggregation based on traffic volume. The process begins at a step 300, and at a step 302 a packet of data</p>

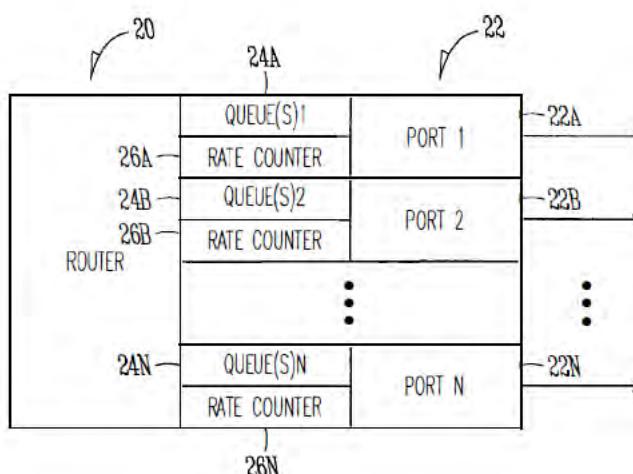
No.	'740 Patent Claim 31	The Reference
		<p>is received for forwarding. At a step 303 the packet's address is analyzed to determine the packet's target port group. At a step 304, the packet's address is hashed and masked into a 6-bit stream ID. Next, in a decision step 306, a determination is made whether or not the bit for that stream ID is set in the current time mark register. If decision step 306 is answered in the negative, the bit for that stream ID is set in the current time mark register, at a step 308. If decision step 306 is answered in the affirmative, the stream state table is checked for the transmit queue to which the packets from the stream corresponding to that stream ID have previously been assigned, and the new packet having the same stream ID is assigned to (pushed onto) that transmit queue, at a step 314. The newly queued packet is also assigned the current transmit queue mark bit.”)</p> <p>DeJager '424 at 9:17-26 (“FIG. 4 shows a block diagram of a load balancing system in accordance with a preferred embodiment of the present invention. In this embodiment, the system 400 includes a “switch” 402, which may be a switch or other packet-forwarding device as described previously, to which inbound links 401 from sources in the network transmitting packets are connected. The incoming packets pass through a mechanism 404 for hashing and masking packet addresses in order to assign each packet an appropriate stream ID, for example as described previously.”)</p> <p>Dontu at [0095] (“In some embodiments, network devices 1220(1) and 1220(2) are aware (e.g., through various state information maintained within each network device) that each virtual link bundle 1350(1) and 1350(2) includes links that are terminated on different network devices in distribution layer 1212. In such an embodiment, network devices 1220(1) and 1220(2) can select a link within a particular virtual link bundle on which to send a packet based on this awareness.”)</p> <p>Dontu at [0097] (“FIG. 13B illustrates another embodiment of the present invention. In FIG. 13B, network devices 1220(1) and 1220(2) operate in the same manner that those network devices would operate if connected to a single network device. By operating in this manner, the use of a virtual link bundle is simplified. For example, if network device 1220(1) is aware</p>

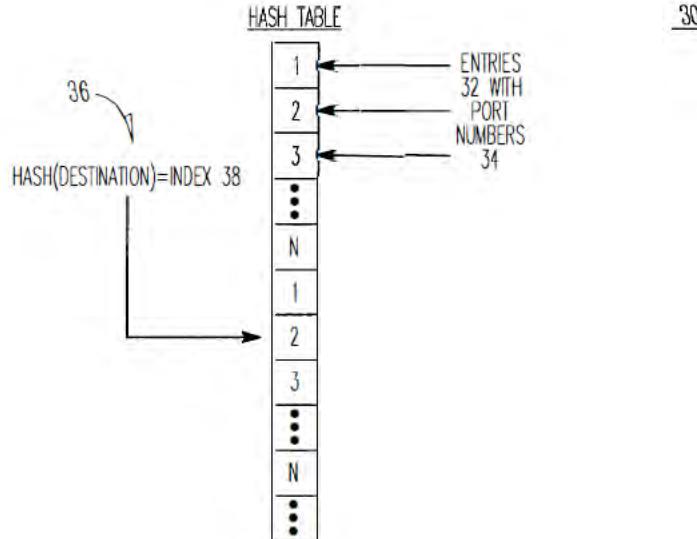
No.	'740 Patent Claim 31	The Reference
		<p>that virtual link bundle 1350(1) terminates at two different network devices, network device 1220(1) selects a link on which to send a particular packet based on Spanning Tree Protocol. The use of Spanning Tree Protocol may involve more overhead and/or be more restrictive with respect to which links can be used to send a given packet (e.g., Spanning Tree Protocol might block all but one of the links, preventing utilization of all but one non-blocked link) than if network device 1220(1) simply views virtual network device 1302 as a single entity. When viewing virtual net-work device 1302 as a single entity, for example, network device 1220(1) simply select a link on which to send a packet based on load-sharing constraints. Similarly, if a link within virtual link bundle 1350(1) fails, there is no need for network device 1220(1) to change how Spanning Tree Protocol is applied. Instead, network device 1220(1) simply continues to use the non-failed links within virtual link bundle 1350(1).”)</p> <p>Dontu at [0108] (“Thus, providing interconnections between virtual network device sub-units 1222(1) and 1222(2) allows virtual network device sub-units 1222(1) and 1222(2) to operate as a single virtual network device 1302. Network devices 1220(1)-1220(3) communicate with virtual network device 1302 in the same way that network devices 1220(1)-1220(3) would communicate with a single physical device. For example, if network device 1220(2) is handling a packet addressed to server 1204(3), network device 1220(2) selects one of the two uplinks in network device bundle 1350(2) on which to send the packet. This selection is based on load-sharing criteria in some embodiments. In such a situation, since virtual network device 1302 appears to be a single network device, network device 1220(2) is just as likely to select the uplink to virtual network device sub-unit 1222(2) as the uplink to virtual network device sub-unit 1222(1), despite the fact that only virtual network device sub-unit 1222(1) has a direct connection to server 1204(3). If the packet is sent to virtual network device sub-unit 1222(2), network device 1222(2) uses one of the uplinks included in virtual network device link 1460 between virtual network device sub-units 1222(1) and 1222(2) to send the packet to virtual network device sub-unit 1222(1), and virtual network device sub-unit 1222(1) can in turn provide the packet to the packet's destination, server 1204(3).”)</p> <p>Dontu at [0109] (“In other embodiments, network devices 1220(1)-1220(3) are aware that virtual link bundles 1350(1) and 1350(2) actually terminate on two different network devices.</p>

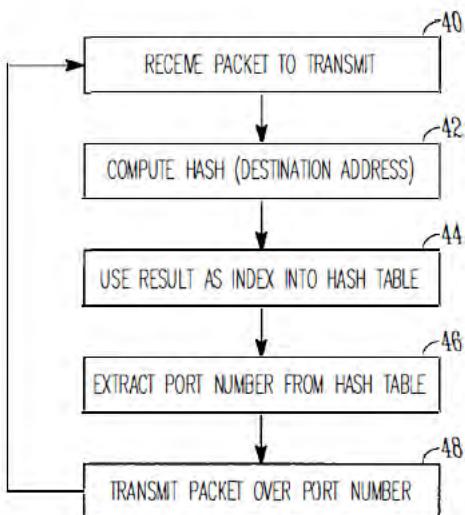
No.	'740 Patent Claim 31	The Reference
		<p>Network devices 1220(1)-1220(3) control packet transmission based on this information. For example, in this situation, network device 1220(2) handles a packet addressed to server 1204(3) by selecting the uplink coupled to virtual network device sub-unit 1222(1) instead of the uplink coupled to virtual network device sub-unit 1222(2), based on the fact that network device 1220(2) recognizes separate connections to two different network devices within the logical link.”)</p> <p>Dontu at [0112] (“The same logical identifiers are used to identify uplink interface bundles by each of virtual network device sub-units 1222(1) and 1222(2), and the virtual network device sub-units coordinate to assign the same logical identifier to each uplink interface within the same uplink interface bundle. When forwarding packets via an uplink interface bundle identified by a particular logical identifier, each virtual network device sub-unit 1222(1) and 1222(2) generates a hash value to select one of the uplink interfaces within that uplink interface bundle on which to send the packet. Each of the virtual network device sub-units uses these hash values to identify local uplink interfaces within that virtual network. Thus, each virtual network device sub-unit will only select an uplink interface that is local to that virtual network device sub-unit. For example, if virtual network device sub-unit 1222(1) is forwarding a packet via the uplink interface bundle that includes interfaces 1420(9), 1420(13), and 1420(16), the hash value generated by virtual network device sub-unit will identify one of interfaces 1420(9) or 1420(13).”)</p> <p>Dontu at [0113] (“In the above example, by associating each hash value with local uplink interfaces in the uplink interface bundle, the usage of virtual switch link 1460 is reduced. Essentially, virtual network device sub-unit 1222(1) favors local uplink interfaces within a particular uplink interface bundle over remote uplink interfaces, in the same uplink interface bundle, on virtual network device sub-unit 1222(2). Likewise, virtual network device sub-unit 1222(2) favors local uplink interfaces within a particular uplink interface bundle over uplink interfaces included in virtual network device sub-unit 1222(1). For example, if virtual network device sub-unit 1222(2) needs to forward a packet via an uplink interface, virtual network device sub-unit 1222(2) will send that packet via uplink interface 1420(12) instead of forwarding that packet across virtual network device link 1460 to be sent via uplink</p>

No.	'740 Patent Claim 31	The Reference
		<p>interface 1420(7). By favoring local interfaces, the amount of traffic sent over virtual network device link 1460 is reduced, since each virtual network device sub-unit 1222(1) and 1222(2) will forward locally-received packets (i.e., packets received via interfaces other than those coupled to virtual network device link 1460) from a local interface.”)</p> <p>Dontu at [0118] (“To operate in this way, each egress uplink interface coupled to a link in a virtual link bundle is configured to filter out traffic received via virtual network device link 1460. For example, a packet is received at virtual network device sub-unit 1222(1) via virtual network device link 1460. The interface 1420(1) or 1420(3) that receives the packet updates information (e.g., in a header) associated with the packet to indicate that the packet was received via virtual network device link 1460 (in alternative embodiments, the sending interface in virtual network device sub-unit 1222(2) can update this information). When virtual network device sub-unit 1222(1) looks up the destination address of the packet in a lookup table, the lookup table returns the logical identifier that identifies local uplink interfaces 1420(9) and 1420(13). The packet is then forwarded to uplink interface 1420(13) (e.g., selected based on load-sharing considerations). When uplink interface 1420(13) receives the packet, uplink interface 1420(13) will only output the packet if the packet was not received via virtual switch link 1460, since if the packet was received via the virtual switch link, the other virtual network device sub-unit 1222(2) will have already sent the packet via the virtual link bundle. Thus, uplink interface 1420(13) can filter the packet from the packet flow being sent via uplink interface 1420(13) based on the information appended to the packet that indicates whether the packet was received via virtual network device link 1460.”)</p> <p>Li '914 at Figure 1</p>

No.	'740 Patent Claim 31	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p>Li '914 at Figure 2</p>

No.	'740 Patent Claim 31	The Reference
		 <p>The diagram illustrates a router architecture. At the top, a curved arrow labeled 20 points to a box labeled 'ROUTER'. Inside the 'ROUTER' box, there are two columns of components. The left column contains 'QUEUE(S)1', 'RATE COUNTER', 'QUEUE(S)2', 'RATE COUNTER', and 'QUEUE(S)N', with vertical ellipses between 'QUEUE(S)2' and 'QUEUE(S)N'. The right column contains 'PORT 1', 'PORT 2', and 'PORT N', also with vertical ellipses between 'PORT 2' and 'PORT N'. Arrows labeled 24A, 24B, and 2AN point from the top of the 'QUEUE(S)1', 'QUEUE(S)2', and 'QUEUE(S)N' rows respectively to the corresponding 'QUEUE(S)' labels. Arrows labeled 26A, 26B, and 26N point from the bottom of the 'RATE COUNTER' row to the corresponding 'RATE COUNTER' labels. Arrows labeled 22A, 22B, and 22N point from the right side of the 'PORT 1', 'PORT 2', and 'PORT N' rows respectively to the corresponding 'PORT' labels. The bottom of the 'ROUTER' box is labeled 26N.</p> <p><i>FIG. 2</i></p> <p>Li '914 at Figure 3</p>

No.	'740 Patent Claim 31	The Reference
		 <p style="text-align: center;"><i>FIG. 3</i></p> <p>Li '914 at Figure 4</p>

No.	'740 Patent Claim 31	The Reference
		 <p style="text-align: center;"><i>FIG. 4</i></p> <p>Li '914 at Figure 6</p>

No.	'740 Patent Claim 31	The Reference
		<p style="text-align: center;"><u>EXAMPLE</u></p> <p>The diagram shows Router A on the left and Router B on the right. Router A has multiple ports labeled PORT 1 (OVER-UTILIZED), PORT 2 (UNDER-UTILIZED), ..., PORT N. Arrows labeled 62A, 62B, ..., 62N point from these ports to Router B. Below this, a vertical stack of boxes is labeled "HASH TABLE 64". The top half of the table contains entries 1, 2, 3, ..., N. The bottom half contains entries 1, 2, 3, ..., N. An arrow points from the label "HASH TABLE 64" to the top of the table. Another arrow points from entry 2 in the top half to entry 2 in the bottom half, indicating a mapping or selection process.</p> <p style="text-align: center;"><i>FIG.6</i></p> <p style="text-align: center;">Li '914 at Figure 7</p>

No.	'740 Patent Claim 31	The Reference
		<pre> graph TD A["PROVIDE MULTIPLE QUEUES PER PORT, SUCH AS 1 QUEUE PER ENTRY IN THE TABLE"] --> B["IF HASH(DESTINATION)=INDEX POINTS TO HASH BUCKET X FOR PORT Y, PLACE PACKET IN QUEUE X OF PORT Y"] B --> C["EXAMINE DEPTH OF QUEUES TO DETERMINE OVER-UTILIZED PORT(S)/QUEUES AND UNDER-UTILIZED PORT(S) QUEUES"] C --> D["MODIFY TABLE BY SUBSTITUTING OVER-UTILIZED PORT(S) WITH UNDER-UTILIZED PORT(S)"] B --> C C --> D </pre> <p>FIG. 7</p> <p>Li '914 at Figure 8</p>

No.	'740 Patent Claim 31	The Reference
		<p style="text-align: center;"><i>FIG. 8</i></p> <p>Li '914 at 1:30-43 (“For instance, in the example of FIG. 1, Router A has an interface 10 with a plurality of ports or links 12 which connect with Router B in order to pass data from Router A to Router B, in this example. When a packet from Router A needs to be transmitted to Router B, Router A determines which port of the plurality of ports 12 should be used to transmit the packet to Router B. Conventionally, a hash operation or function may be used to generate an index into the plurality of ports of Router A. For instance, a hashing function may be performed using the destination address of the packet to generate an index, and unneeded bits may be masked off in order to form an index which is used to select one of the plurality of ports 12 of the interface 10 of Router A upon which to transmit the packet.”)</p>

No.	'740 Patent Claim 31	The Reference
		<p>Li '914 at 1:44-57 ("As recognized by the present inventors, such a process is a static process which is not sensitive to the amount of traffic being handled by particular ports 12 of Router A. In other words, if an amount of traffic builds up on one or more ports 12 of Router A, the above-described hash function does not account for such traffic build-ups in determining which port of interface 10 should be utilized to transmit a packet. As recognized by the present inventors, this problem may be compounded when adjacent routers are of the same make and model and use the same hashing function, such that a build-up of traffic on a particular port in Router A may be propagated and compounded onto a corresponding port of Router B, which degrades the overall performance of Router A and Router B in the network.")</p> <p>Li '914 at 2:39-55 ("In one example, the operation of selecting a port from the list of ports may include performing a hash operation using a destination address of the packet to generate an index value into the list, and selecting a port from the list based on the index value.</p> <p>According to another broad aspect of another embodiment of the invention, disclosed herein is a method for determining an output port upon which to transmit a packet in a router having a plurality of output ports adapted to be coupled with an adjacent or "next-hop" router. The method includes creating a list of output ports that are coupled with the adjacent router; updating the list based on network traffic over the output ports; extracting a destination address from the packet; performing a hash function using the destination address to create an index into the list; at the location of the index in the list, extracting an identifier of an output port; and transmitting the packet over the output port.")</p> <p>Li '914 at 4:41-67 ("In FIG. 3, a data structure or table 30 such as a hash table is shown, in accordance with one embodiment of the present invention. In the example of FIG. 3, the table 30 includes a plurality of entries 32 which in one embodiment, are filled with the port numbers 34 of the ports of the router. In this example, assuming that there are N ports of Router A which are coupled with Router B, then the table contains as entries 32 the port numbers 1 to N. The size of the table is a matter of choice, and in one example, contains 65,536 entries to support 16-bit addressing.</p>

No.	'740 Patent Claim 31	The Reference
		<p>In one example, the destination address 36 of the packet to be transmitted is used as the operand of a hash operation. In general, a hashing function is an operation which produces a unique numeric value based upon a given operand. The result of this operation is used as an index 38 into the table 30 shown in FIG. 3, and the port number 34 contained within the entry 32 indexed is utilized to transmit the packet out of the router. In one example, the hash operation generates a 16-bit result, and in this example, the table 30 is sized to support 65,536 entries. In one example, the table 30 is filled with port numbers 34 in a sequential manner, such as shown in FIG. 3. It is understood that the length of the table 30 is a matter of choice depending upon the particular implementation. Further, it is understood that while a 16-bit result from the hash operation may be used in one example, a portion of the 16-bit result may be masked off to form a result of less than 16 bits, if desired, or a larger address range may be used.”)</p> <p>Li '914 at 5:13-28 (“In FIG. 4 at operation 40, a packet is to be transmitted to a particular adjacent router for “next hop” in the network over one or more ports of the router. For example, in FIG. 1, a packet is to be transmitted from Router A to Router B over one of the plurality of ports 12. In FIG. 4, at operation 42, a hash operation is performed using, in one example, the destination address of the packet. The result of the computation is used at operation 44 as an index into a table or data structure, such as the hash table 30 shown in the example of FIG. 3. In FIG. 4, at operation 46, a port number is extracted from the entry of the table indexed by operation 44. At operation 48, the packet is transmitted from the router along the ports identified by the port number extracted by operation 46. Operations 40-48, or various combinations thereof, may be repeated as needed to handle the transmission of multiple packets over the various ports between routers.”)</p> <p>Li '914 at 5:29-41 (“Referring to the example of FIG. 3, assuming that a packet has a destination address 36 which, upon performing a hash function yields an index 38 which points to the second entry in the table 30, the port number “2” is extracted from the table, and the packet is transmitted to the adjacent router in the network over port number 2, in this example. If another packet to be transmitted had a destination address 36 which, upon performing a hash operation, generates an index 38 pointing to the first entry in the hash table</p>

No.	'740 Patent Claim 31	The Reference
		<p>30, then in this example the port number "1" is extracted from the table and the packet is transmitted to appropriate adjacent router using port number "1." These examples are provided for purposes of illustration only.")</p> <p>Li '914 at 5:66-6:8 ("Upon determining the overutilized and underutilized ports of the router, operation 54 modifies the table 30 by substituting an overutilized port with an underutilized port. In one example, where a hash table 30 contains multiple entries having a port listed multiple times within the table, a single substitution of one instance of an overutilized port is made using an underutilized port. In this manner, the changes in the traffic between the overutilized and underutilized ports are made at a low rate so that the traffic is smoothly distributed across the ports.")</p> <p>Li '914 at 6:65-7:7 ("At operation 72 of FIG. 7, a hash operation is performed using, in one example, the destination address of the packet to be transmitted. The result of the hash operation generates an index into the table, and if the index points to an entry corresponding to a particular port, then the packet is placed in the corresponding queue of the particular port. For example and referring to FIG. 8, if the hash operation generates an index 82 which points to the fifth entry in the table 80 (shown as corresponding to port 2, queue 2), then the packet is placed in queue 2 of port 2 for transmission out of the router 84.")</p> <p>Borgione '125 at 2:8-18 ("Load balancing of data packets transmitted across individual network links within an aggregate of network links can be handled by interface hardware. The individual network links, across which the data load is to be balanced, can be selected in several ways. One such way is to analyze source and destination Ethernet addresses within the data packets to be sent over the logical link and generate a link identifier from that information. Another method for selecting a network link over which to send a packet is a round robin method, wherein each link is selected in order as packets arrive.")</p> <p>Borgione '125 at 3:14-23 ("A multicast packet is typically transmitted as a single packet received by a select group of receivers. The group of receivers is designated by a multicast address. The source node address appears in the header of a multicast packet, and the</p>

No.	'740 Patent Claim 31	The Reference
		<p>multicast address appears as the destination address. A single multicast packet sent by a network node can be replicated at other network nodes, such as link nodes 110 and 120, in order for the receivers to receive the multicast packet. Each replicated multicast packet will have the same source and destination address (the multicast address).”)</p> <p>Borgione '125 at 3:30-49 (“As stated above, packet source and destination addresses can be analyzed to determine which network link in a logical link is to be used to send a packet between link nodes 110 and 120. Commonly, such analysis involves a hashing algorithm that takes the Ethernet addresses and generates a network link identifier. The network link identifier identifies which of the plurality of network links is to be used for sending the packet between link nodes 110 and 120.</p> <p>While the aforementioned method addresses data load balancing for certain types of data transmission (e.g., unicast), the method does not efficiently balance data loads across individual network links within a logical link for more complex data transmission such as multicast packet transmission. To illustrate, if a multicast packet is replicated at a link node (e.g., link node 110 or 120), the source and destination address are the same for replicated multicast packets, and such a hashing algorithm will generate the same link identifier for each replicated multicast packet and therefore send all of those replicated multicast packets on the same network link. This can create an undesirable load imbalance among the plurality of network links.”)</p> <p>Borgione '125 at 4:3-30 (“Accordingly, one aspect of the present invention provides a method for transmitting a replicated multicast packet over one of a plurality of network links that form one logical channel. Selecting the one of the plurality of network links comprises analyzing a destination ethernet address of the replicated multicast packet and a non-ethernet component of the header of the replicated multicast packet.</p> <p>A further aspect of the present invention provides a method for replicating a multicast packet to produce first and second multicast packets, which are transmitted over a first and second link of a logical channel between a pair of network nodes.</p> <p>Another aspect of the present invention provides a system comprising a first network node coupled to a second network node through a plurality of network links. The first network</p>

No.	'740 Patent Claim 31	The Reference
		<p>node selects a destination interface identifier for an outgoing multicast packet, selects one of the plurality of network links using the destination interface identifier, and transmits the outgoing multicast packet to the second network node over the selected network link. Another aspect of the present invention provides a method comprising connecting a first network device to a second network device using a plurality of network links. A multicast packet is provided to the first network device, which is con-figured to replicate the multicast packet thus forming repli-cated multicast packets. Each replicated multicast packet receives a destination interface identifier which is used to select one of the plurality of network links for transmitting the replicated multicast packet by the first network device.”)</p> <p>Borgione '125 at 5:28-54 (“The present invention balances the transmission of repli-cated multicast packets among an aggregate of network links that provide a logical channel or link between network nodes. Prior art link load balancing requires analysis of source and destination Ethernet addresses (i.e., as input to a hashing algorithm). Since replicated multicast packets each have the same source and destination Ethernet addresses, another part of a replicated multicast Ethernet packet must be used in order to differentiate between replicated multicast Ethernet pack-ets. An added tag header can be used to include a destination interface identifier. For example, in a YLAN network envi-ronment, such a tag header is included in packets per IEEE Std. 802.1Q. A portion of an IEEE Std. 802.1Q tag header is a YLAN identifier (YID), which is unique to a particular YLAN. A destination interface identifier within a tag header can be used to select which network link in a logical link is to be used to transmit a replicated multicast packet. Since the destination interface identifier often varies from replicated multicast packet to replicated multicast packet, use of the destination interface identifier to select a network link will lead to a more even distribution of multicast packet transmis-sion across the logical link. Such a distribution can reduce the likelihood of a load imbalance in the logical link.</p> <p>Network packets contain header information and data pay-load information. Header information can include Media Access Control (MAC) addressing such as the source and destination addresses of the packet.”)</p>

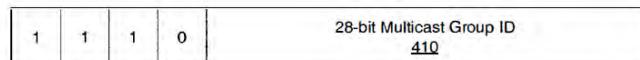
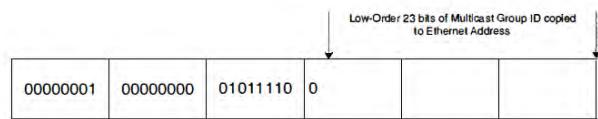
No.	'740 Patent Claim 31	The Reference
		<p>Borgione '125 at 7:1-5 (“A calculation that takes place in step 670 can take any form that generates an output value from an input value. A hash algorithm is one form of such a function. A hash function can have as an input a destination interface identifier (such as YID).”)</p> <p>Borgione '125 at Figure 2-5</p>  <p style="text-align: center;">Figure 2</p>  <p style="text-align: center;">Figure 3</p>  <p style="text-align: center;">Figure 4</p>  <p style="text-align: center;">Figure 5</p>

EXHIBIT E-4
Defendant's First Amended Invalidity Contentions
Orckit Corporation v. Cisco Systems, Inc., 2:22-cv-00276-JRG-RSP

Chart for U.S. Patent 10,652,111 (“the ’111 Patent”)
35 U.S.C. §103

In this chart, “Reference” refers to each of the following the References:

- U.S. Patent Publication No. 2012/0300615 to Kempf et al. (“Kempf”)
- U.S. Patent Publication No. 2013/0322242 to Swenson et al. (“Swenson”)
- U.S. Patent Publication No. 2014/0140211 to Chandrasekaran et al. (“Chandrasekaran”)
- U.S. Patent No. 9,264,400 to Lin et al. (“Lin ’400”)
- U.S. Patent Publication No. 2013/0291088 to Shieh et al. (“Shieh ’088”)
- Cisco Intelligent WAN (“Cisco IWAN System”)
- VMware NSX Network Virtualization (“VMware NSX System”)
- U.S. Patent No. 9,276,877 to Chua ’877 et al. (“Chua ’877”)
- U.S. Patent No. 9,038,151 to Chua ’151 et al. (“Chua ’151”)
- U.S. Patent Publication No. 2005/0210533 to Copeland et al. (“Copeland”)
- U.S. Publication No. 2011/0310901 A1 to Uchida et al. (“Uchida”)

The following references are identified individually:

- OpenFlow Switch Specification Version 1.3.0 (Wire Protocol 0x04) (“OpenFlow”)
- U.S. Patent Publication No. 2006/0083167 to Balakrishnan (“Balakrishnan”)
- U.S. Patent No. 9,467,478 to Khan et al. (“Khan ’478”)
- U.S. Patent No. 8,868,735 to Wang et al. (“Wang ’735”)
- U.S. Patent No. 10,142,254 to Olofsson et al. (“Olofsson ’254”)
- U.S. Patent No. 9,614,739 to Kumar et al. (“Kumar ’739”)

As shown in the chart below, all Asserted Claims of the ’111 Patent are invalid under 35 U.S.C. § 103 because The Reference renders those claims obvious either alone, or in combination with the knowledge of a person having ordinary skill in the art, and in further

combination with the references specifically identified below and in the following claim chart and/or one or more references identified in Defendant's First Amended Invalidity Contentions.

Motivations to combine include at least the similarity in subject matter between the references to the extent they concern methods relating to routing certain network traffic to entities for further analysis and inspection. Insofar as the references cite other patents or publications, or suggest additional changes, one of ordinary skill in the art would look beyond a single reference to other references in the field.

These invalidity contentions are based on Defendant's present understanding of the asserted claims, and Orckit's apparent construction of the claims in its November 3, 2022 Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1, and Orckit's January 19, 2023 First Amended Disclosure of Asserted Claims and Infringement Contentions Pursuant to P.R. 3-1 (Orckit's "Infringement Disclosures"), which is deficient at least insofar as it fails to cite any documents or identify accused structures, acts, or materials in the Accused Products with particularity. Defendant does not agree with Orckit's application of the claims, or that the claims satisfy the requirements of 35 U.S.C. § 112. Defendant's contentions herein are not, and should in no way be seen as, admissions or adoptions as to any particular claim scope or construction, or as any admission that any particular element is met by any accused product in any particular way. Defendant objects to any attempt to imply claim construction from this chart. Defendant's prior art invalidity contentions are made in a variety of alternatives and do not represent Defendant's agreement or view as to the meaning, definiteness, written description support for, or enablement of any claim contained therein.

The following contentions are subject to revision and amendment pursuant to Federal Rule of Civil Procedure 26(e), the Local Rules, and the Orders of record in this matter subject to further investigation and discovery regarding the prior art and the Court's construction of the claims at issue.

No.	'111 Patent Claim 1	The Reference
1[preamble]	A method for use with a packet network including a network node for transporting packets between first and second entities under control of a controller that is external to the network	<p>The Reference discloses a method for use with a packet network including a network node for transporting packets between first and second entities under control of a controller that is external to the network node, the method comprising:</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran,</p>

No.	'111 Patent Claim 1	The Reference
	<p>node, the method comprising:</p> <p>Below are examples of such references.</p> <p>OpenFlow at 6-7</p> <pre> graph TD Controller[Controller] -- "OpenFlow Protocol" --> OFSwitch[OpenFlow Switch] subgraph OFSwitch [OpenFlow Switch] SC[Secure Channel] GT[Group Table] FT1[Flow Table] FT2[Flow Table] FT1 -- Pipeline --> FT2 SC --- GT end </pre>	<p>Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p>

Figure 1: Main components of an OpenFlow switch.

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		<p>2 Switch Components</p> <p>An OpenFlow Switch consists of one or more <i>flow tables</i> and a <i>group table</i>, which perform packet lookups and forwarding, and an <i>OpenFlow channel</i> to an external controller (Figure 1). The switch communicates with the controller and the controller manages the switch via the OpenFlow protocol.</p> <p>Using the OpenFlow protocol, the controller can add, update, and delete <i>flow entries</i> in flow tables, both reactively (in response to packets) and proactively. Each flow table in the switch contains a set of flow entries; each flow entry consists of <i>match fields</i>, <i>counters</i>, and a set of <i>instructions</i> to apply to matching packets (see 5.2).</p> <p>Matching starts at the first flow table and may continue to additional flow tables (see 5.1). Flow entries match packets in priority order, with the first matching entry in each table being used (see 5.3). If a matching entry is found, the instructions associated with the specific flow entry are executed. If no match is found in a flow table, the outcome depends on configuration of the table-miss flow entry: for example, the packet may be forwarded to the controller over the OpenFlow channel, dropped, or may continue to the next flow table (see 5.4).</p> <p>Instructions associated with each flow entry either contain actions or modify pipeline processing (see 5.9). Actions included in instructions describe packet forwarding, packet modification and group table</p> <p>OpenFlow at 21</p> <p>6 OpenFlow Channel</p> <p>The OpenFlow channel is the interface that connects each OpenFlow switch to a controller. Through this interface, the controller configures and manages the switch, receives events from the switch, and sends packets out the switch.</p> <p>Between the datapath and the OpenFlow channel, the interface is implementation-specific, however all OpenFlow channel messages must be formatted according to the OpenFlow protocol. The OpenFlow channel is usually encrypted using TLS, but may be run directly over TCP.</p> <p>OpenFlow at 22</p>

No.	'111 Patent Claim 1	The Reference
		<p>6.1.1 Controller-to-Switch</p> <p>Controller/switch messages are initiated by the controller and may or may not require a response from the switch.</p> <p>Features: The controller may request the capabilities of a switch by sending a features request; the switch must respond with a features reply that specifies the capabilities of the switch. This is commonly performed upon establishment of the OpenFlow channel.</p> <p>Configuration: The controller is able to set and query configuration parameters in the switch. The switch only responds to a query from the controller.</p> <p>Modify-State: Modify-State messages are sent by the controller to manage state on the switches. Their primary purpose is to add, delete and modify flow/group entries in the OpenFlow tables and to set switch port properties.</p> <p>Read-State: Read-State messages are used by the controller to collect various information from the switch, such as current configuration, statistics and capabilities.</p> <p>Packet-out: These are used by the controller to send packets out of a specified port on the switch, and to forward packets received via Packet-in messages. Packet-out messages must contain a full packet or a buffer ID referencing a packet stored in the switch. The message must also contain a list of actions to be applied in the order they are specified; an empty action list drops the packet.</p> <p>Barrier: Barrier request/reply messages are used by the controller to ensure message dependencies have been met or to receive notifications for completed operations.</p> <p>Role-Request: Role-Request messages are used by the controller to set the role of its OpenFlow channel, or query that role. This is mostly useful when the switch connects to multiple controllers (see 6.3.4).</p> <p>Asynchronous-Configuration: The Asynchronous-Configuration message are used by the controller to set an additional filter on the asynchronous messages that it wants to receive on its OpenFlow channel, or to query that filter. This is mostly useful when the switch connects to multiple controllers (see 6.3.4) and commonly performed upon establishment of the OpenFlow channel.</p> <p>6.1.2 Asynchronous</p> <p>Asynchronous messages are sent without a controller soliciting them from a switch. Switches send asynchronous messages to controllers to denote a packet arrival, switch state change, or error. The four main asynchronous message types are described below.</p> <p>Packet-in: Transfer the control of a packet to the controller. For all packets forwarded to the CONTROLLER reserved port using a flow entry or the table-miss flow entry, a packet-in event is always sent to controllers (see 5.12). Other processing, such as TTL checking, may also send packets to the controller using packet-in events.</p>

No.	'111 Patent Claim 1	The Reference
		<p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of an overlay controller. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254 • Kumar '739 <p>Khan '478 at Abstract ("A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.")</p> <p>Khan '478 at Figure 1</p>

No.	'111 Patent Claim 1	The Reference
		<p>FIG. 1</p> <p>Khan '478 at 3:1-7 ("The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.")</p> <p>Khan '478 at 3:49-57 ("Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as</p>

No.	'111 Patent Claim 1	The Reference
		<p>a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).”)</p> <p>Khan '478 at 4:1-17 (“In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described. In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA]. The OMP may be configured to handle overlay routes and transport locators (TLOCs).”)</p> <p>Khan '478 at 4:47-60 (“Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.”)</p> <p>Khan '478 at 6:15-24 (“In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI). In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Khan '478 at 9:53-10:4 ("In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <p>Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network;</p> <p>Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and</p> <p>Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.")</p> <p>Wang '735 at Abstract ("In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.")</p> <p>Wang '735 at 7:14-38 ("The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and RSVP metadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example, bandwidth reservation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS</p>

No.	'111 Patent Claim 1	The Reference
		<p>(Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization sys-tem.”)</p> <p>Wang '735 at 7:55-8:12 (“The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all network devices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to man-age and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p> <p>Wang '735 at 10:53-11:10 (“In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource</p>

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		<p>availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the applicationflows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Pro-tocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth.”)</p> <p>Olofsson '254 at Abstract (“A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.”)</p> <p>Olofsson '254 at Figure 1</p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p style="text-align: center;">..... DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

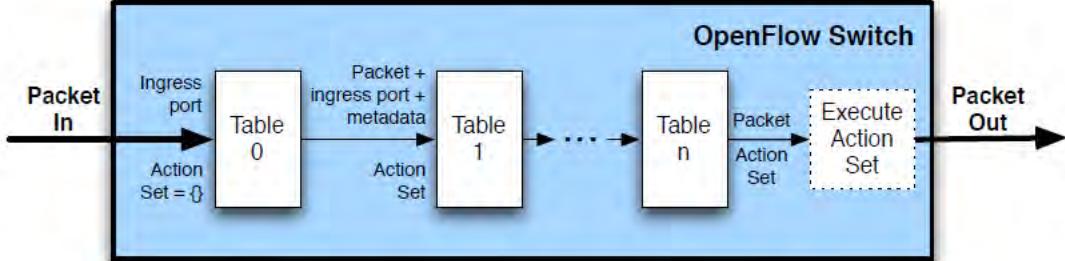
No.	'111 Patent Claim 1	The Reference
	<p>Controller establishes secure control channels with all associated network elements</p> <p>Service-hosting locations advertise service type and associated label to controller</p> <p>Policies are created on controller associating traffic from sites with services</p> <p>Policies for outbound traffic are pushed to edge-nodes and hub nodes</p> <p>Policies for inbound policies are pushed to service locations</p>	<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 ("In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100 using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated</p>

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		<p>overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Olofsson '254 at 6:36-59 ("Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <p>Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges).</p> <p>Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102.</p> <p>Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them.</p> <p>The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service.</p> <p>Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain.</p> <p>Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.")</p> <p>Olofsson '254 at 7:22-28 ("In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.")</p> <p>Kumar '739 at Abstract ("Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one is configured to identify one or more service-functions hosted by each of the service nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p>

No.	'111 Patent Claim 1	The Reference
		<p>3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.”)</p> <p>Kumar '739 at 6:56-62 (“In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.”)</p> <p>Kumar '739 at 8:7-22 (“FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522. Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.”)</p>
1[a]	sending, by the controller to the network node over the packet network, an instruction and a packet-applicable criterion;	<p>The Reference discloses sending, by the controller to the network node over the packet network, an instruction and a packet-applicable criterion.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p>

No.	'111 Patent Claim 1	The Reference
		<p>Below are examples of such references.</p> <p>OpenFlow at 11</p>  <p>(a) Packets are matched against multiple tables in the pipeline</p> <p>OpenFlow Switch</p> <p>Packet In → Table 0 → Table 1 → ... → Table n → Execute Action Set → Packet Out</p> <p>Action Set = {}</p> <p>Packet + ingress port + metadata</p> <p>Action Set</p> <p>Packet</p> <p>Action Set</p> <p>Execute Action Set</p> <p>Packet Out</p> <p>OpenFlow at 12</p>  <p>(b) Per-table packet processing</p> <p>Flow Table ①</p> <p>Match fields: Ingress port + metadata + pkt hdrs</p> <p>Action set</p> <p>② Match fields: Ingress port + metadata + pkt hdrs</p> <p>Action set</p> <p>③ Send match data and action set to next table</p> <p>① Find highest-priority matching flow entry</p> <p>② Apply instructions:</p> <ul style="list-style-type: none"> i. Modify packet & update match fields (apply actions instruction) ii. Update action set (clear actions and/or write actions instructions) iii. Update metadata <p>③ Send match data and action set to next table</p> <p>Figure 2: Packet flow through the processing pipeline</p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"> Match Fields Priority Counters Instructions Timeouts Cookie </p> <p style="text-align: center;">Table 1: Main components of a flow entry in a flow table.</p> <ul style="list-style-type: none"> • match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. • priority: matching precedence of the flow entry • counters: to update for matching packets • instructions to modify the action set or pipeline processing • timeouts: maximum amount of time or idle time before flow is expired by the switch • cookie: opaque data value chosen by the controller. May be used by the controller to filter flow statistics, flow modification and flow deletion, not used when processing packets. <p>A flow table entry is identified by its match fields and priority: the match fields and priority taken together identify a unique flow entry in the flow table. The flow entry that wildcards all fields (all fields omitted) and has priority equal 0 is called the table-miss flow entry (see 5.4).</p> <h3>5.3 Matching</h3> <pre> graph TD Start[Packet In Start at table 0] --> Match{Match in table n?} Match -- Yes --> Update[Update counters Execute instructions: • update action set • update packet/match set fields • update metadata] Update --> Goto{Goto-Table n?} Goto -- Yes --> TableN[] Goto -- No --> Execute[Execute action set] Update --> TableMiss{Table-miss flow entry exists?} TableMiss -- Yes --> Update TableMiss -- No --> Drop[Drop packet] </pre> <p>Figure 3: Flowchart detailing packet flow through an OpenFlow switch.</p> <p>On receipt of a packet, an OpenFlow Switch performs the functions shown in Figure 3. The switch starts by performing a table lookup in the first flow table, and based on pipeline processing, may perform table lookups in other flow tables (see 5.1).</p>

No.	'111 Patent Claim 1	The Reference
		<p>OpenFlow at 16</p> <h3>5.9 Instructions</h3> <p>Each flow entry contains a set of instructions that are executed when a packet matches the entry. These instructions result in changes to the packet, action set and/or pipeline processing.</p> <p>A switch is not required to support all instruction types, just those marked “<i>Required Instruction</i>” below. The controller can also query the switch about which of the “<i>Optional Instruction</i>” it supports.</p> <ul style="list-style-type: none"> • <i>Optional Instruction: Meter meter_id</i>: Direct packet to the specified meter. As the result of the metering, the packet may be dropped. • <i>Optional Instruction: Apply-Actions action(s)</i>: Applies the specific action(s) immediately, without any change to the Action Set. This instruction may be used to modify the packet between two tables or to execute multiple actions of the same type. The actions are specified as an action list (see 5.11). • <i>Optional Instruction: Clear-Actions</i>: Clears all the actions in the action set immediately. • <i>Required Instruction: Write-Actions action(s)</i>: Merges the specified action(s) into the current action set (see 5.10). If an action of the given type exists in the current set, overwrite it, otherwise add it. • <i>Optional Instruction: Write-Metadata metadata / mask</i>: Writes the masked metadata value into the metadata field. The mask specifies which bits of the metadata register should be modified (i.e. new_metadata = old_metadata & ~mask value & mask). <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit’s ’111 patent, including use of an overlay controller. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan ’478 • Wang ’735 • Olofsson ’254 • Kumar ’739 <p>Khan ’478 at Abstract (“A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist</p>

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		<p>exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.”)</p> <p>Khan '478 at Figure 1</p> <p>FIG. 1</p> <p>Khan '478 at 3:1-7 (“The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference exhibit 2019</p>

No.	'111 Patent Claim 1	The Reference
		<p>numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.”)</p> <p>Khan '478 at 3:49-57 (“Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).”)</p> <p>Khan '478 at 4:1-17 (“In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described.</p> <p>In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA].</p> <p>The OMP may be configured to handle overlay routes and transport locators (TLOCs).”)</p> <p>Khan '478 at 4:47-60 (“Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Khan '478 at 6:15-24 (“In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI).</p> <p>In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p> <p>Khan '478 at 9:53-10:4 (“In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <p>Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network;</p> <p>Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and</p> <p>Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.”)</p> <p>Wang '735 at Abstract (“In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Wang '735 at 4:30-45 ("The network device 12 may be, for example, a router (e.g., ISR, ASR), integrated router/switch, or any other network device configured for routing traffic. The router 12 may be an Internet-edge router in communication with an access switch or located at a branch office or data center, for example. The router 12 may be configured to enforce network policies, TCP throttling, provide network assessment/feedback, shape traffic (e.g., up/down speed, intelligent dropping), dynamically adjust queue bandwidth, or provide differentiated services, for example. The router 12 may also be operable to detect a performance issue, network status change (up or down), switch route, or perform preemption. In one or more embodiments, the router 12 is configured for Performance Routing (PfR), which is used to select a next hop to deliver the application traffic based on application performance measurement results and network resource status.")</p> <p>Wang '735 at 7:14-38 ("The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and RSVPmetadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example, bandwidth reservation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS (Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization system.")</p> <p>Wang '735 at 7:55-8:12 ("The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The</p>

No.	'111 Patent Claim 1	The Reference
		<p>policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all network devices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to manage and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p> <p>Wang '735 at 10:53-11:10 (“In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the application flows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Protocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to</p>

No.	'111 Patent Claim 1	The Reference
		<p>regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth.”)</p> <p>Olofsson '254 at Abstract (“A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.”)</p> <p>Olofsson '254 at Figure 1</p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p style="text-align: center;">..... DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

No.	'111 Patent Claim 1	The Reference
	<p>Controller establishes secure control channels with all associated network elements</p> <p>Service-hosting locations advertise service type and associated label to controller</p> <p>Policies are created on controller associating traffic from sites with services</p> <p>Policies for outbound traffic are pushed to edge-nodes and hub nodes</p> <p>Policies for inbound policies are pushed to service locations</p>	<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 ("In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100 using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated</p>

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No.	'111 Patent Claim 1	The Reference
		<p>overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Olofsson '254 at 6:36-59 ("Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <p>Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges).</p> <p>Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102.</p> <p>Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them.</p> <p>The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service.</p> <p>Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain.</p> <p>Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.")</p> <p>Olofsson '254 at 7:22-28 ("In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.")</p> <p>Kumar '739 at Abstract ("Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one is configured to identify one or more service-functions hosted by each of the service nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Kumar '739 at 3:66-4:12 ("The service controller 20 comprises service-function chaining logic 70 and the classifier 30 comprises classification and mapping logic 75. The service nodes 35, 40, 45, 50, and 55 each comprise advertisement logic 80. In operation, the advertisement logic 80 at each of the service nodes 35, 40, 45, 50, and 55 is configured to generate an advertisement or notification that indicates the service-functions that the respective service node hosts (i.e., each service node exposes its service-functions to the central service controller). For example, the advertisement logic 80 at service node 35 may generate an advertisement 85 indicating that the service node 35 hosts service-functions f1, f2, and f3. The advertisement 85 may then be provided to service controller 20 and/or classifier 30")</p> <p>Kumar '739 at 4:13-26 ("The service-function chaining logic 70 at service controller 20 is configured to define one or more "service-function chains" (SFCs) for selection by the classification and mapping logic 75 of classifier 30. As used herein, a "service-function chain" is an ordered list of service-functions defined in terms of the service-functions to be applied, and not in terms of service nodes that apply service-functions (i.e., the service-function chain is not defined in terms of network addresses for devices that host service-functions). More specifically, the location information of service nodes that host service-functions is not part of the defined service-function chain. Rather, as described further below, the selection of the location where the service-functions are available is performed at the classifier 30.")</p> <p>Kumar '739 at 4:64-5:3 ("In the example of FIG. 2, classifier 30 intercepts traffic 90 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 90 should be steered through service-function chain SFC1 that comprises ordered service-functions f1, f2, f6, f7, and f10.")</p> <p>Kumar '739 at 6:21-31 ("As noted, service controller 20 may receive advertisements from service nodes 35, 40, 45, 50, and 55. Based on these advertisements, the service controller 20 determines that service-function f7 is hosted at service node 40. The service controller 20 also determines that service-function f7 is a modifying service-function (i.e., service-function f7 is capable of changing the flow specification of processed traffic). In the example of FIG. 2, classifier 30 intercepts traffic 90 and performs classification and mapping based on the information received from service controller 20. Classifier 30 then selects the appropriate service-function chain to steer traffic 90 through. In this case, classifier 30 selects SFC1, which includes service-functions f1, f2, f6, f7, and f10. The traffic is then processed by these functions in sequence. Finally, the modified traffic is sent to the destination via interface 100. The entire process is controlled by the service controller 20, which manages the service-functions and their locations across the network nodes 35, 40, 45, 50, and 55. The service controller 20 also manages the advertisements sent by the nodes to indicate the presence and location of the service-functions. The classifier 30 then uses this information to make intelligent decisions about how to route traffic based on specific rules and policies defined by the service controller 20. This allows for a highly flexible and efficient network architecture where traffic can be dynamically rerouted and modified based on real-time conditions and user requirements. The use of a central service controller and distributed service-functions provides a robust and scalable solution for managing complex network traffic flows."))</p>

No.	'111 Patent Claim 1	The Reference
		<p>3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.”)</p> <p>Kumar '739 at 6:56-62 (“In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.”)</p> <p>Kumar '739 at 8:7-22 (“FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522. Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.”)</p>
1[b]	receiving, by the network node from the controller, the instruction and the criterion;	<p>The Reference discloses receiving, by the network node from the controller, the instruction and the criterion.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p>

No.	'111 Patent Claim 1	The Reference
		<p>OpenFlow at 11</p> <p>(a) Packets are matched against multiple tables in the pipeline</p> <p>OpenFlow Switch</p> <p>Packet In → Table 0 → Table 1 → ... → Table n → Execute Action Set → Packet Out</p> <p>Match fields: Ingress port + metadata + pkt hdrs Action set</p> <p>① Find highest-priority matching flow entry ② Apply instructions: i. Modify packet & update match fields (apply actions instruction) ii. Update action set (clear actions and/or write actions instructions) iii. Update metadata ③ Send match data and action set to next table</p> <p>(b) Per-table packet processing</p> <p>Figure 2: Packet flow through the processing pipeline</p> <p>OpenFlow at 12</p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"> Match Fields Priority Counters Instructions Timeouts Cookie </p> <p style="text-align: center;">Table 1: Main components of a flow entry in a flow table.</p> <ul style="list-style-type: none"> • match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. • priority: matching precedence of the flow entry • counters: to update for matching packets • instructions to modify the action set or pipeline processing • timeouts: maximum amount of time or idle time before flow is expired by the switch • cookie: opaque data value chosen by the controller. May be used by the controller to filter flow statistics, flow modification and flow deletion, not used when processing packets. <p>A flow table entry is identified by its match fields and priority: the match fields and priority taken together identify a unique flow entry in the flow table. The flow entry that wildcards all fields (all fields omitted) and has priority equal 0 is called the table-miss flow entry (see 5.4).</p> <h3>5.3 Matching</h3> <pre> graph TD Start[Packet In Start at table 0] --> Match{Match in table n?} Match -- Yes --> Update[Update counters Execute instructions: • update action set • update packet/match set fields • update metadata] Update --> Goto{Goto-Table n?} Goto -- Yes --> TableN[] Goto -- No --> Execute[Execute action set] Update --> TableMiss{Table-miss flow entry exists?} TableMiss -- Yes --> Update TableMiss -- No --> Drop[Drop packet] </pre> <p>Figure 3: Flowchart detailing packet flow through an OpenFlow switch.</p> <p>On receipt of a packet, an OpenFlow Switch performs the functions shown in Figure 3. The switch starts by performing a table lookup in the first flow table, and based on pipeline processing, may perform table lookups in other flow tables (see 5.1).</p>

No.	'111 Patent Claim 1	The Reference
		<p>OpenFlow at 16</p> <h3>5.9 Instructions</h3> <p>Each flow entry contains a set of instructions that are executed when a packet matches the entry. These instructions result in changes to the packet, action set and/or pipeline processing.</p> <p>A switch is not required to support all instruction types, just those marked “<i>Required Instruction</i>” below. The controller can also query the switch about which of the “<i>Optional Instruction</i>” it supports.</p> <ul style="list-style-type: none"> • <i>Optional Instruction: Meter meter_id</i>: Direct packet to the specified meter. As the result of the metering, the packet may be dropped. • <i>Optional Instruction: Apply-Actions action(s)</i>: Applies the specific action(s) immediately, without any change to the Action Set. This instruction may be used to modify the packet between two tables or to execute multiple actions of the same type. The actions are specified as an action list (see 5.11). • <i>Optional Instruction: Clear-Actions</i>: Clears all the actions in the action set immediately. • <i>Required Instruction: Write-Actions action(s)</i>: Merges the specified action(s) into the current action set (see 5.10). If an action of the given type exists in the current set, overwrite it, otherwise add it. • <i>Optional Instruction: Write-Metadata metadata / mask</i>: Writes the masked metadata value into the metadata field. The mask specifies which bits of the metadata register should be modified (i.e. new_metadata = old_metadata & ~mask value & mask). <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit’s ’111 patent, including use of an overlay controller. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan ’478 • Wang ’735 • Olofsson ’254 • Kumar ’739 <p>Khan ’478 at Abstract (“A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist</p>

No.	'111 Patent Claim 1	The Reference
		<p>exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.”)</p> <p>Khan '478 at Figure 1</p> <p>FIG. 1</p> <p>Khan '478 at 3:1-7 (“The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference exhibit 2019</p>

No.	'111 Patent Claim 1	The Reference
		<p>numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.”)</p> <p>Khan '478 at 3:49-57 (“Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).”)</p> <p>Khan '478 at 4:1-17 (“In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described.</p> <p>In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA].</p> <p>The OMP may be configured to handle overlay routes and transport locators (TLOCs).”)</p> <p>Khan '478 at 4:47-60 (“Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Khan '478 at 6:15-24 (“In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI).</p> <p>In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p> <p>Khan '478 at 9:53-10:4 (“In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <p>Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network;</p> <p>Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and</p> <p>Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.”)</p> <p>Wang '735 at Abstract (“In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Wang '735 at 4:30-45 ("The network device 12 may be, for example, a router (e.g., ISR, ASR), integrated router/switch, or any other network device configured for routing traffic. The router 12 may be an Internet-edge router in communication with an access switch or located at a branch office or data center, for example. The router 12 may be configured to enforce network policies, TCP throttling, provide network assessment/feedback, shape traffic (e.g., up/down speed, intelligent dropping), dynamically adjust queue bandwidth, or provide differentiated services, for example. The router 12 may also be operable to detect a performance issue, network status change (up or down), switch route, or perform preemption. In one or more embodiments, the router 12 is configured for Performance Routing (PfR), which is used to select a next hop to deliver the application traffic based on application performance measurement results and network resource status.")</p> <p>Wang '735 at 7:14-38 ("The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and RSVPmetadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example, bandwidth reservation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS (Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization system.")</p> <p>Wang '735 at 7:55-8:12 ("The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The</p>

No.	'111 Patent Claim 1	The Reference
		<p>policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all network devices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to manage and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p> <p>Wang '735 at 10:53-11:10 (“In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the application flows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Protocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to</p>

No.	'111 Patent Claim 1	The Reference
		<p>regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth.”)</p> <p>Olofsson '254 at Abstract (“A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.”)</p> <p>Olofsson '254 at Figure 1</p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p style="text-align: center;">..... DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

No.	'111 Patent Claim 1	The Reference
	<p>Controller establishes secure control channels with all associated network elements</p> <p>Service-hosting locations advertise service type and associated label to controller</p> <p>Policies are created on controller associating traffic from sites with services</p> <p>Policies for outbound traffic are pushed to edge-nodes and hub nodes</p> <p>Policies for inbound policies are pushed to service locations</p>	<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 ("In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100 using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated</p>

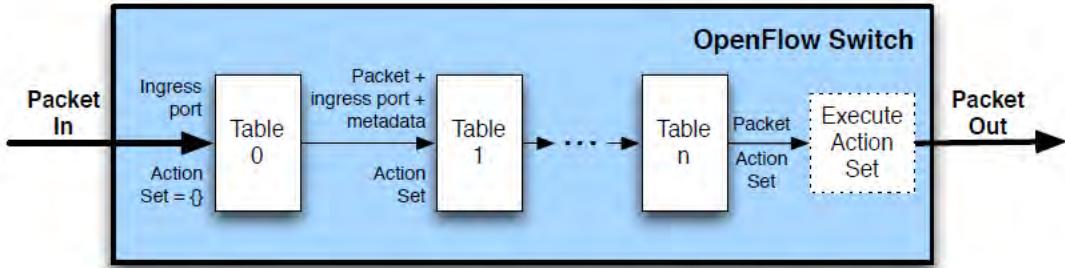
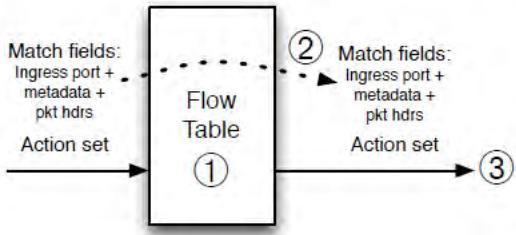
Orckit Exhibit 2019

No.	'111 Patent Claim 1	The Reference
		<p>overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Olofsson '254 at 6:36-59 ("Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <p>Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges).</p> <p>Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102.</p> <p>Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them.</p> <p>The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service.</p> <p>Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain.</p> <p>Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.")</p> <p>Olofsson '254 at 7:22-28 ("In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.")</p> <p>Kumar '739 at Abstract ("Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one is configured to identify one or more service-functions hosted by each of the service nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Kumar '739 at 3:66-4:12 ("The service controller 20 comprises service-function chaining logic 70 and the classifier 30 comprises classification and mapping logic 75. The service nodes 35, 40, 45, 50, and 55 each comprise advertisement logic 80. In operation, the advertisement logic 80 at each of the service nodes 35, 40, 45, 50, and 55 is configured to generate an advertisement or notification that indicates the service-functions that the respective service node hosts (i.e., each service node exposes its service-functions to the central service controller). For example, the advertisement logic 80 at service node 35 may generate an advertisement 85 indicating that the service node 35 hosts service-functions f1, f2, and f3. The advertisement 85 may then be provided to service controller 20 and/or classifier 30")</p> <p>Kumar '739 at 4:13-26 ("The service-function chaining logic 70 at service controller 20 is configured to define one or more "service-function chains" (SFCs) for selection by the classification and mapping logic 75 of classifier 30. As used herein, a "service-function chain" is an ordered list of service-functions defined in terms of the service-functions to be applied, and not in terms of service nodes that apply service-functions (i.e., the service-function chain is not defined in terms of network addresses for devices that host service-functions). More specifically, the location information of service nodes that host service-functions is not part of the defined service-function chain. Rather, as described further below, the selection of the location where the service-functions are available is performed at the classifier 30.")</p> <p>Kumar '739 at 4:64-5:3 ("In the example of FIG. 2, classifier 30 intercepts traffic 90 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 90 should be steered through service-function chain SFC1 that comprises ordered service-functions f1, f2, f6, f7, and f10.")</p> <p>Kumar '739 at 6:21-31 ("As noted, service controller 20 may receive advertisements from service nodes 35, 40, 45, 50, and 55. Based on these advertisements, the service controller 20 determines that service-function f7 is hosted at service node 40. The service controller 20 also determines that service-function f7 is a modifying service-function (i.e., service-function f7 is capable of changing the flow specification of processed traffic). In the example of FIG. 2, classifier 30 receives traffic 90 and classifies it based on the traffic's source and destination. Classifier 30 then selects a service-function chain to steer traffic 90 through. The selected service-function chain includes service-function f7, which is located at service node 40. The service controller 20 provides configuration information to service node 40, which then applies the modifications specified by service-function f7 to the traffic. The modified traffic is then sent to the next service function in the chain, which is service-function f6. This process continues until the traffic reaches its final destination or passes through all service functions in the chain.")</p>

No.	'111 Patent Claim 1	The Reference
		<p>3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.”)</p> <p>Kumar '739 at 6:56-62 (“In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.”)</p> <p>Kumar '739 at 8:7-22 (“FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522. Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.”)</p>
1[c]	receiving, by the network node from the first entity over the packet network, a packet addressed to the second entity;	<p>The Reference discloses receiving, by the network node from the controller, the instruction and the criterion.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p>

No.	'111 Patent Claim 1	The Reference
		<p>OpenFlow at 11</p>  <p>The diagram illustrates the packet flow through an OpenFlow Switch. A packet enters via an Ingress port, which is associated with Table 0. The table has an Action Set = {} and sends the packet along with its ingress port and metadata to Table 1. This process continues sequentially through Table 1, ..., Table n. Finally, the packet reaches the Execute Action Set, which performs the actions defined in the Action Set. The resulting packet then exits via the Packet Out port.</p> <p>(a) Packets are matched against multiple tables in the pipeline</p>  <p>This diagram shows the internal process for a single table. It starts with Match fields: Ingress port + metadata + pkt hdrs, leading to an Action set. Step ① identifies the highest-priority matching flow entry. Step ② applies instructions: i. Modify packet & update match fields (apply actions instruction), ii. Update action set (clear actions and/or write actions instructions), iii. Update metadata. Step ③ sends the match data and action set to the next table.</p> <p>(b) Per-table packet processing</p> <p>Figure 2: Packet flow through the processing pipeline</p>

No.	'111 Patent Claim 1	The Reference
		<p>The flow tables of an OpenFlow switch are sequentially numbered, starting at 0. Pipeline processing always starts at the first flow table: the packet is first matched against flow entries of flow table 0. Other flow tables may be used depending on the outcome of the match in the first table.</p> <p>When processed by a flow table, the packet is matched against the flow entries of the flow table to select a flow entry (see [5.3]). If a flow entry is found, the instruction set included in that flow entry is executed, those instructions may explicitly direct the packet to another flow table (using the Goto Instruction, see [5.9]), where the same process is repeated again. A flow entry can only direct a packet to a flow table number which is greater than its own flow table number, in other words pipeline processing can only go forward and not backward. Obviously, the flow entries of the last table of the pipeline can not include the Goto instruction. If the matching flow entry does not direct packets to another flow table, pipeline processing stops at this table. When pipeline processing stops, the packet is processed with its associated action set and usually forwarded (see [5.10]).</p> <p>If a packet does not match a flow entry in a flow table, this is a table miss. The behavior on a table miss depends on the table configuration (see [5.4]). A table-miss flow entry in the flow table may specify how to process unmatched packets: Options include dropping them, passing them to another table or sending them to the controller over the control channel via packet-in messages (see [6.1.2]).</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254 • Kumar '739 <p>Khan '478 at 3:58-67 (“ In one embodiment, secure tunnels may be established between one overlay edge router (OER) and another overlay edge router (OER). For example, reference numeral 124 shows a secure tunnel that may exist as an IPSec tunnel between the overlay edge router (OER) 102 and the overlay edge router (OER) 106. The tunnel 124 is through the transport network 120 and is used to transport data between its end points in a secure manner. The plurality of tunnels established between the various overlay edge routers (OERs) together form a secure overlay data plane (ODP).”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Khan '478 at 4:18-26 (“ In one embodiment, an overlay route may include the prefixes that establish reachability between endpoints. An overlay route may represent services in a central data center, services at a branch office or collections of hosts and other endpoints in any location of the overlay network. An overlay route may require and resolve onto TLOCs for functional forwarding. In comparison with BGP, an overlay route may be considered to be the equivalent of a prefix carried in any of the BGP AFI/SAFI constructs.”)</p> <p>Khan '478 at 4:27-37 (“ In one embodiment, a transport locator (TLOC) ties an overlay route to a physical location. The TLOC is the only visible entity of the OMP routing domain to the underlying transport network 120, and is reachable via routing in the transport network 120. A TLOC can be directly reachable via an entry in the routing table of the physical network or be represented by a prefix residing on the outside of a NAT device, also present in the aforementioned routing table. The TLOC acts as the next-hop for overlay routes, to continue the BGP-analogy.”)</p> <p>Khan '478 at 5:53-61 (“ TLOCs (Transport locations) are the location ids, e.g. a WAN interface connecting into a carrier. TLOCs are denoted by {System-IP, Link-color} as described below. The reason for not using an interface IP address to denote a TLOC is that IP addresses can move or change (e.g. if it is DHCP assigned). Using {system-IP, color} to denote TLOCs ensures that a transport endpoint can be identified irrespective of the interface IP addressing.”)</p> <p>Wang '735 at 3:41-64 (“The embodiments described herein operate in the context of a data communication system including multiple network elements. Referring now to the drawings, and first to FIG. 1, an example of a network in which embodiments described herein may be implemented is shown. The communication system comprises a plurality of endpoints 10 in communication through a plurality of network devices (e.g., routers) 12 and over networks 14. The communication system may include any number of networks (e.g., local area network, metropolitan area network, wide area network, enterprise network, Internet, intranet, radio access network, public switched network, or any other network or combination of networks). The flow path between the endpoints 10 may include any number or type of intermediate nodes (e.g., routers, switches, gateways, management stations, appliances, or other network devices), which facilitate passage of data between the</p>

No.	'111 Patent Claim 1	The Reference
		<p>endpoints. Also, there may be any number of endpoints 10. The endpoints 10 may be located at a branch office, for example, and in communication with an ISR (Inte-grated Services Router) 12 connected to a WAN access link 13. The ISRs may be in communication with ASRs (Aggre-gated Services Router) 12 operating at the network edge, for example. The routers 12 communicate over a wide area net-work.”)</p> <p>Wang '735 at 4:9-29 (“The endpoints 10 are configured to originate or terminate communications over the network. The endpoints 10 may be any device or combination of devices configured for receiv-ing, transmitting, or receiving and transmitting traffic. As described below, the network device 12 receives application traffic from one or more endpoints. This includes, for example, receiving traffic from one or more upstream net- work devices. Traffic may include audio, video, text, or other data or combination thereof. The endpoint 10 may be, for example, a server that stores media locally or receives the media from another server or media source via another net-work, satellite, cable, or any other communication device. The endpoint 10 may also be, for example, a personal com-puter, set-top box, personal digital assistant (PDA), VoIP phone, tablet, Internet connected television, cellular tele-phone, TelePresence device, media center device, or any other network device that receives or transmits packets. As described below, the endpoints 10 may be configured for FEC (Forward Error Correction), rate-adaptation, error conceal-ment, RTCP (Real-time Transport Control Protocol) feed-back or other protocols or technologies.”)</p> <p>Wang '735 at 4:30-45 (“The network device 12 may be, for example, a router (e.g., ISR, ASR), integrated router/switch, or any other network device configured forrouting traffic. The router 12 may be an Internet-edge router in communication with an access switch or located at a branch office or data center, for example. The router 12 may be configured to enforce network policies, TCP throttling, provide network assessment/feedback, shape traf-fic (e.g., up/down speed, intelligent dropping), dynamically adjust queue bandwidth, or provide differentiated services, for example. The router 12 may also be operable to detect a performance issue, network status change (up or down), switch route, or perform preemption. In one or more embodi-ments, the router 12 is configured for Performance</p>

No.	'111 Patent Claim 1	The Reference
		<p>Routing (PfR), which is used to select a next hop to deliver the application traffic based on application performance measurement results and network resource status.”)</p> <p>Wang '735 at 5:7-19 (“Nodes 12 configured with the optimization system 18 are preferably operable to automatically discover other nodes configured with the optimization system on the media path. Notifications and requests may be sent among these discovered optimization system devices 12 to effectively manage the media traffic bandwidth and to optimize the media application performance. The received notifications or requests from adjacent optimization system devices (upstream and downstream devices along the media path) can generate additional requests or notifications to other components (e.g., service routing, optimizer, policy manager, scheduler). The nodes 12 may use RSVP (Resource Reservation Protocol) for communication with one another, for example.”)</p> <p>Olofsson '254 at Figure 4</p>

No.	'111 Patent Claim 1	The Reference
		<pre> graph TD A[Hub2 advertises Service information: (Service-Type, Label) to Controller] --> B[Controller: create policy(in) for Edge-sites: For Classified Traffic destined for any endpoint, use Hub2 Address and Service Label. Then advertise.] A --> C[Controller: create policy(in) for Service Locations: For received Traffic arriving tagged with Service-Label, forward traffic to service-element. Then advertise] A --> D[Controller: Create policy(out) for Service Locations: For traffic arriving from a service, send to specified location. Then advertise] B --> E[Edge-location: Install policy(in) as received from controller and apply to incoming traffic sourced from the local network] C --> F[Service-location: Install policy(in) as received from controller and apply to incoming traffic from outside network tagged with service-label] D --> G[Service-location: Install policy(out) as received from controller and apply to traffic from local service-element] </pre> <p>FIG. 4</p> <p>Olofsson '254 at Figure 5</p>

No.	'111 Patent Claim 1	The Reference
	<p>C1 Hub2 and Edge3 has advertised Service-types and associated labels to controller</p> <p>C2 Controller has created and distributed policies to all network elements in the network for enforcing a policy for traffic to/from Network entry/exit point(Hub3)</p> <p>C3 Every network element has also distributed and received information to/from controller on how to reach every endpoint in the network</p> <p><i>FIG. 5</i></p>	<p>Edge1 receives a packet destined for Network Entry/Exit point. Local policy tells Edge1 that the path to reach Hub3 is through Hub2 500</p> <p>Edge1 encapsulates the packet with information containing the address of Hub2 and the Label for the Service at Hub2, then sends it off 502</p> <p>Hub2 receives the packet from Hub2 with the Service-label. The service label is removed and forwarded to the service-element 504</p> <p>Hub2 later receives the packet from the service-element. Local policy dictates that traffic destined for Hub3 should use the address of Hub3 506</p> <p>Hub2 encapsulates the packet with information containing the address of Hub3, then sends it off 510</p>

Olofsson '254 at 4:60-5:28 ("The setup and distribution of policies will take place as follows:

Block 400: Hub2 advertises Service information: (Service-Type, Label) to the controller 102.

Block 402: The overlay controller 102 constructs the set of required policies and distributes them accordingly. This means that each node involved will be assigned a policy for managing the required traffic flow.

Block 404: Every Edge-router receives an outbound policy (for traffic towards the Internet) stipulating that all the traffic matching the routes received from Hub3, will be encapsulated

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No.	'111 Patent Claim 1	The Reference
		<p>in a packet with a Service-label of 1, matching the Firewall Service, and a next-hop address of Hub2. This will ensure all traffic destined for the Internet is using the tunnel from the Edge-router to Hub2</p> <p>Block 406: The controller 102 creates a policy (in) for Service Locations: For received Traffic arriving tagged with Service-Label, forward traffic to service-element and advertises the policy thus created to each hub.</p> <p>Block 408: Hub2 receives an inbound policy (for traffic received on its external interface), stipulating that all received traffic matching Service Label 1 is sent to the firewall. Exactly how the traffic is forwarded is a local decision.</p> <p>Block 410: Hub2 receives an outbound policy (for traffic received from the Firewall) stipulating that the traffic destined for the Internet must be encapsulated with a next-hop address of Hub3, taking the traffic to the Internet. Any traffic destined for an Edge-router in the overlay network is sent with an encapsulation of that Edge-routers next-hop address</p> <p>Block 412: Hub3 receives an inbound policy (for traffic received from the Internet) stipulating that all traffic matching a destination advertised from an Edge-router is encapsulated in a packet with a Service-label of 1, matching the Firewall Service, and a next-hop address of Hub2”)</p> <p>Olofsson '254 at 5:33-67 (“Using the same setup as in the prior example, but adding that all traffic returning from the Internet must not only pass through the Firewall Service, but also pass through an Intrusion Detection Service, the modification required to the infrastructure is the following:</p> <p>Hub1 hosts the Intrusion Detection service and advertises a service with a Label of 2 and a Service-type of Intrusion Detection.</p> <p>The changes in setup and distribution of policies is following:</p> <p>Hub2, hosting the firewall service, has one addition to its outbound policy. All traffic destined for any Edge-router in the overlay network must be encapsulated with a Service-label of 2 and a next-hop address of Hub1.</p>

No.	'111 Patent Claim 1	The Reference
		<p>Hub1 is equipped with policies identical to what Hub2 had in the previous example, with the differences being that the label matching is done on Service-label 2 for inbound traffic. FIG. 5 shows a flowchart corresponding to this example of service chaining. Referring the FIG. 5, C1 to C3 are conditions and the processing blocks are as provided below:</p> <p>Block 500: Edge1 receives a packet destined for Network Entry/Exit point. Local policy tells Edge1 that the path to reach Hub3 is through Hub2</p> <p>Block 502: Edge1 encapsulates the packet with information containing the address of Hub2 and the Label for the Service at Hub2, then sends it off.</p> <p>Block 504: Hub2 receives the packet from Hub1 with the Service-label. The service label is removed and forwarded to the service-element.</p> <p>Block 506 Hub2 later receives the packet from the service-element. Local policy dictates that traffic destined for Hub3 should use the address of Hub3.</p> <p>Block 508 Hub2 encapsulates the packet with information containing the address of Hub3, then sends it off.”)</p> <p>Kumar '739 at 5:34-51 (“In the example of FIG. 2, classification and mapping logic 75 selects service-functions from several different service nodes. In particular, classification and mapping logic 75 selects service-functions f1 and f2 at service node 35, service-functions f6 and f7 at service node 40, and service-function f10 at service node 45. The path for service-function chain SFC1 selected by classification and mapping logic 75 is shown in FIG. 2 by broken line 100. The classifier 30 sends traffic 90 along the path 100 using one or more L2/L3/L4 service overlays in the network. In other words, a service header is appended to the traffic 90 for forwarding through the service chain and the service header enables the carrying of service metadata in addition to the original data/payload.</p> <p>Service-function f10 is the end of the service-function chain SFC1. After processing the traffic 90, the service-function f10 may forward the traffic 90 to its original or other destination.”)</p>

No.	'111 Patent Claim 1	The Reference
		<p>Kumar '739 at 7:11-25 ("The path for service-function sub-chain SFC3a selected by classification and mapping logic 75 is shown in FIG. 3 by broken line 160, while the path for service-function sub-chain SFC3b selected by classification and mapping logic 75 is shown in FIG. 3 by broken line 170. The classifier 30 sends traffic 150 along the path 160 using one or more L2/L3/L4 service overlays in the network. Once the traffic 150 is serviced by f7, the traffic 150 is re-classified at classifier 130 to choose SFC3b and is steered through f10.</p> <p>After processing the traffic 150, the service-function f10 may forward the traffic 150 to its original or other destination. In the above example, the first primary classifier 30 only performs mapping for the first sub-chain SFC3a while the second primary classifier 130 performs mapping for the second sub-chain SFC3b.")</p>
1[d]	checking, by the network node, if the packet satisfies the criterion;	<p>The Reference discloses checking, by the network node, if the packet satisfies the criterion.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p><u>OpenFlow at 6-7</u></p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"> OpenFlow Switch </p> <p>Figure 1: Main components of an OpenFlow switch.</p> <h2>2 Switch Components</h2> <p>An OpenFlow Switch consists of one or more <i>flow tables</i> and a <i>group table</i>, which perform packet lookups and forwarding, and an <i>OpenFlow channel</i> to an external controller (Figure 1). The switch communicates with the controller and the controller manages the switch via the OpenFlow protocol.</p> <p>Using the OpenFlow protocol, the controller can add, update, and delete <i>flow entries</i> in flow tables, both reactively (in response to packets) and proactively. Each flow table in the switch contains a set of flow entries; each flow entry consists of <i>match fields</i>, <i>counters</i>, and a set of <i>instructions</i> to apply to matching packets (see 5.2).</p> <p>Matching starts at the first flow table and may continue to additional flow tables (see 5.1). Flow entries match packets in priority order, with the first matching entry in each table being used (see 5.3). If a matching entry is found, the instructions associated with the specific flow entry are executed. If no match is found in a flow table, the outcome depends on configuration of the table-miss flow entry: for example, the packet may be forwarded to the controller over the OpenFlow channel, dropped, or may continue to the next flow table (see 5.4).</p> <p>Instructions associated with each flow entry either contain actions or modify pipeline processing (see 5.9). Actions included in instructions describe packet forwarding, packet modification and group table</p> <p style="text-align: center;">OpenFlow at 11</p>

No.	'111 Patent Claim 1	The Reference
		<p>The diagram illustrates the packet flow through an OpenFlow switch pipeline and per-table processing.</p> <p>(a) Packets are matched against multiple tables in the pipeline: A packet enters the "OpenFlow Switch" via an "Ingress port". It is processed by a sequence of tables: Table 0, Table 1, ..., Table n. The process involves matching the packet against the ingress port, metadata, and previous actions, and then applying an action set. The resulting packet + ingress port + metadata is passed to the next table in the sequence.</p> <p>(b) Per-table packet processing: A flow table (labeled ①) processes the packet. It matches the packet against match fields (ingress port + metadata + pkt hdrs) and applies an action set. The steps are:</p> <ol style="list-style-type: none"> ① Find highest-priority matching flow entry ② Apply instructions: <ol style="list-style-type: none"> i. Modify packet & update match fields (apply actions instruction) ii. Update action set (clear actions and/or write actions instructions) iii. Update metadata ③ Send match data and action set to next table

Figure 2: Packet flow through the processing pipeline

No.	'111 Patent Claim 1	The Reference
		<p>The flow tables of an OpenFlow switch are sequentially numbered, starting at 0. Pipeline processing always starts at the first flow table: the packet is first matched against flow entries of flow table 0. Other flow tables may be used depending on the outcome of the match in the first table.</p> <p>When processed by a flow table, the packet is matched against the flow entries of the flow table to select a flow entry (see [5.3]). If a flow entry is found, the instruction set included in that flow entry is executed, those instructions may explicitly direct the packet to another flow table (using the Goto Instruction, see [5.9]), where the same process is repeated again. A flow entry can only direct a packet to a flow table number which is greater than its own flow table number, in other words pipeline processing can only go forward and not backward. Obviously, the flow entries of the last table of the pipeline can not include the Goto instruction. If the matching flow entry does not direct packets to another flow table, pipeline processing stops at this table. When pipeline processing stops, the packet is processed with its associated action set and usually forwarded (see [5.10]).</p> <p>If a packet does not match a flow entry in a flow table, this is a table miss. The behavior on a table miss depends on the table configuration (see [5.4]). A table-miss flow entry in the flow table may specify how to process unmatched packets: Options include dropping them, passing them to another table or sending them to the controller over the control channel via packet-in messages (see [6.1.2]).</p> <p>OpenFlow at 12</p>

No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;">Match Fields Priority Counters Instructions Timeouts Cookie</p> <p style="text-align: center;">Table 1: Main components of a flow entry in a flow table.</p> <ul style="list-style-type: none"> • match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. • priority: matching precedence of the flow entry • counters: to update for matching packets • instructions to modify the action set or pipeline processing • timeouts: maximum amount of time or idle time before flow is expired by the switch • cookie: opaque data value chosen by the controller. May be used by the controller to filter flow statistics, flow modification and flow deletion, not used when processing packets. <p>A flow table entry is identified by its match fields and priority: the match fields and priority taken together identify a unique flow entry in the flow table. The flow entry that wildcards all fields (all fields omitted) and has priority equal 0 is called the table-miss flow entry (see [5.4]).</p> <h3>5.3 Matching</h3> <pre> graph TD A[Packet In Start at table 0] --> B{Match in table n?} B -- Yes --> C[Update counters Execute instructions: • update action set • update packet/match set fields • update metadata] C --> D{Goto-Table n?} D -- Yes --> E[Execute action set] D -- No --> F[Execute action set] B -- No --> G{Table-miss flow entry exists?} G -- Yes --> E G -- No --> H[Drop packet] </pre> <p>Figure 3: Flowchart detailing packet flow through an OpenFlow switch.</p> <p>On receipt of a packet, an OpenFlow Switch performs the functions shown in Figure 3. The switch starts by performing a table lookup in the first flow table, and based on pipeline processing, may perform table lookups in other flow tables (see [5.1]).</p>

No.	'111 Patent Claim 1	The Reference
		<p>Balakrishnan at [0010] (“In an embodiment of the invention, a system for managing network traffic in a network device is provided. The system includes means for providing a plurality of objects associated with a packet of the network traffic; and means for correspondingly matching at least one object of the network device with at least one object associated with the packet.”).</p> <p>Balakrishnan at [0021] (“At step 106, the packet is accepted, if the plurality of objects satisfies the set of criteria. For example, the set of criteria may include a ‘set criterion’ such that the packets having the TCP flags syn and ack set, are accepted. Further, the set of criteria may include a ‘not-set criterion’, for example, the packets having TCP flag fin not set are accepted.”).</p> <p>Balakrishnan at [0022] (“At step 202, a network device having a plurality of control list objects is provided. The control list objects may be, for example, filtering TCP packets in an Access Control List (ACL). The ACL is generally implemented as a data structure such as a tree that has the information (to be used by the network operating system) to determine the access rights of each packet. At step 204, at least one control list object of the network device is correspondingly matched with at least one object of the application. The matching is performed based on the objects being present or not present in the application. The matching criteria may be at least one of a ‘set criterion’ or a ‘not-set criterion’, as described in conjunction with FIG. 1. In an embodiment of the invention, a ‘+’ sign is prefixed to a control list object in the ‘set criterion’, to signify that the packet is considered a match if the control list object is present in the packet. In another embodiment of the invention, a ‘-’ sign is prefixed to a control list object, to signify that the packet is considered a match if the control list object is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device.”).</p> <p>Balakrishnan at [0023] (“At step 302, a router including an ACL having match condition based on packet flags is provided. At step 304, a packet having packet flags is transmitted to an interface of the router that has an ACL. Subsequently, at least one packet flag of the packet is matched with at least one packet flag as specified in the ACL of the router, at step 306. The matching is performed based on the matching criteria for indicating that the packet is acceptable for transmitting to the network. If the packet flag(s) match the</p>

No.	'111 Patent Claim 1	The Reference
		<p>conditions in the ACL, then the packet is transmitted to the network. If the packet flag(s) do not satisfy the conditions in the ACL, the packet is not transmitted.”).</p> <pre> graph TD Start([Start]) --> Step1[Provide a plurality of objects associated with a packet of the network traffic] Step1 --> Step2[Create a set of criteria corresponding to the type of objects] Step2 --> Step3[Accept the packet if the plurality of objects satisfies the set of criteria] Step3 --> Stop([Stop]) style Step3 stroke:#008000,stroke-width:2px </pre> <p>FIG. 1 Balakrishnan Figure 1 (annotation added).</p>

No.	'111 Patent Claim 1	The Reference
		<pre> graph TD Start([Start]) --> A[Provide a network device having a plurality of control list objects. The matching criterion uses the "+" and "-" prefix on the control list object to be matched to specify if a match is based on that control list object being present in the packet or not present in the packet respectively.] A --> B[Match correspondingly at least one control list object of the network device with at least one object of an application based on the objects being present or not present in the application] B --> Stop([Stop]) </pre> <p>FIG. 2 Balakrishnan Figure 2 (annotation added).</p> <p>The flowchart illustrates a process starting with an oval labeled "Start". An arrow points down to a rectangular box containing the following text:</p> <p>Provide a network device having a plurality of control list objects. The matching criterion uses the "+" and "-" prefix on the control list object to be matched to specify if a match is based on that control list object being present in the packet or not present in the packet respectively.</p> <p>An annotation number "202" is placed to the right of this box, with a curved line pointing to the right edge of the box.</p> <p>From the bottom of this box, an arrow points down to another rectangular box containing the following text:</p> <p>Match correspondingly at least one control list object of the network device with at least one object of an application based on the objects being present or not present in the application</p> <p>An annotation number "204" is placed to the right of this box, with a curved line pointing to the right edge of the box.</p> <p>From the bottom of this box, an arrow points down to an oval labeled "Stop".</p>

No.	'111 Patent Claim 1	The Reference
		<pre> graph TD Start([Start]) --> 302[Provide a router including an access control lists having access flags. The matching criterion uses the "+" and "-" prefix on the access flags to be matched to specify if a match is based on that access flag being present in the packet or not present in the packet respectively.] 302 --> 304[Transmit a packet having packet flags to an access control list interface of the router] 304 --> 306[Match, in a corresponding one to one relationship, at least one packet flag of the packet with at least one access flag of the access control lists of the router for indicating that the packet is acceptable for transmitting to the network] 306 --> Stop([Stop]) </pre> <p>The flowchart illustrates a process for packet transmission and matching. It begins with a start node, followed by a step to provide a router with access control lists. The next step is to transmit a packet to the router's access control list interface. Finally, the process matches the packet flags against the access control lists to determine if the packet is acceptable for transmission.</p> <p>FIG. 3 Balakrishnan Figure 3 (annotation added).</p>

No.	'111 Patent Claim 1	The Reference
		<pre> graph TD Start([Start]) --> Provide[Provide a router including an access control lists having access flags. The matching criterion uses the "+" and "-" prefix on the access flags to be matched to specify if a match is based on that access flag being present in the packet or not present in the packet respectively.] Provide --> Receive[Receive a packet having packet flags via an access control list interface of the router] Receive --> Match[Match, in a corresponding one to one relationship, at least one packet flag of the packet with at least one access flag of the access control lists of the router for indicating that the packet is acceptable for transmitting to the router] Match --> Stop([Stop]) </pre> <p>The flowchart illustrates a process for packet matching. It begins with a 'Start' node, followed by a step where a router is provided with access control lists having access flags. The matching criterion uses '+' and '-' prefixes on the access flags to determine if a match is based on the access flag being present or not present in the packet. Next, a packet is received via an access control list interface of the router. Finally, the process matches at least one packet flag with at least one access flag of the access control lists to determine if the packet is acceptable for transmission.</p>
1[e]	responsive to the packet not satisfying	<p>The Reference discloses responsive to the packet not satisfying the criterion, sending, by the network node over the packet network, the packet to the second entity.</p> <p style="text-align: right;">Orckit Exhibit 2019</p>

No.	'111 Patent Claim 1	The Reference
	the criterion, sending, by the network node over the packet network, the packet to the second entity; and	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 11</p> <p>The diagram illustrates the packet flow through an OpenFlow Switch. It shows a sequence of tables (Table 0, Table 1, ..., Table n) and an Execute Action Set block. The process starts with a Packet In entering the Ingress port. The packet is processed by Table 0, which has an Action Set = {} and outputs a Packet + ingress port + metadata to Table 1. This pattern continues through Table 1, ..., Table n, and finally to the Execute Action Set block, which contains a Packet Action Set. The final output is a Packet Out.</p> <p>(a) Packets are matched against multiple tables in the pipeline</p> <p>The diagram also shows a detailed view of per-table processing:</p> <ul style="list-style-type: none"> Match fields: Ingress port + metadata + pkt hdrs Action set Flow Table (1) ② Match fields: Ingress port + metadata + pkt hdrs Action set ③ Send match data and action set to next table <p>(b) Per-table packet processing</p>

Figure 2: Packet flow through the processing pipeline

No.	'111 Patent Claim 1	The Reference
		<p>The flow tables of an OpenFlow switch are sequentially numbered, starting at 0. Pipeline processing always starts at the first flow table: the packet is first matched against flow entries of flow table 0. Other flow tables may be used depending on the outcome of the match in the first table.</p> <p>When processed by a flow table, the packet is matched against the flow entries of the flow table to select a flow entry (see [5.3]). If a flow entry is found, the instruction set included in that flow entry is executed, those instructions may explicitly direct the packet to another flow table (using the Goto Instruction, see [5.9]), where the same process is repeated again. A flow entry can only direct a packet to a flow table number which is greater than its own flow table number, in other words pipeline processing can only go forward and not backward. Obviously, the flow entries of the last table of the pipeline can not include the Goto instruction. If the matching flow entry does not direct packets to another flow table, pipeline processing stops at this table. When pipeline processing stops, the packet is processed with its associated action set and usually forwarded (see [5.10]).</p> <p>If a packet does not match a flow entry in a flow table, this is a table miss. The behavior on a table miss depends on the table configuration (see [5.4]). A table-miss flow entry in the flow table may specify how to process unmatched packets: Options include dropping them, passing them to another table or sending them to the controller over the control channel via packet-in messages (see [6.1.2]).</p> <p>OpenFlow at 12</p>

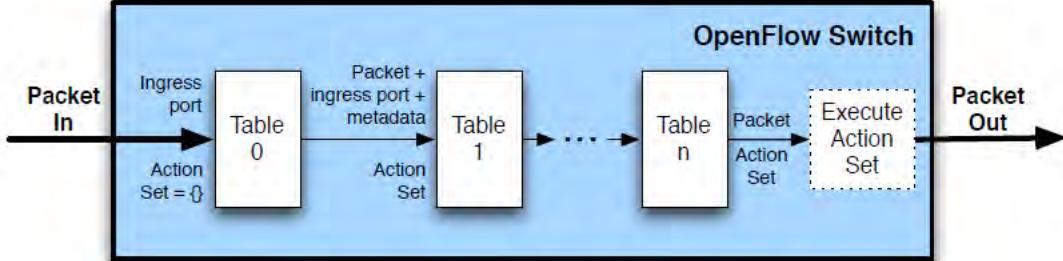
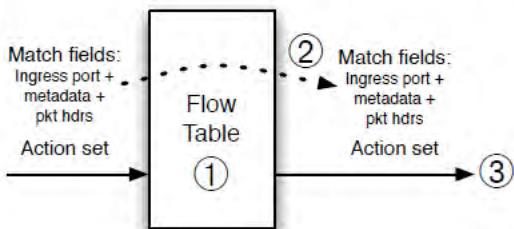
No.	'111 Patent Claim 1	The Reference
		<p style="text-align: center;"> Match Fields Priority Counters Instructions Timeouts Cookie </p> <p style="text-align: center;">Table 1: Main components of a flow entry in a flow table.</p> <ul style="list-style-type: none"> • match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. • priority: matching precedence of the flow entry • counters: to update for matching packets • instructions to modify the action set or pipeline processing • timeouts: maximum amount of time or idle time before flow is expired by the switch • cookie: opaque data value chosen by the controller. May be used by the controller to filter flow statistics, flow modification and flow deletion, not used when processing packets. <p>A flow table entry is identified by its match fields and priority: the match fields and priority taken together identify a unique flow entry in the flow table. The flow entry that wildcards all fields (all fields omitted) and has priority equal 0 is called the table-miss flow entry (see 5.4).</p> <h3>5.3 Matching</h3> <pre> graph TD Start[Packet In Start at table 0] --> Match{Match in table n?} Match -- Yes --> Update[Update counters Execute instructions: • update action set • update packet/match set fields • update metadata] Update --> Goto{Goto-Table n?} Goto -- Yes --> TableN[] Goto -- No --> Execute[Execute action set] Update --> TableMiss{Table-miss flow entry exists?} TableMiss -- Yes --> Update TableMiss -- No --> Drop[Drop packet] </pre> <p>Figure 3: Flowchart detailing packet flow through an OpenFlow switch.</p> <p>On receipt of a packet, an OpenFlow Switch performs the functions shown in Figure 3. The switch starts by performing a table lookup in the first flow table, and based on pipeline processing, may perform table lookups in other flow tables (see 5.1).</p>

No.	'111 Patent Claim 1	The Reference
1[f]	<p>responsive to the packet satisfying the criterion, sending the packet, by the network node over the packet network, to an entity that is included in the instruction and is other than the second entity.</p>	<p>The Reference discloses responsive to the packet satisfying the criterion, sending the packet, by the network node over the packet network, to an entity that is included in the instruction and is other than the second entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>Balakrishnan at [0020] ("In an embodiment of the invention, a '+' sign is prefixed to a packet flag in the 'set criterion', to signify that the packet is considered a match if the packet flag is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to a packet flag, to signify that the packet is considered a match if the packet flag is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device.").</p> <p>Balakrishnan at [0021] ("At step 106, the packet is accepted, if the plurality of objects satisfies the set of criteria. For example, the set of criteria may include a set criterion Such that the packets having the TCP flags syn and ack set, are accepted. Further, the set of criteria may include a not-set criterion, for example, the packets having TCP flag fin not set are accepted. In an embodiment of the invention, the accepted packet is transmitted to the network, if the accepted packet is an outgoing packet. In another embodiment of the invention, the accepted packet is transmitted to the network device, if the accepted packet is an incoming packet.").</p> <p>Balakrishnan at [0022] ("FIG. 2 is a flowchart illustrating a method for matching objects in an application with the corresponding objects of a network device, in accordance with an exemplary embodiment of the invention. In various embodiments of the invention, the matching of objects may be performed to manage network traffic in a network. The</p>

No.	'111 Patent Claim 1	The Reference
		<p>application may be a packet of the network traffic. At step 202, a network device having a plurality of control list objects is provided. The control list objects may be, for example, filtering TCP packets in an Access Control List (ACL). The ACL is generally implemented as a data structure such as a tree that has the information (to be used by the network operating system) to determine the access rights of each packet. At step 204, at least one control list object of the network device is correspondingly matched with at least one object of the application. The matching is performed based on the objects being present or not present in the application. The matching criteria may be at least one of a 'set criterion' or a 'not-set criterion', as described in conjunction with FIG. 1. In an embodiment of the invention, a '+' sign is prefixed to a control list object in the 'set criterion', to signify that the packet is considered a match if the control list object is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to a control list object, to signify that the packet is considered a match if the control list object is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device..").</p> <p>Balakrishnan et al [0023] ("FIG. 3 is a flowchart illustrating a method for determining if a packet is acceptable for transmitting to a network, in accordance with an exemplary embodiment of the invention. At step 302, a router including an ACL having match condition based on packet flags is provided. At step 304, a packet having packet flags is transmitted to an interface of the router that has an ACL. Subsequently, at least one packet flag of the packet is matched with at least one packet flag as specified in the ACL of the router, at step 306. The matching is performed based on the matching criteria for indicating that the packet is acceptable for transmitting to the network. If the packet flag(s) match the conditions in the ACL, then the packet is transmitted to the network. If the packet flag(s) do not satisfy the conditions in the ACL, the packet is not transmitted. The matching criteria may be at least one of a 'set criterion' or a 'not-set criterion', as described in conjunction with FIG. 1. In an embodiment of the invention, a '+' sign is prefixed to an access flag in the 'set criterion', to signify that the packet is considered a match if the access flag is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to an access flag, to signify that the packet is considered a match if the access flag is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device..").</p>

No.	'111 Patent Claim 1	The Reference
		Balakrishnan atat [0040] ("FIG. 4 is a flowchart illustrating a method for determining if a packet is allowed to transit a router, in accordance with an exemplary embodiment of the invention. At step 402, a router including an ACL having access flags is provided. At step 404, a packet having packet flags is received via an interface of the router that has an ACL. Subsequently, at least one packet flag of the packet is matched in a corresponding one-to-one relationship with at least one access flag of the ACL of the router, at step 406. The matching is performed based on the matching criteria for indicating that the packet is acceptable for transmitting to the router. If the packet flag(s) and the access flag(s) match, then the packet is transmitted to the router. If the packet flag(s) and the access flag(s) do not match, the packet is not transmitted. The matching criteria may be at least one of a 'set criterion' or a 'not-set criterion', as described in conjunction with FIG.1. In an embodiment of the invention, a '+' sign is prefixed to an access flag in the 'set criterion', to signify that the packet is considered a match if the access flag is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to an access flag, to signify that the packet is considered a match if the access flag is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device..").

No.	'111 Patent Claim 2	The Reference
2[a]	The method according to claim 1, wherein the instruction is 'probe', 'mirror', or 'terminate' instruction, and	<p>The Reference discloses the method according to claim 1, wherein the instruction is 'probe', 'mirror', or 'terminate' instruction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 11</p>

No.	'111 Patent Claim 2	The Reference
		 <p>(a) Packets are matched against multiple tables in the pipeline</p>  <p>(b) Per-table packet processing</p> <p>① Find highest-priority matching flow entry</p> <p>② Apply instructions:</p> <ul style="list-style-type: none"> i. Modify packet & update match fields (apply actions instruction) ii. Update action set (clear actions and/or write actions instructions) iii. Update metadata <p>③ Send match data and action set to next table</p> <p>Figure 2: Packet flow through the processing pipeline</p>

No.	'111 Patent Claim 2	The Reference
		<p>The flow tables of an OpenFlow switch are sequentially numbered, starting at 0. Pipeline processing always starts at the first flow table: the packet is first matched against flow entries of flow table 0. Other flow tables may be used depending on the outcome of the match in the first table.</p> <p>When processed by a flow table, the packet is matched against the flow entries of the flow table to select a flow entry (see [5.3]). If a flow entry is found, the instruction set included in that flow entry is executed, those instructions may explicitly direct the packet to another flow table (using the Goto Instruction, see [5.9]), where the same process is repeated again. A flow entry can only direct a packet to a flow table number which is greater than its own flow table number, in other words pipeline processing can only go forward and not backward. Obviously, the flow entries of the last table of the pipeline can not include the Goto instruction. If the matching flow entry does not direct packets to another flow table, pipeline processing stops at this table. When pipeline processing stops, the packet is processed with its associated action set and usually forwarded (see [5.10]).</p> <p>If a packet does not match a flow entry in a flow table, this is a table miss. The behavior on a table miss depends on the table configuration (see [5.4]). A table-miss flow entry in the flow table may specify how to process unmatched packets: Options include dropping them, passing them to another table or sending them to the controller over the control channel via packet-in messages (see [6.1.2]).</p> <p>OpenFlow at 12</p>

No.	'111 Patent Claim 2	The Reference
		<p style="text-align: center;"> Match Fields Priority Counters Instructions Timeouts Cookie </p> <p style="text-align: center;">Table 1: Main components of a flow entry in a flow table.</p> <ul style="list-style-type: none"> • match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. • priority: matching precedence of the flow entry • counters: to update for matching packets • instructions to modify the action set or pipeline processing • timeouts: maximum amount of time or idle time before flow is expired by the switch • cookie: opaque data value chosen by the controller. May be used by the controller to filter flow statistics, flow modification and flow deletion, not used when processing packets. <p>A flow table entry is identified by its match fields and priority: the match fields and priority taken together identify a unique flow entry in the flow table. The flow entry that wildcards all fields (all fields omitted) and has priority equal 0 is called the table-miss flow entry (see [5.4]).</p> <h3>5.3 Matching</h3> <pre> graph TD Start[Packet In Start at table 0] --> Match{Match in table n?} Match -- Yes --> Update[Update counters Execute instructions: • update action set • update packet/match set fields • update metadata] Update --> Goto{Goto-Table n?} Goto -- Yes --> TableN[] Goto -- No --> Execute[Execute action set] Update --> TableMiss{Table-miss flow entry exists?} TableMiss -- Yes --> Update TableMiss -- No --> Drop[Drop packet] </pre> <p>Figure 3: Flowchart detailing packet flow through an OpenFlow switch.</p> <p>On receipt of a packet, an OpenFlow Switch performs the functions shown in Figure 3. The switch starts by performing a table lookup in the first flow table, and based on pipeline processing, may perform table lookups in other flow tables (see [5.1]).</p>

No.	'111 Patent Claim 2	The Reference
		<p>OpenFlow at 13</p> <h4>5.4 Table-miss</h4> <p>Every flow table must support a table-miss flow entry to process table misses. The table-miss flow entry specifies how to process packets unmatched by other flow entries in the flow table (see 5.1), and may, for example send packets to the controller, drop packets or direct packets to a subsequent table.</p> <p>The table-miss flow entry is identified by its match and its priority (see 5.2), it wildcards all match fields (all fields omitted) and has the lowest priority (0). The match of the table-miss flow entry may fall outside the normal range of matches supported by a flow table, for example an exact match table would not support wildcards for other flow entries but must support the table-miss flow entry wildcarding all fields. The table-miss flow entry may not have the same capability as regular flow entry (see A.3.5.5). Implementations are encouraged to support for table-miss flow entries at minimum the same capability as the table-miss processing of previous versions of OpenFlow: send packets to the controller, drop packets or direct packets to a subsequent table.</p> <p>The table-miss flow entry behave in most ways like any other flow entry : it does not exist by default in a flow table, the controller may add it or remove it at any time (see 6.4), and it may expire (see 5.5). The table-miss flow entry matches packets in the table as expected from its set of match fields and priority (see 5.3), it matches packets unmatched by other flow entries in the flow table. The table-miss flow entry instructions are applied to packets matching the table-miss flow entry (see 5.9). If the table-miss flow entry directly sends packets to the controller using the CONTROLLER port (see 4.5), the packet-in reason must identify a table-miss (see A.4.1).</p> <p>If the table-miss flow entry does not exist, by default packets unmatched by flow entries are dropped (discarded). A switch configuration, for example using the OpenFlow Configuration Protocol, may override this default and specify another behaviour.</p>
2[b]	upon receiving by the network node the 'terminate' instruction, the method further comprising blocking, by the network node, the packet from being sent to the second entity and to the controller.	<p>The Reference discloses upon receiving by the network node the 'terminate' instruction, the method further comprising blocking, by the network node, the packet from being sent to the second entity and to the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, and Balakrishnan.</p>

No.	'111 Patent Claim 2	The Reference
		<p>Below are examples of such references.</p> <p>OpenFlow at 13</p> <p>5.4 Table-miss</p> <p>Every flow table must support a table-miss flow entry to process table misses. The table-miss flow entry specifies how to process packets unmatched by other flow entries in the flow table (see [5.1]), and may, for example send packets to the controller, drop packets or direct packets to a subsequent table.</p> <p>The table-miss flow entry is identified by its match and its priority (see [5.2]), it wildcards all match fields (all fields omitted) and has the lowest priority (0). The match of the table-miss flow entry may fall outside the normal range of matches supported by a flow table, for example an exact match table would not support wildcards for other flow entries but must support the table-miss flow entry wildcarding all fields. The table-miss flow entry may not have the same capability as regular flow entry (see [A.3.5.5]). Implementations are encouraged to support for table-miss flow entries at minimum the same capability as the table-miss processing of previous versions of OpenFlow: send packets to the controller, drop packets or direct packets to a subsequent table.</p> <p>The table-miss flow entry behave in most ways like any other flow entry : it does not exist by default in a flow table, the controller may add it or remove it at any time (see [6.4]), and it may expire (see [5.5]). The table-miss flow entry matches packets in the table as expected from its set of match fields and priority (see [5.3]), it matches packets unmatched by other flow entries in the flow table. The table-miss flow entry instructions are applied to packets matching the table-miss flow entry (see [5.9]). If the table-miss flow entry directly sends packets to the controller using the CONTROLLER port (see [4.5]), the packet-in reason must identify a table-miss (see [A.4.1]).</p> <p>If the table-miss flow entry does not exist, by default packets unmatched by flow entries are dropped (discarded). A switch configuration, for example using the OpenFlow Configuration Protocol, may override this default and specify another behaviour.</p> <p>Balakrishnan at [0020] (“In an embodiment of the invention, a ‘+’ sign is prefixed to a packet flag in the ‘set criterion’, to signify that the packet is considered a match if the packet flag is present in the packet. In another embodiment of the invention, a ‘-’ sign is prefixed to a packet flag, to signify that the packet is considered a match if the packet flag is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device.”).</p> <p>Balakrishnan at [0021] (“At step 106, the packet is accepted, if the plurality of objects satisfies the set of criteria. For example, the set of criteria may include a set criterion Such that the packets having the TCP flags syn and ack set, are accepted. Further, the set of</p>

No.	'111 Patent Claim 2	The Reference
		<p>criteria may include a not-set criterion, for example, the packets having TCP flag fin not set are accepted. In an embodiment of the invention, the accepted packet is transmitted to the network, if the accepted packet is an outgoing packet. In another embodiment of the invention, the accepted packet is transmitted to the network device, if the accepted packet is an incoming packet.”).</p> <p>Balakrishnan at [0022] (“FIG. 2 is a flowchart illustrating a method for matching objects in an application with the corresponding objects of a network device, in accordance with an exemplary embodiment of the invention. In various embodiments of the invention, the matching of objects may be performed to manage network traffic in a network. The application may be a packet of the network traffic. At step 202, a network device having a plurality of control list objects is provided. The control list objects may be, for example, filtering TCP packets in an Access Control List (ACL). The ACL is generally implemented as a data structure such as a tree that has the information (to be used by the network operating system) to determine the access rights of each packet. At step 204, at least one control list object of the network device is correspondingly matched with at least one object of the application. The matching is performed based on the objects being present or not present in the application. The matching criteria may be at least one of a 'set criterion' or a 'not-set criterion', as described in conjunction with FIG. 1. In an embodiment of the invention, a '+' sign is prefixed to a control list object in the 'set criterion', to signify that the packet is considered a match if the control list object is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to a control list object, to signify that the packet is considered a match if the control list object is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device..”).</p> <p>Balakrishnan at [0023] (“FIG. 3 is a flowchart illustrating a method for determining if a packet is acceptable for transmitting to a network, in accordance with an exemplary embodiment of the invention. At step 302, a router including an ACL having match condition based on packet flags is provided. At step 304, a packet having packet flags is transmitted to an interface of the router that has an ACL. Subsequently, at least one packet flag of the packet is matched with at least one packet flag as specified in the ACL of the router, at step 306. The matching is performed based on the matching criteria for indicating that the packet is acceptable for transmitting to the network. If the packet flag(s) match the</p>

No.	'111 Patent Claim 2	The Reference
		<p>conditions in the ACL, then the packet is transmitted to the network. If the packet flag(s) do not satisfy the conditions in the ACL, the packet is not transmitted. The matching criteria may be at least one of a 'set criterion' or a 'not-set criterion', as described in conjunction with FIG. 1. In an embodiment of the invention, a '+' sign is prefixed to an access flag in the 'set criterion', to signify that the packet is considered a match if the access flag is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to an access flag, to signify that the packet is considered a match if the access flag is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device.”).</p> <p>Balakrishnan et al [0040] (“FIG. 4 is a flowchart illustrating a method for determining if a packet is allowed to transit a router, in accordance with an exemplary embodiment of the invention. At step 402, a router including an ACL having access flags is provided. At step 404, a packet having packet flags is received via an interface of the router that has an ACL. Subsequently, at least one packet flag of the packet is matched in a corresponding one-to-one relationship with at least one access flag of the ACL of the router, at step 406. The matching is performed based on the matching criteria for indicating that the packet is acceptable for transmitting to the router. If the packet flag(s) and the access flag(s) match, then the packet is transmitted to the router. If the packet flag(s) and the access flag(s) do not match, the packet is not transmitted. The matching criteria may be at least one of a 'set criterion' or a 'not-set criterion', as described in conjunction with FIG.1. In an embodiment of the invention, a '+' sign is prefixed to an access flag in the 'set criterion', to signify that the packet is considered a match if the access flag is present in the packet. In another embodiment of the invention, a '-' sign is prefixed to an access flag, to signify that the packet is considered a match if the access flag is not present in the packet. Once a packet matches the criteria specified, the packet can be accepted or rejected based on the policy of the network device..”).</p>

No.	'111 Patent Claim 3	The Reference
3[a]	The method according to claim 1, wherein the instruction is a ‘probe’, a ‘mirror’, or	<p>The Reference discloses the method according to claim 1, wherein the instruction is a ‘probe’, a ‘mirror’, or a ‘terminate’ instruction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was.</p>

No.	'111 Patent Claim 3	The Reference
	a 'terminate' instruction, and	known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.
3[b]	upon receiving by the network node the 'mirror' instruction and responsive to the packet satisfying the criterion, method further comprising sending the packet, by the network node, to the second entity and to the controller.	<p>The Reference discloses upon receiving by the network node the 'mirror' instruction and responsive to the packet satisfying the criterion, method further comprising sending the packet, by the network node, to the second entity and to the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 4	The Reference
4[a]	The method according to claim 1, wherein the instruction is 'probe', 'mirror', or 'terminate' instruction, and	<p>The Reference discloses the method according to claim 1, wherein the instruction is 'probe', 'mirror', or 'terminate' instruction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 4	The Reference
4[b]	<p>upon receiving by the network node the ‘probe’ instruction and responsive to the packet satisfying the criterion, the method further comprising: sending the packet, by the network node, to the controller;</p>	<p>The Reference discloses upon receiving by the network node the ‘probe’ instruction and responsive to the packet satisfying the criterion, the method further comprising: sending the packet, by the network node, to the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 22-23</p> <p>6.1.2 Asynchronous</p> <p>Asynchronous messages are sent without a controller soliciting them from a switch. Switches send asynchronous messages to controllers to denote a packet arrival, switch state change, or error. The four main asynchronous message types are described below.</p> <p>Packet-in: Transfer the control of a packet to the controller. For all packets forwarded to the CONTROLLER reserved port using a flow entry or the table-miss flow entry, a packet-in event is always sent to controllers (see [5.12]). Other processing, such as TTL checking, may also send packets to the controller using packet-in events.</p>

No.	'111 Patent Claim 4	The Reference
		<p>Packet-in events can be configured to buffer packets. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]). If the packet-in event is configured to buffer packets and the switch has sufficient memory to buffer them, the packet-in events contain only some fraction of the packet header and a buffer ID to be used by a controller when it is ready for the switch to forward the packet. Switches that do not support internal buffering, are configured to not buffer packets for the packet-in event, or have run out of internal buffering, must send the full packet to controllers as part of the event. Buffered packets will usually be processed via a Packet-out message from a controller, or automatically expired after some time.</p> <p>If the packet is buffered, the number of bytes of the original packet to include in the packet-in can be configured. By default, it is 128 bytes. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]).</p> <p>Flow-Removed: Inform the controller about the removal of a flow entry from a flow table. Flow-Removed messages are only sent for flow entries with the OFPFF_SEND_FLOW_Rem flag set. They are generated as the result of a controller flow delete requests or the switch flow expiry process when one of the flow timeout is exceeded (see [5.5]).</p> <p>Port-status: Inform the controller of a change on a port. The switch is expected to send port-status messages to controllers as port configuration or port state changes. These events include change in port configuration events, for example if it was brought down directly by a user, and port state change events, for example if the link went down.</p> <p>Error: The switch is able to notify controllers of problems using error messages.</p> <p>OpenFlow at 36</p>

No.	'111 Patent Claim 4	The Reference
		<p>The port numbers use the following conventions:</p> <pre> /* Port numbering. Ports are numbered starting from 1. */ enum ofp_port_no { /* Maximum number of physical and logical switch ports. */ OFPP_MAX = 0xffffffff00, /* Reserved OpenFlow Port (fake output "ports"). */ OFPP_IN_PORT = 0xffffffff8, /* Send the packet out the input port. This reserved port must be explicitly used in order to send back out of the input port. */ OFPP_TABLE = 0xffffffff9, /* Submit the packet to the first flow table NB: This destination port can only be used in packet-out messages. */ OFPP_NORMAL = 0xfffffffffa, /* Process with normal L2/L3 switching. */ OFPP_FLOOD = 0xfffffffffb, /* All physical ports in VLAN, except input port and those blocked or link down. */ OFPP_ALL = 0xfffffffffc, /* All physical ports except input port. */ OFPP_CONTROLLER = 0xfffffffffd, /* Send to controller. */ OFPP_LOCAL = 0xfffffffffe, /* Local openflow "port". */ } </pre> <p>OpenFlow at 76-77</p>

No.	'111 Patent Claim 4	The Reference
		<p>A.4 Asynchronous Messages</p> <p>A.4.1 Packet-In Message</p> <p>When packets are received by the datapath and sent to the controller, they use the OFPT_PACKET_IN message:</p> <pre>/* Packet received on port (datapath -> controller). */ struct ofp_packet_in { struct ofp_header header; uint32_t buffer_id; /* ID assigned by datapath. */ uint16_t total_len; /* Full length of frame. */ uint8_t reason; /* Reason packet is being sent (one of OFPR_*) */ uint8_t table_id; /* ID of the table that was looked up */ uint64_t cookie; /* Cookie of the flow entry that was looked up. */ struct ofp_match match; /* Packet metadata. Variable size. */ /* Followed by: * - Exactly 2 all-zero padding bytes, then * - An Ethernet frame whose length is inferred from header.length. * The padding bytes preceding the Ethernet frame ensure that the IP * header (if any) following the Ethernet header is 32-bit aligned. */ //uint8_t pad[2]; /* Align to 64 bit + 16 bit */ //uint8_t data[0]; /* Ethernet frame */ }; OFP_ASSERT(sizeof(struct ofp_packet_in) == 32);</pre> <p>The <code>buffer_id</code> is an opaque value used by the datapath to identify a buffered packet. When a packet is buffered, some number of bytes from the message will be included in the data portion of the message. If the packet is sent because of a “send to controller” action, then <code>max_len</code> bytes from the <code>oof_action_output</code> of the flow setup request are sent. If the packet is sent for other reasons, such as an invalid TTL, then at least <code>miss_send_len</code> bytes from the OFPT_SET_CONFIG message are sent. The default <code>miss_send_len</code> is 128 bytes. If the packet is not buffered - either because of no available buffers, or because of explicitly requested via OFPCML_NO_BUFFER - the entire packet is included in the data portion, and the <code>buffer_id</code> is OFP_NO_BUFFER.</p> <p>Switches that implement buffering are expected to expose, through documentation, both the amount of available buffering, and the length of time before buffers may be reused. A switch must gracefully handle</p>

No.	'111 Patent Claim 4	The Reference
		<p>the case where a buffered <code>packet_in</code> message yields no response from the controller. A switch should prevent a buffer from being reused until it has been handled by the controller, or some amount of time (indicated in documentation) has passed.</p> <p>The <code>data</code> field contains the packet itself, or a fraction of the packet if the packet is buffered. The packet header reflect any changes applied to the packet in previous processing.</p> <p>The <code>reason</code> field can be any of these values:</p> <pre data-bbox="741 470 1453 589">/* Why is this packet being sent to the controller? */ enum ofp_packet_in_reason { OFPR_NO_MATCH = 0, /* No matching flow (table-miss flow entry). */ OFPR_ACTION = 1, /* Action explicitly output to controller. */ OFPR_INVALID_TTL = 2, /* Packet has invalid TTL. */ };</pre> <p><code>OFPR_INVALID_TTL</code> indicates that a packets with an invalid IP TTL or MPLS TTL was rejected by the OpenFlow pipeline and passed to the controller. Checking for invalid TTL does not need to be done for every packet, but it must be done at a minimum every time a <code>OFPAT_DEC_MPLS_TTL</code> or <code>OFPAT_DEC_NW_TTL</code> action is applied to a packet.</p> <p>The <code>cookie</code> field contains the cookie of the flow entry that caused the packet to be sent to the controller. This field must be set to -1 (0xffffffffffff) if a cookie cannot be associated with a particular flow. For example, if the packet-in was generated in a group bucket or from the action set.</p> <p>The <code>match</code> field reflect the packet's headers and context when the event that triggers the packet-in message occurred and contains a set of OXM TLVs. This context includes any changes applied to the packet in previous processing, including actions already executed, if any, but not any changes in the action set. The OXM TLVs must include context fields, that is, fields whose values cannot be determined from the packet data. The standard context fields are <code>OFPXMT_OFB_IN_PORT</code>, <code>OFPXMT_OFB_IN_PHY_PORT</code>, <code>OFPXMT_OFB_METADATA</code> and <code>OFPXMT_OFB_TUNNEL_ID</code>. Fields whose values are all-bits-zero may be omitted. Optionally, the OXM TLVs may also include packet header fields that were previously extracted from the packet, including any modifications of those in the course of the processing.</p> <p>When a packet is received directly on a physical port and not processed by a logical port, <code>OFPXMT_OFB_IN_PORT</code> and <code>OFPXMT_OFB_IN_PHY_PORT</code> have the same value, the OpenFlow <code>port_no</code> of this physical port. <code>OFPXMT_OFB_IN_PHY_PORT</code> may be omitted if it has the same value as <code>OFPXMT_OFB_IN_PORT</code>.</p> <p>When a packet is received on a logical port by way of a physical port, <code>OFPXMT_OFB_IN_PORT</code> is the logical port's <code>port_no</code> and <code>OFPXMT_OFB_IN_PHY_PORT</code> is the physical port's <code>port_no</code>. For example, consider a packet received on a tunnel interface defined over a link aggregation group (LAG) with two physical port members. If the tunnel interface is the logical port bound to OpenFlow, then <code>OFPXMT_OFB_IN_PORT</code> is the tunnel <code>port_no</code> and <code>OFPXMT_OFB_IN_PHY_PORT</code> is the physical <code>port_no</code> member of the LAG on which the tunnel is configured.</p> <p>The port referenced by the <code>OFPXMT_OFB_IN_PORT</code> TLV must be the port used for matching flow entries (see [5.3]) and must be available to OpenFlow processing (i.e. OpenFlow can forward packet to this port, depending on port flags). <code>OFPXMT_OFB_IN_PHY_PORT</code> need not be available for matching or OpenFlow processing.</p>

No.	'111 Patent Claim 4	The Reference
4[c]	responsive to receiving the packet, analyzing the packet, by the controller;	<p>The Reference discloses responsive to receiving the packet, analyzing the packet, by the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>
4[d]	sending the packet, by the controller, to the network node; and	<p>The Reference discloses sending the packet, by the controller, to the network node.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 22</p> <p>Packet-out: These are used by the controller to send packets out of a specified port on the switch, and to forward packets received via Packet-in messages. Packet-out messages must contain a full packet or a buffer ID referencing a packet stored in the switch. The message must also contain a list of actions to be applied in the order they are specified; an empty action list drops the packet.</p> <p>OpenFlow at 74</p>

No.	'111 Patent Claim 4	The Reference
		<p>A.3.7 Packet-Out Message</p> <p>When the controller wishes to send a packet out through the datapath, it uses the <code>OFPT_PACKET_OUT</code> message:</p> <pre> /* Send packet (controller -> datapath). */ struct ofp_packet_out { struct ofp_header header; uint32_t buffer_id; /* ID assigned by datapath (OFP_NO_BUFFER if none). */ uint32_t in_port; /* Packet's input port or OFPP_CONTROLLER. */ uint16_t actions_len; /* Size of action array in bytes. */ uint8_t pad[6]; struct ofp_action_header actions[0]; /* Action list. */ /* uint8_t data[0]; */ /* Packet data. The length is inferred from the length field in the header. (Only meaningful if buffer_id == -1.) */ }; OFP_ASSERT(sizeof(struct ofp_packet_out) == 24); </pre> <p>The <code>buffer_id</code> is the same given in the <code>ofp_packet_in</code> message. If the <code>buffer_id</code> is <code>OFP_NO_BUFFER</code>, then the packet data is included in the data array.</p> <p>The <code>in_port</code> field is the ingress port that must be associated with the packet for OpenFlow processing. It must be set to either a valid standard switch port or <code>OFPP_CONTROLLER</code>.</p> <p>The <code>action</code> field is an action list defining how the packet should be processed by the switch. It may include packet modification, group processing and an output port. The action list of an <code>OFPT_PACKET_OUT</code> message can also specify the <code>OFPP_TABLE</code> reserved port as an output action to process the packet through the existing flow entries, starting at the first flow table. If <code>OFPP_TABLE</code> is specified, the <code>in_port</code> field is used as the ingress port in the flow table lookup.</p> <p>Packets sent to <code>OFPP_TABLE</code> may be forwarded back to the controller as the result of a flow entry action or table miss. Detecting and taking action for such controller-to-switch loops is outside the scope of this specification. In general, OpenFlow messages are not guaranteed to be processed in order, therefore if a <code>OFPT_PACKET_OUT</code> message using <code>OFPP_TABLE</code> depends on a flow that was recently sent to the switch (with a <code>OFPT_FLOW_MOD</code> message), a <code>OFPT_BARRIER_REQUEST</code> message may be required prior to the <code>OFPT_PACKET_OUT</code> message to make sure the flow entry was committed to the flow table prior to execution of <code>OFPP_TABLE</code>.</p>
4[e]	responsive to receiving the packet, sending the packet, by the network node, to the second entity.	<p>The Reference discloses responsive to receiving the packet, sending the packet, by the network node, to the second entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art.</p>

No.	'111 Patent Claim 4	The Reference
		<p>skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 22</p> <p>Packet-out: These are used by the controller to send packets out of a specified port on the switch, and to forward packets received via Packet-in messages. Packet-out messages must contain a full packet or a buffer ID referencing a packet stored in the switch. The message must also contain a list of actions to be applied in the order they are specified; an empty action list drops the packet.</p> <p>OpenFlow at 74</p>

No.	'111 Patent Claim 4	The Reference
		<p>A.3.7 Packet-Out Message</p> <p>When the controller wishes to send a packet out through the datapath, it uses the OFPT_PACKET_OUT message:</p> <pre> /* Send packet (controller -> datapath). */ struct ofp_packet_out { struct ofp_header header; uint32_t buffer_id; /* ID assigned by datapath (OFP_NO_BUFFER if none). */ uint32_t in_port; /* Packet's input port or OFPP_CONTROLLER. */ uint16_t actions_len; /* Size of action array in bytes. */ uint8_t pad[6]; struct ofp_action_header actions[0]; /* Action list. */ /* uint8_t data[0]; */ /* Packet data. The length is inferred from the length field in the header. (Only meaningful if buffer_id == -1.) */ }; OFP_ASSERT(sizeof(struct ofp_packet_out) == 24); </pre> <p>The <code>buffer_id</code> is the same given in the <code>ofp_packet_in</code> message. If the <code>buffer_id</code> is <code>OFP_NO_BUFFER</code>, then the packet data is included in the data array.</p> <p>The <code>in_port</code> field is the ingress port that must be associated with the packet for OpenFlow processing. It must be set to either a valid standard switch port or <code>OFPP_CONTROLLER</code>.</p> <p>The <code>action</code> field is an action list defining how the packet should be processed by the switch. It may include packet modification, group processing and an output port. The action list of an <code>OFPT_PACKET_OUT</code> message can also specify the <code>OFPP_TABLE</code> reserved port as an output action to process the packet through the existing flow entries, starting at the first flow table. If <code>OFPP_TABLE</code> is specified, the <code>in_port</code> field is used as the ingress port in the flow table lookup.</p> <p>Packets sent to <code>OFPP_TABLE</code> may be forwarded back to the controller as the result of a flow entry action or table miss. Detecting and taking action for such controller-to-switch loops is outside the scope of this specification. In general, OpenFlow messages are not guaranteed to be processed in order, therefore if a <code>OFPT_PACKET_OUT</code> message using <code>OFPP_TABLE</code> depends on a flow that was recently sent to the switch (with a <code>OFPT_FLOW_MOD</code> message), a <code>OFPT_BARRIER_REQUEST</code> message may be required prior to the <code>OFPT_PACKET_OUT</code> message to make sure the flow entry was committed to the flow table prior to execution of <code>OFPP_TABLE</code>.</p>

No.	'111 Patent Claim 5	The Reference
5	<p>The method according to claim 1, further comprising responsive to the packet satisfying the criterion and to the instruction, sending the packet or a portion thereof, by the network node, to the controller.</p>	<p>The Reference discloses the method according to claim 1, further comprising responsive to the packet satisfying the criterion and to the instruction, sending the packet or a portion thereof, by the network node, to the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 22-23</p> <p>6.1.2 Asynchronous</p> <p>Asynchronous messages are sent without a controller soliciting them from a switch. Switches send asynchronous messages to controllers to denote a packet arrival, switch state change, or error. The four main asynchronous message types are described below.</p> <p>Packet-in: Transfer the control of a packet to the controller. For all packets forwarded to the CONTROLLER reserved port using a flow entry or the table-miss flow entry, a packet-in event is always sent to controllers (see [5.12]). Other processing, such as TTL checking, may also send packets to the controller using packet-in events.</p>

No.	'111 Patent Claim 5	The Reference
		<p>Packet-in events can be configured to buffer packets. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]). If the packet-in event is configured to buffer packets and the switch has sufficient memory to buffer them, the packet-in events contain only some fraction of the packet header and a buffer ID to be used by a controller when it is ready for the switch to forward the packet. Switches that do not support internal buffering, are configured to not buffer packets for the packet-in event, or have run out of internal buffering, must send the full packet to controllers as part of the event. Buffered packets will usually be processed via a Packet-out message from a controller, or automatically expired after some time.</p> <p>If the packet is buffered, the number of bytes of the original packet to include in the packet-in can be configured. By default, it is 128 bytes. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]).</p> <p>Flow-Removed: Inform the controller about the removal of a flow entry from a flow table. Flow-Removed messages are only sent for flow entries with the OFPFF_SEND_FLOW_Rem flag set. They are generated as the result of a controller flow delete requests or the switch flow expiry process when one of the flow timeout is exceeded (see [5.5]).</p> <p>Port-status: Inform the controller of a change on a port. The switch is expected to send port-status messages to controllers as port configuration or port state changes. These events include change in port configuration events, for example if it was brought down directly by a user, and port state change events, for example if the link went down.</p> <p>Error: The switch is able to notify controllers of problems using error messages.</p> <p>OpenFlow at 36</p>

No.	'111 Patent Claim 5	The Reference
		<p>The port numbers use the following conventions:</p> <pre> /* Port numbering. Ports are numbered starting from 1. */ enum ofp_port_no { /* Maximum number of physical and logical switch ports. */ OFPP_MAX = 0xffffffff00, /* Reserved OpenFlow Port (fake output "ports"). */ OFPP_IN_PORT = 0xffffffff8, /* Send the packet out the input port. This reserved port must be explicitly used in order to send back out of the input port. */ OFPP_TABLE = 0xffffffff9, /* Submit the packet to the first flow table NB: This destination port can only be used in packet-out messages. */ OFPP_NORMAL = 0xfffffffffa, /* Process with normal L2/L3 switching. */ OFPP_FLOOD = 0xfffffffffb, /* All physical ports in VLAN, except input port and those blocked or link down. */ OFPP_ALL = 0xfffffffffc, /* All physical ports except input port. */ OFPP_CONTROLLER = 0xfffffffffd, /* Send to controller. */ OFPP_LOCAL = 0xfffffffffe, /* Local openflow "port". */ </pre>

No.	'111 Patent Claim 6	The Reference
6	The method according to claim 5, further comprising storing the received packet or a portion thereof, by the controller, in a memory.	<p>The Reference discloses the method according to claim 5, further comprising storing the received packet or a portion thereof, by the controller, in a memory.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 7	The Reference
7	<p>The method according to claim 5, further comprising responsive to the packet satisfying the criterion and to instruction, sending a portion of the packet, by the network node, to the controller.</p>	<p>The Reference discloses the method according to claim 5, further comprising responsive to the packet satisfying the criterion and to instruction, sending a portion of the packet, by the network node, to the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 22-23</p> <p>6.1.2 Asynchronous</p> <p>Asynchronous messages are sent without a controller soliciting them from a switch. Switches send asynchronous messages to controllers to denote a packet arrival, switch state change, or error. The four main asynchronous message types are described below.</p> <p>Packet-in: Transfer the control of a packet to the controller. For all packets forwarded to the CONTROLLER reserved port using a flow entry or the table-miss flow entry, a packet-in event is always sent to controllers (see [5.12]). Other processing, such as TTL checking, may also send packets to the controller using packet-in events.</p>

No.	'111 Patent Claim 7	The Reference
		<p>Packet-in events can be configured to buffer packets. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]). If the packet-in event is configured to buffer packets and the switch has sufficient memory to buffer them, the packet-in events contain only some fraction of the packet header and a buffer ID to be used by a controller when it is ready for the switch to forward the packet. Switches that do not support internal buffering, are configured to not buffer packets for the packet-in event, or have run out of internal buffering, must send the full packet to controllers as part of the event. Buffered packets will usually be processed via a Packet-out message from a controller, or automatically expired after some time.</p> <p>If the packet is buffered, the number of bytes of the original packet to include in the packet-in can be configured. By default, it is 128 bytes. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]).</p> <p>Flow-Removed: Inform the controller about the removal of a flow entry from a flow table. Flow-Removed messages are only sent for flow entries with the <code>OFPFF_SEND_FLOW_Rem</code> flag set. They are generated as the result of a controller flow delete requests or the switch flow expiry process when one of the flow timeout is exceeded (see [5.5]).</p> <p>Port-status: Inform the controller of a change on a port. The switch is expected to send port-status messages to controllers as port configuration or port state changes. These events include change in port configuration events, for example if it was brought down directly by a user, and port state change events, for example if the link went down.</p> <p>Error: The switch is able to notify controllers of problems using error messages.</p>

No.	'111 Patent Claim 8	The Reference
8[a]	The method according to claim 7, wherein the portion of the packet consists of multiple consecutive bytes, and	<p>The Reference discloses the method according to claim 7, wherein the portion of the packet consists of multiple consecutive bytes.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p>

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No.	'111 Patent Claim 8	The Reference
		<p>OpenFlow at 22-23</p> <p>6.1.2 Asynchronous</p> <p>Asynchronous messages are sent without a controller soliciting them from a switch. Switches send asynchronous messages to controllers to denote a packet arrival, switch state change, or error. The four main asynchronous message types are described below.</p> <p>Packet-in: Transfer the control of a packet to the controller. For all packets forwarded to the CONTROLLER reserved port using a flow entry or the table-miss flow entry, a packet-in event is always sent to controllers (see [5.12]). Other processing, such as TTL checking, may also send packets to the controller using packet-in events.</p> <p>Packet-in events can be configured to buffer packets. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]). If the packet-in event is configured to buffer packets and the switch has sufficient memory to buffer them, the packet-in events contain only some fraction of the packet header and a buffer ID to be used by a controller when it is ready for the switch to forward the packet. Switches that do not support internal buffering, are configured to not buffer packets for the packet-in event, or have run out of internal buffering, must send the full packet to controllers as part of the event. Buffered packets will usually be processed via a Packet-out message from a controller, or automatically expired after some time.</p> <p>If the packet is buffered, the number of bytes of the original packet to include in the packet-in can be configured. By default, it is 128 bytes. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]).</p> <p>Flow-Removed: Inform the controller about the removal of a flow entry from a flow table. Flow-Removed messages are only sent for flow entries with the OFPFF_SEND_FLOW_REM flag set. They are generated as the result of a controller flow delete requests or the switch flow expiry process when one of the flow timeout is exceeded (see [5.5]).</p> <p>Port-status: Inform the controller of a change on a port. The switch is expected to send port-status messages to controllers as port configuration or port state changes. These events include change in port configuration events, for example if it was brought down directly by a user, and port state change events, for example if the link went down.</p> <p>Error: The switch is able to notify controllers of problems using error messages.</p>
8[b]	wherein the instruction comprises	The Reference discloses wherein the instruction comprises identification of the consecutive bytes in the packet.

No.	'111 Patent Claim 8	The Reference
	<p>identification of the consecutive bytes in the packet.</p>	<p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 22-23</p> <p>6.1.2 Asynchronous</p> <p>Asynchronous messages are sent without a controller soliciting them from a switch. Switches send asynchronous messages to controllers to denote a packet arrival, switch state change, or error. The four main asynchronous message types are described below.</p> <p>Packet-in: Transfer the control of a packet to the controller. For all packets forwarded to the CONTROLLER reserved port using a flow entry or the table-miss flow entry, a packet-in event is always sent to controllers (see 5.12). Other processing, such as TTL checking, may also send packets to the controller using packet-in events.</p>

No.	'111 Patent Claim 8	The Reference
		<p>Packet-in events can be configured to buffer packets. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]). If the packet-in event is configured to buffer packets and the switch has sufficient memory to buffer them, the packet-in events contain only some fraction of the packet header and a buffer ID to be used by a controller when it is ready for the switch to forward the packet. Switches that do not support internal buffering, are configured to not buffer packets for the packet-in event, or have run out of internal buffering, must send the full packet to controllers as part of the event. Buffered packets will usually be processed via a Packet-out message from a controller, or automatically expired after some time.</p> <p>If the packet is buffered, the number of bytes of the original packet to include in the packet-in can be configured. By default, it is 128 bytes. For packet-in generated by an output action in a flow entries or group bucket, it can be specified individually in the output action itself (see [A.2.5]), for other packet-in it can be configured in the switch configuration (see [A.3.2]).</p> <p>Flow-Removed: Inform the controller about the removal of a flow entry from a flow table. Flow-Removed messages are only sent for flow entries with the <code>OFPFF_SEND_FLOW_Rem</code> flag set. They are generated as the result of a controller flow delete requests or the switch flow expiry process when one of the flow timeout is exceeded (see [5.5]).</p> <p>Port-status: Inform the controller of a change on a port. The switch is expected to send port-status messages to controllers as port configuration or port state changes. These events include change in port configuration events, for example if it was brought down directly by a user, and port state change events, for example if the link went down.</p> <p>Error: The switch is able to notify controllers of problems using error messages.</p>

No.	'111 Patent Claim 9	The Reference
9	The method according to claim 5, further comprising responsive to receiving the packet, analyzing the packet, by the controller.	<p>The Reference discloses the method according to claim 5, further comprising responsive to receiving the packet, analyzing the packet, by the controller.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 12	The Reference
12	The method according to claim 9, wherein the analyzing comprises applying security or data analytic application.	<p>The Reference discloses the method according to claim 9, wherein the analyzing comprises applying security or data analytic application.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

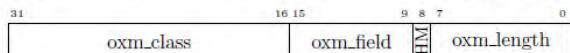
No.	'111 Patent Claim 13	The Reference
13	The method according to claim 9, wherein the analyzing comprises applying security application that comprises firewall or intrusion detection functionality.	<p>The Reference discloses the method according to claim 9, wherein the analyzing comprises applying security application that comprises firewall or intrusion detection functionality.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 14	The Reference
14	The method according to claim 9, wherein the analyzing comprises performing Deep Packet Inspection (DPI) or using a DPI engine on the packet.	<p>The Reference discloses the method according to claim 9, wherein the analyzing comprises performing Deep Packet Inspection (DPI) or using a DPI engine on the packet.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 15	The Reference
15[a]	The method according to claim 9, wherein the packet comprises distinct header and payload fields, and	<p>The Reference discloses the method according to claim 9, wherein the packet comprises distinct header and payload fields.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 12</p>

No.	'111 Patent Claim 15	The Reference
		<p style="text-align: center;"> Match Fields Priority Counters Instructions Timeouts Cookie </p> <p style="text-align: center;">Table 1: Main components of a flow entry in a flow table.</p> <ul style="list-style-type: none"> • match fields: to match against packets. These consist of the ingress port and packet headers, and optionally metadata specified by a previous table. • priority: matching precedence of the flow entry • counters: to update for matching packets • instructions to modify the action set or pipeline processing • timeouts: maximum amount of time or idle time before flow is expired by the switch • cookie: opaque data value chosen by the controller. May be used by the controller to filter flow statistics, flow modification and flow deletion, not used when processing packets. <p>A flow table entry is identified by its match fields and priority: the match fields and priority taken together identify a unique flow entry in the flow table. The flow entry that wildcards all fields (all fields omitted) and has priority equal 0 is called the table-miss flow entry (see [5.4]).</p> <h3>5.3 Matching</h3> <pre> graph TD Start[Packet In Start at table 0] --> Match{Match in table n?} Match -- Yes --> Update[Update counters Execute instructions: - update action set - update packet/match set fields - update metadata] Update --> Goto{Goto-Table n?} Goto -- Yes --> TableN[] Goto -- No --> Execute[Execute action set] Match -- No --> TableMiss{Table-miss flow entry exists?} TableMiss -- Yes --> Start TableMiss -- No --> Drop[Drop packet] </pre> <p>Figure 3: Flowchart detailing packet flow through an OpenFlow switch.</p> <p>On receipt of a packet, an OpenFlow Switch performs the functions shown in Figure 3. The switch starts by performing a table lookup in the first flow table, and based on pipeline processing, may perform table lookups in other flow tables (see [5.1]).</p>

No.	'111 Patent Claim 15	The Reference
		<p>OpenFlow at 13</p> <p>Packet match fields are extracted from the packet. Packet match fields used for table lookups depend on the packet type, and typically include various packet header fields, such as Ethernet source address or IPv4 destination address (see A.2.3). In addition to packet headers, matches can also be performed against the ingress port and metadata fields. Metadata may be used to pass information between tables in a switch. The packet match fields represent the packet in its current state, if actions applied in a previous table using the <i>Apply-Actions</i> changed the packet headers, those changes are reflected in the packet match fields.</p> <p>A packet matches a flow table entry if the values in the packet match fields used for the lookup match those defined in the flow table entry. If a flow table entry field has a value of ANY (field omitted), it matches all possible values in the header. If the switch supports arbitrary bitmasks on specific match fields, these masks can more precisely specify matches.</p> <p>The packet is matched against the table and <i>only</i> the highest priority flow entry that matches the packet must be selected. The counters associated with the selected flow entry must be updated and the instruction set included in the selected flow entry must be applied. If there are multiple matching flow entries with the same highest priority, the selected flow entry is explicitly undefined. This case can only arise when a controller writer never sets the OFPFF_CHECK_OVERLAP bit on flow mod messages and adds overlapping entries.</p> <p>IP fragments must be reassembled before pipeline processing if the switch configuration contains the OFPC_FRAG_REASM flag (see A.3.2).</p> <p>This version of the specification does <i>not</i> define the expected behavior when a switch receives a malformed or corrupted packet.</p> <p>OpenFlow at 38-41</p> <p>A.2.3 Flow Match Structures</p> <p>An OpenFlow match is composed of a flow match header and a sequence of zero or more flow match fields.</p>

No.	'111 Patent Claim 15	The Reference								
		<p>A.2.3.1 Flow Match Header</p> <p>The flow match header is described by the <code>ofp_match</code> structure:</p> <pre> /* Fields to match against flows */ struct ofp_match { uint16_t type; /* One of OFPMT_* */ uint16_t length; /* Length of ofp_match (excluding padding) */ /* Followed by: * - Exactly (length - 4) (possibly 0) bytes containing OXM TLVs, then * - Exactly ((length + 7)/8*8 - length) (between 0 and 7) bytes of * all-zero bytes * In summary, ofp_match is padded as needed, to make its overall size * a multiple of 8, to preserve alignment in structures using it. */ uint8_t oxm_fields[4]; /* OXMs start here - Make compiler happy */ }; OFP_ASSERT(sizeof(struct ofp_match) == 8); </pre> <p>The type field is set to <code>OFPMT_OXM</code> and length field is set to the actual length of <code>ofp_match</code> structure including all match fields. The payload of the OpenFlow match is a set of OXM Flow match fields.</p> <pre> /* The match type indicates the match structure (set of fields that compose the * match) in use. The match type is placed in the type field at the beginning * of all match structures. The "OpenFlow Extensible Match" type corresponds * to OXM TLV format described below and must be supported by all OpenFlow * switches. Extensions that define other match types may be published on the * ONF wiki. Support for extensions is optional. */ enum ofp_match_type { OFPMT_STANDARD = 0, /* Deprecated. */ OFPMT_OXM = 1, /* OpenFlow Extensible Match */ }; </pre> <p>The only valid match type in this specification is <code>OFPMT_OXM</code>, the OpenFlow 1.1 match type <code>OFPMT_STANDARD</code> is deprecated. If an alternate match type is used, the match fields and payload may be set differently, but this is outside the scope of this specification.</p> <p>A.2.3.2 Flow Match Field Structures</p> <p>The flow match fields are described using the OpenFlow Extensible Match (OXM) format, which is a compact type-length-value (TLV) format. Each OXM TLV is 5 to 259 (inclusive) bytes long. OXM TLVs are not aligned on or padded to any multibyte boundary. The first 4 bytes of an OXM TLV are its header, followed by the entry's body.</p> <p>An OXM TLV's header is interpreted as a 32-bit word in network byte order (see figure 4).</p>  <table border="1" style="margin-left: auto; margin-right: auto;"> <tr> <td style="text-align: center;">31</td> <td style="text-align: center;">16 15</td> <td style="text-align: center;">9 8 7</td> <td style="text-align: center;">0</td> </tr> <tr> <td style="text-align: center;">oxm_class</td> <td style="text-align: center;">oxm_field</td> <td style="text-align: center;">[]</td> <td style="text-align: center;">oxm_length</td> </tr> </table> <p>Figure 4: OXM TLV header layout</p> <p>The OXM TLV's header fields are defined in Table 9</p> <p>The <code>oxm_class</code> is a OXM match class that contains related match types, and is described in section A.2.3.3. <code>oxm_field</code> is a class-specific value, identifying one of the match types within the match class. The combination of <code>oxm_class</code> and <code>oxm_field</code> (the most-significant 23 bits of the header) are collectively <code>oxm_type</code>. The <code>oxm_type</code> normally designates a protocol header field, such as the Ethernet type, but it can also refer to packet metadata, such as the switch port on which a packet arrived.</p>	31	16 15	9 8 7	0	oxm_class	oxm_field	[]	oxm_length
31	16 15	9 8 7	0							
oxm_class	oxm_field	[]	oxm_length							

No.	'111 Patent Claim 15	The Reference																			
		<table border="1"> <thead> <tr> <th colspan="2">Name</th> <th>Width</th> <th>Usage</th> </tr> </thead> <tbody> <tr> <td rowspan="2">oxm_type</td> <td>oxm_class</td> <td>16</td> <td>Match class: member class or reserved class</td> </tr> <tr> <td>oxm_field</td> <td>7</td> <td>Match field within the class</td> </tr> <tr> <td></td> <td>oxm_hasmask</td> <td>1</td> <td>Set if OXM include a bitmask in payload</td> </tr> <tr> <td></td> <td>oxm_length</td> <td>8</td> <td>Length of OXM payload</td> </tr> </tbody> </table> <p>Table 9: OXM TLV header fields</p> <p>oxm_hasmask defines if the OXM TLV contains a bitmask, more details is explained in section A.2.3.5.</p> <p>oxm_length is a positive integer describing the length of the OXM TLV payload in bytes. The length of the OXM TLV, including the header, is exactly 4 + oxm_length bytes.</p> <p>For a given oxm_class, oxm_field, and oxm_hasmask value, oxm_length is a constant. It is included only to allow software to minimally parse OXM TLVs of unknown types. (Similarly, for a given oxm_class, oxm_field, and oxm_length, oxm_hasmask is a constant.)</p> <p>A.2.3.3 OXM classes</p> <p>The match types are structured using OXM match classes. The OpenFlow specification distinguish two types of OXM match classes, ONF member classes and ONF reserved classes, differentiated by their high order bit. Classes with the high order bit set to 1 are ONF reserved classes, they are used for the OpenFlow specification itself. Classes with the high order bit set to zero are ONF member classes, they are allocated by the ONF on an as needed basis, they uniquely identify an ONF member and can be used arbitrarily by that member. Support for ONF member classes is optional.</p> <p>The following OXM classes are defined:</p> <pre> /* OXM Class IDs. * The high order bit differentiate reserved classes from member classes. * Classes 0x0000 to 0x7FFF are member classes, allocated by ONF. * Classes 0x8000 to 0xFFFF are reserved classes, reserved for standardisation. */ enum ofp_oxm_class { OFPXML_NXM_0 = 0x0000, /* Backward compatibility with NXM */ OFPXML_NXM_1 = 0x0001, /* Backward compatibility with NXM */ OFPXML_OPENFLOW_BASIC = 0x8000, /* Basic class for OpenFlow */ OFPXML_EXPERIMENTER = 0xFFFF, /* Experimenter class */ }; </pre> <p>The class OFPXML_OPENFLOW_BASIC contains the basic set of OpenFlow match fields (see A.2.3.7). The optional class OFPXML_EXPERIMENTER is used for experimenter matches (see A.2.3.8). Other ONF reserved classes are reserved for future uses such as modularisation of the specification. The first two ONF member classes OFPXML_NXM_0 and OFPXML_NXM_1 are reserved for backward compatibility with the Nicira Extensible Match (NXM) specification.</p> <p>A.2.3.4 Flow Matching</p> <p>A zero-length OpenFlow match (one with no OXM TLVs) matches every packet. Match fields that should be wildcarded are omitted from the OpenFlow match.</p> <p>An OXM TLV places a constraint on the packets matched by the OpenFlow match:</p> <ul style="list-style-type: none"> If oxm_hasmask is 0, the OXM TLV's body contains a value for the field, called oxm_value. The OXM TLV match matches only packets in which the corresponding field equals oxm_value. 	Name		Width	Usage	oxm_type	oxm_class	16	Match class: member class or reserved class	oxm_field	7	Match field within the class		oxm_hasmask	1	Set if OXM include a bitmask in payload		oxm_length	8	Length of OXM payload
Name		Width	Usage																		
oxm_type	oxm_class	16	Match class: member class or reserved class																		
	oxm_field	7	Match field within the class																		
	oxm_hasmask	1	Set if OXM include a bitmask in payload																		
	oxm_length	8	Length of OXM payload																		

No.	'111 Patent Claim 15	The Reference
15[b]	wherein the analyzing comprises checking part of, or whole of, the payload field.	<p>The Reference discloses wherein the analyzing comprises checking part of, or whole of, the payload field.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 16	The Reference
16[a]	The method according to claim 1, wherein the packet comprises distinct header and payload fields,	<p>The Reference discloses the method according to claim 1, wherein the packet comprises distinct header and payload fields.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic comprised of packets with headers and payloads. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254

No.	'111 Patent Claim 16	The Reference
		<ul style="list-style-type: none"> • Kumar '739 <p>Khan '478 at 7:31-49 ("TLOC: A TLOC is similar to the NEXT HOP attribute in BGP and is carried in the overlay route NLRI with a type value of 1. The actual TLOC is not carried as an immediate attribute to the prefix, but rather the System-IP of the OMP speaker originating the overlay route. Carrying the System-IP allows for the mapping between overlay routes and TLOCs irrespectively of what the actual TLOC happens to be. This is important since TLOCs can change and will change when traversing NATs, something that OMP is designed to take into consideration. This TLOC attribute points the TLOC AFI/SAFI. Within the SAFI for each TLOC, the detailed information on each specific TLOC can be found. This includes detailed information on the actual next-hop address to use, the actual TLOC. This information includes the public IP address of the TLOC and if NAT is involved, the private and non-translated TLOC-address. This separation of information allows for individual advertisement and invalidation of overlay routes or TLOCs without having to invalidate the other dependent entity.")</p> <p>Wang '735 at 5:20-39 ("In one example, routers 12 at the ends of WAN link 16 comprise the optimization system 18. An RTP (Real-time Transport Protocol) trunk may be used between two adjacent optimization system devices 12 on a media path. For example, as shown in FIG. 1, the RTP trunk may be between two optimization system devices 12 connected by WAN link 16 on which bandwidth optimization is needed. The RTP trunk is preferably configured to support one of more of the following features to reduce overhead, reduce redundant copies of streams, create a branch out RTP trunk, or carry additional flags or markings. For example, RTP header compression and session multiplexing may be used to reduce overhead. Data Redundancy Elimination (DRE) may be used with UDP-based real-time multimedia applications to reduce redundant copies of video/audio streams in a multiple point conference or live-streaming applications. In order to provide DRE, both WAN optimization devices 12 maintain a synchronization RTP payload cache. The RTP header may be pre-served over the trunk and the payload may be encoded by an index in a cache buffer, for example.")</p> <p>Wang '735 at 11:27-49 ("Another media optimization mechanism that the system may utilize is congestion control for variable bit-rate video applications. Congestion may occur, for example, when over-subscription occurs and applications generate more traffic than a</p>

No.	'111 Patent Claim 16	The Reference
		<p>network link can transport, when multiple video encoders send burst traffic onto the network, when there is no QoS provisioning or inappropriate QoS settings on router, or when network bandwidth changes mid-session. In these cases, the router has to drop packets if no buffer is available to store the excess traffic. Conversation video streams should not be buffered during congestion in order to minimize the delay and jitter. Techniques that may be used include video DPI (Deep Packet Inspection) and video specific application parsing (e.g., parse the H.264 RTP header and video payload) to extract video specification information from a flow or from multiple packets of the flow, such as priority, entropy of a packet or flow, video quality score, frame boundary, etc. Intelligent (selective) packet dropping to drop less important packets first or SVC (Scalable Video Coding) layer filtering and forwarding may also be used. Bandwidth and resource CAC may be used during session setup and mid-call (e.g., preemption, over-subscription/down speeding, resume/re-cover).”)</p> <p>Olofsson '254 at 7:29-43 (“In one embodiment, each service router in the path of a service chain accepts inbound traffic based on the destination TLOC and VPN Label in the received packet and forwards it out the associated interface for the specific service being associated with the TLOC/Label combination. In the outbound direction, each service router must be equipped with policy describing what the next hop is for the particular destination. This allows for each service router to support multiple service chains and different policies for each direction of traffic. Since the outgoing direction is controlled by policy, this allows for great flexibility in choosing the next point in the service chain based on individually defined criteria for that service chain, service, or service router.”)</p> <p>Kumar '739 at 1:62-2:22 (“Service chaining primarily involves the interception of traffic and steering the traffic through a series of service nodes (i.e., physical or virtual devices) that each host one or more service-functions. The traffic is intercepted through the use of a classifier function at a node (i.e., switch, router, etc.) that serves as a head-end node to the service chain. The node that executes the classifier function is sometimes referred to herein as a “classifier” or “classifier node.” In general, the traffic is steered from the classifier through the service-functions using one or more Layer 2 (L2)/Layer 3 (L3) service overlays in the network. In addition, a service header is appended to the traffic for forwarding through the service chain and the service header enables the carrying of service metadata in addition to the original data/payload.</p>

No.	'111 Patent Claim 16	The Reference
		<p>A service header is part of the data-plane of a service chain and includes metadata specifically formatted for consumption by a service-function. The metadata may include, for example, an application identifier (ID), flow or path ID, and client or user ID, network classification information used for deriving targeted service policies and profiles, common metadata related to a particular service such as finer classification that can be passed to the service-functions further down the service-path. In other words, service-functions benefit from metadata derived both from the network as well as the service-functions that form a given service chain. Metadata can also be passed between network nodes and be used, for example, to determine forwarding state at the end of a service chain.”)</p> <p>Kumar '739 at 3:47-65 (“The service nodes 35, 40, 45, 50, and 55 each host/support one or more service-functions (services) for application to the payload of traffic passing through the respective service node. More specifically, service node 35 hosts service-functions 65(1) (service-function f1), 65(2) (service-function f2), and 65(3) (service-function f3), while service node 40 hosts service-functions 65(3) (service-function f3), 65(5)(service-function f5), 65(6) (service-function f6), and 65(7)(service-function f7). Service node 45 hosts service-functions 65(1) (service-function f1), 65(5) (service-function f5), and 65(10) (service-function f10), while service node 50 hosts service-functions 65(3) (service-function f3), 65(5)(service-function f5), and 65(10) (service-function f10). Finally, service node 55 hosts service-functions 65(2) (service-function f2) and 65(3) (service-function f3). As shown, service-functions may appear in multiple instances on different service nodes or on the same service node. For example, service-function f3 is hosted on each of the service nodes 35, 40, 50 and 55.”)</p> <p>Kumar '739 at 5:34-47 (“In the example of FIG. 2, classification and mapping logic 75 selects service-functions from several different service nodes. In particular, classification and mapping logic 75 selects service-functions f1 and f2 at service node 35, ser-vice-functions f6 and f7 at service node 40, and service-function f10 at service node 45. The path for service-function chain SFC1 selected by classification and mapping logic 75 is shown in FIG. 2 by broken line 100. The classifier 30 sends traffic 90 along the path 100 using one or more L2/L3/L4 service overlays in the network. In other words, a service header is appended to the traffic 90 for forwarding through the service chain and the service header enables the carrying of service metadata in addition to the original data/payload.”)</p>

No.	'111 Patent Claim 16	The Reference
16[b]	the header comprises one or more flag bits, and	<p>The Reference discloses the header comprises one or more flag bits.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 56</p>

No.	'111 Patent Claim 16	The Reference
		<p>The <code>flags</code> field may include the following flags:</p> <pre>enum ofp_flow_mod_flags { OFPFF_SEND_FLOW_REM = 1 << 0, /* Send flow removed message when flow * expires or is deleted. */ OFPFF_CHECK_OVERLAP = 1 << 1, /* Check for overlapping entries first. */ OFPFF_RESET_COUNTS = 1 << 2, /* Reset flow packet and byte counts. */ OFPFF_NO_PKT_COUNTS = 1 << 3, /* Don't keep track of packet count. */ OFPFF_NO_BYT_COUNTS = 1 << 4, /* Don't keep track of byte count. */ };</pre> <p>When the <code>OFPFF_SEND_FLOW_REM</code> flag is set, the switch must send a flow removed message when the flow entry expires or is deleted.</p> <p>When the <code>OFPFF_CHECK_OVERLAP</code> flag is set, the switch must check that there are no conflicting entries with the same priority prior to inserting it in the flow table. If there is one, the flow mod fails and an error message is returned (see [6.4]).</p> <p>When the <code>OFPFF_NO_PKT_COUNTS</code> flag is set, the switch does not need to keep track of the flow packet count. When the <code>OFPFF_NO_BYT_COUNTS</code> flag is set, the switch does not need to keep track of the flow byte count. Setting those flags may decrease the processing load on some OpenFlow switches, however those counters may not be available in flow statistics and flow removed messages for this flow entry. A switch is not required to honor those flags and may keep track of a flow count and return it despite the corresponding flag being set. If a switch does not keep track of a flow count, the corresponding counter is not available and must be set to the maximum field value (see [6.8]).</p> <p>When a flow entry is inserted in a table, its <code>flags</code> field is set with the values from the message. When a flow entry is matched and modified (<code>OFPFC_MODIFY</code> or <code>OFPFC_MODIFY_STRICT</code> messages), the <code>flags</code> field is ignored.</p> <p>The <code>instructions</code> field contains the instruction set for the flow entry when adding or modifying entries. If the instruction set is not valid or supported, the switch must generate an error (see [6.4]).</p> <p>Balakrishnan at [0019] (“A set of criteria corresponding to the type of objects is then created, at step 104. In various embodiments of the invention, the set of criteria corresponds to at least one packet field associated with a configuration of the network device. A packet flag refers to one of the packet fields. In various embodiments of the invention, a packet flag corresponds to the type of objects. For example, if the packet is a TCP packet, then the packet flag is a TCP flag. Exemplary TCP flags may be for example, syn, ack, fin, urg, psh, and rst. In an embodiment of the invention, the set of criteria include at least one ‘set criterion’ for selecting the packet. In another embodiment of the invention, the set of criteria include at least one ‘not-set criterion’ for rejecting the packet. In yet another embodiment of the invention, the set of criteria includes at least one ‘set criterion’ and at least one ‘not-set criterion’.”)</p>

No.	'111 Patent Claim 16	The Reference
		<p>criterion' for managing the network traffic in the network device. Further, in various embodiments of the invention, if a packet flag is not present in the ACL, then it does not matter if the packet flag is present or not present in the packet.”)</p> <p>Balakrishnan at [0026] (“Means for providing 502 provides a plurality of objects associated with a packet of the network traffic. The objects associated with the packet may be, for example the packet fields. In an embodiment of the invention, the object associated with a TCP packet may be TCP flags. In various embodiments of the invention, means for providing 502 may be a software module.”)</p> <p>Balakrishnan at [0028]-[0035] (“Means for matching 506 correspondingly matches the objects of the network device with the objects associated with the packet based upon the set of criteria, as described in conjunction with FIG. 1. In various embodiments of the invention, means for matching 506 may be a software module. In an embodiment of the invention, the matching is performed by using keywords such as 'match-any' and 'match-all', programmed on an Internetworking Operation Systems (IOS) of the network device. 'Match-any' keyword transmits the packet if at least one criterion from the set of criteria is satisfied. For example, a command for TCP flag filtering, also referred to as Access Control Entry (ACE), entered on the Command Line Interface (CLI) may be,</p> <pre>permit tcp <src> <dst> match-any+syn+ack</pre> <p>This ACE allows the packet if at least one of syn and ack are set on the packet. In an embodiment of the invention, the code for 'Match-any' keyword may be as follows:</p> <pre>if ((match_flags & match_mask) '(-packet_flags & match_mask))</pre> <p>'Match-all' keyword transmits the packet if each criterion from the set of criteria is satisfied. For example, an ACE, for TCP flag filtering entered on the CLI may be,</p> <pre>permit tcp <src> <dst> match-all+syn+ack-fin</pre> <p>This ACE allows the packet only if syn and ack are set and fin is not set on the packet. In an embodiment of the invention, the code for 'Match-any' keyword may be as follows:</p> <pre>if ((match_flags & match_mask)==(packet_flags</pre>

No.	'111 Patent Claim 16	The Reference
		<p>& match_mask))</p> <p>In a further example, an ip ACL for TCP flag filtering entered on the CLI may be,</p> <p>permit tcp any any match-all+syn+ack-fin</p> <p>deny tcp any any match-any+psh-rst</p> <p>permit ip any any</p> <p>The first ACE allows the packet only if syn and ack are set and fin is not set on the packet. The second ACE does not allow the packet if psh is set or rst is not set on the packet. The last ACE accepts all packets.”)</p>
16[c]	wherein the packet applicable criterion is that one or more of the flag bits is set.	<p>The Reference discloses wherein the packet applicable criterion is that one or more of the flag bits is set.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 56</p>

No.	'111 Patent Claim 16	The Reference
		<p>The flags field may include the following flags:</p> <pre>enum ofp_flow_mod_flags { OFPFF_SEND_FLOW_REM = 1 << 0, /* Send flow removed message when flow * expires or is deleted. */ OFPFF_CHECK_OVERLAP = 1 << 1, /* Check for overlapping entries first. */ OFPFF_RESET_COUNTS = 1 << 2, /* Reset flow packet and byte counts. */ OFPFF_NO_PKT_COUNTS = 1 << 3, /* Don't keep track of packet count. */ OFPFF_NO_BYT_COUNTS = 1 << 4, /* Don't keep track of byte count. */ };</pre> <p>When the OFPFF_SEND_FLOW_REM flag is set, the switch must send a flow removed message when the flow entry expires or is deleted.</p> <p>When the OFPFF_CHECK_OVERLAP flag is set, the switch must check that there are no conflicting entries with the same priority prior to inserting it in the flow table. If there is one, the flow mod fails and an error message is returned (see 6.4).</p> <p>When the OFPFF_NO_PKT_COUNTS flag is set, the switch does not need to keep track of the flow packet count. When the OFPFF_NO_BYT_COUNTS flag is set, the switch does not need to keep track of the flow byte count. Setting those flags may decrease the processing load on some OpenFlow switches, however those counters may not be available in flow statistics and flow removed messages for this flow entry. A switch is not required to honor those flags and may keep track of a flow count and return it despite the corresponding flag being set. If a switch does not keep track of a flow count, the corresponding counter is not available and must be set to the maximum field value (see 5.8).</p> <p>When a flow entry is inserted in a table, its flags field is set with the values from the message. When a flow entry is matched and modified (OFPFC_MODIFY or OFPFC_MODIFY_STRICT messages), the flags field is ignored.</p> <p>The instructions field contains the instruction set for the flow entry when adding or modifying entries. If the instruction set is not valid or supported, the switch must generate an error (see 6.4).</p> <p>Balakrishnan at [0028]-[0035] ("Means for matching 506 correspondingly matches the objects of the network device with the objects associated with the packet based upon the set of criteria, as described in conjunction with FIG. 1. In various embodiments of the invention, means for matching 506 may be a software module. In an embodiment of the invention, the matching is performed by using keywords such as 'match-any' and 'match-all', programmed on an Internetworking Operation Systems (IOS) of the network device. 'Match-any' keyword transmits the packet if at least one criterion from the set of criteria is satisfied. For example, a command for TCP flag filtering, also referred to as Access Control Entry (ACE), entered on the Command Line Interface (CLI) may be,</p>

No.	'111 Patent Claim 16	The Reference
		<p>permit tcp <src> <dst> match-any+syn+ack This ACE allows the packet if at least one of syn and ack are set on the packet. In an embodiment of the invention, the code for 'Match-any' keyword may be as follows:</p> <pre>if ((match_flags & match_mask) (-packet_flags & match_mask))</pre> <p>'Match-all' keyword transmits the packet if each criterion from the set of criteria is satisfied. For example, an ACE, for TCP flag filtering entered on the CLI may be,</p> <pre>permit tcp <src> <dst> match-all+syn+ack+fin</pre> <p>This ACE allows the packet only if syn and ack are set and fin is not set on the packet. In an embodiment of the invention, the code for 'Match-any' keyword may be as follows:</p> <pre>if ((match_flags & match_mask)==(packet_flags & match_mask))</pre> <p>In a further example, an ip ACL for TCP flag filtering entered on the CLI may be,</p> <pre>permit tcp any any match-all+syn+ack+fin</pre> <pre>deny tcp any any match-any+psh-rst</pre> <pre>permit ip any any</pre> <p>The first ACE allows the packet only if syn and ack are set and fin is not set on the packet. The second ACE does not allow the packet if psh is set or rst is not set on the packet. The last ACE accepts all packets.”)</p>

No.	'111 Patent Claim 17	The Reference
17[a]	The method according to claim 16, wherein the packet is an Transmission Control Protocol (TCP) packet, and	<p>The Reference discloses the method according to claim 16, wherein the packet is an Transmission Control Protocol (TCP) packet.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of Orckit Exhibit 2019.</p>

No.	'111 Patent Claim 17	The Reference
		<p>the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Balakrishnan, Khan '478, Wang '735, and Olofsson '254.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 24-25</p> <p>6.3 OpenFlow Channel Connections</p> <p>The OpenFlow channel is used to exchange OpenFlow message between an OpenFlow switch and an OpenFlow controller. A typical OpenFlow controller manages multiple OpenFlow channels, each one to a different OpenFlow switch. An OpenFlow switch may have one OpenFlow channel to a single controller, or multiple channels for reliability, each to a different controller (see 6.3.4).</p> <p>An OpenFlow controller typically manages an OpenFlow switch remotely over one or more networks. The specification of the networks used for the OpenFlow channels is outside the scope of the present specification. It may be a separate dedicated network, or the OpenFlow channel may use the network managed by the OpenFlow switch (in-band controller connection). The only requirement is that it should provide TCP/IP connectivity.</p> <p>The OpenFlow channel is usually instantiated as a single network connection, using TLS or plain TCP (see 6.3.3). The OpenFlow channel may be composed of multiple network connections to exploit parallelism (see 6.3.5). The OpenFlow switch always initiates a connection to an OpenFlow controller (see 6.3.1).</p> <p>6.3.1 Connection Setup</p> <p>The switch must be able to establish communication with a controller at a user-configurable (but otherwise fixed) IP address, using a user-specified port. If the switch knows the IP address of the controller, the switch initiates a standard TLS or TCP connection to the controller. Traffic to and from the OpenFlow channel is not run through the OpenFlow pipeline. Therefore, the switch must identify incoming traffic as local before checking it against the flow tables.</p> <p>When an OpenFlow connection is first established, each side of the connection must immediately send an <code>OFPT_HELLO</code> message with the <code>version</code> field set to the highest OpenFlow protocol version supported by the sender. Upon receipt of this message, the recipient may calculate the OpenFlow protocol version to be used as the smaller of the version number that it sent and the one that it received.</p> <p>If the negotiated version is supported by the recipient, then the connection proceeds. Otherwise, the recipient must reply with an <code>OFPT_ERROR</code> message with a <code>type</code> field of <code>OFPET_HELLO_FAILED</code>, a <code>code</code> field of <code>OFPHFC_COMPATIBLE</code>, and optionally an ASCII string explaining the situation in <code>data</code>, and then terminate the connection.</p>

No.	'111 Patent Claim 17	The Reference
		<p>Balakrishnan at [0018] (“FIG. 1 is a flowchart illustrating a method for managing network traffic in a network device, in accordance with an exemplary embodiment of the invention. At step 102, a plurality of objects associated with a packet of the network traffic is provided. A packet generally refers to a unit of data, which can be of any protocol type. A packet may be a Transmission Control Protocol (TCP) packet. The objects associated with the packet may be, for example, a source port of the packet and a destination port of the packet.”)</p> <p>Balakrishnan at [0019] (“A set of criteria corresponding to the type of objects is then created, at step 104. In various embodiments of the invention, the set of criteria corresponds to at least one packet field associated with a configuration of the network device. A packet flag refers to one of the packet fields. In various embodiments of the invention, a packet flag corresponds to the type of objects. For example, if the packet is a TCP packet, then the packet flag is a TCP flag. Exemplary TCP flags may be for example, syn, ack, fin, urg, psh, and rst. In an embodiment of the invention, the set of criteria include at least one ‘set criterion’ for selecting the packet. In another embodiment of the invention, the set of criteria include at least one ‘not-set criterion’ for rejecting the packet. In yet another embodiment of the invention, the set of criteria includes at least one ‘set criterion’ and at least one ‘not-set criterion’ for managing the network traffic in the network device. Further, in various embodiments of the invention, if a packet flag is not present in the ACL, then it does not matter if the packet flag is present or not present in the packet.”)</p> <p>Balakrishnan at [0026] (“Means for providing 502 provides a plurality of objects associated with a packet of the network traffic. The objects associated with the packet may be, for example the packet fields. In an embodiment of the invention, the object associated with a TCP packet may be TCP flags. In various embodiments of the invention, means for providing 502 may be a software module.”)</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit’s ’111 patent, including use of a network node for routing network traffic using a TCP protocol. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan ’478 • Wang ’735

No.	'111 Patent Claim 17	The Reference
		<ul style="list-style-type: none"> • Olofsson '254 <p>Khan '478 at 4:10-15 ("In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA].")</p> <p>Wang '735 at 1:41-62 ("In one embodiment, a method generally comprises receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network.</p> <p>In another embodiment, an apparatus generally comprises a performance manager for measuring application performance for application traffic received at the apparatus, a UDP optimizer for optimizing UDP applications, a TCP optimizer for optimizing TCP applications, a policy manager for processing policy input received at the apparatus, and a plurality of queues for queuing the application traffic received at the apparatus based on input from the performance manager and policy manager. The application traffic shares available bandwidth in a wide area network in accordance with the measured performance and policy input.")</p> <p>Wang '735 at 2:38-62 ("The embodiments described herein provide a complete solution to effectively integrate all optimization features to minimize deployment cost and provide additional features and value to users. As described in detail below, the embodiments manage business critical applications, real time voice/ video, and best effort traffic automatically and effectively. The embodiments provide for efficient use of network resources for TCP traffic and UDP traffic including real-time communications and non-real-time streaming. The embodiments may be used, for example, to effectively optimize UDP-based real-time conversational UC&C (Unified Communications and Collaboration) applications. Different types of application traffic contending the WAN bandwidth are</p>

No.	'111 Patent Claim 17	The Reference
		<p>managed together and a system-level and network-level architecture for a complete solution is provided. This allows for pervasive video deployment, management of application performance requirements, and effective delivery of quality of services to networked applications and users. As described below, WAN optimization system components may be embedded in a network device such as a router, therefore eliminating the need for additional network devices. The WAN optimization system components may operate at a branch office, data center, or Internet edge, thus eliminating the need for different solutions for different network locations.”)</p> <p>Wang '735 at 6:55-7:2 (“FIG. 3 illustrates an example of the WAN optimization system 18 at one of the optimization system devices 12. The system 18 includes a performance manager (APM (application performance manager)/VQM (video/voice quality manager)) 30, policy manager 32, TCP-based application optimization module 34, UDP-based application optimization module 36, and scheduling and queuing module 38. The node 12 receives input (signaling) to the optimization system 18 from other nodes (e.g., routers with optimization system installed, endpoints 10). The node 12 also sends optimization system output to other routers and endpoints 10. Policy input is provided to the node 12 from an external policy source, as described below. The input may be received from downstream and upstream devices.”)</p> <p>Wang '735 at 8:24-35 (“The TCP-based application optimization module 34 is configured to optimize all TCP-based applications (also referred to herein as TCP applications), including streaming video and audio. In one embodiment, the optimization module 34 is a Cisco WAAS (Wide Area Application Services) module. The optimization module 34 may request additional bandwidth from the scheduler 38 to meet application performance baseline requirements, for example. The scheduler 38 may proactively throttle the TCP output to yield more bandwidth to higher priority UDP-based video applications that are managed by the media optimization module 36, based on policy and performance baselines.”)</p> <p>Olofsson '254 at 2:27-44 (“In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay network on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100</p>

No.	'111 Patent Claim 17	The Reference
		<p>includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p>
17[b]	wherein the one or more flag bits comprises comprise a SYN flag bit, an ACK flag bit, a FIN flag bit, a RST flag bit, or any combination thereof.	<p>The Reference discloses wherein the one or more flag bits comprises comprise a SYN flag bit, an ACK flag bit, a FIN flag bit, a RST flag bit, or any combination thereof.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>Balakrishnan at [0019] (“A set of criteria corresponding to the type of objects is then created, at step 104. In various embodiments of the invention, the set of criteria corresponds to at least one packet field associated with a configuration of the network device. A packet flag refers to one of the packet fields. In various embodiments of the invention, a packet flag corresponds to the type of objects. For example, if the packet is a TCP packet, then the packet flag is a TCP flag. Exemplary TCP flags may be for example, syn, ack, fin, urg, psh, and rst. In an embodiment of the invention, the set of criteria include at least one 'set criterion' for selecting the packet. In another embodiment of the invention, the set of criteria include at least one 'not-set criterion' for rejecting the packet. In yet another embodiment of the invention, the set of criteria includes at least one 'set criterion' and at least one 'not-set criterion'.”)</p>

No.	'111 Patent Claim 17	The Reference
		<p>criterion' for managing the network traffic in the network device. Further, in various embodiments of the invention, if a packet flag is not present in the ACL, then it does not matter if the packet flag is present or not present in the packet.”)</p> <p>Balakrishnan at [0026] (“Means for providing 502 provides a plurality of objects associated with a packet of the network traffic. The objects associated with the packet may be, for example the packet fields. In an embodiment of the invention, the object associated with a TCP packet may be TCP flags. In various embodiments of the invention, means for providing 502 may be a software module.”)</p> <p>Balakrishnan at [0028]-[0035] (“Means for matching 506 correspondingly matches the objects of the network device with the objects associated with the packet based upon the set of criteria, as described in conjunction with FIG. 1. In various embodiments of the invention, means for matching 506 may be a software module. In an embodiment of the invention, the matching is performed by using keywords such as 'match-any' and 'match-all', programmed on an Internetworking Operation Systems (IOS) of the network device. 'Match-any' keyword transmits the packet if at least one criterion from the set of criteria is satisfied. For example, a command for TCP flag filtering, also referred to as Access Control Entry (ACE), entered on the Command Line Interface (CLI) may be,</p> <pre>permit tcp <src> <dst> match-any+syn+ack</pre> <p>This ACE allows the packet if at least one of syn and ack are set on the packet. In an embodiment of the invention, the code for 'Match-any' keyword may be as follows:</p> <pre>if ((match_flags & match_mask) '(-packet_flags & match_mask))</pre> <p>'Match-all' keyword transmits the packet if each criterion from the set of criteria is satisfied. For example, an ACE, for TCP flag filtering entered on the CLI may be,</p> <pre>permit tcp <src> <dst> match-all+syn+ack-fin</pre> <p>This ACE allows the packet only if syn and ack are set and fin is not set on the packet. In an embodiment of the invention, the code for 'Match-any' keyword may be as follows:</p> <pre>if ((match_flags & match_mask)==(packet_flags</pre>

No.	'111 Patent Claim 17	The Reference
		<p>& match_mask))</p> <p>In a further example, an ip ACL for TCP flag filtering entered on the CLI may be,</p> <p>permit tcp any any match-all+syn+ack-fin</p> <p>deny tcp any any match-any+psh-rst</p> <p>permit ip any any</p> <p>The first ACE allows the packet only if syn and ack are set and fin is not set on the packet. The second ACE does not allow the packet if psh is set or rst is not set on the packet. The last ACE accepts all packets.”)</p>

No.	'111 Patent Claim 18	The Reference
18[a]	The method according to claim 1, wherein the packet comprises distinct header and payload fields,	<p>The Reference discloses the method according to claim 1, wherein the packet comprises distinct header and payload fields.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic comprised of packets with headers and payloads. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254

Orckit Exhibit 2019

No.	'111 Patent Claim 18	The Reference
		<ul style="list-style-type: none"> • Kumar '739 <p>Khan '478 at 7:31-49 ("TLOC: A TLOC is similar to the NEXT HOP attribute in BGP and is carried in the overlay route NLRI with a type value of 1. The actual TLOC is not carried as an immediate attribute to the prefix, but rather the System-IP of the OMP speaker originating the overlay route. Carrying the System-IP allows for the mapping between overlay routes and TLOCs irrespectively of what the actual TLOC happens to be. This is important since TLOCs can change and will change when traversing NATs, something that OMP is designed to take into consideration. This TLOC attribute points the TLOC AFI/SAFI. Within the SAFI for each TLOC, the detailed information on each specific TLOC can be found. This includes detailed information on the actual next-hop address to use, the actual TLOC. This information includes the public IP address of the TLOC and if NAT is involved, the private and non-translated TLOC-address. This separation of information allows for individual advertisement and invalidation of overlay routes or TLOCs without having to invalidate the other dependent entity.")</p> <p>Wang '735 at 5:20-39 ("In one example, routers 12 at the ends of WAN link 16 comprise the optimization system 18. An RTP (Real-time Transport Protocol) trunk may be used between two adjacent optimization system devices 12 on a media path. For example, as shown in FIG. 1, the RTP trunk may be between two optimization system devices 12 connected by WAN link 16 on which bandwidth optimization is needed. The RTP trunk is preferably configured to support one of more of the following features to reduce overhead, reduce redundant copies of streams, create a branch out RTP trunk, or carry additional flags or markings. For example, RTP header compression and session multiplexing may be used to reduce overhead. Data Redundancy Elimination (DRE) may be used with UDP-based real-time multimedia applications to reduce redundant copies of video/audio streams in a multiple point conference or live-streaming applications. In order to provide DRE, both WAN optimization devices 12 maintain a synchronization RTP payload cache. The RTP header may be preserved over the trunk and the payload may be encoded by an index in a cache buffer, for example.")</p> <p>Wang '735 at 11:27-49 ("Another media optimization mechanism that the system may utilize is congestion control for variable bit-rate video applications. Congestion may occur, for example, when over-subscription occurs and applications generate more traffic than a</p>

No.	'111 Patent Claim 18	The Reference
		<p>network link can transport, when multiple video encoders send burst traffic onto the network, when there is no QoS provisioning or inappropriate QoS settings on router, or when network bandwidth changes mid-session. In these cases, the router has to drop packets if no buffer is available to store the excess traffic. Conversation video streams should not be buffered during congestion in order to minimize the delay and jitter. Techniques that may be used include video DPI (Deep Packet Inspection) and video specific application parsing (e.g., parse the H.264 RTP header and video payload) to extract video specification information from a flow or from multiple packets of the flow, such as priority, entropy of a packet or flow, video quality score, frame boundary, etc. Intelligent (selective) packet dropping to drop less important packets first or SVC (Scalable Video Coding) layer filtering and forwarding may also be used. Bandwidth and resource CAC may be used during session setup and mid-call (e.g., preemption, over-subscription/down speeding, resume/re-cover).”)</p> <p>Olofsson '254 at 7:29-43 (“In one embodiment, each service router in the path of a service chain accepts inbound traffic based on the destination TLOC and VPN Label in the received packet and forwards it out the associated interface for the specific service being associated with the TLOC/Label combination. In the outbound direction, each service router must be equipped with policy describing what the next hop is for the particular destination. This allows for each service router to support multiple service chains and different policies for each direction of traffic. Since the outgoing direction is controlled by policy, this allows for great flexibility in choosing the next point in the service chain based on individually defined criteria for that service chain, service, or service router.”)</p> <p>Kumar '739 at 1:62-2:22 (“Service chaining primarily involves the interception of traffic and steering the traffic through a series of service nodes (i.e., physical or virtual devices) that each host one or more service-functions. The traffic is intercepted through the use of a classifier function at a node (i.e., switch, router, etc.) that serves as a head-end node to the service chain. The node that executes the classifier function is sometimes referred to herein as a “classifier” or “classifier node.” In general, the traffic is steered from the classifier through the service-functions using one or more Layer 2 (L2)/Layer 3 (L3) service overlays in the network. In addition, a service header is appended to the traffic for forwarding through the service chain and the service header enables the carrying of service metadata in addition to the original data/payload.</p>

No.	'111 Patent Claim 18	The Reference
		<p>A service header is part of the data-plane of a service chain and includes metadata specifically formatted for consumption by a service-function. The metadata may include, for example, an application identifier (ID), flow or path ID, and client or user ID, network classification information used for deriving targeted service policies and profiles, common metadata related to a particular service such as finer classification that can be passed to the service-functions further down the service-path. In other words, service-functions benefit from metadata derived both from the network as well as the service-functions that form a given service chain. Metadata can also be passed between network nodes and be used, for example, to determine forwarding state at the end of a service chain.”)</p> <p>Kumar '739 at 3:47-65 (“The service nodes 35, 40, 45, 50, and 55 each host/support one or more service-functions (services) for application to the payload of traffic passing through the respective service node. More specifically, service node 35 hosts service-functions 65(1) (service-function f1), 65(2) (service-function f2), and 65(3) (service-function f3), while service node 40 hosts service-functions 65(3) (service-function f3), 65(5)(service-function f5), 65(6) (service-function f6), and 65(7)(service-function f7). Service node 45 hosts service-functions 65(1) (service-function f1), 65(5) (service-function f5), and 65(10) (service-function f10), while service node 50 hosts service-functions 65(3) (service-function f3), 65(5)(service-function f5), and 65(10) (service-function f10). Finally, service node 55 hosts service-functions 65(2) (service-function f2) and 65(3) (service-function f3). As shown, service-functions may appear in multiple instances on different service nodes or on the same service node. For example, service-function f3 is hosted on each of the service nodes 35, 40, 50 and 55.”)</p> <p>Kumar '739 at 5:34-47 (“In the example of FIG. 2, classification and mapping logic 75 selects service-functions from several different service nodes. In particular, classification and mapping logic 75 selects service-functions f1 and f2 at service node 35, ser-vice-functions f6 and f7 at service node 40, and service-function f10 at service node 45. The path for service-function chain SFC1 selected by classification and mapping logic 75 is shown in FIG. 2 by broken line 100. The classifier 30 sends traffic 90 along the path 100 using one or more L2/L3/L4 service overlays in the network. In other words, a service header is appended to the traffic 90 for forwarding through the service chain and the service header enables the carrying of service metadata in addition to the original data/payload.”)</p>

No.	'111 Patent Claim 18	The Reference
18[b]	<p>the header comprises at least the first and second entities addresses in the packet network, and</p>	<p>The Reference discloses the header comprises at least the first and second entities addresses in the packet network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 24-25</p> <p>6.3 OpenFlow Channel Connections</p> <p>The OpenFlow channel is used to exchange OpenFlow message between an OpenFlow switch and an OpenFlow controller. A typical OpenFlow controller manages multiple OpenFlow channels, each one to a different OpenFlow switch. An OpenFlow switch may have one OpenFlow channel to a single controller, or multiple channels for reliability, each to a different controller (see 6.3.4).</p> <p>An OpenFlow controller typically manages an OpenFlow switch remotely over one or more networks. The specification of the networks used for the OpenFlow channels is outside the scope of the present specification. It may be a separate dedicated network, or the OpenFlow channel may use the network managed by the OpenFlow switch (in-band controller connection). The only requirement is that it should provide TCP/IP connectivity.</p> <p>The OpenFlow channel is usually instantiated as a single network connection, using TLS or plain TCP (see 6.3.3). The OpenFlow channel may be composed of multiple network connections to exploit</p>

No.	'111 Patent Claim 18	The Reference
		<p>parallelism (see [6.3.5]). The OpenFlow switch always initiates a connection to an OpenFlow controller (see [6.3.1]).</p> <p>6.3.1 Connection Setup</p> <p>The switch must be able to establish communication with a controller at a user-configurable (but otherwise fixed) IP address, using a user-specified port. If the switch knows the IP address of the controller, the switch initiates a standard TLS or TCP connection to the controller. Traffic to and from the OpenFlow channel is not run through the OpenFlow pipeline. Therefore, the switch must identify incoming traffic as local before checking it against the flow tables.</p> <p>When an OpenFlow connection is first established, each side of the connection must immediately send an OFPT_HELLO message with the version field set to the highest OpenFlow protocol version supported by the sender. Upon receipt of this message, the recipient may calculate the OpenFlow protocol version to be used as the smaller of the version number that it sent and the one that it received.</p> <p>If the negotiated version is supported by the recipient, then the connection proceeds. Otherwise, the recipient must reply with an OFPT_ERROR message with a type field of OFPET_HELLO_FAILED, a code field of OFPHFC_COMPATIBLE, and optionally an ASCII string explaining the situation in data, and then terminate the connection.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic comprised of packets with source, destination, and port addresses. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Olofsson '254 • Kumar '739 <p>Khan '478 at 7:31-49 (“TLOC: A TLOC is similar to the NEXT HOP attribute in BGP and is carried in the overlay route NLRI with a type value of 1. The actual TLOC is not carried as an immediate attribute to the prefix, but rather the System-IP of the OMP speaker originating the overlay route. Carrying the System-IP allows for the mapping between overlay routes and TLOCs irrespectively of what the actual TLOC happens to be. This is important since TLOCs can change and will change when traversing NATs, something that OMP is designed to take into consideration. This TLOC attribute points the TLOC AFI/SAFI. Within the SAFI for each TLOC, the detailed information on each specific TLOC</p>

No.	'111 Patent Claim 18	The Reference
		<p>can be found. This includes detailed information on the actual next-hop address to use, the actual TLOC. This information includes the public IP address of the TLOC and if NAT is involved, the private and non-translated TLOC-address. This separation of information allows for individual advertisement and invalidation of overlay routes or TLOCs without having to invalidate the other dependent entity.”)</p> <p>Olofsson '254 at 2:45-63 (“In one embodiment, in order for the overlay control plane protocol to provide a functional architecture, it distributes overlay routes that are learned from each location where an overlay network element is present, together with external addresses used as next-hop addresses for the overlay routes. The external addresses may be assigned to the physical interfaces of the overlay network elements that attach to the underlying network 108. In one embodiment, the overlay routes may only be accessed through the overlay network 100 and the next-hop addresses can only be reached through the underlying network 108. Together, the overlay routes and next-hop addresses provide for a complete and functional overlay architecture, as will be explained. As far as the underlying network 108 is concerned, the only element used to forward traffic between the sites is the next-hop address. The underlying network 108 does not know about any other routes, addresses or labels that may be used for providing a functional network infrastructure within the overlay network 100 itself.”)</p> <p>Olofsson '254 at 5:1-7 (“Block 404: Every Edge-router receives an outbound policy (for traffic towards the Internet) stipulating that all the traffic matching the routes received from Hub3, will be encapsulated in a packet with a Service-label of 1, matching the Firewall Service, and a next-hop address of Hub2. This will ensure all traffic destined for the Internet is using the tunnel from the Edge-router to Hub2”)</p> <p>Kumar '739 at 5:61-6:6 (“In certain circumstances, service-functions may change the flow specification (e.g., the 5-tuple comprises the source IP address, destination IP address, source port number, destination port number and the protocol in use) of processed packets. That is, traffic may be received at a service-function with a certain 5 tuple, but, after processing at the service-function, the traffic will include a different 5 tuple. When the flow specification of traffic is changed, the traffic may need to be processed by different service-functions than those identified in the initial service-function chain. Service-functions that are</p>

No.	'111 Patent Claim 18	The Reference
		capable of changing the flow specification of traffic are sometimes referred to herein as modifying service-functions.”)
18[c]	wherein the packet-applicable criterion is that the first entity address, the second entity address, or both match a predetermined address or addresses.	<p>The Reference discloses wherein the packet-applicable criterion is that the first entity address, the second entity address, or both match a predetermined address or addresses.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 24-25</p> <p>6.3 OpenFlow Channel Connections</p> <p>The OpenFlow channel is used to exchange OpenFlow message between an OpenFlow switch and an OpenFlow controller. A typical OpenFlow controller manages multiple OpenFlow channels, each one to a different OpenFlow switch. An OpenFlow switch may have one OpenFlow channel to a single controller, or multiple channels for reliability, each to a different controller (see 6.3.4).</p> <p>An OpenFlow controller typically manages an OpenFlow switch remotely over one or more networks. The specification of the networks used for the OpenFlow channels is outside the scope of the present specification. It may be a separate dedicated network, or the OpenFlow channel may use the network managed by the OpenFlow switch (in-band controller connection). The only requirement is that it should provide TCP/IP connectivity.</p> <p>The OpenFlow channel is usually instantiated as a single network connection, using TLS or plain TCP (see 6.3.3). The OpenFlow channel may be composed of multiple network connections to exploit</p>

No.	'111 Patent Claim 18	The Reference
		<p>parallelism (see [6.3.5]). The OpenFlow switch always initiates a connection to an OpenFlow controller (see [6.3.1]).</p> <p>6.3.1 Connection Setup</p> <p>The switch must be able to establish communication with a controller at a user-configurable (but otherwise fixed) IP address, using a user-specified port. If the switch knows the IP address of the controller, the switch initiates a standard TLS or TCP connection to the controller. Traffic to and from the OpenFlow channel is not run through the OpenFlow pipeline. Therefore, the switch must identify incoming traffic as local before checking it against the flow tables.</p> <p>When an OpenFlow connection is first established, each side of the connection must immediately send an OFPT_HELLO message with the version field set to the highest OpenFlow protocol version supported by the sender. Upon receipt of this message, the recipient may calculate the OpenFlow protocol version to be used as the smaller of the version number that it sent and the one that it received.</p> <p>If the negotiated version is supported by the recipient, then the connection proceeds. Otherwise, the recipient must reply with an OFPT_ERROR message with a type field of OFPET_HELLO_FAILED, a code field of OFPHFC_COMPATIBLE, and optionally an ASCII string explaining the situation in data, and then terminate the connection.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic comprised of packets with source, destination, and port addresses. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Olofsson '254 • Kumar '739 <p>Khan '478 at 7:31-49 (“TLOC: A TLOC is similar to the NEXT HOP attribute in BGP and is carried in the overlay route NLRI with a type value of 1. The actual TLOC is not carried as an immediate attribute to the prefix, but rather the System-IP of the OMP speaker originating the overlay route. Carrying the System-IP allows for the mapping between overlay routes and TLOCs irrespectively of what the actual TLOC happens to be. This is important since TLOCs can change and will change when traversing NATs, something that OMP is designed to take into consideration. This TLOC attribute points the TLOC AFI/SAFI. Within the SAFI for each TLOC, the detailed information on each specific TLOC can be found. This includes detailed information on the actual next-hop address to use.”)</p>

No.	'111 Patent Claim 18	The Reference
		<p>actual TLOC. This information includes the public IP address of the TLOC and if NAT is involved, the private and non-translated TLOC-address. This separation of information allows for individual advertisement and invalidation of overlay routes or TLOCs without having to invalidate the other dependent entity.”)</p> <p>Olofsson '254 at 2:45-63 (“In one embodiment, in order for the overlay control plane protocol to provide a functional architecture, it distributes overlay routes that are learned from each location where an overlay network element is present, together with external addresses used as next-hop addresses for the overlay routes. The external addresses may be assigned to the physical interfaces of the overlay network elements that attach to the underlying network 108. In one embodiment, the overlay routes may only be accessed through the overlay network 100 and the next-hop addresses can only be reached through the underlying network 108. Together, the overlay routes and next-hop addresses provide for a complete and functional overlay architecture, as will be explained. As far as the underlying network 108 is concerned, the only element used to forward traffic between the sites is the next-hop address. The underlying network 108 does not know about any other routes, addresses or labels that may be used for providing a functional network infrastructure within the overlay network 100 itself.”)</p> <p>Olofsson '254 at 5:1-7 (“Block 404: Every Edge-router receives an outbound policy (for traffic towards the Internet) stipulating that all the traffic matching the routes received from Hub3, will be encapsulated in a packet with a Service-label of 1, matching the Firewall Service, and a next-hop address of Hub2. This will ensure all traffic destined for the Internet is using the tunnel from the Edge-router to Hub2”)</p> <p>Kumar '739 at 5:61-6:6 (“In certain circumstances, service-functions may change the flow specification (e.g., the 5-tuple comprises the source IP address, destination IP address, source port number, destination port number and the protocol in use) of processed packets. That is, traffic may be received at a service-function with a certain 5 tuple, but, after processing at the service-function, the traffic will include a different 5 tuple. When the flow specification of traffic is changed, the traffic may need to be processed by different service-functions than those identified in the initial service-function chain. Service-functions that are capable of changing the flow specification of traffic are sometimes referred to herein as modifying service-functions.”)</p>

No.	'111 Patent Claim 19	The Reference
19	The method according to claim 18, wherein the addresses are Internet Protocol (IP) addresses.	<p>The Reference discloses the method according to claim 18, wherein the addresses are Internet Protocol (IP) addresses.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, and Balakrishnan,</p> <p>Below are examples of such references.</p> <p>OpenFlow at 24-25</p> <p>6.3 OpenFlow Channel Connections</p> <p>The OpenFlow channel is used to exchange OpenFlow message between an OpenFlow switch and an OpenFlow controller. A typical OpenFlow controller manages multiple OpenFlow channels, each one to a different OpenFlow switch. An OpenFlow switch may have one OpenFlow channel to a single controller, or multiple channels for reliability, each to a different controller (see 6.3.4).</p> <p>An OpenFlow controller typically manages an OpenFlow switch remotely over one or more networks. The specification of the networks used for the OpenFlow channels is outside the scope of the present specification. It may be a separate dedicated network, or the OpenFlow channel may use the network managed by the OpenFlow switch (in-band controller connection). The only requirement is that it should provide TCP/IP connectivity.</p> <p>The OpenFlow channel is usually instantiated as a single network connection, using TLS or plain TCP (see 6.3.3). The OpenFlow channel may be composed of multiple network connections to exploit</p>

No.	'111 Patent Claim 19	The Reference
		<p>parallelism (see 6.3.5). The OpenFlow switch always initiates a connection to an OpenFlow controller (see 6.3.1).</p> <p>6.3.1 Connection Setup</p> <p>The switch must be able to establish communication with a controller at a user-configurable (but otherwise fixed) IP address, using a user-specified port. If the switch knows the IP address of the controller, the switch initiates a standard TLS or TCP connection to the controller. Traffic to and from the OpenFlow channel is not run through the OpenFlow pipeline. Therefore, the switch must identify incoming traffic as local before checking it against the flow tables.</p> <p>When an OpenFlow connection is first established, each side of the connection must immediately send an OFPT_HELLO message with the <code>version</code> field set to the highest OpenFlow protocol version supported by the sender. Upon receipt of this message, the recipient may calculate the OpenFlow protocol version to be used as the smaller of the version number that it sent and the one that it received.</p> <p>If the negotiated version is supported by the recipient, then the connection proceeds. Otherwise, the recipient must reply with an OFPT_ERROR message with a <code>type</code> field of OFPET_HELLO_FAILED, a <code>code</code> field of OFPHFC_COMPATIBLE, and optionally an ASCII string explaining the situation in <code>data</code>, and then terminate the connection.</p> <p>Balakrishnan at [0018] (“FIG. 1 is a flowchart illustrating a method for managing network traffic in a network device, in accordance with an exemplary embodiment of the invention. At step 102, a plurality of objects associated with a packet of the network traffic is provided. A packet generally refers to a unit of data, which can be of any protocol type. A packet may be a Transmission Control Protocol (TCP) packet. The objects associated with the packet may be, for example, a source port of the packet and a destination port of the packet.”)</p>

No.	'111 Patent Claim 20	The Reference
20[a]	<p>The method according to claim 1, wherein the packet is an Transmission Control Protocol (TCP) packet that comprises source and destination TCP ports, a TCP sequence number, and TCP sequence mask fields, and</p>	<p>The Reference discloses the method according to claim 1, wherein the packet is an Transmission Control Protocol (TCP) packet that comprises source and destination TCP ports, a TCP sequence number, and TCP sequence mask fields.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>Balakrishnan at [0018] ("FIG. 1 is a flowchart illustrating a method for managing network traffic in a network device, in accordance with an exemplary embodiment of the invention. At step 102, a plurality of objects associated with a packet of the network traffic is provided. A packet generally refers to a unit of data, which can be of any protocol type. A packet may be a Transmission Control Protocol (TCP) packet. The objects associated with the packet may be, for example, a source port of the packet and a destination port of the packet.")</p> <p>Balakrishnan at [0019] ("A set of criteria corresponding to the type of objects is then created, at step 104. In various embodiments of the invention, the set of criteria corresponds to at least one packet field associated with a configuration of the network device. A packet flag refers to one of the packet fields. In various embodiments of the invention, a packet flag corresponds to the type of objects. For example, if the packet is a TCP packet, then the packet flag is a TCP flag. Exemplary TCP flags may be for example, syn, ack, fin, urg, psh, and rst. In an embodiment of the invention, the set of criteria include at least one 'set criterion' for selecting the packet. In another embodiment of the invention, the set of criteria include at least one 'not-set criterion' for rejecting the packet. In yet another embodiment of the invention, the set of criteria includes at least one 'set criterion' and at least one 'not-set criterion' for managing the network traffic in the network device. Further, in various embodiments of the invention,</p>

No.	'111 Patent Claim 20	The Reference
		<p>if a packet flag is not present in the ACL, then it does not matter if the packet flag is present or not present in the packet.”)</p>
20[b]	<p>wherein the packet-applicable criterion is that the source TCP port, the destination TCP port, the TCP sequence number, the TCP sequence mask, or any combination thereof, matches a predetermined value or values.</p>	<p>The Reference discloses wherein the packet-applicable criterion is that the source TCP port, the destination TCP port, the TCP sequence number, the TCP sequence mask, or any combination thereof, matches a predetermined value or values.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 24-25</p> <p>6.3 OpenFlow Channel Connections</p> <p>The OpenFlow channel is used to exchange OpenFlow message between an OpenFlow switch and an OpenFlow controller. A typical OpenFlow controller manages multiple OpenFlow channels, each one to a different OpenFlow switch. An OpenFlow switch may have one OpenFlow channel to a single controller, or multiple channels for reliability, each to a different controller (see [6.3.4]).</p> <p>An OpenFlow controller typically manages an OpenFlow switch remotely over one or more networks. The specification of the networks used for the OpenFlow channels is outside the scope of the present specification. It may be a separate dedicated network, or the OpenFlow channel may use the network managed by the OpenFlow switch (in-band controller connection). The only requirement is that it should provide TCP/IP connectivity.</p> <p>The OpenFlow channel is usually instantiated as a single network connection, using TLS or plain TCP (see [6.3.3]). The OpenFlow channel may be composed of multiple network connections to exploit</p>

No.	'111 Patent Claim 20	The Reference
		<p>parallelism (see 6.3.5). The OpenFlow switch always initiates a connection to an OpenFlow controller (see 6.3.1).</p> <p>6.3.1 Connection Setup</p> <p>The switch must be able to establish communication with a controller at a user-configurable (but otherwise fixed) IP address, using a user-specified port. If the switch knows the IP address of the controller, the switch initiates a standard TLS or TCP connection to the controller. Traffic to and from the OpenFlow channel is not run through the OpenFlow pipeline. Therefore, the switch must identify incoming traffic as local before checking it against the flow tables.</p> <p>When an OpenFlow connection is first established, each side of the connection must immediately send an OFPT_HELLO message with the version field set to the highest OpenFlow protocol version supported by the sender. Upon receipt of this message, the recipient may calculate the OpenFlow protocol version to be used as the smaller of the version number that it sent and the one that it received.</p> <p>If the negotiated version is supported by the recipient, then the connection proceeds. Otherwise, the recipient must reply with an OFPT_ERROR message with a type field of OFPET_HELLO_FAILED, a code field of OFPHFC_COMPATIBLE, and optionally an ASCII string explaining the situation in data, and then terminate the connection.</p> <p>Balakrishnan at [0019] (“A set of criteria corresponding to the type of objects is then created, at step 104. In various embodiments of the invention, the set of criteria corresponds to at least one packet field associated with a configuration of the network device. A packet flag refers to one of the packet fields. In various embodiments of the invention, a packet flag corresponds to the type of objects. For example, if the packet is a TCP packet, then the packet flag is a TCP flag. Exemplary TCP flags may be for example, syn, ack, fin, urg, psh, and rst. In an embodiment of the invention, the set of criteria include at least one ‘set criterion’ for selecting the packet. In another embodiment of the invention, the set of criteria include at least one ‘not-set criterion’ for rejecting the packet. In yet another embodiment of the invention, the set of criteria includes at least one ‘set criterion’ and at least one ‘not-set criterion’ for managing the network traffic in the network device. Further, in various embodiments of the invention, if a packet flag is not present in the ACL, then it does not matter if the packet flag is present or not present in the packet.”)</p>

No.	'111 Patent Claim 21	The Reference
21	<p>The method according to claim 1, wherein the packet network comprises a Wide Area Network (WAN), Local Area Network (LAN), the Internet, Metropolitan Area Network (MAN), Internet service Provider (ISP) backbone datacenter network, or inter - datacenter network.</p>	<p>The Reference discloses the method according to claim 1, wherein the packet network comprises a Wide Area Network (WAN), Local Area Network (LAN), the Internet, Metropolitan Area Network (MAN), Internet service Provider (ISP) backbone datacenter network, or inter - datacenter network.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Khan '478, Wang '735, and Olofsson '254.</p> <p>Below are examples of such references.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic over a variety of networks. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254 <p>Khan '478 at 3:15-22 (“In use, the overlay domain (OD) 100 may rely on a transport network 120 to provide network transport functionality, as will be described later. The transport network 120 may include any wide area network (WAN) and in some embodiments may include the Internet, other public WAN, a Metro Ethernet or MPLS. Typically, the transport network 120 may include circuits and networks provided by third parties such as carriers, and service providers (SPs).”)</p> <p>Khan '478 at 10:20-38 (“The hardware also typically receives a number of inputs and outputs for communicating information externally. For interface with a user or operator, the hardware</p>

No.	'111 Patent Claim 21	The Reference
		<p>may include one or more user input output devices 806 (e.g., a keyboard, mouse, etc.) and a display 808. For additional storage, the hardware 800 may also include one or more mass storage devices 810, e.g., a Universal Serial Bus (USB) or other removable disk drive, a hard disk drive, a Direct Access Storage Device (DASD), an optical drive (e.g. a Compact Disk (CD) drive, a Digital Versatile Disk (DVD) drive, etc.) and/or a USB drive, among others. Furthermore, the hardware may include an interface with one or more networks 812 (e.g., a local area network (LAN), a wide area network (WAN), a wireless network, and/or the Internet among others) to permit the communication of information with other computers coupled to the networks. It should be appreciated that the hardware typically includes suitable analog and/or digital interfaces between the processor 812 and each of the components, as is well known in the art.”)</p> <p>Wang '735 at Abstract (“In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available band-width in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.”)</p> <p>Wang '735 at 1:41-62 (“In one embodiment, a method generally comprises receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network.</p> <p>In another embodiment, an apparatus generally comprises a performance manager for measuring application performance for application traffic received at the apparatus, a UDP optimizer for optimizing UDP applications, a TCP optimizer for optimizing TCP applications, a policy manager for processing policy input received at the apparatus, and a</p>

No.	'111 Patent Claim 21	The Reference
		<p>plurality of queues for queuing the application traffic received at the apparatus based on input from the performance manager and policy manager. The application traffic shares available band-width in a wide area network in accordance with the measured performance and policy input.”)</p> <p>Wang '735 at 2:12-36 (“Multi-tiered services operate over WANs (Wide Area Net-works) and many applications share bandwidth on the net-work. These include, for example, unmanaged applications (e.g., Internet streaming, Internet VoIP (Voice over Internet Protocol)), video applications (e.g., IP video conference, sur-veillance, video telephony, HD (High Definition) video con-ference), voice applications (e.g., IP Telephony), and data applications (e.g., application sharing, Internet, messaging, e-mail). When deploying video applications on wide area network links that are usually oversubscribed and overloaded, business critical applications compete against lower priority traffic and video applications for bandwidth. Because both TCP (Transmission Control Protocol) and UDP (User Data- gram Protocol) run over the same network, an increase in UDP traffic in conventional systems impacts the performance of applications using TCP. Conventional systems used to deploy video applications do not monitor and optimize the performance of each application. Business productivity applications may be impacted by non-critical traffic without any visibility and protection. Also, unused bandwidth in one queue may not be used by other queues automatically and effectively, which results in inefficiency of a dedicated queue. Conventional systems do not automatically react in real-time to the bandwidth demand needed to maintain satisfied user experience.”)</p> <p>Wang '735 at 3:41-64 (“The embodiments described herein operate in the context of a data communication system including multiple network elements. Referring now to the drawings, and first to FIG. 1, an example of a network in which embodiments described herein may be implemented is shown. The communication system comprises a plurality of endpoints 10 in communica-tion through a plurality of network devices (e.g., routers) 12 and over networks 14. The communication system may include any number of networks (e.g., local area network, metropolitan area network, wide area network, enterprise network, Internet, intranet, radio access network, public switched network, or any other network or combination of networks). The flow path between the endpoints 10 may include any number or type of intermediate nodes (e.g., rout-ers, switches, gateways, management stations, appliances, or</p>

No.	'111 Patent Claim 21	The Reference
		<p>other network devices), which facilitate passage of data between the endpoints. Also, there may be any number of endpoints 10. The endpoints 10 may be located at a branch office, for example, and in communication with an ISR (Inte-grated Services Router) 12 connected to a WAN access link 13. The ISRs may be in communication with ASRs (Aggre-gated Services Router) 12 operating at the network edge, for example. The routers 12 communicate over a wide area net-work.”)</p> <p>Wang '735 at 9:29-49 (“FIG. 4 is a flowchart illustrating an overview of a process for WAN optimization, in accordance with one embodiment. At step 40, the network device (e.g., router 12) receives poli-cies and input from other WAN optimization system nodes and endpoints 10. The network device 12 identifies applica-tion traffic received from a plurality of endpoints 10 and associated with a plurality of applications (step 42). As described above, various methods may be used to recognize applications and identify flows. Flow information may be stored at the device 12. The performance manager 30 moni-tors the input traffic and measures the application perfor-mance at the network device 12 (step 44). As previously described, the application performance may be compared against baseline performance requirements. The router opti-mizes TCP and UDP traffic based on the measured perfor-mance and policy input (step 46). The traffic is queued at the network device (step 48) and transmitted from the network device 12 over the wide area network (step 50). The applica-tion traffic (for UDP and TCP applications) share available WAN bandwidth in accordance with the measured perfor-mance and policy input.</p> <p>Olofsson '254 at 8:60-9:11 (“The hardware also typically receives a number of inputs and outputs for communicating information externally. For interface with a user or operator, the hardware may include one or more user input output devices 606 (e.g., a keyboard, mouse, etc.) and a display 608. For additional storage, the hardware 600 may also include one or more mass storage devices 610, e.g., a Universal Serial Bus (USB) or other removable disk drive, a hard disk drive, a Direct Access Storage Device (DASD), an optical drive (e.g. a Compact Disk (CD) drive, a Digital Versatile Disk (DVD) drive, etc.) and/or a USB drive, among others. Furthermore, the hard-ware may include an interface with one or more networks 612 (e.g., a local area network (LAN), a wide area network (WAN), a wireless network, and/or the Internet among others) to permit the communication of information with other computers coupled to the networks. It should be appreciated that the hardware typically</p>

No.	'111 Patent Claim 21	The Reference
		includes suitable analog and/or digital interfaces between the processor 612 and each of the components, as is well known in the art.”)

No.	'111 Patent Claim 22	The Reference
22	The method according to claim 1, wherein the first entity is a server device and the second entity is a client device, or wherein the first entity is a client device and the second entity is a server device. .	The Reference discloses the method according to claim 1, wherein the first entity is a server device and the second entity is a client device, or wherein the first entity is a client device and the second entity is a server device. To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.

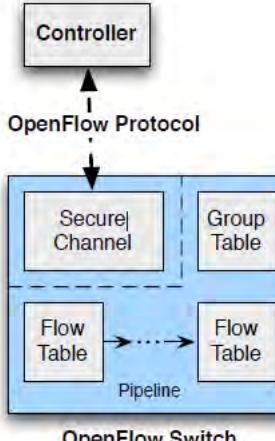
No.	'111 Patent Claim 23	The Reference
23[a]	The method according to claim 22, wherein the server device comprises a web server.	The Reference discloses the method according to claim 22, wherein the server device comprises a web server. To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the Exhibits 2019

No.	'111 Patent Claim 23	The Reference
		the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.
23[b]	wherein the client device comprises a smartphone, a tablet computer, a personal computer, a laptop computer, or a wearable computing device.	<p>The Reference discloses wherein the client device comprises a smartphone, a tablet computer, a personal computer, a laptop computer, or a wearable computing device.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 24	The Reference
24	The method according to claim 22, wherein the communication between the network node and the controller is based on, or uses, a standard protocol.	<p>The Reference discloses the method according to claim 22, wherein the communication between the network node and the controller is based on, or uses, a standard protocol.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 24	The Reference

No.	'111 Patent Claim 27	The Reference
27	The method according to claim 1, wherein the network node comprises a router, a switch, or a bridge.	<p>The Reference discloses the method according to claim 1, wherein the network node comprises a router, a switch, or a bridge.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and OpenFlow.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 6-7</p>

No.	'111 Patent Claim 27	The Reference
		 <p>Figure 1: Main components of an OpenFlow switch.</p> <h2>2 Switch Components</h2> <p>An OpenFlow Switch consists of one or more <i>flow tables</i> and a <i>group table</i>, which perform packet lookups and forwarding, and an <i>OpenFlow channel</i> to an external controller (Figure 1). The switch communicates with the controller and the controller manages the switch via the OpenFlow protocol.</p> <p>Using the OpenFlow protocol, the controller can add, update, and delete <i>flow entries</i> in flow tables, both reactively (in response to packets) and proactively. Each flow table in the switch contains a set of flow entries; each flow entry consists of <i>match fields</i>, <i>counters</i>, and a set of <i>instructions</i> to apply to matching packets (see 5.2).</p> <p>Matching starts at the first flow table and may continue to additional flow tables (see 5.1). Flow entries match packets in priority order, with the first matching entry in each table being used (see 5.3). If a matching entry is found, the instructions associated with the specific flow entry are executed. If no match is found in a flow table, the outcome depends on configuration of the table-miss flow entry: for example, the packet may be forwarded to the controller over the OpenFlow channel, dropped, or may continue to the next flow table (see 5.4).</p> <p>Instructions associated with each flow entry either contain actions or modify pipeline processing (see 5.9). Actions included in instructions describe packet forwarding, packet modification and group table</p>

No.	'111 Patent Claim 27	The Reference
		<p>processing. Pipeline processing instructions allow packets to be sent to subsequent tables for further processing and allow information, in the form of metadata, to be communicated between tables. Table pipeline processing stops when the instruction set associated with a matching flow entry does not specify a next table; at this point the packet is usually modified and forwarded (see [5.10]).</p> <p>Flow entries may forward to a <i>port</i>. This is usually a physical port, but it may also be a logical port defined by the switch or a reserved port defined by this specification (see [4.1]). Reserved ports may specify generic forwarding actions such as sending to the controller, flooding, or forwarding using non-OpenFlow methods, such as “normal” switch processing (see [4.5]), while switch-defined logical ports may specify link aggregation groups, tunnels or loopback interfaces (see [4.4]).</p> <p>Actions associated with flow entries may also direct packets to a group, which specifies additional processing (see [5.6]). Groups represent sets of actions for flooding, as well as more complex forwarding semantics (e.g. multipath, fast reroute, and link aggregation). As a general layer of indirection, groups also enable multiple flow entries to forward to a single identifier (e.g. IP forwarding to a common next hop). This abstraction allows common output actions across flow entries to be changed efficiently.</p> <p>The group table contains group entries; each group entry contains a list of <i>action buckets</i> with specific semantics dependent on group type (see [5.6.1]). The actions in one or more action buckets are applied to packets sent to the group.</p> <p>Switch designers are free to implement the internals in any way convenient, provided that correct match and instruction semantics are preserved. For example, while a flow entry may use an all group to forward to multiple ports, a switch designer may choose to implement this as a single bitmask within the hardware forwarding table. Another example is matching; the pipeline exposed by an OpenFlow switch may be physically implemented with a different number of hardware tables.</p>

No.	'111 Patent Claim 28	The Reference
28	The method according to claim 1, wherein the packet network is an Internet Protocol (IP) network, and the packet is an IP packet.	<p>The Reference discloses the method according to claim 1, wherein the packet network is an Internet Protocol (IP) network, and the packet is an IP packet.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran,</p>

No.	'111 Patent Claim 28	The Reference
		<p>Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, and Balakrishnan.</p> <p>Below are examples of such references.</p> <p>Balakrishnan at [0018] ("FIG. 1 is a flowchart illustrating a method for managing network traffic in a network device, in accordance with an exemplary embodiment of the invention. At step 102, a plurality of objects associated with a packet of the network traffic is provided. A packet generally refers to a unit of data, which can be of any protocol type. A packet may be a Transmission Control Protocol (TCP) packet. The objects associated with the packet may be, for example, a source port of the packet and a destination port of the packet.")</p>

No.	'111 Patent Claim 29	The Reference
29	The method according to claim 28, wherein the packet network is an Transmission Control Protocol (TCP) network, and the packet is an TCP packet.	<p>The Reference discloses the method according to claim 28, wherein the packet network is an Transmission Control Protocol (TCP) network, and the packet is an TCP packet.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Balakrishnan, Khan '478, Wang '735, and Olofsson '254.</p> <p>Below are examples of such references.</p> <p>Balakrishnan at [0018] ("FIG. 1 is a flowchart illustrating a method for managing network traffic in a network device, in accordance with an exemplary embodiment of the invention. At step 102, a plurality of objects associated with a packet of the network traffic is provided. A packet generally refers to a unit of data, which can be of any protocol type. A packet may be a Transmission Control Protocol (TCP) packet. The objects associated with the packet may be, for example, a source port of the packet and a destination port of the packet.")</p>

No.	'111 Patent Claim 29	The Reference
		<p>be a Transmission Control Protocol (TCP) packet. The objects associated with the packet may be, for example, a source port of the packet and a destination port of the packet.”)</p> <p>Balakrishnan at [0019] (“A set of criteria corresponding to the type of objects is then created, at step 104. In various embodiments of the invention, the set of criteria corresponds to at least one packet field associated with a configuration of the network device. A packet flag refers to one of the packet fields. In various embodiments of the invention, a packet flag corresponds to the type of objects. For example, if the packet is a TCP packet, then the packet flag is a TCP flag. Exemplary TCP flags may be for example, syn, ack, fin, urg, psh, and rst. In an embodiment of the invention, the set of criteria include at least one 'set criterion' for selecting the packet. In another embodiment of the invention, the set of criteria include at least one 'not-set criterion' for rejecting the packet. In yet another embodiment of the invention, the set of criteria includes at least one 'set criterion' and at least one 'not-set criterion' for managing the network traffic in the network device. Further, in various embodiments of the invention, if a packet flag is not present in the ACL, then it does not matter if the packet flag is present or not present in the packet.”)</p> <p>Balakrishnan at [0026] (“Means for providing 502 provides a plurality of objects associated with a packet of the network traffic. The objects associated with the packet may be, for example the packet fields. In an embodiment of the invention, the object associated with a TCP packet may be TCP flags. In various embodiments of the invention, means for providing 502 may be a software module.”)</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic using a TCP protocol. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254

No.	'111 Patent Claim 29	The Reference
		<p>Khan '478 at 4:10-15 ("In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA].")</p> <p>Wang '735 at 1:41-62 ("In one embodiment, a method generally comprises receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network.</p> <p>In another embodiment, an apparatus generally comprises a performance manager for measuring application performance for application traffic received at the apparatus, a UDP optimizer for optimizing UDP applications, a TCP optimizer for optimizing TCP applications, a policy manager for processing policy input received at the apparatus, and a plurality of queues for queuing the application traffic received at the apparatus based on input from the performance manager and policy manager. The application traffic shares available bandwidth in a wide area network in accordance with the measured performance and policy input.")</p> <p>Wang '735 at 2:38-62 ("The embodiments described herein provide a complete solution to effectively integrate all optimization features to minimize deployment cost and provide additional features and value to users. As described in detail below, the embodiments manage business critical applications, real time voice/ video, and best effort traffic automatically and effectively. The embodiments provide for efficient use of network resources for TCP traffic and UDP traffic including real-time communications and non-real-time streaming. The embodiments may be used, for example, to effectively optimize UDP-based real-time conversational UC&C (Unified Communications and Collaboration) applications. Different types of application traffic contending the WAN bandwidth are managed together and a system-level and network-level architecture for a complete solution is provided. This allows for pervasive video deployment, management of application</p>

No.	'111 Patent Claim 29	The Reference
		<p>performance requirements, and effective delivery of quality of services to networked applications and users. As described below, WAN optimization system components may be embedded in a network device such as a router, therefore eliminating the need for additional network devices. The WAN optimization system components may operate at a branch office, data center, or Internet edge, thus eliminating the need for different solutions for different network locations.”)</p> <p>Wang '735 at 6:55-7:2 (“FIG. 3 illustrates an example of the WAN optimization system 18 at one of the optimization system devices 12. The system 18 includes a performance manager (APM (application performance manager)/VQM (video/voice quality manager)) 30, policy manager 32, TCP-based application optimization module 34, UDP-based application optimization module 36, and scheduling and queuing module 38. The node 12 receives input (signaling) to the optimization system 18 from other nodes (e.g., routers with optimization system installed, endpoints 10). The node 12 also sends optimization system output to other routers and endpoints 10. Policy input is provided to the node 12 from an external policy source, as described below. The input may be received from downstream and upstream devices and the output may be transmitted to downstream and upstream devices.”)</p> <p>Wang '735 at 8:24-35 (“The TCP-based application optimization module 34 is configured to optimize all TCP-based applications (also referred to herein as TCP applications), including streaming video and audio. In one embodiment, the optimization module 34 is a Cisco WAAS (Wide Area Application Services) module. The optimization module 34 may request additional bandwidth from the scheduler 38 to meet application performance baseline requirements, for example. The scheduler 38 may proactively throttle the TCP output to yield more bandwidth to higher priority UDP-based video applications that are managed by the media optimization module 36, based on policy and performance baselines.”)</p> <p>Olofsson '254 at 2:27-44 (“In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay network on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network.”)</p>

No.	'111 Patent Claim 29	The Reference
		secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)

No.	'111 Patent Claim 30	The Reference
30[a]	The method according to claim 1, further comprising: receiving, by the network node from the first entity over the packet network, one or more additional packets;	<p>The Reference discloses the method according to claim 1, further comprising: receiving, by the network node from the first entity over the packet network, one or more additional packets.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic flows of multiple packets. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254

No.	'111 Patent Claim 30	The Reference
		<ul style="list-style-type: none"> • Kumar '739 <p>Khan '478 at Abstract ("A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.")</p> <p>Khan '478 at Figure 1</p>

No.	'111 Patent Claim 30	The Reference
		<p>FIG. 1</p> <p>Khan '478 at 3:1-7 (“The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.”)</p> <p>Khan '478 at 3:49-57 (“Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as</p>

No.	'111 Patent Claim 30	The Reference
		<p>a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).”)</p> <p>Khan '478 at 4:1-17 (“In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described. In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA]. The OMP may be configured to handle overlay routes and transport locators (TLOCs).”)</p> <p>Khan '478 at 4:47-60 (“Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.”)</p> <p>Khan '478 at 6:15-24 (“In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI). In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p>

No.	'111 Patent Claim 30	The Reference
		<p>Khan '478 at 9:53-10:4 ("In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <p>Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network;</p> <p>Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and</p> <p>Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.")</p> <p>Wang '735 at Abstract ("In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.")</p> <p>Wang '735 at 7:14-38 ("The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and RSVP metadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example,</p>

No.	'111 Patent Claim 30	The Reference
		<p>bandwidth reservation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS (Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization system.”)</p> <p>Wang '735 at 7:55-8:12 (“The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all network devices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to manage and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p> <p>Wang '735 at 10:53-11:10 (“In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control</p>

No.	'111 Patent Claim 30	The Reference
		<p>message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the applicationflows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Pro-tocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth.)”)</p> <p>Olofsson '254 at Abstract (“A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.”)</p> <p>Olofsson '254 at Figure 1</p>

No.	'111 Patent Claim 30	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p style="text-align: center;">.....</p> <p style="text-align: center;">DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

No.	'111 Patent Claim 30	The Reference
		<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 (“In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100.”)</p>

No.	'111 Patent Claim 30	The Reference
		<p>using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are</p>

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		<p>related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p> <p>Olofsson '254 at 6:36-59 (“Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <ul style="list-style-type: none"> Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges). Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102. Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them. <p>The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service.</p> <p>Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain.</p> <p>Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.”)</p> <p>Olofsson '254 at 7:22-28 (“In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.”)</p> <p>Kumar '739 at Abstract (“Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one or more service functions from a service function manager. The service controller identifies one or more service functions to be chained together based on a service function chaining rule. The service controller configures the service nodes to receive the one or more service functions and to forward the one or more service functions to the one or more service functions identified to be chained together. The service controller may also receive one or more service functions from a service function manager and identify one or more service functions to be chained together based on a service function chaining rule. The service controller may then configure the service nodes to receive the one or more service functions and to forward the one or more service functions to the one or more service functions identified to be chained together.”)</p>

No.	'111 Patent Claim 30	The Reference
		<p>nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p> <p>Kumar '739 at 3:66-4:12 ("The service controller 20 comprises service-function chaining logic 70 and the classifier 30 comprises classification and mapping logic 75. The service nodes 35, 40, 45, 50, and 55 each comprise advertisement logic 80. In operation, the advertisement logic 80 at each of the service nodes 35, 40, 45, 50, and 55 is configured to generate an advertisement or notification that indicates the service-functions that the respective service node hosts (i.e., each service node exposes its service-functions to the central service controller). For example, the advertisement logic 80 at service node 35 may generate an advertisement 85 indicating that the service node 35 hosts service-functions f1, f2, and f3. The advertisement 85 may then be provided to service controller 20 and/or classifier 30")</p> <p>Kumar '739 at 4:13-26 ("The service-function chaining logic 70 at service controller 20 is configured to define one or more "service-function chains" (SFCs) for selection by the classification and mapping logic 75 of classifier 30. As used herein, a "service-function chain" is an ordered list of service-functions defined in terms of the service-functions to be applied, and not in terms of service nodes that apply service-functions (i.e., the service-function chain is not defined in terms of network addresses for devices that host service-functions). More specifically, the location information of service nodes that host service-functions is not part of the defined service-function chain. Rather, as described further below, the selection of the location where the service-functions are available is performed at the classifier 30.")</p> <p>Kumar '739 at 4:64-5:3 ("In the example of FIG. 2, classifier 30 intercepts traffic 90 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 90 should be steered through service-function chain SFC1 that comprises ordered service-functions f1, f2, f6, f7, and f10.")</p>

No.	'111 Patent Claim 30	The Reference
		<p>Kumar '739 at 6:21-31 ("As noted, service controller 20 may receive advertisements from service nodes 35, 40, 45, 50, and 55. Based on these advertisements, the service controller 20 determines that service-function f7 is hosted at service node 40. The service controller 20 also determines that service-function f7 is a modifying service-function (i.e., service-function f7 is capable of changing the flow specification of processed traffic). In the example of FIG. 3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.")</p> <p>Kumar '739 at 6:56-62 ("In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.")</p> <p>Kumar '739 at 8:7-22 ("FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522. Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.")</p>
30[b]	checking, by the network node, if any one of the one or more additional	<p>The Reference discloses checking, by the network node, if any one of the one or more additional packets satisfies the criterion.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was</p>

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	packets satisfies the criterion;	known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.
30[c]	responsive to an additional packet not satisfying the criterion, sending, by the network node over the packet network, the additional packet to the second entity; and	<p>The Reference discloses responsive to an additional packet not satisfying the criterion, sending, by the network node over the packet network, the additional packet to the second entity.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>
30[d]	responsive to the additional packet satisfying the criterion, sending the additional packet, by the network node over the packet network, in response to the instruction.	<p>The Reference discloses responsive to the additional packet satisfying the criterion, sending the additional packet, by the network node over the packet network, in response to the instruction.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, and Uchida.</p>

No.	'111 Patent Claim 31	The Reference
31[a]	The method according to claim 1, wherein the packet network is a Software Defined Network (SDN),	<p>The Reference discloses the method according to claim 1, wherein the packet network is a Software Defined Network (SDN).</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, OpenFlow, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>OpenFlow at 6-7</p> <pre> graph TD Controller[Controller] -- "OpenFlow Protocol" --> OFSwitch[OpenFlow Switch] subgraph OFSwitch [] SC[Secure Channel] GT[Group Table] FT1[Flow Table] FT2[Flow Table] FT1 -- Pipeline --> FT2 end </pre> <p>Figure 1: Main components of an OpenFlow switch.</p>

No.	'111 Patent Claim 31	The Reference
		<p>2 Switch Components</p> <p>An OpenFlow Switch consists of one or more <i>flow tables</i> and a <i>group table</i>, which perform packet lookups and forwarding, and an <i>OpenFlow channel</i> to an external controller (Figure 1). The switch communicates with the controller and the controller manages the switch via the OpenFlow protocol.</p> <p>Using the OpenFlow protocol, the controller can add, update, and delete <i>flow entries</i> in flow tables, both reactively (in response to packets) and proactively. Each flow table in the switch contains a set of flow entries; each flow entry consists of <i>match fields</i>, <i>counters</i>, and a set of <i>instructions</i> to apply to matching packets (see 5.2).</p> <p>Matching starts at the first flow table and may continue to additional flow tables (see 5.1). Flow entries match packets in priority order, with the first matching entry in each table being used (see 5.3). If a matching entry is found, the instructions associated with the specific flow entry are executed. If no match is found in a flow table, the outcome depends on configuration of the table-miss flow entry: for example, the packet may be forwarded to the controller over the OpenFlow channel, dropped, or may continue to the next flow table (see 5.4).</p> <p>Instructions associated with each flow entry either contain actions or modify pipeline processing (see 5.9). Actions included in instructions describe packet forwarding, packet modification and group table</p>

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		<p>processing. Pipeline processing instructions allow packets to be sent to subsequent tables for further processing and allow information, in the form of metadata, to be communicated between tables. Table pipeline processing stops when the instruction set associated with a matching flow entry does not specify a next table; at this point the packet is usually modified and forwarded (see [5.10]).</p> <p>Flow entries may forward to a <i>port</i>. This is usually a physical port, but it may also be a logical port defined by the switch or a reserved port defined by this specification (see [4.1]). Reserved ports may specify generic forwarding actions such as sending to the controller, flooding, or forwarding using non-OpenFlow methods, such as “normal” switch processing (see [4.5]), while switch-defined logical ports may specify link aggregation groups, tunnels or loopback interfaces (see [4.4]).</p> <p>Actions associated with flow entries may also direct packets to a group, which specifies additional processing (see [5.6]). Groups represent sets of actions for flooding, as well as more complex forwarding semantics (e.g. multipath, fast reroute, and link aggregation). As a general layer of indirection, groups also enable multiple flow entries to forward to a single identifier (e.g. IP forwarding to a common next hop). This abstraction allows common output actions across flow entries to be changed efficiently.</p> <p>The group table contains group entries; each group entry contains a list of <i>action buckets</i> with specific semantics dependent on group type (see [5.6.1]). The actions in one or more action buckets are applied to packets sent to the group.</p> <p>Switch designers are free to implement the internals in any way convenient, provided that correct match and instruction semantics are preserved. For example, while a flow entry may use an all group to forward to multiple ports, a switch designer may choose to implement this as a single bitmask within the hardware forwarding table. Another example is matching; the pipeline exposed by an OpenFlow switch may be physically implemented with a different number of hardware tables.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit’s ’111 patent, including use of a network node for routing network traffic using an overlay controller. Some examples of Cisco’s patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan ’478 • Wang ’735 • Olofsson ’254 • Kumar ’739 <p>Khan ’478 at Abstract (“A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers</p>

No.	'111 Patent Claim 31	The Reference
		<p>based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.”)</p> <p>Khan '478 at Figure 1</p>

No.	'111 Patent Claim 31	The Reference
		<p><i>FIG. 1</i></p> <p>Khan '478 at 3:1-7 (“The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.”)</p> <p>Khan '478 at 3:49-57 (“Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as</p>

No.	'111 Patent Claim 31	The Reference
		<p>a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).”)</p> <p>Khan '478 at 4:1-17 (“In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described. In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA]. The OMP may be configured to handle overlay routes and transport locators (TLOCs).”)</p> <p>Khan '478 at 4:47-60 (“Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.”)</p> <p>Khan '478 at 6:15-24 (“In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI). In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p>

No.	'111 Patent Claim 31	The Reference
		<p>Khan '478 at 9:53-10:4 ("In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <p>Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network;</p> <p>Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and</p> <p>Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.")</p> <p>Wang '735 at Abstract ("In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.")</p> <p>Wang '735 at 7:14-38 ("The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and RSVP metadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example,</p>

No.	'111 Patent Claim 31	The Reference
		<p>bandwidth reservation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS (Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization system.”)</p> <p>Wang '735 at 7:55-8:12 (“The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all network devices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to manage and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p> <p>Wang '735 at 10:53-11:10 (“In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control</p>

No.	'111 Patent Claim 31	The Reference
		<p>message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the application flows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Protocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth.)”)</p> <p>Olofsson '254 at Abstract (“A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.”)</p> <p>Olofsson '254 at Figure 1</p>

No.	'111 Patent Claim 31	The Reference
		<p>FIG. 1</p> <p>.....</p> <p>DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

No.	'111 Patent Claim 31	The Reference
		<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 (“In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100.”)</p>

No.	'111 Patent Claim 31	The Reference
		<p>using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are</p>

No.	'111 Patent Claim 31	The Reference
		<p>related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p> <p>Olofsson '254 at 6:36-59 (“Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <p>Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges).</p> <p>Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102.</p> <p>Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them.</p> <p>The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service.</p> <p>Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain.</p> <p>Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.”)</p> <p>Olofsson '254 at 7:22-28 (“In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.”)</p> <p>Kumar '739 at Abstract (“Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one is configured to identify one or more service-functions hosted by each of the servic</p> <p style="text-align: right;">SECRET Exhibit 2019</p>

No.	'111 Patent Claim 31	The Reference
		<p>nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p> <p>Kumar '739 at 3:66-4:12 ("The service controller 20 comprises service-function chaining logic 70 and the classifier 30 comprises classification and mapping logic 75. The service nodes 35, 40, 45, 50, and 55 each comprise advertisement logic 80. In operation, the advertisement logic 80 at each of the service nodes 35, 40, 45, 50, and 55 is configured to generate an advertisement or notification that indicates the service-functions that the respective service node hosts (i.e., each service node exposes its service-functions to the central service controller). For example, the advertisement logic 80 at service node 35 may generate an advertisement 85 indicating that the service node 35 hosts service-functions f1, f2, and f3. The advertisement 85 may then be provided to service controller 20 and/or classifier 30")</p> <p>Kumar '739 at 4:13-26 ("The service-function chaining logic 70 at service controller 20 is configured to define one or more "service-function chains" (SFCs) for selection by the classification and mapping logic 75 of classifier 30. As used herein, a "service-function chain" is an ordered list of service-functions defined in terms of the service-functions to be applied, and not in terms of service nodes that apply service-functions (i.e., the service-function chain is not defined in terms of network addresses for devices that host service-functions). More specifically, the location information of service nodes that host service-functions is not part of the defined service-function chain. Rather, as described further below, the selection of the location where the service-functions are available is performed at the classifier 30.")</p> <p>Kumar '739 at 4:64-5:3 ("In the example of FIG. 2, classifier 30 intercepts traffic 90 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 90 should be steered through service-function chain SFC1 that comprises ordered service-functions f1, f2, f6, f7, and f10.")</p>

No.	'111 Patent Claim 31	The Reference
		<p>Kumar '739 at 6:21-31 ("As noted, service controller 20 may receive advertisements from service nodes 35, 40, 45, 50, and 55. Based on these advertisements, the service controller 20 determines that service-function f7 is hosted at service node 40. The service controller 20 also determines that service-function f7 is a modifying service-function (i.e., service-function f7 is capable of changing the flow specification of processed traffic). In the example of FIG. 3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.")</p> <p>Kumar '739 at 6:56-62 ("In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.")</p> <p>Kumar '739 at 8:7-22 ("FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522. Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.")</p>

No.	'111 Patent Claim 31	The Reference
31[b]	the packet is routed as part of a data plane and	<p>The Reference discloses the packet is routed as part of a data plane.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person or ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic using an overlay controller. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254 • Kumar '739 <p>Khan '478 at Abstract ("A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.")</p>

No.	'111 Patent Claim 31	The Reference
		<p>Khan '478 at Figure 1</p> <p>FIG. 1</p> <p>Khan '478 at 3:1-7 ("The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.")</p>

No.	'111 Patent Claim 31	The Reference
		<p>Khan '478 at 3:49-57 ("Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).")</p> <p>Khan '478 at 4:1-17 ("In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described. In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA]. The OMP may be configured to handle overlay routes and transport locators (TLOCs).")</p> <p>Khan '478 at 4:47-60 ("Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.")</p> <p>Khan '478 at 6:15-24 ("In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI).</p>

No.	'111 Patent Claim 31	The Reference
		<p>In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p> <p>Khan '478 at 9:53-10:4 (“In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <ul style="list-style-type: none"> Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network; Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.”) <p>Wang '735 at Abstract (“In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.”)</p> <p>Wang '735 at 7:14-38 (“The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and</p>

No.	'111 Patent Claim 31	The Reference
		<p>RSVPimetadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example, bandwidth res-ervation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS (Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization sys-tem.”)</p> <p>Wang '735 at 7:55-8:12 (“The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all networkdevices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to man-age and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p>

No.	'111 Patent Claim 31	The Reference
		<p>Wang '735 at 10:53-11:10 ("In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the applicationflows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Pro-tocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth).")</p> <p>Olofsson '254 at Abstract ("A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.")</p> <p>Olofsson '254 at Figure 1</p>

No.	'111 Patent Claim 31	The Reference
		<p style="text-align: center;"><i>FIG. 1</i></p> <p style="text-align: right;">.....</p> <p style="text-align: right;">DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

No.	'111 Patent Claim 31	The Reference
		<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 (“In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100.”)</p>

No.	'111 Patent Claim 31	The Reference
		<p>using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are</p>

No.	'111 Patent Claim 31	The Reference
		<p>related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p> <p>Olofsson '254 at 6:36-59 (“Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <ul style="list-style-type: none"> Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges). Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102. Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them. The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service. Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain. Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.”) <p>Olofsson '254 at 7:22-28 (“In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.”)</p> <p>Kumar '739 at Abstract (“Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one is configured to identify one or more service-functions hosted by each of the servic</p> <p style="text-align: right;">SECRET Exhibit 2019</p>

No.	'111 Patent Claim 31	The Reference
		<p>nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p> <p>Kumar '739 at 2:9-22 (“A service header is part of the data-plane of a service chain and includes metadata specifically formatted for consumption by a service-function. The metadata may include, for example, an application identifier (ID), flow or path ID, and client or user ID, network classification information used for deriving targeted service policies and profiles, common metadata related to a particular service such as finer classification that can be passed to the service-functions further down the service-path. In other words, service functions benefit from metadata derived both from the network as well as the service-functions that form a given service chain. Metadata can also be passed between network nodes and be used, for example, to determine forwarding state at the end of a service chain.”)</p> <p>Kumar '739 at 3:66-4:12 (“The service controller 20 comprises service-function chaining logic 70 and the classifier 30 comprises classification and mapping logic 75. The service nodes 35, 40, 45, 50, and 55 each comprise advertisement logic 80. In operation, the advertisement logic 80 at each of the service nodes 35, 40, 45, 50, and 55 is configured to generate an advertisement or notification that indicates the service-functions that the respective service node hosts (i.e., each service node exposes its service-functions to the central service controller). For example, the advertisement logic 80 at service node 35 may generate an advertisement 85 indicating that the service node 35 hosts service-functions f1, f2, and f3. The advertisement 85 may then be provided to service controller 20 and/or classifier 30”)</p> <p>Kumar '739 at 4:13-26 (“The service-function chaining logic 70 at service controller 20 is configured to define one or more “service-function chains” (SFCs) for selection by the classification and mapping logic 75 of classifier 30. As used herein, a “service-function chain” is an ordered list of service-functions defined in terms of the service-functions to be applied, and not in terms of service nodes that apply service-functions (i.e., the service-function chain is not defined in terms of network addresses for devices that host service-functions). More specifically, the location information of service nodes that host service-</p>

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		<p>functions is not part of the defined service-function chain. Rather, as described further below, the selection of the location where the service-functions are available is performed at the classifier 30.”)</p> <p>Kumar '739 at 4:64-5:3 (“In the example of FIG. 2, classifier 30 intercepts traffic 90 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 90 should be steered through service-function chain SFC1 that comprises ordered service-functions f1, f2, f6, f7, and f10.”)</p> <p>Kumar '739 at 6:21-31 (“As noted, service controller 20 may receive advertisements from service nodes 35, 40, 45, 50, and 55. Based on these advertisements, the service controller 20 determines that service-function f7 is hosted at service node 40. The service controller 20 also determines that service-function f7 is a modifying service-function (i.e., service-function f7 is capable of changing the flow specification of processed traffic). In the example of FIG. 3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.”)</p> <p>Kumar '739 at 6:56-62 (“In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.”)</p> <p>Kumar '739 at 8:7-22 (“FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522.</p>

No.	'111 Patent Claim 31	The Reference
		<p>Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.”)</p>
31[c]	the network node communication with the controller serves as a control plane.	<p>The Reference discloses the network node communication with the controller serves as a control plane.</p> <p>To the extent that the Reference alone does not anticipate or render this claim obvious, this claim, including this element would have been obvious to one skilled in the art, as it was known. For example, this claim, including this element, would have been obvious in light of the disclosures of the Reference in combination with the knowledge of a person of ordinary skill in the art and/or any of the following references: Kempf, Swenson, Chandrasekaran, Lin '400, Shieh '088, Cisco IWAN System, VMware NSX System, Chua '877, Chua '151, Copeland, Uchida, Khan '478, Wang '735, Olofsson '254, and Kumar '739.</p> <p>Below are examples of such references.</p> <p>Cisco also innovated, patented, or otherwise acquired various features of SD-WAN <i>before</i> Orckit's '111 patent, including use of a network node for routing network traffic using an overlay controller. Some examples of Cisco's patents for that technology that are relevant to this limitation include:</p> <ul style="list-style-type: none"> • Khan '478 • Wang '735 • Olofsson '254 • Kumar '739 <p>Khan '478 at Abstract (“A method for creating a secure network is provided. The method comprises establishing an overlay domain to control routing between overlay edge routers.</p>

No.	'111 Patent Claim 31	The Reference
		<p>based on an underlying transport network, wherein said establishing comprises running an overlay management protocol to exchange information within the overlay domain; in accordance with the overlay management protocol defining service routes that exist exclusively within the overlay domain wherein each overlay route includes information on at least service availability within the overlay domain; and selectively using the service routes to control routing between the overlay edge routers; wherein the said routing is through the underlying transport network in a manner in which said overlay routes is shared with the overlay edge routers but not with the underlying transport network via the overlay management protocol.”)</p> <p>Khan '478 at Figure 1</p>

No.	'111 Patent Claim 31	The Reference
		<p><i>FIG. 1</i></p> <p>Khan '478 at 3:1-7 (“The overlay domain (OD) 100 further comprises at least one overlay controller (OC). In FIG. 1 two overlay controllers are shown and are indicated by reference numerals 110, and 112, respectively. As with the case of the number of the overlay edge routers (OERs), it is to be understood that the overlay domain (OD) 100 may include more or less overlay controllers than the illustrated number.”)</p> <p>Khan '478 at 3:49-57 (“Referring to FIG. 1, reference numeral 122 shows an example of a control channel that was established as a DTLS tunnel between the overlay edge router (OER) 104 and the overlay controller (OC) 110 via the transport network 120 as</p>

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		<p>a result of the bring up procedure. In one embodiment, the plurality of secure communications channels established between each overlay edge router (OER) and an assigned overlay controller (OC) together define an overlay control plane (OCP).”)</p> <p>Khan '478 at 4:1-17 (“In one embodiment, communications between an overlay edge router (OER) and an overlay controller (OC) may be facilitated by the use of the overlay protocol (OMP). The OMP may be used to exchange routing, policy, security, and management information between an overlay controller (OC) and an overlay edge router (OER). In one embodiment, the OMP may be used to advertise routing information within the overlay domain (OD) 100, as will be described. In one embodiment, the OMP uses TCP as its transport protocol. This eliminates the need to implement explicit update fragmentation, retransmission, acknowledgement, and sequencing. The OMP may listen on TCP port [17900, assigned through IRNA]. The OMP may be configured to handle overlay routes and transport locators (TLOCs).”)</p> <p>Khan '478 at 4:47-60 (“Since the OMP operates in an overlay networking environment, the notion of routing peers is different from a traditional environment. From a logical point of view, the overlay environment consists of a central controller and a number of edge-devices. Each edge-device advertises the imported overlay routes to the central controller and the central controller, based on policy-decisions, further distributes the overlay routing information to other edge-devices in the network. Edge-devices are not configured to advertise routing information to each other using the OMP. The OMP-peering sessions between overlay controller (OC) and each overlay edge router (OER) are used exclusively for the exchange of control plane traffic, whereas the overlay data plane (ODP) channels are used for data traffic.”)</p> <p>Khan '478 at 6:15-24 (“In one embodiment, service routes represent services connected to an overlay edge router (OER). The service routes may be advertised by the overlay edge routers (OERs) within the overlay domain (OD) 100 to the overlay controllers (OCs) using service address family Network Layer Reachability Information (NLRI). In one embodiment the OMP may be configured to redistribute the following types of routes automatically it learns either locally or from its routing peers: connected, static, OSPF intra area routes, and OSPF inter area routes.”)</p>

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		<p>Khan '478 at 9:53-10:4 ("In another embodiment, the OMP may be used to perform a method for routing. This method is illustrated in the flowchart of FIG. 7. Referring to FIG. 7, the method may include the following processing blocks:</p> <p>Block 700: provide an overlay network comprising at least one overlay controller; and a plurality of overlay edge routers communicatively coupled to the at least one overlay controller; wherein the overlay network is associated with an underlying transport network;</p> <p>Block 702: collect by the overlay controller, routing information comprising at least one of authentication information, service information, encryption information, policy information, and access control information; wherein said routing information is carried by an overlay management protocol; and</p> <p>Block 704: orchestrate by the overlay controller, routing through the underlying transport network based on the routing information; wherein said routing information is not exposed to elements of the underlying transport network.")</p> <p>Wang '735 at Abstract ("In one embodiment, a method includes receiving application traffic at a network device from one or more endpoints, measuring performance of applications at the network device, optimizing TCP (Transmission Control Protocol) applications and UDP (User Datagram Protocol) applications based on the measured performance and policy input received at the network device, queuing the application traffic at the network device such that the application traffic shares available bandwidth in accordance with the measured performance and the policy input, and transmitting the application traffic over a wide area network. An apparatus is also disclosed.")</p> <p>Wang '735 at 7:14-38 ("The performance manager 30 may receive input from an application recognition mechanism (not shown). Application recognition features such as Cisco NBAR2 (Network Based Application Recognition 2) and MSP (Media Service Proxy) may be used to recognize networked applications. SIP/H.323/ RTSP signaling protocols may also provide a means to recognize a media flow. Other input such as Cisco FnF (Flexible NetFlow) and RSVP metadata signaling protocols may be used to aid in application recognition. An application ID or CAC (Call Admission Control) ID that is carried by RSVP/ metadata may provide additional information about the flow. The metadata may also provide, for example,</p>

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		<p>bandwidth reservation (admitted or un-admitted status), application user ID, codec type (e.g., H.264AVC, H.264 SVC, H.263, MPEG-2, etc.), maximum bandwidth (TIAS (Transport Independent Application Specific)), and minimum admitted bandwidth (for H.264: profile and level, RTP protocol and restrictions), and endpoint device capabilities (rate-adaption, Cisco Flux version supported by endpoint). The media stream may also be identified, for example, using a form of DPI (Deep Packet Inspection) or configured IP 5-tuples defining the stream. Flow information may be stored in a flow/metadata database (not shown). The flow/metadata database may be distributed to other nodes 12 incorporating the WAN optimization system.”)</p> <p>Wang '735 at 7:55-8:12 (“The policy manager 32 receives input from a policy server configured to receive policy information from a network administrator, for example. The policies are set up to manage application performance and resource allocations. For example, location service and service announcements may be provided for local endpoints. Policy is set up based on SLA, target performance, bit-rate, etc. Priorities are set up to meet business needs (e.g., HD used for business is more important than regular desktop HD phone calls). The policy server may include an external network policy manager that allows the network administrator to specify application classes, performance baselines per class, bandwidth usage rules, and per user SLO/SLA, etc. The policy is provisioned on all network devices 12 that incorporate the WAN optimization system 18 and may be implemented by a network management system (NMS), for example.</p> <p>The policy manager 32 includes a network policy enforcement engine for processing policy input received at the network device 12 and managing the application delivery and performance assurance. The engine uses bandwidth pools and bandwidth usage rules defined by the policy manager to manage and provide feedback to the other components of the optimization system 18. As shown in FIG. 3, the policy manager 32 provides input to the optimization modules 34, 36, and scheduler 38.”)</p> <p>Wang '735 at 10:53-11:10 (“In one embodiment, CAC (Call Admission Control) and flow policing is used to optimize media applications. CAC and flow policing may be used for on control path or when no explicit control path is involved. For on control path the control</p>

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		<p>message/protocol is terminated or handled by the WAN optimization system 18. In one example, RSVP (Re-source Reservation Protocol), which is used to reserve resources across the network, is used as the control protocol. The system checks the bandwidth and resource availability to decide whether the application traffic flow is admitted or rejected. For the case with no explicit control path involvement, the control message/protocol (for example, RTSP, H.323, SIP, HTTP, etc.) is not terminated or processed on the router 12. In this case, the system uses NBAR2, flow metadata information, etc., to extract information on the applicationflows. The system may, for example, sniff the SIP (Session Initiation Protocol), H.323, RTSP (Real-Time Streaming Pro-tocol), HTTP (Hypertext Transfer Protocol) content, and the like, to extract information for the application flows. If there is insufficient bandwidth resource for the flow, the traffic flow is marked as best effort or unadmitted class. Appropriate feedback messages are sent to the source of the application traffic flow to regulate the bandwidth consumption by these flows (e.g., quench the traffic from source or lower the video bit rate to the minimum available bandwidth.)”)</p> <p>Olofsson '254 at Abstract (“A method for routing is disclosed. The method comprises establishing an overlay network, comprising a plurality of network elements and an overlay controller; wherein the overlay controller is in communication with each network element via a secure tunnel established through an under-lying transport network; receiving by the overlay controller, information from each service-hosting network element information said information identifying a service hosted at that service-hosting network element, and label associated with the service-hosting network element; identifying by the overlay controller, at least one policy that associates traffic from a site with a service; and causing by said overly controller, the at least one policy to be executed so that traffic from the site identified in the policy is routed using the underlying transport network to the service-hosting network element associated with the said service.”)</p> <p>Olofsson '254 at Figure 1</p>

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		<p style="text-align: center;"><i>FIG. 1</i></p> <p style="text-align: center;">.....</p> <p style="text-align: center;">DTLS tunnel</p> <p>Olofsson '254 at Figure 3</p>

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		<pre> graph TD A[Controller establishes secure control channels with all associated network elements] --> B[Service-hosting locations advertise service type and associated label to controller] B --> C[Policies are created on controller associating traffic from sites with services] C --> D[Policies for outbound traffic are pushed to edge-nodes and hub nodes] D --> E[Policies for inbound policies are pushed to service locations] </pre> <p>FIG. 3</p> <p>Olofsson '254 at 2:27-44 (“In one embodiment, to realize the service chain construct, network elements may be interconnected across a regular network infrastructure in order to provide an overlay net-work on top of the regular network infrastructure. FIG. 1 shows an embodiment 100 of the overlay network. Referring to FIG. 1, the overlay network 100 includes an overlay controller 102, a mapping server 104, and a plurality of overlay edge routers 106. The overlay controller 102 is configured to orchestrate the overlay network 100.”)</p>

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		<p>using a secure transport (TLS, Transport Layer Security, IETF RFC5246) and a designated overlay control plane protocol over underlying network infrastructure 108. In one embodiment, the network infrastructure 108 may include a public network such as the Internet. The overlay control plane protocol may operate in a similar fashion to BGP (IETF RFC4271), in functions related to route and policy distribution, reliable transport over TCP (IETF RFC793), and optimal path selection process and distributed state creation.”)</p> <p>Olofsson '254 at 3:3-14 (“In one embodiment, within the overlay network 100, the overlay controller 102 processes control plane traffic, but does not get involved in the processing of data traffic. All data traffic is processed by the network elements present at site locations, such as a branch office, or central locations, such as a data center or a headquarters location. These network elements if, at a branch location is referred to as an “edge” and if, at a central location, is referred to as a “hub”. In FIG. 1 hubs are indicated by reference numeral 110, whereas edges are indicated by reference numeral 106. In one embodiment, secure peer-to-peer links between the hubs and services define a forwarding plane, as shown in FIG. 2.”)</p> <p>Olofsson '254 at 3:64-4:9 (“In one embodiment, the overlay controller may be provisioned with or at least have access to traffic policy functions. These traffic policy functions may be distributed to selected hubs and edges and may be used to direct traffic. In one embodiment, the use of labels that identify services and provide for a forwarding tag, allows the overlay network 100 to overcome all of the previously presented challenges. Labels that represent Virtual Private Networks (VPN) may be combined, in some embodiments, with the Service labels to provide services that are VPN-specific and are reached using VPN-specific policies, versus general overlay network policies for reaching a service identified solely by a service label applicable to the entire overlay network.”)</p> <p>Olofsson '254 at 4:34-42 (“Based on the advertisements of routes from each edge and hub router and the advertisements of service labels from each hub router hosting a service, potentially restricted on a per-VPN basis by associating a service-label with a VPN-label, the overlay controller 102 constructs policies that are subsequently distributed to the network elements (hubs and edges) involved. The set of policies and their required contents are</p>

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		<p>related to the exact nature of the service chain that is being constructed. Two examples are provided below.”)</p> <p>Olofsson '254 at 6:36-59 (“Establishment of a Service Chain In one embodiment, to a method for establishing a service chain is shown in FIG. 3. Referring to FIG. 3, the method includes the following blocks:</p> <p>Block 300: The overlay controller 102 establishes secure control channel with all associated network elements (hubs and edges).</p> <p>Block 302: The service-hosting locations (hubs) advertise their service type and associated label to the overlay controller 102.</p> <p>Block 304: The overlay controller 102 uses the service information received when constructing policies for the edge routers that are to use them.</p> <p>The central controller can either: Apply the service policy to overlay routes before sending those to edge nodes with overlay next hop and label changed to that of service.</p> <p>Block 306: The central controller pushes the service policies to the edge routers. These policies link traffic to the ultimate destination with a service chain.</p> <p>Block 308: The central controller can also push policies to the service hosting routers, instructing them of their role in a given service chain and how to forward inbound and outbound traffic related to each VPN and each Service.”)</p> <p>Olofsson '254 at 7:22-28 (“In one embodiment, each edge node uses existing destination routes that are given a next-hop TLOC pointing to the entry point of a service chain. This route to TLOC assignment can be done by the central controller as a way of enforcing central service-chain policy, or by edge router when enforcing policies either distributed by the central controller or created locally on the device.”)</p> <p>Kumar '739 at Abstract (“Presented herein are service-function chaining techniques. In one example, a service controller in a network comprising a plurality of service nodes receives one is configured to identify one or more service-functions hosted by each of the servic</p> <p style="text-align: right;">SECRET Exhibit 2019</p>

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		<p>nodes. The service controller defines a service-function chain in terms of service-functions to be applied to traffic in the network and provides information descriptive of the service-function chain to a classifier node.)</p> <p>Kumar '739 at 3:66-4:12 ("The service controller 20 comprises service-function chaining logic 70 and the classifier 30 comprises classification and mapping logic 75. The service nodes 35, 40, 45, 50, and 55 each comprise advertisement logic 80. In operation, the advertisement logic 80 at each of the service nodes 35, 40, 45, 50, and 55 is configured to generate an advertisement or notification that indicates the service-functions that the respective service node hosts (i.e., each service node exposes its service-functions to the central service controller). For example, the advertisement logic 80 at service node 35 may generate an advertisement 85 indicating that the service node 35 hosts service-functions f1, f2, and f3. The advertisement 85 may then be provided to service controller 20 and/or classifier 30")</p> <p>Kumar '739 at 4:13-26 ("The service-function chaining logic 70 at service controller 20 is configured to define one or more "service-function chains" (SFCs) for selection by the classification and mapping logic 75 of classifier 30. As used herein, a "service-function chain" is an ordered list of service-functions defined in terms of the service-functions to be applied, and not in terms of service nodes that apply service-functions (i.e., the service-function chain is not defined in terms of network addresses for devices that host service-functions). More specifically, the location information of service nodes that host service-functions is not part of the defined service-function chain. Rather, as described further below, the selection of the location where the service-functions are available is performed at the classifier 30.")</p> <p>Kumar '739 at 4:64-5:3 ("In the example of FIG. 2, classifier 30 intercepts traffic 90 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 90 should be steered through service-function chain SFC1 that comprises ordered service-functions f1, f2, f6, f7, and f10.")</p>

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		<p>Kumar '739 at 6:21-31 (“As noted, service controller 20 may receive advertisements from service nodes 35, 40, 45, 50, and 55. Based on these advertisements, the service controller 20 determines that service-function f7 is hosted at service node 40. The service controller 20 also determines that service-function f7 is a modifying service-function (i.e., service-function f7 is capable of changing the flow specification of processed traffic). In the example of FIG. 3, service controller 20 defines service-function chains that include service-function f7 to account for the capability of service-function f7 to change the flow specification.”)</p> <p>Kumar '739 at 6:56-62 (“In the example of FIG. 3, classifier 30 intercepts traffic 150 for steering through a service-function chain defined by service controller 20. Using information (e.g., rules, policies, etc.) provided by service controller 20, classification and mapping logic 75 determines that traffic 150 should be steered through service-function chain SFC3 that comprises first sub-chain SFC3a and the second sub-chain SFC3b.”)</p> <p>Kumar '739 at 8:7-22 (“FIG. 5 is an example block diagram of service controller 20. It should be understood that a virtual controller would be a software-emulated or virtualized version of what is shown in FIG. 5, such as software running on commodity hardware in a data center. The service controller 20 includes one or more processors 510, memory 522, a bus 530 and a network interface unit 540. The processor 510 may be a microprocessor or microcontroller. The network interface unit 540 facilitates network communications between the service controller 20 and network nodes (e.g., classifiers, service nodes, etc.). The processor 510 executes instructions associated with software stored in memory 522. Specifically, the memory 522 stores service-function chaining software 550 that, when executed by the processor 510, causes the processor 510 to perform the service-function chaining operations described herein.”)</p>