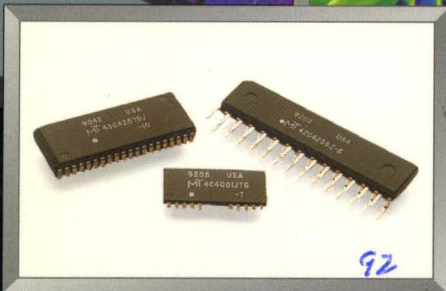
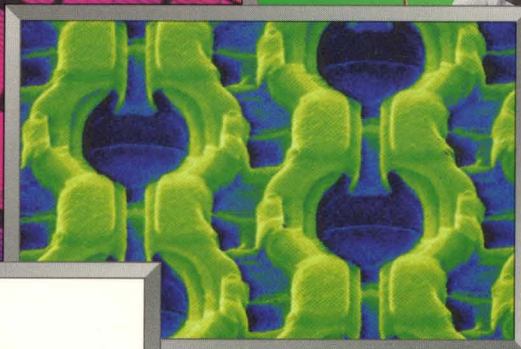


# DRAM DATA BOOK



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# DRAM DATA BOOK

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**ABOUT THE COVER:**

Front — Clockwise from left, 1) Micron's 16 Meg DRAM wafer; 2) More than 4,000 Micron team members give painstaking attention to every step of the production process; 3) Scanning electron microscope (SEM) photograph of Micron's DRAM mini-stack process; and 4) Micron's Triple Port, Dual Port and 4 Meg DRAMs in SOJ, ZIP and TSOP packages.

Back — Micron's Boise, Idaho, corporate headquarters including three fabrication facilities.

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