

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

XILINX, INC.

Petitioner

v.

POLARIS INNOVATIONS LIMITED

Patent Owner

Case IPR2023-00516

U.S. Patent 6,157,589

PETITIONER'S UPDATED EXHIBIT LIST

UPDATED EXHIBIT LIST

Exhibit	Description
1001	U.S. Patent No. 6,157,589 (the “’589 Patent”)
1002	U.S. Prosecution History of the ’589 Patent
1003	Declaration of Stephen W. Melvin
1004	U.S. Patent No. 5,559,753 to Kocis entitled “Apparatus and Method for Preventing Bus Contention During Power-Up in a Computer System With Two or More DRAM Banks” (“Kocis”)
1005	U.S. Patent No. 5,774,402 to Lee entitled “Initialization Circuit for a Semiconductor Memory Device” (“Lee”)
1006	JEDEC Standard No. 21-C, entitled “Configurations for Solid State Memories,” Compilation of Releases 1 through 7, dated January 1997 (“JESD 21-C”)
1007	U.S. Patent No. 5,703,510 to Iketani et al. entitled “Power On Reset Circuit For Generating Reset Signal at Power On” (“Iketani”)
1008	Prosecution History of EPO Patent Application No. 99 113 048.5 (Original)
1009	Excerpt of Prosecution History of EPO Patent Application No. 99 113 048.5 (Original)
1010	Excerpt of Prosecution History of EPO Patent Application No. 99 113 048.5 (English Translation)
1011	Japanese Patent Publication No. JP 09 106668 A to Samsung Electronics Co. Ltd. dated April 22, 1997 (“Tetsuka”)
1012	Declaration of Julie Carson
1013	JEDEC Standard No. 21-C, entitled “Configurations for Solid State Memories,” Release 7, dated January 1997
1014	Scheduling Order, <i>Polaris Innovations Limited v. Xilinx, Inc.</i> , 1:22-cv-00174-RGA, Docket No. 20 (May 31, 2022)

Exhibit	Description
1015	United States District Courts — Federal Court Management Statistics, National Judicial Caseload Profile (June 30, 2022), available at https://www.uscourts.gov/sites/default/files/fcms_na_distprofile0630.2022_0.pdf
1016	Motion Success for Stay Pending IPR before Judge Richard G. Andrews in the District of Delaware (Docket Navigator data from 2020 to 1/20/2023)
1017	Excerpt of Micron Technology, Inc., “DRAM Data Book” (1992)
1018	Excerpt of Samsung Electronics Co., Ltd., “Data Book: DRAM” (Dec. 1995)
1019	Samsung Electronics, “4M x 8Bit x 4 Banks Synchronous DRAM” Doc. No. KM48S16030, Rev. 2 (March 1998)
1020	Patent License, Tolling Stand-Still and Settlement Agreement

Dated: February 8, 2024

Respectfully submitted,

/Brian W. Oaks/

Brian W. Oaks (Reg. No. 44,981)
Aashish G. Kapadia (Reg. No. 78,844)
McDERMOTT WILL & EMERY LLP
300 Colorado Street, Suite 2200
Austin, TX 78701
TEL: 512-726-2600
FAX: 512-532-0002
boaks@mwe.com
akapadia@mwe.com

*Attorneys for Petitioner,
Xilinx, Inc.*

CERTIFICATE OF SERVICE

Pursuant to 37 C.F.R. § 42.6, the undersigned certifies that on February 8, 2024, a copy of the PETITIONER'S UPDATED EXHIBIT LIST and EXHIBIT 1020 was electronically served on the following counsel of record for Patent Owner:

david@nelbum.com
bbumgardner@nbclaw.net

/Brian W. Oaks/
Brian W. Oaks
Lead Counsel for Petitioner