

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

AKAMAI TECHNOLOGIES, INC.,
Petitioner

v.

EQUIL IP HOLDINGS LLC,
Patent Owner

Case IPR2023-00332
U.S. Patent No. 9,158,745

**DECLARATION OF DR. MARK T. JONES
IN SUPPORT OF PATENT OWNER'S PRELIMINARY RESPONSE**

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I, Mark. T. Jones, hereby declare as follows.

I. INTRODUCTION

1. I have been retained on behalf of Patent Owner Equil IP Holdings LLC (“Equil IP”) for the above-captioned *inter partes* review (IPR). I understand that this proceeding involves U.S. Patent No. 9,158,745 (“the ’745 patent”) titled “Optimization of media content using generated intermediate media content.”

2. I understand that Sean Barger, Brian Rice, Matt Butler, and David Pochron are the named inventors of the ’745 patent. I also understand that the ’745 patent is currently assigned to Equil IP.

3. I have reviewed and am familiar with the following documents:

Paper or Exhibit No.	Description
2	Akamai Technologies’ Petition for <i>Inter Partes</i> Review of U.S. Patent No. 9,158,745
1001	U.S. Patent No. 9,158,745 (“the ’745 patent”)
1002	File History of U.S. Application No. 13/752,110
1007	U.S. Patent Application Publication No. US 2002/0078093 (“Samaniego”)
1010	File History of U.S. Application No. 11/269,916
1013	Redline Comparison of specifications from Samaniego and the ’745 patent
1021	Redline Comparison of specifications from Samaniego and Publication No. 2006/0265476 of Application No. 11/269,916

Paper or Exhibit No.	Description
1026	File History of U.S. Application No. 12/173,747
1027	File History of U.S. Application No. 12/238,842
1028	Redline Comparison of specifications from Samaniego and Publication No. 2009/0070485 of Application No. 12/173,747
1029	Redline Comparison of specifications from Samaniego and Publication No. 2009/0089422 of Application No. 12/238,842
1035	File History of U.S. Application No. 09/929,904
2002	Curriculum Vitae of Dr. Mark T. Jones

II. QUALIFICATIONS

4. My CV is attached as EX2002. I am a Professor Emeritus of Electrical and Computer Engineering at Virginia Tech in Blacksburg, Virginia. I graduated *summa cum laude* from Clemson University in 1986 with a B.S. in Computer Science and a minor in Computer Engineering, while holding a National Merit Scholarship and the R. F. Poole Scholarship. I then graduated from Duke University in 1990 with a Ph.D. in Computer Science, while holding the Von Neumann Fellowship.

5. Upon graduation, I joined the Department of Energy at its Argonne National Laboratory facility. My responsibilities there included the design and use of software for computers with hundreds of processing elements. This software was designed for compatibility with new parallel computer architectures as they

became available, as well as with other large software components being written in the Department of Energy. While with the Department of Energy, I received the IEEE Gordon Bell Prize.

6. In 1994, I joined the Computer Science faculty at the University of Tennessee. My teaching responsibilities included computer architecture and computer networking. My research interests included the design and use of software that used the collective power of large groups of workstations. While at the University of Tennessee, I received a CAREER Award from the National Science Foundation.

7. In 1997, I joined the Electrical and Computer Engineering faculty at Virginia Tech. My teaching responsibilities have included the design of embedded systems, computer organization, computer architecture, a variety of programming courses, and parallel computing. I have been cited multiple times on the College of Engineering's Dean's List for teaching.

8. In addition to the activities, education, and professional experience listed above, I have been involved in research projects that contribute to my expertise relating to this report. While at Virginia Tech, I have been a primary or co-investigator on government and industrial research grants and contracts in excess of five million dollars.

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