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Tzannes et al.

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(54) **COMPUTER READABLE MEDIUM WITH INSTRUCTIONS FOR RESOURCE SHARING IN A TELECOMMUNICATIONS ENVIRONMENT**

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(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

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This patent is subject to a terminal disclaimer.

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U.S. Appl. No. 12/783,758, filed May 20, 2010, Tzannes.
U.S. Appl. No. 12/760,728, filed Apr. 15, 2010, Tzannes.
U.S. Appl. No. 12/783,765, filed May 20, 2010, Tzannes.

Related U.S. Application Data

(63) Continuation of application No. 11/246,163, filed on Oct. 11, 2005.

(Continued)

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Assistant Examiner—Mark Pfitzenmayer
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(51) **Int. Cl.**
H03M 13/00 (2006.01)

(52) **U.S. Cl.** **714/774; 714/784; 375/222**

(58) **Field of Classification Search** **709/215; 375/222; 714/774, 784; 711/147, 153, 157, 711/170, 173; 379/93.01**

See application file for complete search history.

(57) **ABSTRACT**

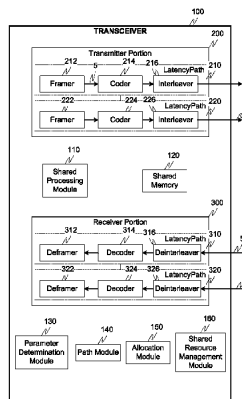
A transceiver is designed to share memory and processing power amongst a plurality of transmitter and/or receiver latency paths, in a communications transceiver that carries or supports multiple applications. For example, the transmitter and/or receiver latency paths of the transceiver can share an interleaver/deinterleaver memory. This allocation can be done based on the data rate, latency, BER, impulse noise protection requirements of the application, data or information being transported over each latency path, or in general any parameter associated with the communications system.

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8 Claims, 3 Drawing Sheets



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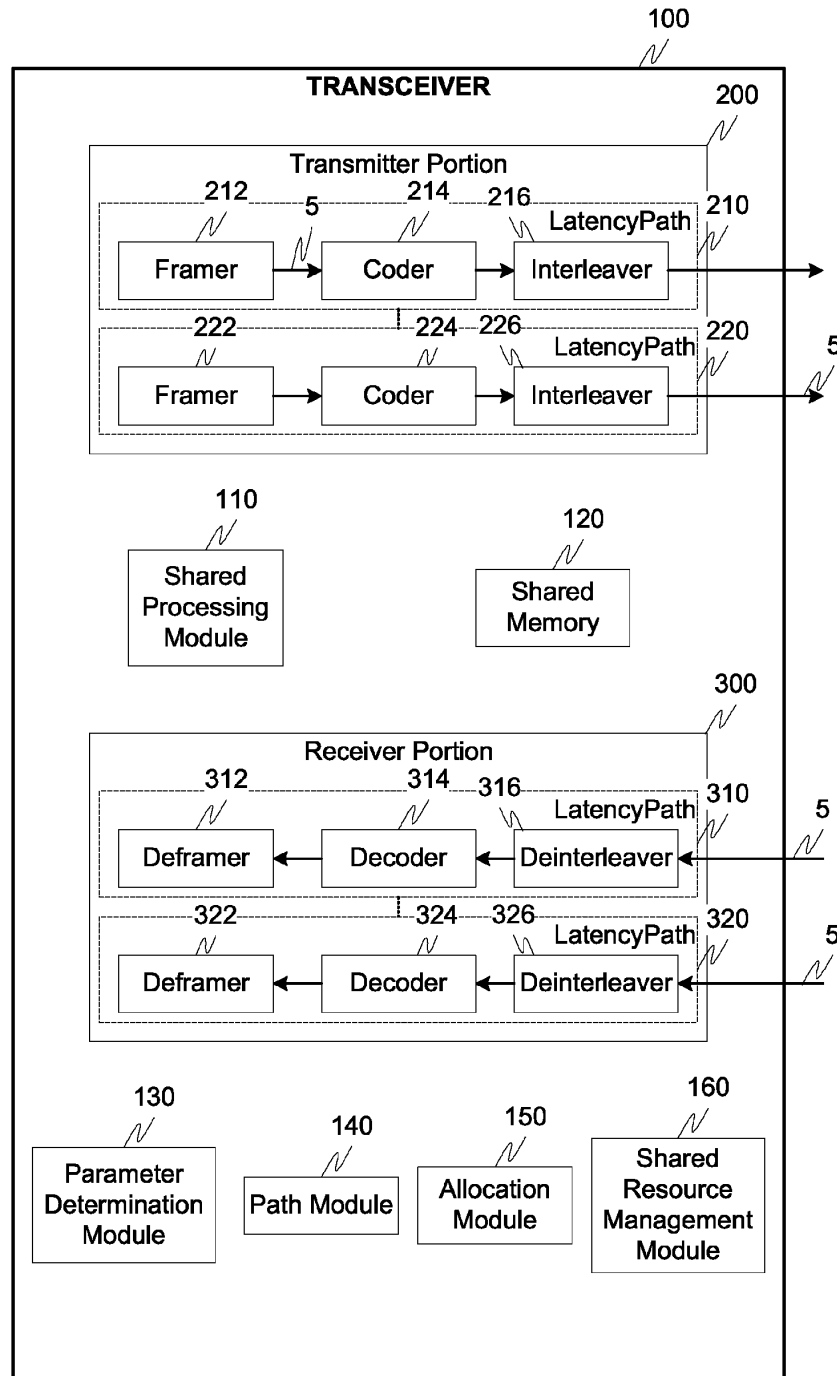


Fig. 1

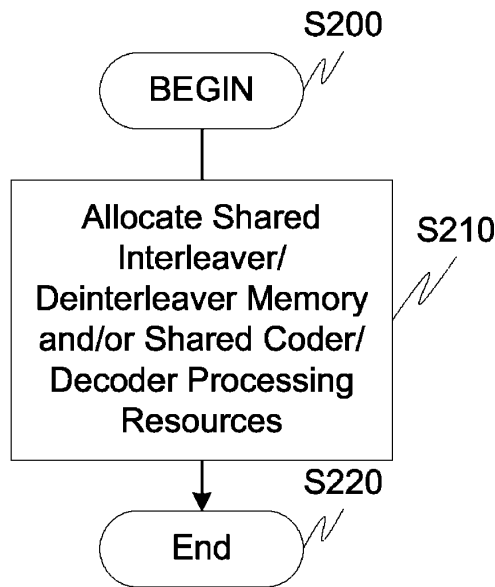


Fig. 2

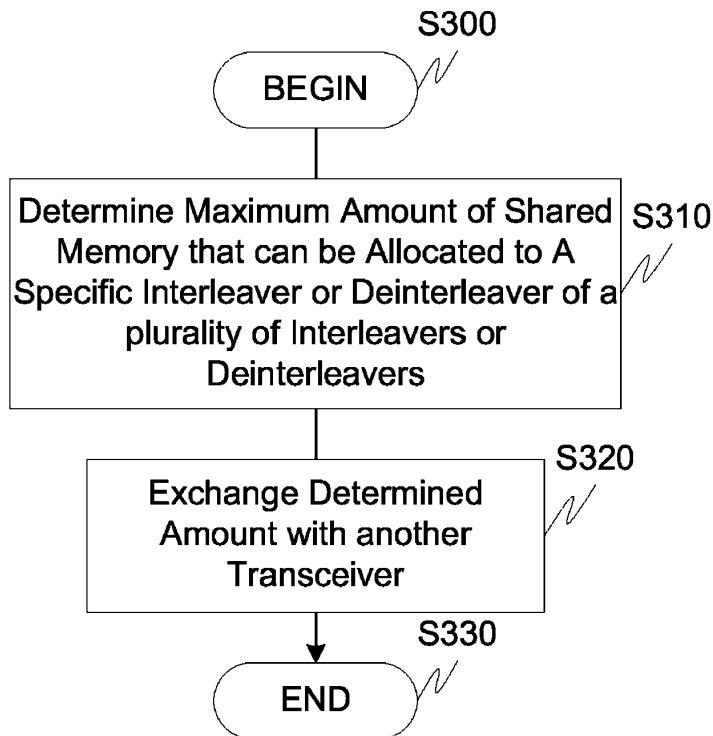


Fig. 3

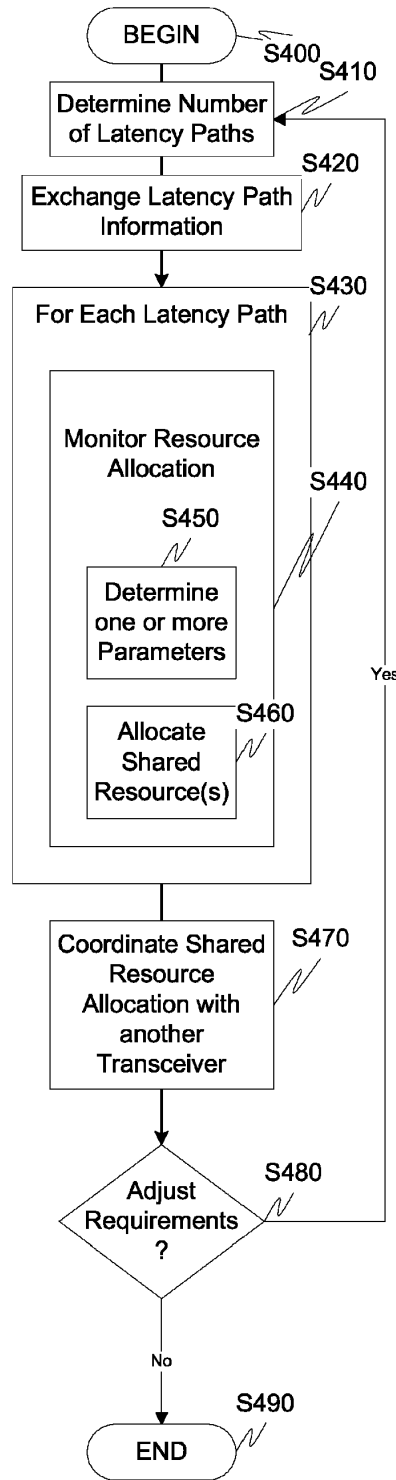


Fig. 4

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