

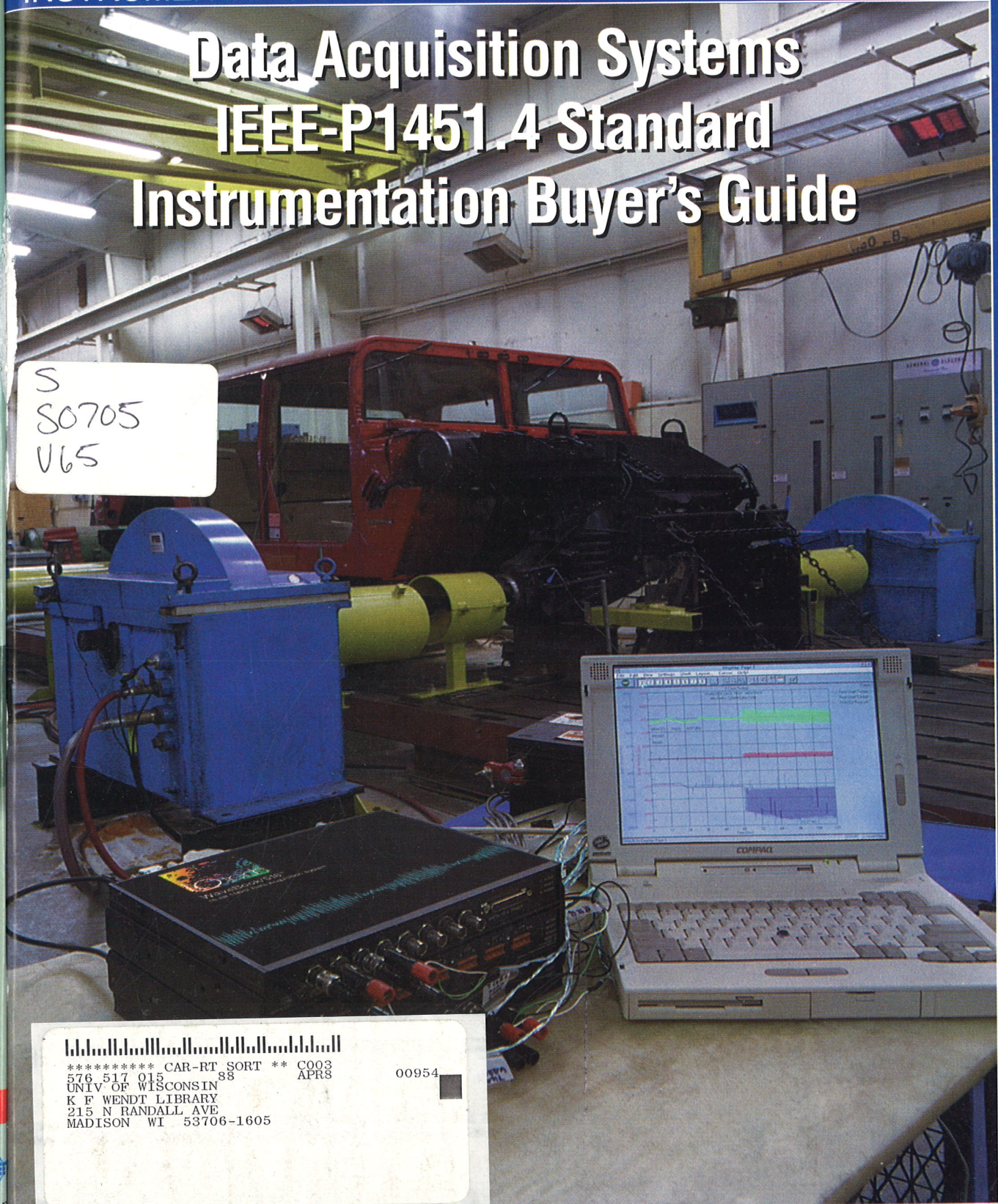
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INSTRUMENTATION REFERENCE ISSUE

APRIL 1998

Data Acquisition Systems IEEE-P1451.4 Standard Instrumentation Buyer's Guide

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COVER

The cover photograph depicts IOtech's new WaveBook/516 portable PC-based data acquisition system being used in "Hummer" vehicle testing at AM General Corporation's testing facility in Livonia, MI. The WaveBook /516 provides 1-MHz speed data acquisition while maintaining 16-bit resolution. This performance accommodates multichannel data collection for test and measurement applications such as: vehicle noise and vibration testing which require high resolution. (Photo courtesy of IOtech, Cleveland, OH.)

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Interface for Sensors and Actuators

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This article discusses some of the key issues of the proposed IEEE P1451.4 standard – the existing mixed-mode transducer communication schemes, the Transducer Electronic Data Sheet (TEDS) requirements, compatibility with legacy systems, and utilization of results of other P1451 developments to leverage existing and emerging sensor-networking technologies.

Today, the transducer industry produces and utilizes mainly analog transducers. Interfacing these transducers to measurement and control systems is a major and costly undertaking. While digital communication is the trend of the future, the issue of interfacing analog transducers with additional smart features to legacy systems should be addressed.

The test and measurement community requires transducers with built-in identification which also fulfill more common requirements: 2 wire system, small size, low cost, low power consumption, etc. The test and measurement community will be best served with a standardized transducer interface and a uniquely identifiable set of standardized protocols.

Due to the lack of such a standard, some transducer manufacturers have introduced various solutions but have seen limited acceptance. An independent and openly defined standard will reduce risk for potential users, transducer and system manufacturers, and system integrators. This will accelerate the emergence and acceptance of this technology. Therefore, the project, IEEE P1451.4, was established to develop a standard that allows analog transducers to communicate digital information (mixed-mode operation) for the purposes of self-identification and configuration.

The IEEE P1451 Working Groups have been working on a uniform approach for connecting sensors and actuators to communication networks, control systems and measurement systems. The P1451.1, 1451.2 and P1451.3 efforts focused on network-capable sensors and actuators with digital readings. The P1451.4 effort proposes a mixed-mode smart transducer communication protocol based on existing analog connections. It also specifies TEDS formats for interfacing analog transducers with additional smart features to the legacy systems. The proposed interface will be designed to be compatible with other P1451 network-capable transducer interfaces.

P1451.4 Proposed Standard

The proposed standard will define an interface for mixed-mode transducers (i.e., analog transducers with digital output for control and self-describing purposes) as part of the P1451 family of standards (see Figure 1). It will establish a standard that allows analog-output, mixed-mode transducers to communicate digital information with an IEEE P1451 compliant object. Both sensors and actuators are supported and the existence of the P1451.4 interface is invisible from the network viewpoint.

It is the intent that all of the standards in the IEEE 1451 family can be used either as stand-alone or with each other. For example, a 'black box' transducer with a P1451.1 object model combined with a P1451.4-compliant transducer is within the

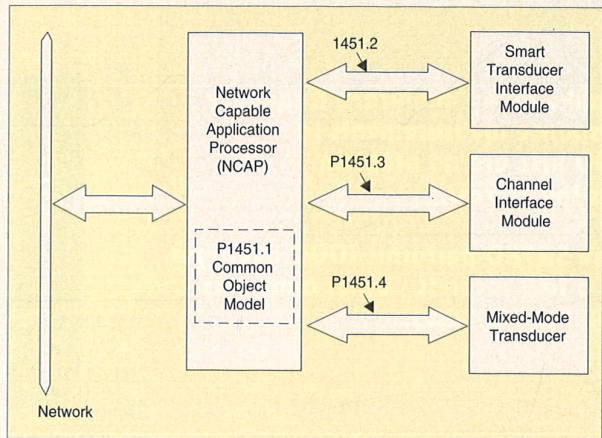


Figure 1. IEEE P1451 family relationship.

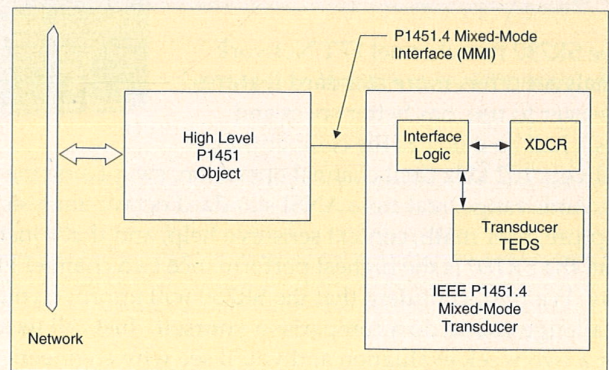


Figure 2. IEEE P1451.4 interface.

definition of the P1451 family specification. The IEEE P1451.4 interface is needed both to allow the use of existing analog transducer wiring and also for those demanding applications where it is not practical to physically include the Network Capable Application Processor (NCAP) with the transducer. Examples of the latter include very small transducers and very harsh operating environments.

Each P1451.4-compliant mixed-mode transducer would consist of at least one transducer and the interface logic required to control and transfer data across various existing analog interfaces (see Figure 2). The transducer TEDS will be minimized and defined such that it contains enough information to allow a higher level P1451 object to fill any gaps in its TEDS.

Scope. This P1451.4 Working Group will propose a standard that allows analog transducers to communicate digital information with an IEEE P1451 object. The standard will define the protocol and interface. It will also define the format of the transducer TEDS. The transducer TEDS will be based on the IEEE 1451.2 TEDS. The standard will not specify the transducer design, signal conditioning or the specific use of the TEDS.

Purpose. A standard is needed that allows analog transducers to communicate digital information for the purposes of self-identification and configuration. Due to the lack of a standard, some transducer manufacturers have introduced various solutions but have seen limited acceptance. An independent and

ICP is a registered trademark of PCB Piezotronics, Inc.
MicroLAN is a registered trademark of Dallas Semiconductor, Inc.
LabVIEW is a registered trademark of National Instruments, Inc.
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DeltaTron is a registered trademark of Brüel & Kjær

openly defined standard will reduce risk for potential users, transducer and system manufacturers and system integrators. This will accelerate the emergence and acceptance of this technology.

TEDS. The P1451.4 TEDS shall be compatible with the 1451.2 TEDS with the goal to minimize the size of nonvolatile memory. The key consideration for TEDS design are: relevant information that help the user, "plug-and-play" functionality, support for all transducers, openness for individual needs and compatibility to 1451.2. The P1451.4 TEDS shall include the following categories:

1. Identification Parameters

- a. Manufacturer name
- b. Model number
- c. Series number
- d. Revision number
- e. Date code

2. Device Parameters

- a. Sensor type
- b. Sensitivity
- c. Bandwidth
- d. Units

3. Calibration Parameters

- a. Last calibration date
- b. Correction engine coefficients

4. Application Parameter

- a. Channel identification
- b. Channel grouping
- c. Sensor location and orientation

The most recent proposed TEDS includes a 256-bit, non-human-readable compact-TEDS and additional human-readable TEDS. The compact-TEDS enables the minimum implementation of a P1451.4 TEDS for transducers which have limited memory or size. Table 1 indicates the latest proposal for the P1451.4 compact-TEDS which will include parameters, such as bandwidth, which are critical for higher bandwidth transducers.

Implementation. To take advantage of the results from the P1451 effort, P1451.4 will establish a standard that allows

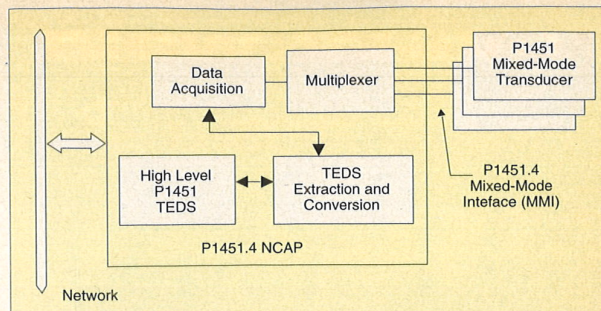


Figure 3. IEEE P1451.4 compatible NCAP.

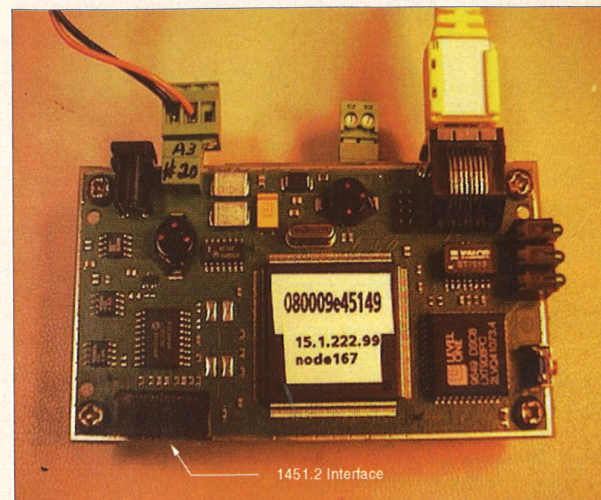


Figure 4. Ethernet-to-1451.2 NCAP created for a demonstration of concept (courtesy of Hewlett-Packard).

analog-output mixed-mode transducers to communicate digital information with a high-level IEEE P1451 object.

Table 1. Proposed IEEE P1451.4 Transducer Electronic Data Sheet (TEDS) measuring template formats.

Field Description	Type	Access	# of Bits	Bit Position	Example	Unit	
ROM Part							
Manufacturer	Brüel & Kjær	Manufacturer	RO-Manuf.	11	0	Brüel & Kjær	
Type number		UNINT	RO-Manuf.	16	11	1234	
Version letter		Chr5	RO-Manuf.	5	27	" "	
Version number		UNINT	RO-Manuf.	7	32	0	
Serial number		UNINT	RO-Manuf.	25	39	1234567	
E²-PROM Part							
CRC8 for the first 64+256 bits		CRC8	R/W-all	8	0		
Selector of template descriptor	IEEE 1451.4	SelOfDescriptor	R/W-Cal.	2	8	IEEE 1451.4	
Template identification	Accelerometer			8	10	Acceleration	
Sensitivity @ F _{ref}		S(1 + r) ⁿ	R/W-Cal.	16	18	100.0E+6	V/ms ²
F _{ref}		S(1 + r) ⁿ	R/W-Cal.	8	34	10.0E+0	Hz
Calibration date		date	R/W-Cal.	16	42	July 18, 1997	
Transfer function approximation							
F _{hp} electrical	(n=0: section disabled)	S(1 + r) ⁿ	R/W-Cal.	12	58	10.0E-3	Hz
F _{lp} electrical	(n=0: section disabled)	S(1 + r) ⁿ	R/W-Cal.	12	70	100.0E+0	Hz
F _{mounted} resonance	(n=0: section disabled)	S(1 + r) ⁿ	R/W-Cal.	9	82	100.0E+0	Hz
Mounted Q	(n=0: section disabled)	S(1 + r) ⁿ	R/W-Cal.	8	91	300.0E-3	
Temperature coefficient	(n=0: section disabled)	S(1 + r) ⁿ	R/W-Cal.	9	99	1.0E-6	
Sensitivity direction (x,y,z)		enum	R/W-Cal.	2	108	x=0,y=1,z=2	
Measurement position ID (0...511)		position	R/W-user	8	110		
Selector of extended template descriptor	User	SelOfExtDescript	R/W-Cal	2	118	User	
User data			R/W-user	136	120		
Total number of predefined bits				184			
Total number of bits				320			

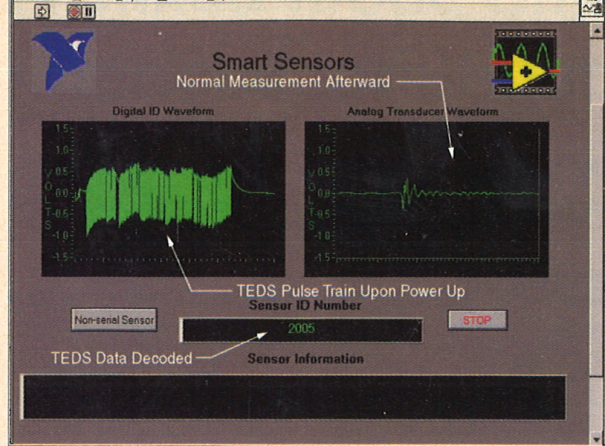


Figure 5. TEDS decoding with LabVIEW (courtesy of National Instruments).

One of the possible implementations is shown in Figure 3 – a multichannel P1451.4 NCAP, based on a 1451.2 NCAP, with optional multiplexer, data acquisition, logic or firmware for TEDS extraction and conversion, as well as high-level P1451 TEDS. Figure 4 shows a compatible 1451.2 NCAP created by Hewlett-Packard Co.* as a demonstration of concept.

Compatibility with Legacy Systems

P1451.4 transducers with TEDS will be compatible with legacy data acquisition systems by utilizing existing analog connections. Under normal operating condition, P1451.4 transducers will behave like traditional analog transducers. Upon power-up or interrogation, P1451.4 transducers will enter a digital communication mode.

Legacy systems can be updated to support P1451.4 transducers, as follows:

1. Hardware – Extra circuitry can be added at the front-end to control the P1451.4 transducers and to decode the TEDS. This will require modification of the legacy systems.
2. Patch Panel – A patch panel can be used between P1451.4 transducers and legacy systems to toggle power to the transducers or send out interrogation signals to activate the digital mode. It can also decode and upload the digital TEDS data to a controller. Additional hardware is necessary, but no modification of the legacy systems is required.
3. Firmware/Software – The embedded controller or the software in the legacy system can extract the TEDS data from the waveform memory after power up or interrogation. This solution requires no modification of legacy systems, nor additional hardware. Two previous demonstrations included a National Instruments LabVIEW® program with PC plug-in ADC (Analog to Digital Converter) cards (see Figure 5) and an IBASIC program with the HP35670A Dynamic Signal Analyzer.

Current Approaches

Several transducer manufacturers have supplied mixed-mode transducers with built-in intelligence for improvement such as self identification. These updated mixed-mode transducers utilize existing analog interfaces such as the ICP® and 4-20 mA current loop interfaces with or without an additional digital connection.

Several commercial mixed-mode smart transducers are available. Members of the P1451.4 working group have proposed two types of implementation. Brüel & Kjær has proposed a bi-directional, multi-drop communication scheme (see Figures 6,

*Certain commercial products are identified in this article in order to adequately describe the proposed standard. Such identification does not imply recommendation or endorsement by the National Institute of Standards and Technology.

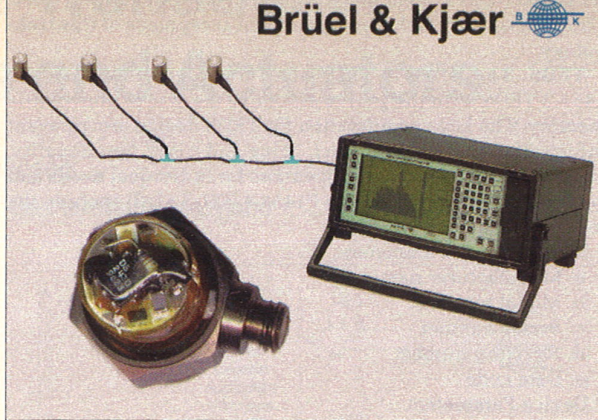


Figure 6. Example of possible multi-drop smart transducer (courtesy of Brüel & Kjær).

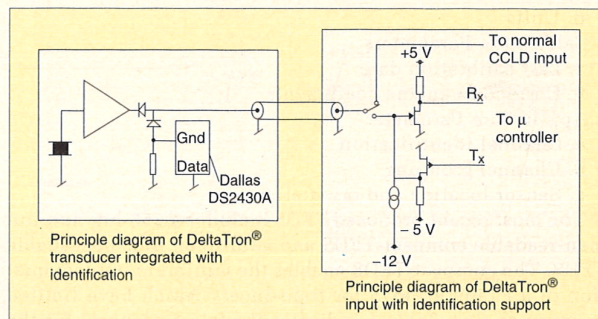


Figure 7. Example of current implementation (courtesy of Brüel & Kjær).

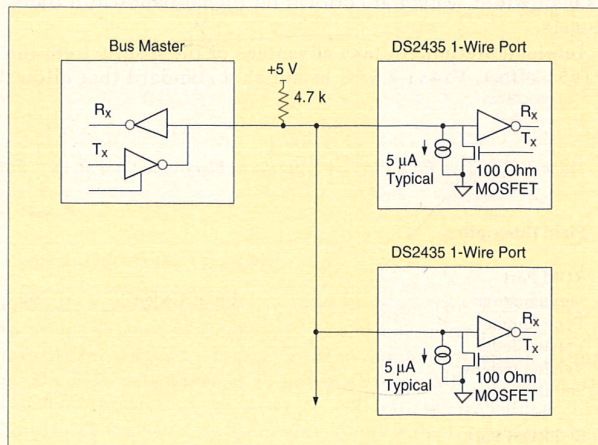


Figure 8. Coupling network between bus master (e.g., instrument) and 1-wire port (e.g., transducer). Proposed by Brüel & Kjær.

7 and 8). This proposal is based on standard MicroLAN® components from Dallas Semiconductor and the digital communication is activated by switching the normal (positive) supply current to a negative current. Other members have proposed point-to-point systems (such as shown in Figure 9). Digital communication is activated upon power-up (Kistler) or by a 2 mA drop of supply current for a predefined time period (Endevco and Wilcoxon).

Currently, the working group is very interested in the 1-wire MicroLAN® due to its one wire nature and commercially available components. Note in Figure 10 that the TEDS electronics does not increase the size of a modal accelerometer.

Future Development

The P1451.4 working group has formed a TEDS subgroup and the subgroup has achieved good progress. Hewlett-Packard and

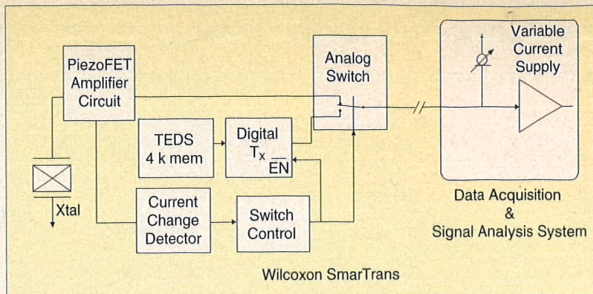


Figure 9. Example of current implementations (courtesy of Wilcoxon Research).

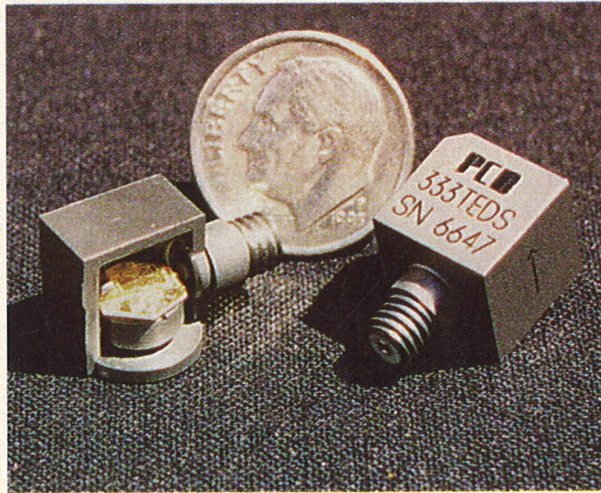


Figure 10. The challenge of implementing TEDS in a modal accelerometer (courtesy of The Modal Shop).

Kistler Instruments have agreed to provide licenses for their individual patents related to the P1451.4 working group effort. Dallas Semiconductors has also agreed to provide a license of their patents related to the 1-wire MicroLAN®.


Involvement by other companies with various sensing technologies will be highly welcome.

For more information on the P1451 working groups contact: Kang Lee, National Institute of Standards and Technology; Phone: 301-975-6602; e-mail: Kang.Lee@NIST.gov.

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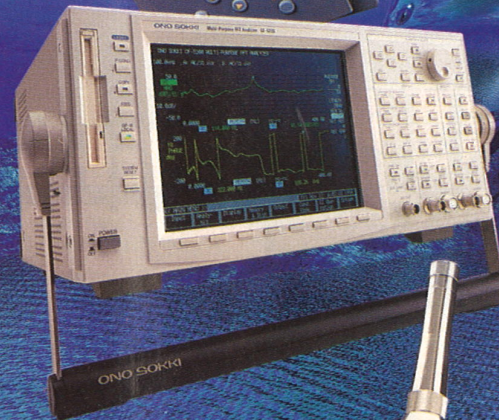
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