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JC960 U.S. PTO

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File: 2000.107700/TT5494

May 1, 2003

MS PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF EXPRESS MAILING UNDER C.F.R. § 1.8	
EXPRESS MAIL NO.	EV 291 349 099 US
DATE OF DEPOSIT:	May 1, 2003
I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "EXPRESS MAIL" addressed to: MS PATENT APPLICATION, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.	
<i>Mary Paul</i> Signature	

RE: *U.S. Patent Application Entitled: METHOD AND APPARATUS FOR FILTERING METROLOGY DATA BASED ON COLLECTION PURPOSE*
James Broc Stirton
(2000.107700/TT5494)

Sir:

Transmitted herewith for filing are:

- (1) 21-page patent specification with 20 claims and an abstract (also Figures 1-2 on 2 sheets);
- (2) Declaration;
- (3) Power of Attorney;
- (4) Assignment and Assignment Cover Sheet; and
- (5) Request and Certification Under 35 U.S.C. § 122(b)(2)(B)(i).

All correspondence, notices, official letters and other communications should be directed to Scott F. Diring, Williams, Morgan & Amerson, P.C., 10333 Richmond, Suite 1100, Houston, TX 77042, and all telephone calls should be directed to Scott F. Diring at (608) 833-0748.

The Assistant Commissioner is authorized to deduct the amount of the total filing fee (listed below) from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT5494. In the event the monies in that account are insufficient, the Assistant Commissioner is authorized

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 Page 2


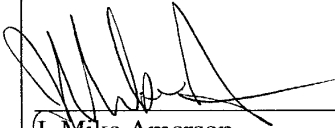
to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.107700/SFD.

FILING FEE CALCULATION

FOR		Small Entity	Large Entity
Total Claims	20 - 20 = 0	x \$9 = \$	or x \$18 = \$ 0.00
Independent Claims	3 - 3 = 0	x \$42 = \$	or x \$84 = \$ 0.00
Multiple Dependent Claim(s)		+ \$140 = \$	or + \$280 = \$ 0.00
Basic Fee:		+ \$370 = \$	or + \$740 = \$ 740.00
Assignment Recording Fee:	(\$40 per assignee)	+ = \$	+ = \$ 40.00
TOTAL FILING FEES		\$ 0.00	\$780.00

Pursuant to 37 C.F.R. § 1.10 the Applicants request the Patent and Trademark Office to accept this application and accord a serial number and filing date as of the date this application is deposited with the U.S. Postal Service for Express Mail.

Please date stamp and return the enclosed postcards to evidence receipt of these materials.

<p>Date: <u>May 1, 2003</u></p>  <p>23720 PATENT TRADEMARK OFFICE</p>	<p>Respectfully submitted,</p>  <p>J. Mike Amerson Reg. No. 35,426 WILLIAMS, MORGAN & AMERSON, P.C. 10333 Richmond, Suite 1100 Houston, Texas 77042 (713) 934-7000 (713) 934-7011 (facsimile) ATTORNEY FOR APPLICANT(S)</p>
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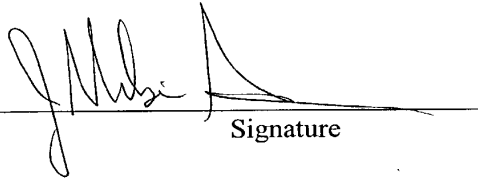
JMA/ym
 Enclosures

cc: Ms. Samantha Cardona (w/enc.)

<p align="center">REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)</p>	<p>First Named Inventor: JAMES BROC STIRTON</p> <p>Title: METHOD AND APPARATUS FOR FILTERING METROLOGY DATA BASED ON COLLECTION PURPOSE</p> <p>Attorney Docket Number: 2000.107700/TT5494</p>
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I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

Date: May 1, 2003



Signature

J Mike Amerson
Typed or printed name

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

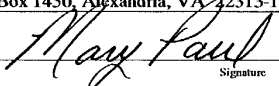
Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**

Burden Hour Statement: This collection of information is required by 37 CFR 1.213(a). The information is used by the public to request that an application not be published under 35 U.S.C. 122(b) (and the PTO to process that request). Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This form is estimated to take 6 minutes to complete. This time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.**

Application for United States Letters Patent
for
**METHOD AND APPARATUS FOR FILTERING METROLOGY DATA
BASED ON COLLECTION PURPOSE**

by
James Broc Stirton

CERTIFICATE OF EXPRESS MAILING UNDER C.F.R. § 1.8	
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Signature	

METHOD AND APPARATUS FOR FILTERING METROLOGY DATA BASED ON COLLECTION PURPOSE

BACKGROUND OF THE INVENTION

5 1. FIELD OF THE INVENTION

This invention relates generally to an industrial process, and, more particularly, to a method and apparatus for filtering metrology data based on collection purpose in a semiconductor device manufacturing environment.

2. DESCRIPTION OF THE RELATED ART

10 There is a constant drive within the semiconductor industry to increase the quality, reliability and throughput of integrated circuit devices, *e.g.*, microprocessors, memory devices, and the like. This drive is fueled by consumer demands for higher quality computers and electronic devices that operate more reliably. These demands have resulted in a continual improvement in the manufacture of semiconductor devices, *e.g.*, transistors, as well
15 as in the manufacture of integrated circuit devices incorporating such transistors. Additionally, reducing the defects in the manufacture of the components of a typical transistor also lowers the overall cost per transistor as well as the cost of integrated circuit devices incorporating such transistors.

 Generally, a set of processing steps is performed on a wafer using a variety of
20 processing tools, including photolithography steppers, etch tools, deposition tools, polishing tools, rapid thermal processing tools, implantation tools, *etc.* One technique for improving the operation of a semiconductor processing line includes using a factory wide control system to automatically control the operation of the various processing tools. The manufacturing tools communicate with a manufacturing framework or a network of processing modules.

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