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# United States Patent [19]

Kizuka

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[54] **METHOD OF AND APPARATUS FOR DEALING WITH PROCESSOR ABNORMALITY IN MULTIPROCESSOR SYSTEM**

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## [57] ABSTRACT

[21] Appl. No.: **536,739**

A multiprocessor system has processors for processing distributed works, a monitoring facility for detecting an abnormality in any one of the processors, an administration facility for providing information about the abnormal processor and information about a redundant processor, and a work allocation facility for seeking the distributed works of the abnormal processor from a work table according to these pieces of information and allocating the sought works to given ones of the processors. The system includes an abnormality measures table that selectively describes measures to be taken for each of the distributed works against an abnormality. The work allocation facility determines, for each of the distributed works of the abnormal processor, a measure to be taken according to the abnormality measures table and allocates the distributed works of the abnormal processor to given ones of the processors. If the abnormality is recursive, allocating any work for which a specific measure such as rerun or continuation is to be taken is suspended. If the redundant processor is being initialized, allocating works to the redundant processor is delayed.

[22] Filed: **Sep. 29, 1995**

### [30] Foreign Application Priority Data

Sep. 29, 1994 [JP] Japan ..... 6-235422

[51] Int. Cl.<sup>6</sup> ..... **G06F 11/00**

[52] U.S. Cl. .... **395/182.11**; 395/182.09; 395/182.05; 364/268; 364/268.3

[58] Field of Search ..... 395/182.11, 182.09, 395/182.01, 181, 182.05

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**4 Claims, 18 Drawing Sheets**

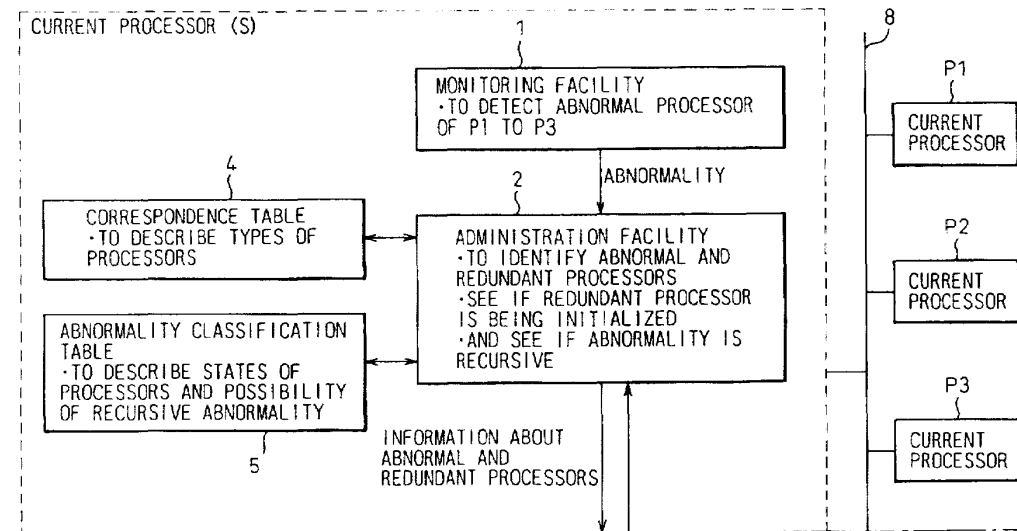


Fig.1A

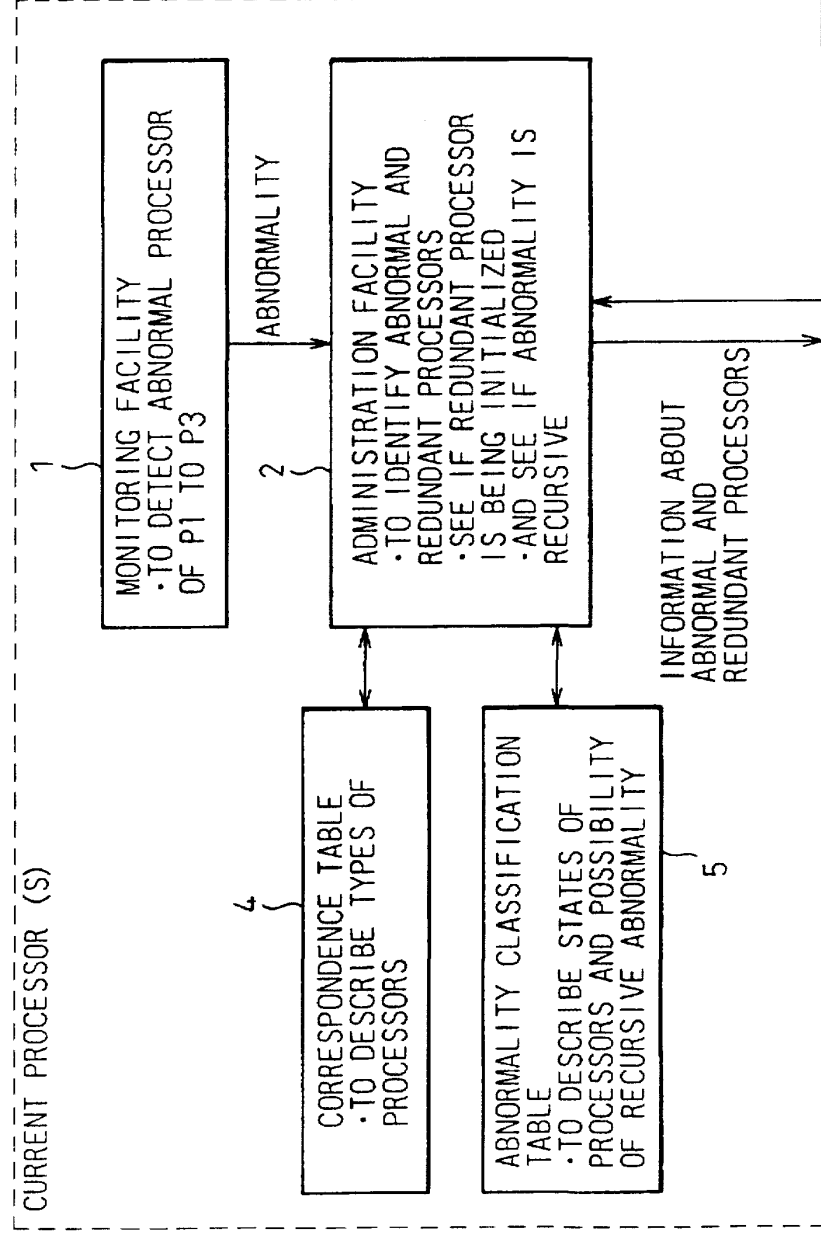


Fig. 1B

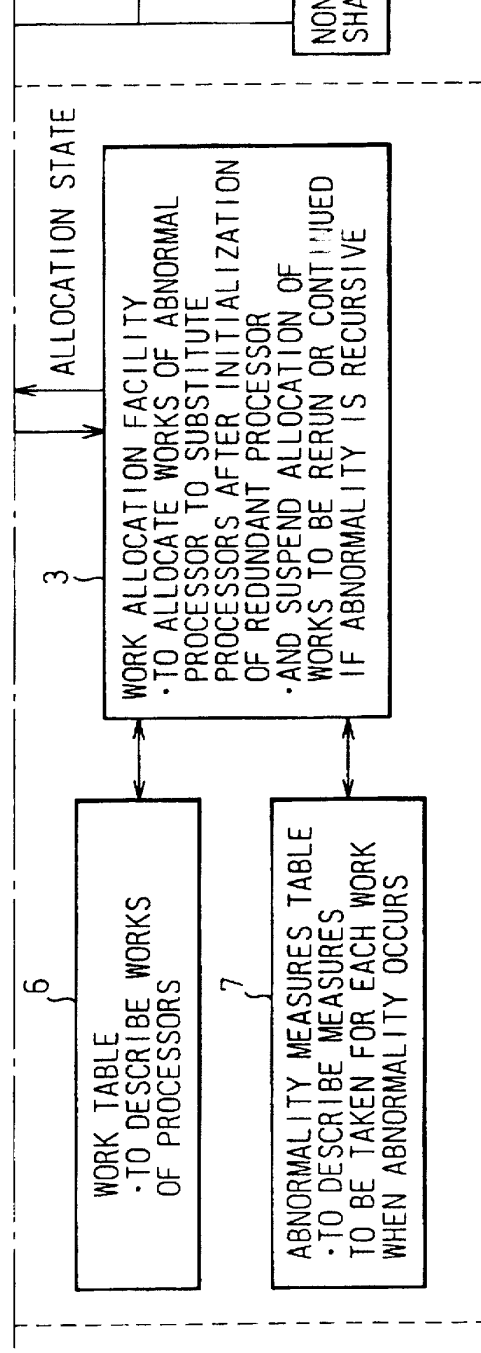


Fig. 2A

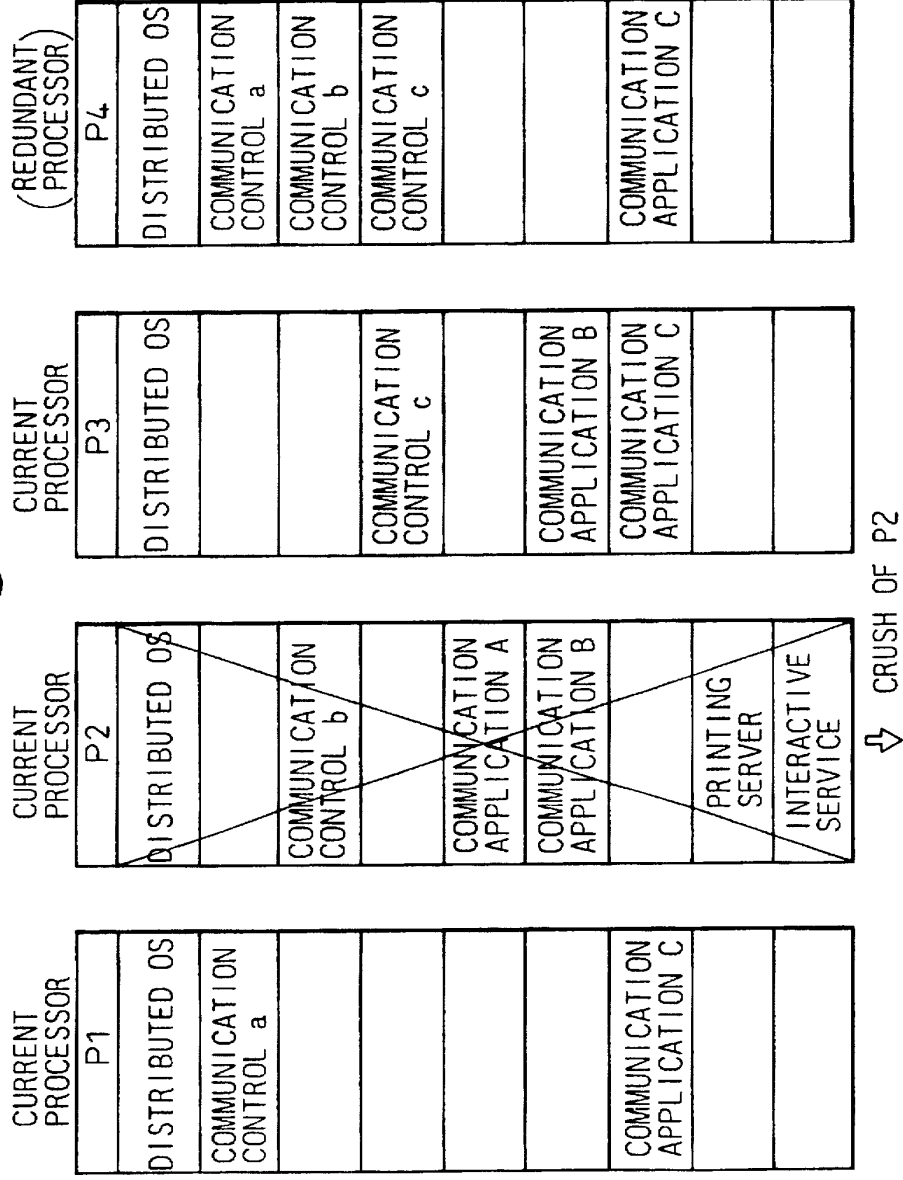
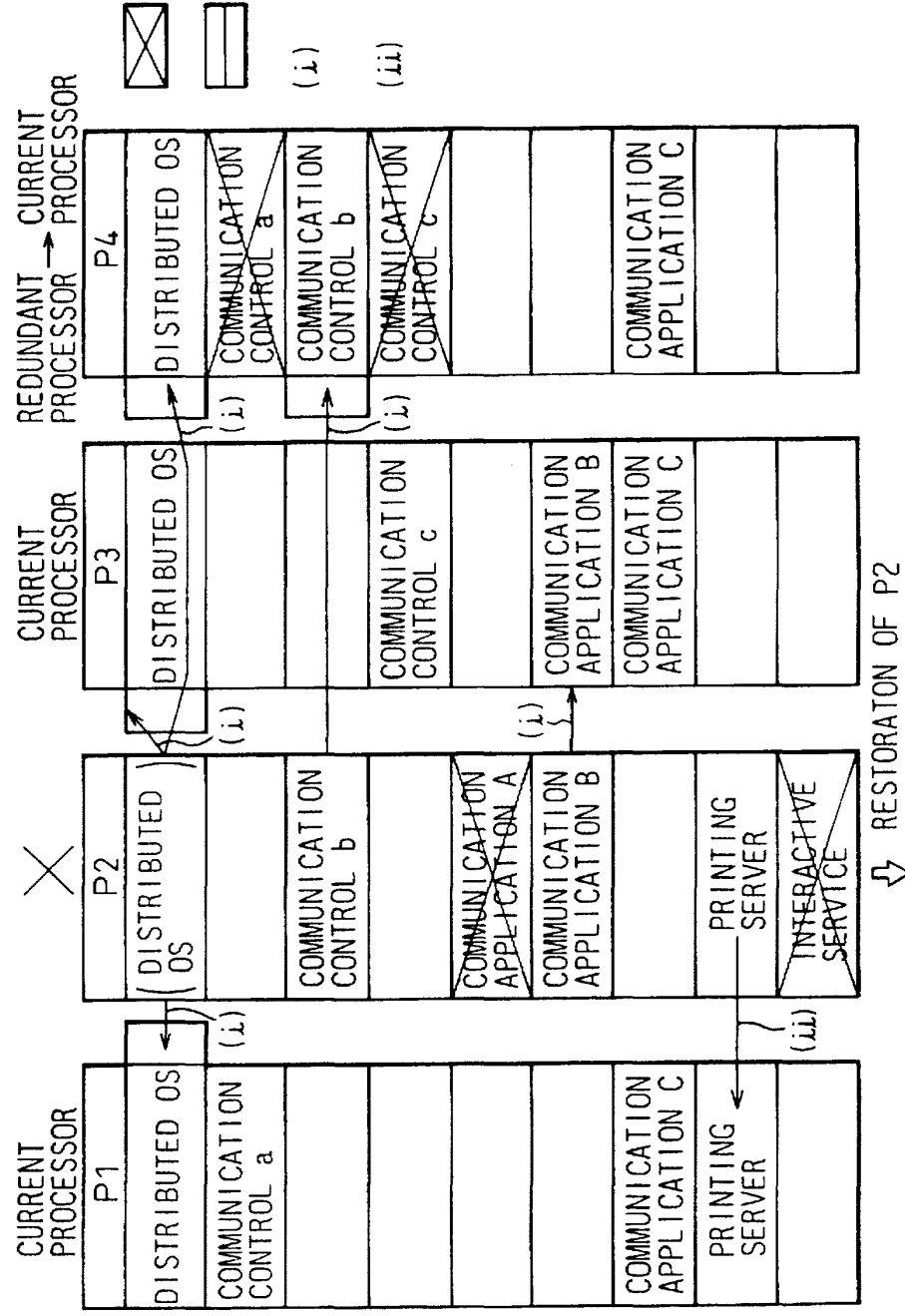


Fig. 2B



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