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(54) <b>MULTI-PROCESSOR DATA COHERENCY</b>	4,868,738 A	9/1989	Kish et al. ....	364/200
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(57) **ABSTRACT**

A method for maintaining coherent data in a multiprocessor system having a plurality of processors coupled to main memory, where each processor has an internal cache which is externally unreadable outside the processor. The method includes requesting data associated with a memory location in main memory and determining if an external cache coupled to an application specific integrated circuit associated with a second processor contains a reference to the requested data. A snoop cycle is performed on the second processor if the external cache has a reference to the requested data, whereupon a determination is made as to whether the requested data has been modified.

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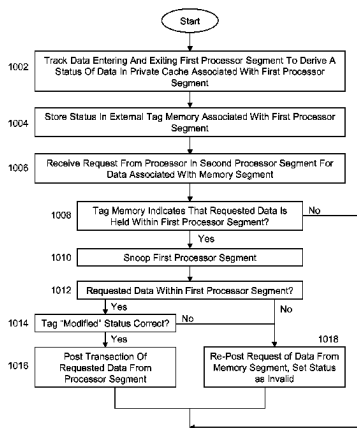
(58) **Field of Classification Search** ..... None  
See application file for complete search history.

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**6 Claims, 11 Drawing Sheets**



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