

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent of: Sanjay K. Rao, et al.  
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Appl. Serial No.: 13/621,294  
Filing Date: September 17, 2012  
Title: SYSTEM TO INTERFACE INTERNET PROTOCOL (IP) BASED  
WIRELESS DEVICES WITH SUBTASKS AND CHANNELS

**SECOND DECLARATION OF DR. MICHAEL ALLEN JENSEN**

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1. This Declaration expands on the conclusions that I have formed based on the analysis provided in my first declaration (EX-1003, incorporated herein by reference in its entirety; “Original Declaration”). Consistent with my findings provided in my Original Declaration and based upon my knowledge and experience and my review of the prior art publications listed in the first and this declarations, a POSITA would have found that claims 1-9 and 12-20 (“the Challenged Claims”) of the ’943 patent are rendered obvious by at least the combinations of references set forth in my Original Declaration.

**I. THE BYRNE GROUNDS RENDER THE CHALLENGED CLAIMS OBVIOUS**

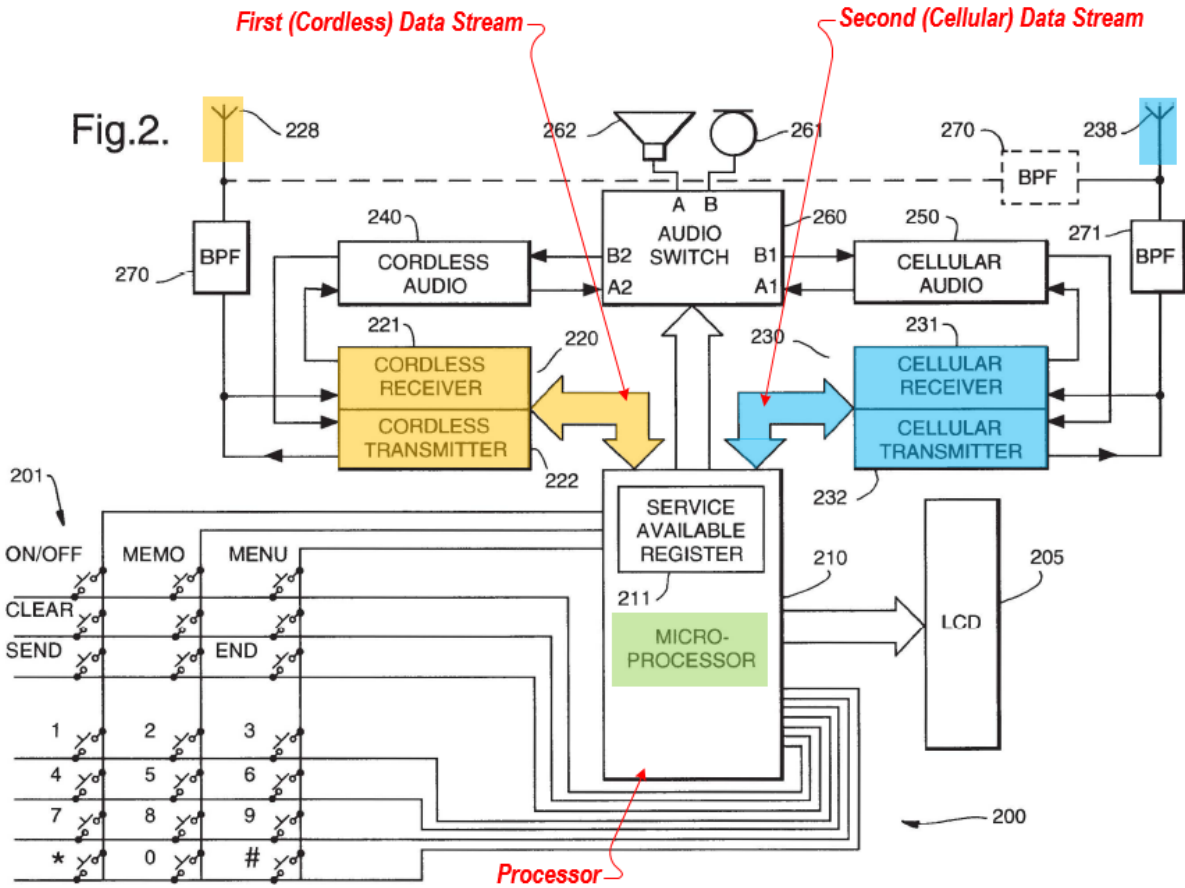
**A. Byrne Renders Obvious The “Processor” Limitations In Claims 1, 3-9, And 12 (Grounds 1A-1C)**

1. **Byrne’s “Microprocessor” Receives and Processes First and Second Data Streams**

2. In the Patent Owner’s response (“POR”), Patent Owner contends that Byrne’s “microprocessor 210” merely controls transceivers 220, 230 and audio switch 260, and does not receive or process data streams. POR, 7-13. While Byrne describes the microprocessor as performing certain control operations, the operations are not limited to what is disclosed in Byrne. EX-1008, 8:16-28. Based on my review of Byrne’s disclosure and a POSITA’s knowledge of processors by the Critical Date, a POSITA would have understood and found obvious that Byrne’s

microprocessor receives and processes data streams. EX-1049, 20:13-21:4 (Dr. Cooklev recognized that processors as of 1999 were multitask capable).

3. First, FIG. 2 clearly shows that Byrne’s microprocessor receives data from its cellular and cordless transceivers:



EX-1008, Figure 2 (annotated)

4. Reviewing Byrne’s FIG. 2, a POSITA would have considered the arrows from Byrne’s transceivers to its microprocessor as depicting a flow of data received by the transceivers to the microprocessor. According to Patent Owner, these arrows allegedly depict a flow of “instructions,” not data. However, neither Patent

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