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# (12) United States Patent

Mayega et al.

## (54) SINGLE INDUCTOR DUAL OUTPUT BUCK CONVERTER WITH FREQUENCY AND TIME VARYING OFFSET CONTROL

(75) Inventors: Valerian Mayega, Dallas, TX (US); Jun Chen, Allen, TX (US); James L.

Krug, Carrollton, TX (US); David W.

Evans, Allen, TX (US)

(73) Assignee: Texas Instruments Incorporated,

Dallas, TX (US)

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(65) **Prior Publication Data** 

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(51) **Int. Cl. G05F 1/577** (2006.01)

See application file for complete search history.

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## (10) Patent No.: US 7,061,214 B2

(45) **Date of Patent:** 

Jun. 13, 2006

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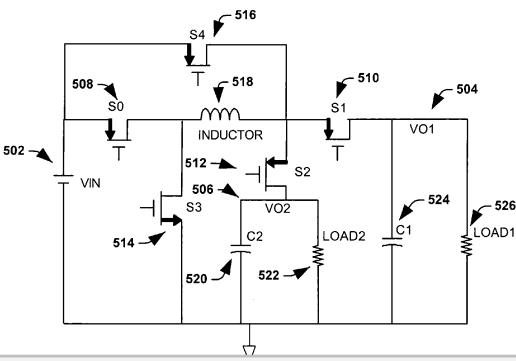
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Primary Examiner—Adolf Berhane (74) Attorney, Agent, or Firm—J. Dennis Moore; W. James Brady, III; Frederick J. Telecky, Jr.

#### (57) ABSTRACT

A single-inductor dual-output buck converter and control method that facilitates power conversion by converting a single DC power source/supply into two separate DC outputs, each of which can be configured to provide a selected/ desired voltage by selection of respective duty cycles. The topology of the inverter includes a pair of diodes or switches that can selectively re-circulate inductor current. The converter is generally operated at a fixed frequency with four stages of operation. A first and third stage of operation provide power to a first and second output, respectively. A second and fourth stage of operation re-circulate inductor current and can partially recharge a battery type power source. The power output for each stage (voltage and current) can be selectively obtained by computing and employing appropriate time periods for the stages of operation that correspond to appropriate duty cycles.

#### 23 Claims, 17 Drawing Sheets





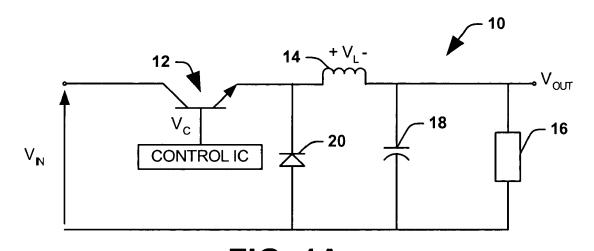


FIG. 1A
(PRIOR ART)

12(ON)

12(ON)

12(ON)

14

18

16

FIG. 1B (PRIOR ART)

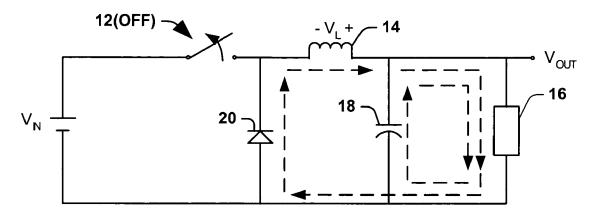
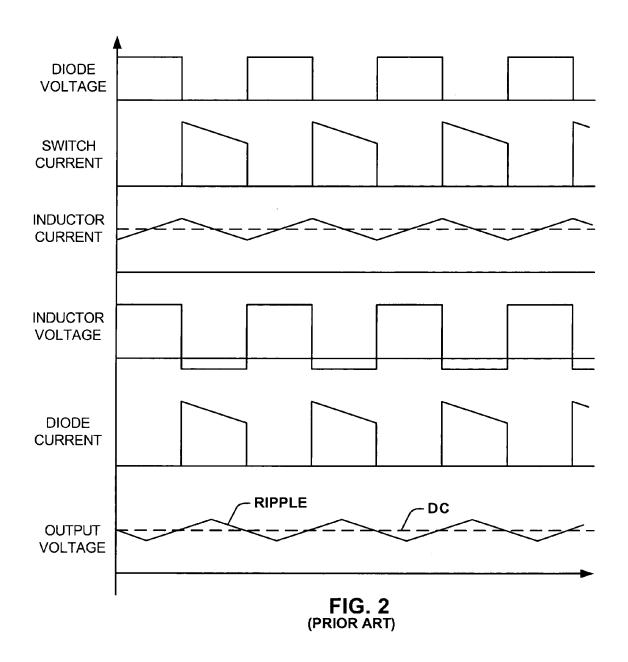
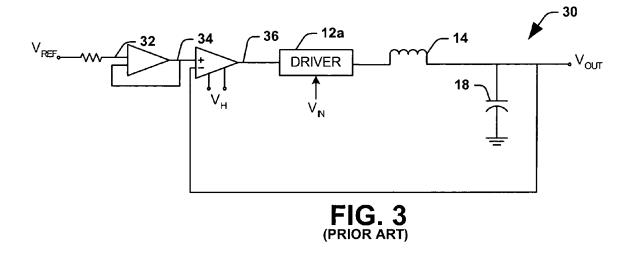


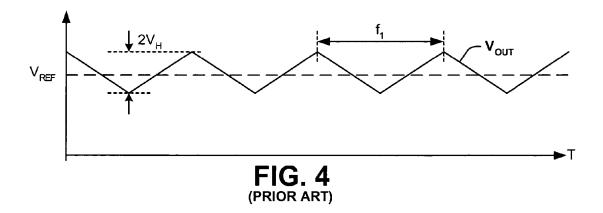
FIG. 1C (PRIOR ART)











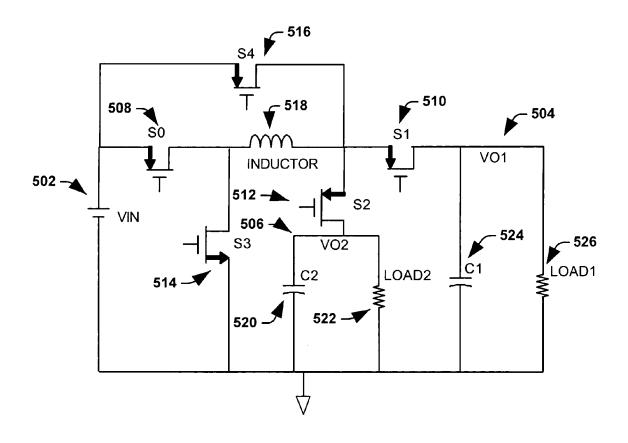


FIG. 5

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