



(19) **United States**

(12) **Patent Application Publication**  
**Amidi**

(10) **Pub. No.: US 2008/0101147 A1**

(43) **Pub. Date: May 1, 2008**

(54) **CLOCK AND POWER FAULT DETECTION FOR MEMORY MODULES**

(52) **U.S. Cl. .... 365/229**

(57) **ABSTRACT**

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A system, method and apparatus for clock and power fault detection for a memory module is provided. In one embodiment, a system is provided. The system includes a voltage detection circuit and a clock detection circuit. The system further includes a controller coupled to the voltage detection circuit and the clock detection circuit. The system also includes a memory control state machine coupled to the controller. The system includes volatile memory coupled to the memory control state machine. The system further includes a battery and battery regulation circuitry coupled to the controller and the memory control state machine. The battery, battery regulation circuitry, volatile memory, memory control state machine, controller, clock detection circuit and voltage detection circuit are all collectively included in a unitary memory module.

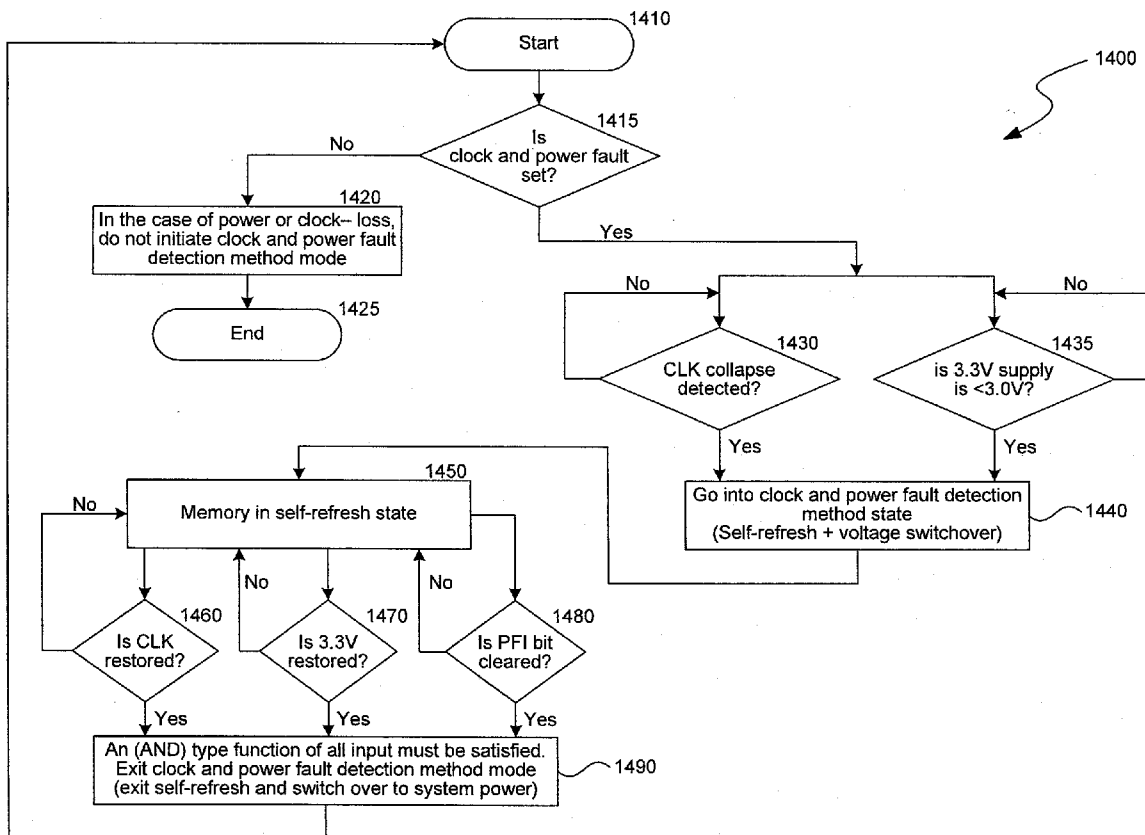
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(21) **Appl. No.: 11/552,949**

(22) **Filed: Oct. 25, 2006**

**Publication Classification**

(51) **Int. Cl. GIIC 5/14 (2006.01)**



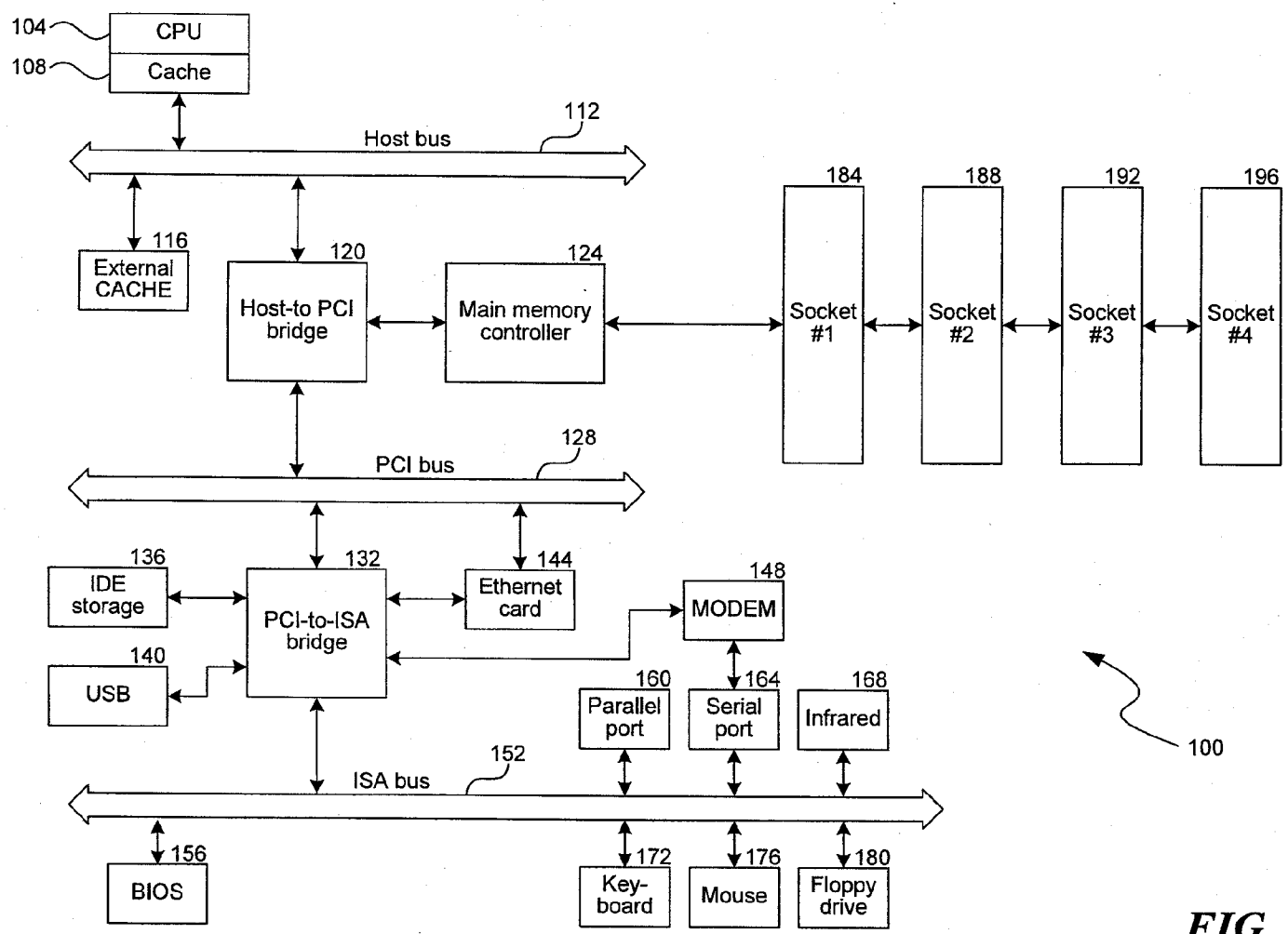


FIG. 1

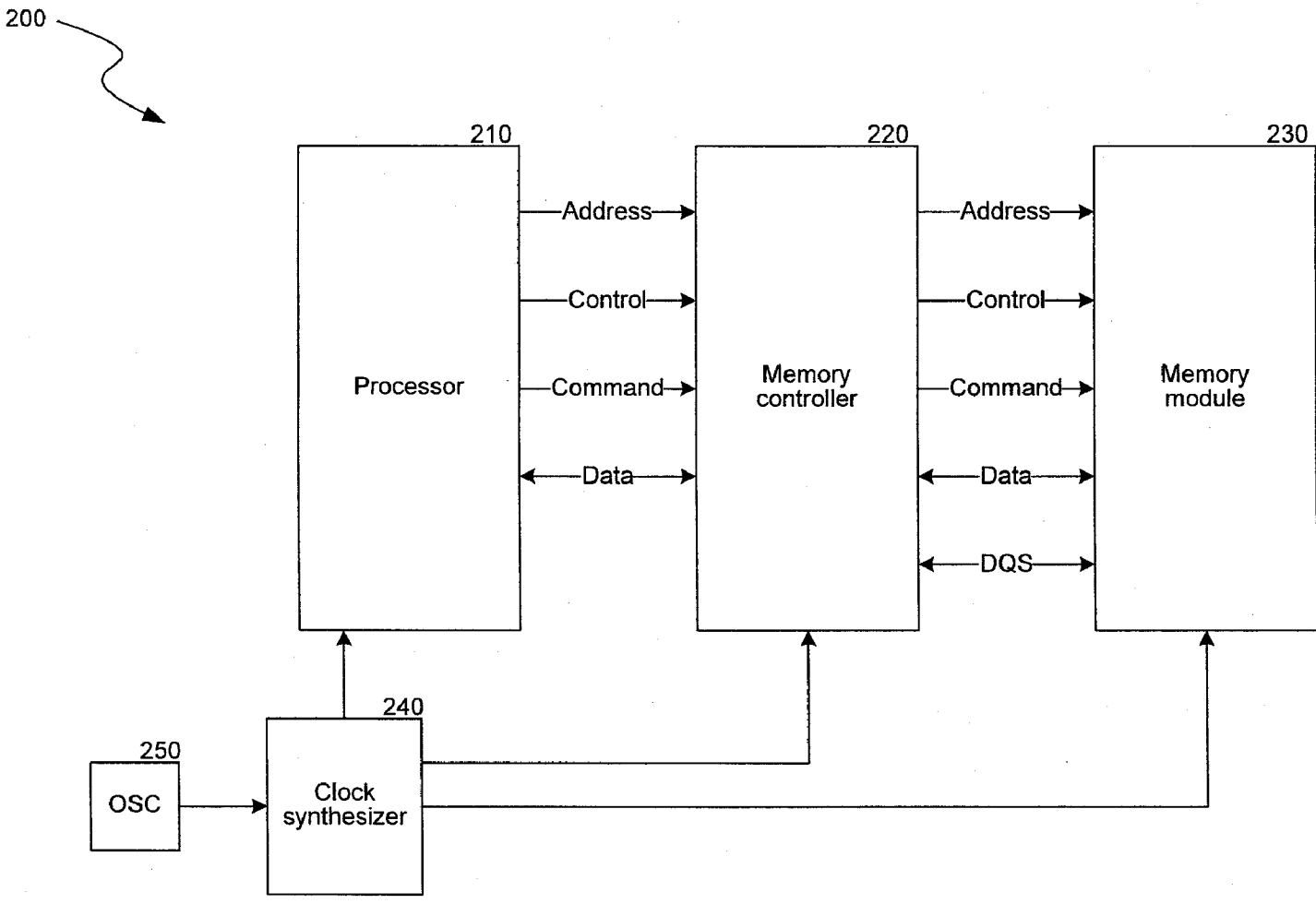


FIG. 2

300

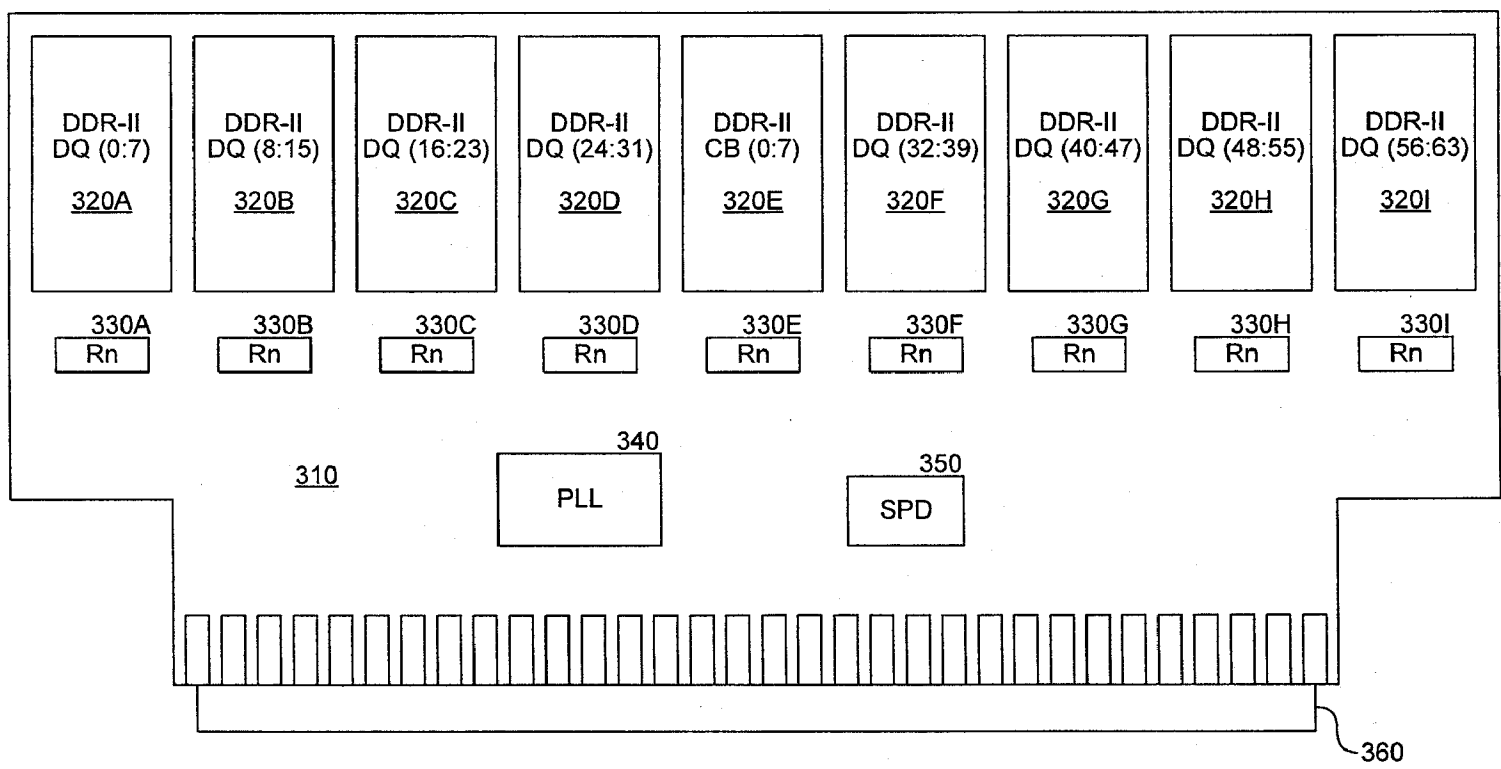


FIG. 3

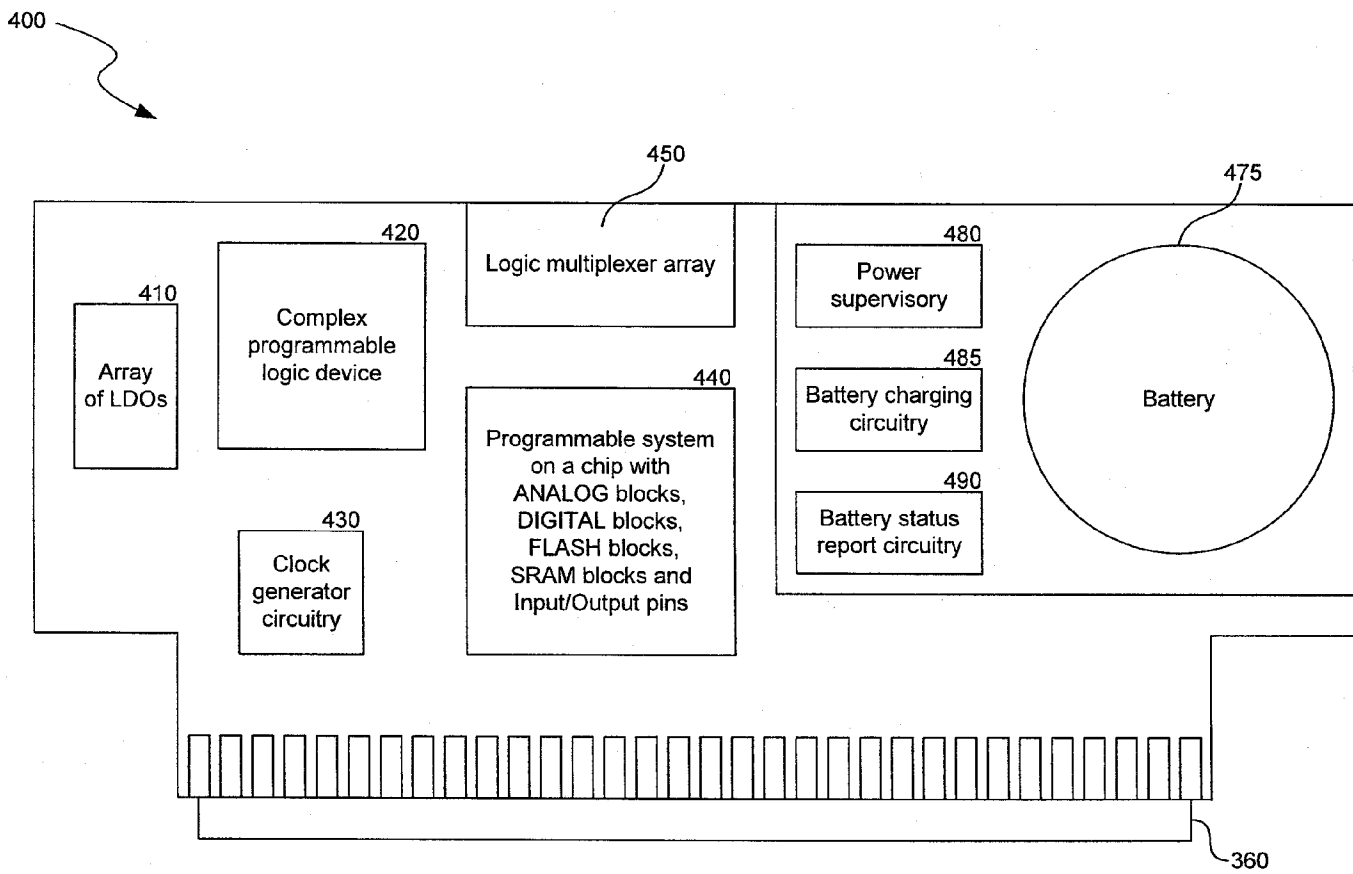


FIG. 4

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