

UNITED STATES PATENT AND TRADEMARK OFFICE

BEFORE THE PATENT TRIAL AND APPEAL BOARD

SAMSUNG ELECTRONICS CO., LTD.,

Petitioner,

v.

NETLIST, INC.,

Patent Owner

Patent No. 11,016,918

Issued: May 25, 2021

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Inventors: Chi-She Chen, Jeffrey C. Solomon, Scott H. Milton, Jayesh R. Bhakta

Title: Flash-DRAM Hybrid Memory Module

Inter Partes Review No. IPR2022-00996

PETITIONER'S UPDATED EXHIBIT LIST

EXHIBIT LIST

Exhibit #	Description
1001	U.S. Patent No. 11,016,918
1002	File History of U.S. Patent No. 11,016,918
1003	Declaration of Dr. Andrew Wolfe
1004	Curriculum Vitae of Dr. Andrew Wolfe
1005	File History of U.S. Provisional Application No. 60/941,586
1006	File History of U.S. Patent Application No. 12/131,873
1007	File History of U.S. Patent Application No. 12/240,916
1008	File History of U.S. Provisional Application No. 61/512,871
1009	File History of U.S. Patent Application No. 13/559,476
1010	File History of U.S. Patent Application No. 14/489,269
1011	File History of U.S. Patent Application No. 14/840,865
1012	File History of U.S. Patent Application No. 15/934,416
1013	[Intentionally Omitted]
1014	<i>SanDisk Corp. v. Netlist, Inc.</i> , IPR2014-00994, Paper No. 1 (PTAB June 20, 2014) (833 Patent IPR Petition)
1015	<i>SanDisk Corp. v. Netlist, Inc.</i> , IPR2014-00994, Paper No. 8 (PTAB Dec. 16, 2014) (833 Patent Institution Decision)
1016	<i>Smart Modular Techs. Inc. v. Netlist, Inc.</i> , IPR2014-01370, Paper No. 8 (PTAB Sept. 22, 2014) (833 Patent IPR Corrected Petition)
1017	<i>Smart Modular Techs. Inc. v. Netlist, Inc.</i> , IPR2014-01370, Paper No. 13 (PTAB Mar. 13, 2015) (833 Patent Institution Decision)

Exhibit #	Description
1018	<i>SK hynix Inc. et al. v. Netlist, Inc.</i> , IPR2017-00649, Paper No. 1 (PTAB Jan. 13, 2017) (833 Patent IPR Petition)
1019	<i>SK hynix Inc. et al. v. Netlist, Inc.</i> , IPR2017-00649, Paper No. 7 (PTAB July 24, 2017) (833 Patent Institution Decision)
1020	<i>SK hynix Inc. et al. v. Netlist, Inc.</i> , IPR2017-00692, Paper No. 1 (PTAB Jan. 17, 2017) (831 Patent IPR Petition)
1021	<i>SK hynix Inc. et al. v. Netlist, Inc.</i> , IPR2017-00692, Paper No. 25 (PTAB July 5, 2018) (831 Patent Final Written Decision)
1022	<i>Micron Tech., Inc. et al. v. Netlist, Inc.</i> , IPR2022-00418, Paper No. 2 (PTAB Jan. 14, 2022) (833 Patent IPR Petition)
1023	U.S. Patent Application Publication No. 2006/0174140 to Harris <i>et al.</i>
1024	U.S. Patent No. 7,724,604 to Amidi <i>et al.</i>
1025	U.S. Patent Application Publication No. 2006/0080515 to Spiers <i>et al.</i>
1026	JEDEC Standard, DDR2 SDRAM Specification, JESD79-2B (January 2005) (“JESD79-2B”)
1027	JEDEC Standard, FBDIMM: Advanced Memory Buffer (AMB), JESD82-20 (March 2007) (“JESD82-20”)
1028	JEDEC Standard, FBDIMM Specification: DDR2 SDRAM Fully Buffered DIMM (FBDIMM) Design Specification, JESD205 (March 2007) (“JESD205”)
1029	Declaration of Julie Carlson for JESD82-20 and JESD205
1030	U.S. Patent No. 7,719,866 to Boldo
1031	PCI Local Bus Specification Revision 2.2 (1998)

Exhibit #	Description
1032	Mohan et al., Power Electronics: Converters, Applications, and Design (2d ed. 1995)
1033	U.S. Patent No. 7,721,130 to Prete <i>et al.</i>
1034	U.S. Patent No. 6,798,709 to Sim <i>et al.</i>
1035	[Intentionally Omitted]
1036	[Intentionally Omitted]
1037	U.S. Patent Application Publication No. 2008/0238536 to Hayashi <i>et al.</i>
1038	U.S. Patent No. 6,856,556 to Hajeck
1039	U.S. Patent Application Publication No. 2010/0257304 to Rajan <i>et al.</i>
1040	Texas Instruments, TPS51020 Datasheet (December 2003)
1041	Fairchild Semiconductor, FAN5026 Datasheet (October 2005)
1042	Murata Power Supply Reference Guide for Xilinx FPGAs (September 2006)
1043	Murata Power Supply Reference Guide for Altera FPGAs (February 2008)
1044	U.S. Patent Application Publication No. 2010/0205470 to Moshayedi <i>et al.</i>
1045	JEDEC Standard, Double Data Rate (DDR) SDRAM Specification, JESD79 (June 2000) (“JESD79”)
1046	JEDEC Standard, DDR3 SDRAM, JESD79-3A (September 2007) (“JESD79-3A”)
1047	U.S. Patent No. 7,023,187 to Shearon <i>et al.</i>

Exhibit #	Description
1048	Murata, DC-DC Converter Specification (DRAFT), MPD4S014S Datasheet (Jan. 21, 2008)
1049	Micron, NAND Flash Memory Datasheet (January 2006)
1050	U.S. Patent No. 7,692,938 to Petter
1051	[Intentionally Omitted]
1052	[Intentionally Omitted]
1053	[Intentionally Omitted]
1054	[Intentionally Omitted]
1055	U.S. Patent Application Publication No. 2008/0101147 to Amidi
1056	U.S. Patent No. 5,563,839 to Herdt <i>et al.</i>
1057	U.S. Patent No. 6,693,840 to Shimada <i>et al.</i>
1058	Lenk, John D., <i>Simplified Design of Switching Power Supplies</i> (1995)
1059	U.S. Patent No. 7,061,214 to Mayega <i>et al.</i>
1060	U.S. Patent No. 5,630,096 to Zuravleff <i>et al.</i>
1061	Analog Devices, ADM1066 Datasheet (2006)
1062	Alan Moloney, <i>Power-Supply Management—Principles, Problems, and Parts</i> , Analog Dialogue (May 2006)
1063	National Semiconductor, LMC6953 PCI Local Bus Power Supervisor Datasheet (October 1996)
1064	U.S. Patent Application Publication No. 2007/0136523 to Bonella <i>et al.</i>
1065	U.S. Patent Application Publication No. 2009/0034354 to Resnick

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