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DEPOSITION OF ANDREW WOLFE, Ph.D.,  
appearing remotely, located in Santa Clara,  
California, taken on behalf of the Petitioner,  
beginning at 9:06 a.m., on Thursday, March 16, 2023,  
sworn remotely by Janis Jennings, Certified Shorthand  
Reporter No. 3942, CCRR, located in the City of  
Walnut Creek, County of Contra Costa, State of  
California.

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WITNESS PAGE  
ANDREW WOLFE, Ph.D.  
VOLUME I (Pages 1 - 199)

EXAMINATION BY MS. ZHONG 5

E X H I B I T S

EXHIBIT PAGE  
Exhibit 2101 Intel Developer Forum "Fully 41  
Buffered DIMM (FB-DIMM) Design  
Considerations" dated 2/18/04

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THURSDAY, MARCH 16, 2023; 9:06 A.M.

ANDREW WOLFE, Ph.D.,

the witness herein, was sworn and  
testified as follows:

EXAMINATION 08:54

BY MS. ZHONG: 08:54

Q. Good morning, Dr. Wolfe. 08:54

A. How are you? 09:07

Q. Can you please -- good. 09:07

Can you please state your full name for the  
record? 09:07

A. Andrew Wolfe. 09:07

Q. Okay. Dr. Wolfe, you're familiar with fully  
buffered dual in-line memory modules; correct? 09:07

(Clarification requested by Reporter.) 09:07

BY MS. ZHONG: 09:07

Q. Dr. Wolfe, you are familiar with fully  
buffered dual in-line memory modules; correct? 09:07

A. I'm familiar with that term as it is used in  
certain JEDEC standards. 09:07

Q. And fully buffered dual in-line memory  
modules are abbreviated as FB-DIMM; correct? 09:08

A. Sometimes they are. 09:08

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