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(54) REMOTE MONITORING AND CONTROL OF **EQUIPMENT OVER COMPUTER** NETWORKS USING A SINGLE WEB INTERFACING CHIP

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U.S. Cl. 712/29; 712/28; 712/31; 709/203; 709/230; 709/236; 709/211; 709/328; 717/118; 717/136; 717/148

(58) Field of Search 709/319, 320, 709/311, 332, 219, 210, 315, 329, 218, 203, 230, 224, 232, 226, 234, 236, 245, 250, 253, 328, 324, 211, 221, 209, 231; 235/462.15, 472.01; 712/217, 28, 30, 31, 29, 38, 32; 713/201, 167, 153, 165, 200, 151, 202; 702/122; 707/513, 501, 2, 103, 104, 5, 3, 10; 705/51.14, 54; 717/118, 136,

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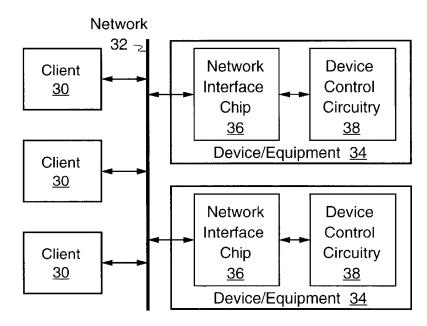
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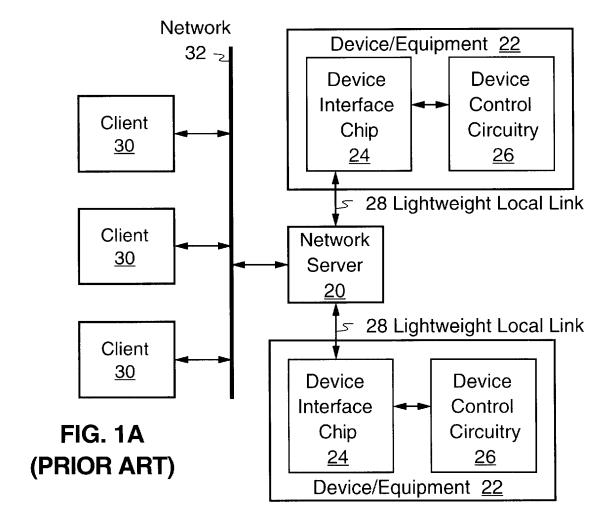
ABSTRACT (57)

A single integrated circuit chip interfaces device control circuitry of a device to a client machine via a computer network. The chip comprises an internal data bus; a central processing unit connected to the internal data bus; an internal memory connected to the internal data bus; a device interface connected to the internal data bus, wherein the device interface comprises circuit blocks for communicating digital information between the integrated circuit and the device control circuitry; and a network interface connected to the internal data bus, wherein the network interface comprises circuit blocks for communicating digital information between the integrated circuit and the computer network. The internal memory comprises instructions for implementing complete internet protocol functionality on the network; translating information between network protocol formats and a format of the device; transferring information between the network and the device control circuitry; and sending customized software to the client machine over the network, wherein the software is executable on the client machine, and wherein the software enables the client machine to generate device control signals and to receive device status information. The single integrated circuit chip provides inexpensive, compact, powerful, and versatile interfacing of a large variety of devices to high performance computer networks.

11 Claims, 12 Drawing Sheets









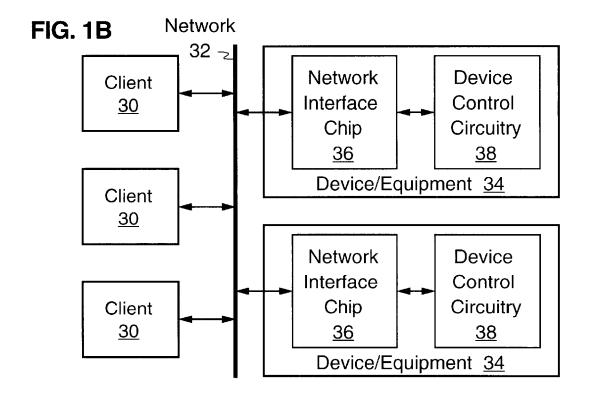




FIG. 2

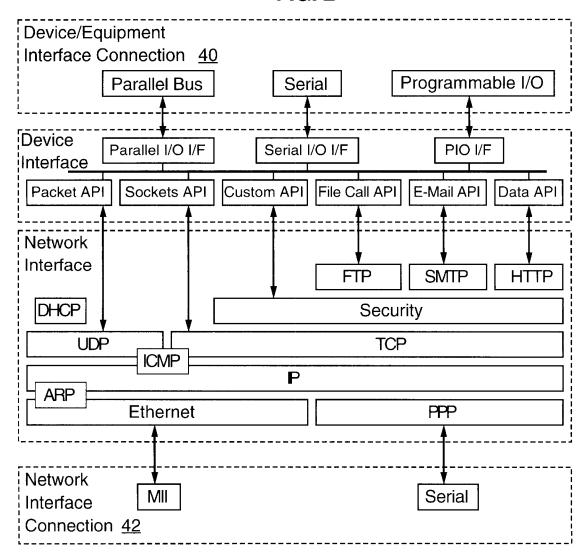




FIG. 3

DEVICE PROTOCOL: DEVICE I/F HARDWARE: Variable Query/Change Parallel CPU Bus Procedure Call Serial Raw Data Programmable I/O CHIP SERVICES: **CHIP HARDWARE:** Full Web Server Single Chip Optional Off-Chip PROM E-mail Sockets Data Pass-Through **NETWORK PROTOCOL: NETWORK HARDWARE:** TCP/IP Internet HTTP Intranet/Ethernet Modem/PPP SMTP Raw Socket Data Wireless **Custom Protocols CLIENT SOFTWARE: CLIENT HARDWARE:** Java Applet Workstation HTML Interface **Laptop Computer** Palmtop Computer **Custom Application**



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