

CINCUIT DESIGN, LAYOUT, AND SIMULATION



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Process Information for Hand-Calculations

Process	CN20 NMOS	CN20 PMOS	CMOS14 NMOS	CMOS14 PMOS
<u> </u>				
Information	App. A	App. A	App. C	App. C
VDD (VSS = 0)	5 V	5 V	3.3 V	3.3 V
minimum $L_{\scriptscriptstyle drawn}$	2.0 μm	2.0 µm	0.6 µm	0.6 µm
$DL = L_{drawn} - L_{eff}$	0.6 µm	0.8 μm	0.06 µm	0.09 µm
minimum $W_{\scriptscriptstyle drawn}$	3.0 µm	3.0 µm	0.9 µm	0.9 µm
$DW = W_{drawn} - W_{eff}$	0.14 μm	0.16 μm	0.35 μm	0.35 μm
$V_{\scriptscriptstyle THN}$ or $V_{\scriptscriptstyle THP}$	0.83 V	0.91 V	0.7 V	0.9 V
KP, μΑ/V²	50	17	170	50
λ, V ⁻¹	0.06 for $L \ge 5$ μm	0.06 for $L \ge 5$ μm		
I _{drive} (μΑ/μm)			380 (typ.)	190 (typ.)
C'_{ox}	800 aF/μm²	800 aF/μm²	3.7 fF/μm²	3.7 fF/µm²
R_n or R_p	12 kΩ · (<i>L/W</i>)	36 kΩ · (L/W)	(9 kΩ·μm)/W	(18 kΩ·μm)/W
$\tau_{_{\!\scriptscriptstyle R}}$ or $\tau_{_{\!\scriptscriptstyle P}}$	38 ps	114 ps	20 ps	40ps

Multipliers

Name	Symbol	Value
terra	T	1012
giga	G	10°
mega	M or (MEG)	10 ⁶
kilo	k	10³
milli	m	10-3
micro	μ (or u)	10 ⁻⁶
nano	n	10-9
pico	p	10 ⁻¹²
femto	f	10 ⁻¹⁵
atto	a	10 ⁻¹⁸



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Physical Constants

Name	Symbol	Value/Units
Vacuum dielectric constant	ϵ_0	8.85 aF/μm
Silicon dielectric constant	$oldsymbol{arepsilon}_{ ext{si}}$	11.7ε ₀
SiO ₂ dielectric constant	$\epsilon_{ m ox}$	$3.97\epsilon_{\scriptscriptstyle 0}$
SiN ₃ dielectric constant	$\mathcal{E}_{ ext{Ni}}$	16ε ₀
Boltzmann's constant	k	$1.38 \times 10^{-23} \text{ J/K}$
Electronic charge	q	$1.6 \times 10^{-19} \mathrm{C}$
Temperature	Т	K
Thermal voltage	$V_{_T}$	kT/q = 26 mV @ 300 K

Equations

Parameter	NMOS equations in terms of BSIM1 parameters
	for PMOS use V_{SD} , V_{SG} , V_{BS} , and V_{THP}
$V_{\scriptscriptstyle THN}$	$VFB + PHI + K1 \cdot \sqrt{PHI + V_{SB}} - K2 \cdot (PHI + V_{SB})$
C'_{ox}	ϵ_{ox}/TOX
KP	$MUZ \cdot C'_{ox}$
β	$KP \cdot (W/L)$
I_D (triode) $V_{DS} \le V_{GS} - V_{THN}$	$\beta \left((V_{GS} - V_{THN}) V_{DS} - \frac{V_{DS}^2}{2} \right) \text{ (for Long L)}$
I_D (saturation) $V_{DS} \ge V_{GS} - V_{THN}$	$\frac{\beta}{2}(V_{GS} - V_{THN})^2 [1 + \lambda(V_{DS} - V_{DS,sat})] \text{ (for Long L)}$
\mathcal{E}_m	$\beta(V_{GS} - V_{THN}) = \sqrt{2\beta I_D} \text{ or } I_D / (V_T \cdot N0)$
η	$K1 \cdot \left(2\sqrt{PHI + V_{SB}}\right)^{-1} - K2 \text{ and } g_{mb} = g_m \cdot \eta$
r_o	$1/(\lambda I_D)$

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CMOS Circuit Design, Layout, and Simulation

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